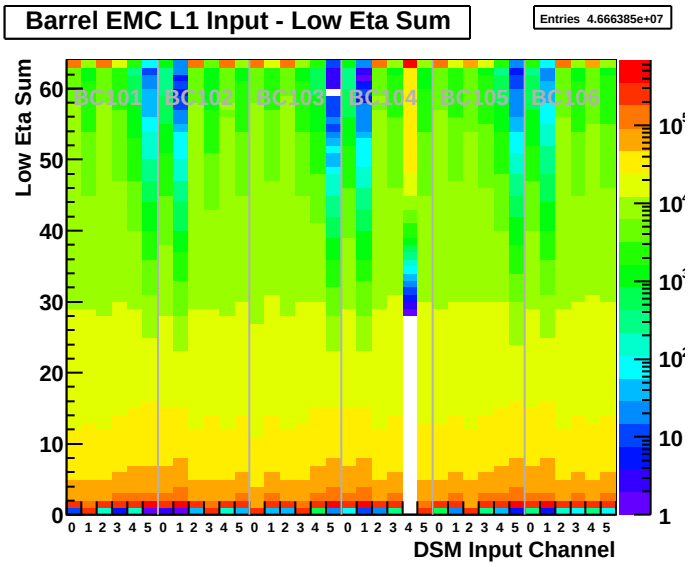
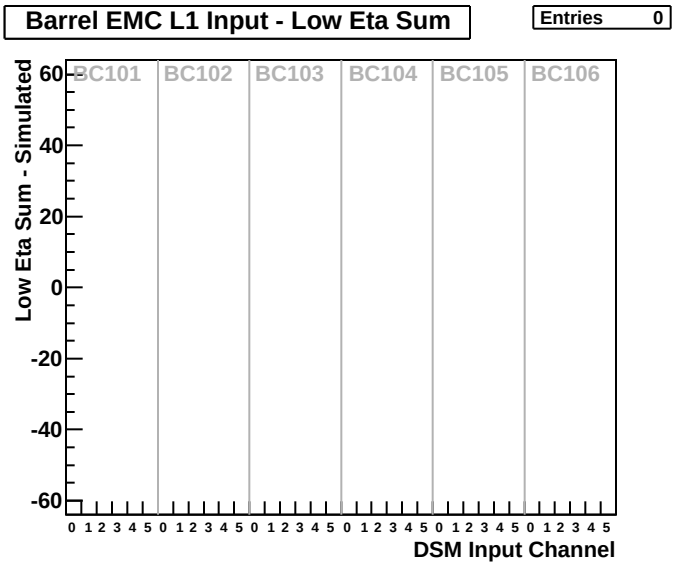
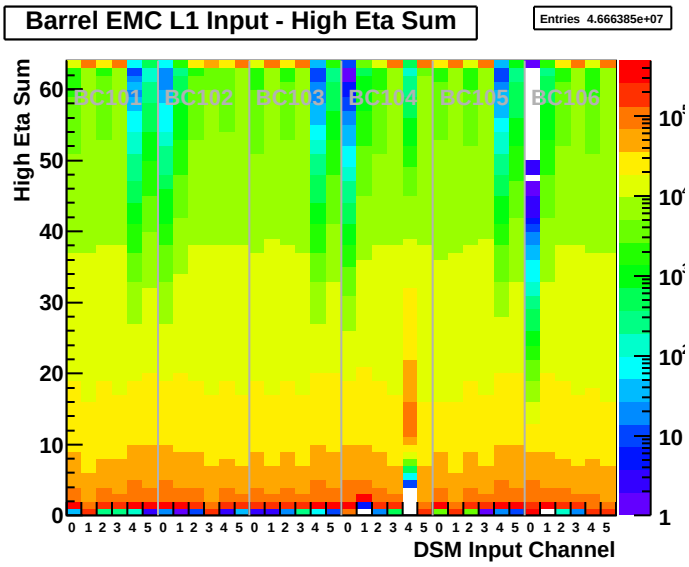
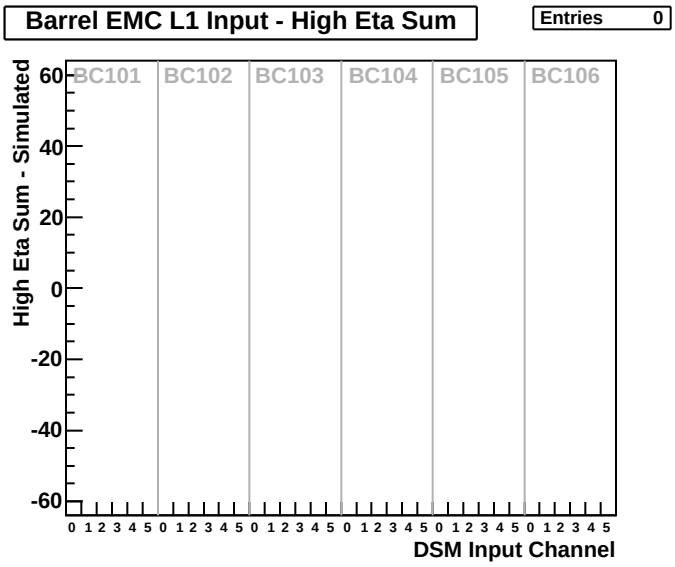
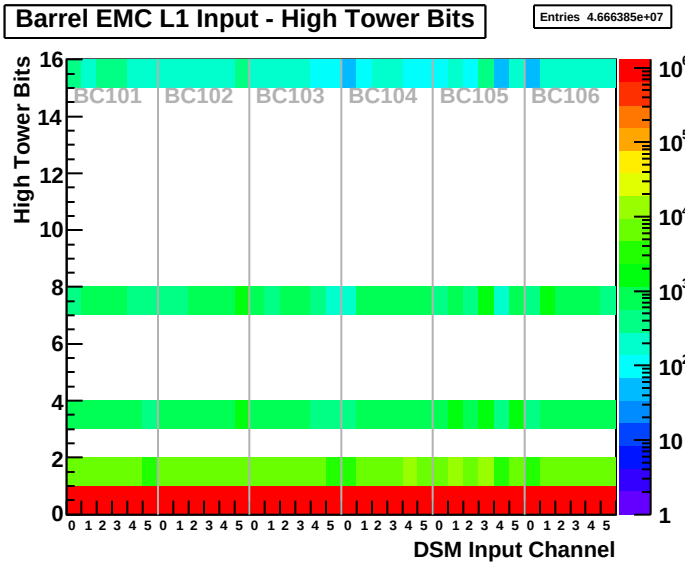
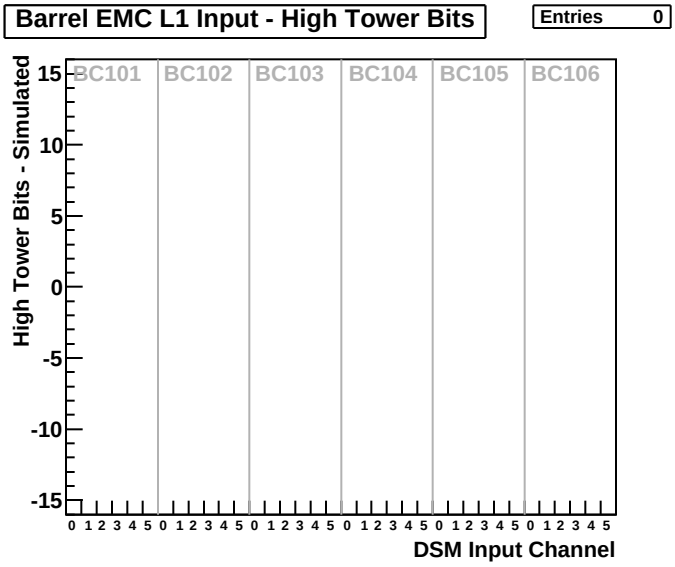

Barrel EMC L1 Input - Low Eta Sum
Entries 4.666385e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

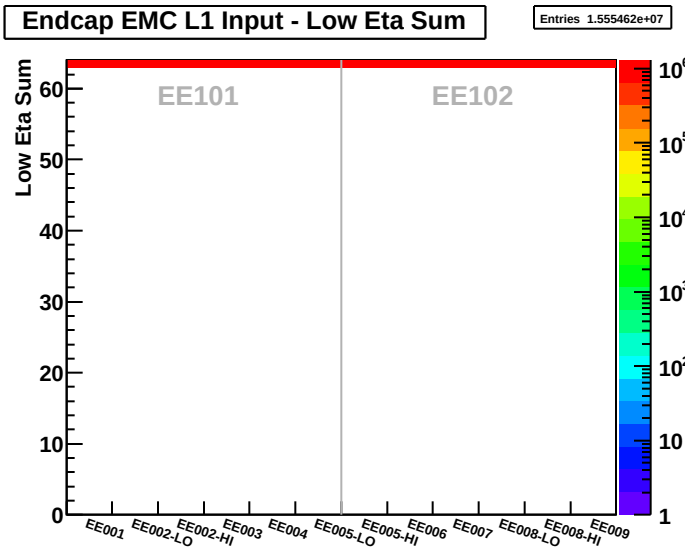

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

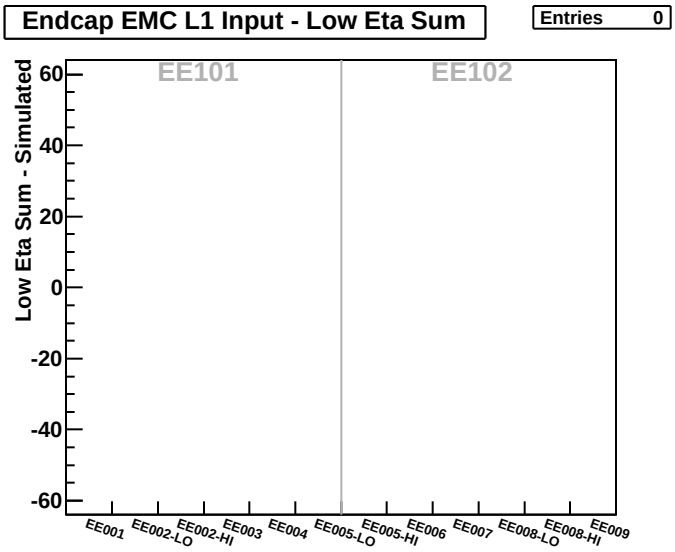

Barrel EMC L1 Input - High Eta Sum
Entries 4.666385e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

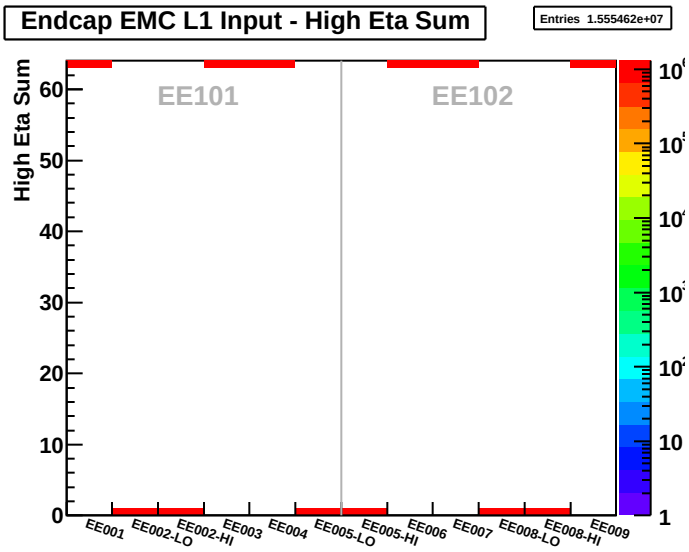

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

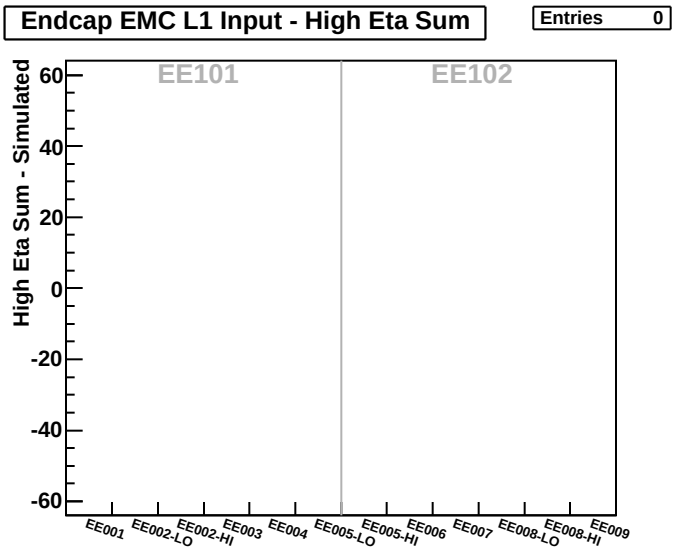

Barrel EMC L1 Input - High Tower Bits
Entries 4.666385e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

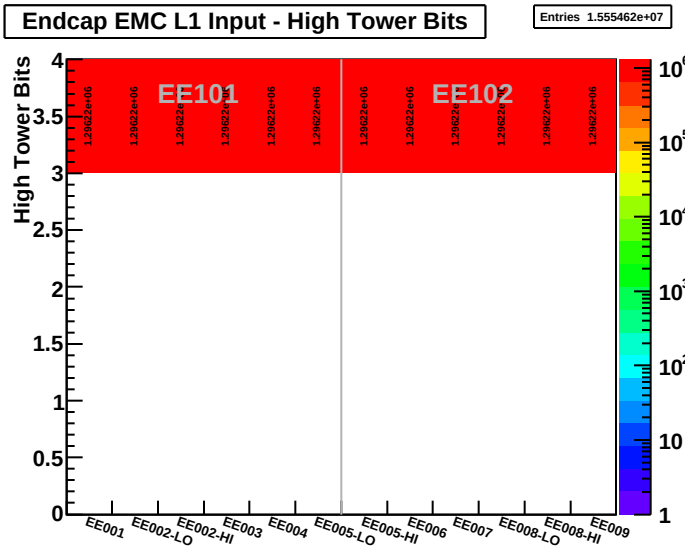

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

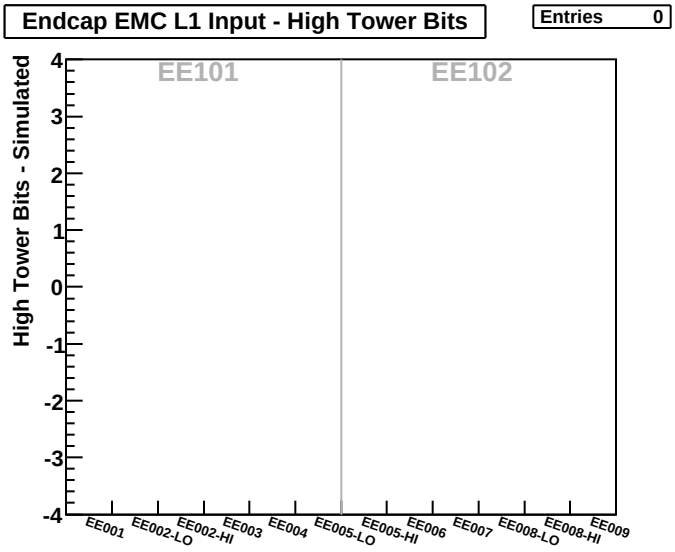

Endcap EMC L1 Input - Low Eta Sum
Entries 1.555462e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

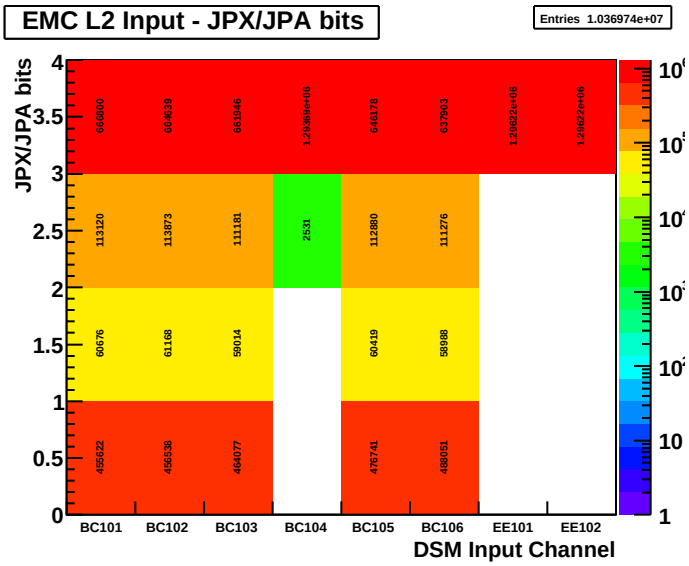

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

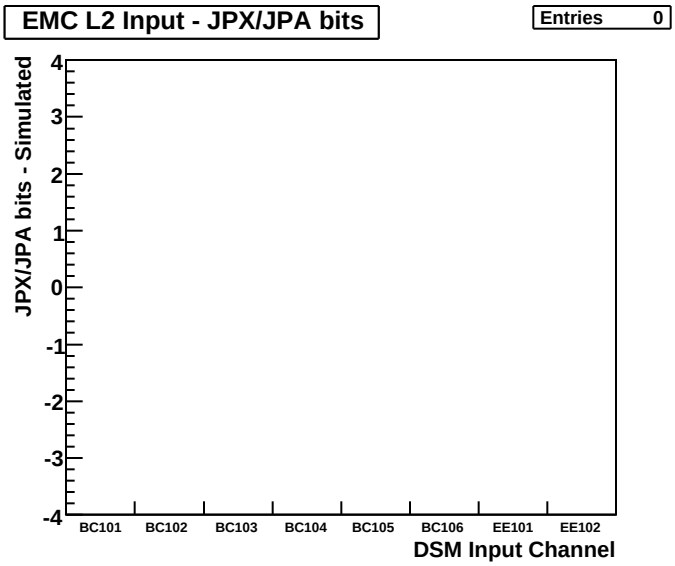

Endcap EMC L1 Input - High Eta Sum
Entries 1.555462e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

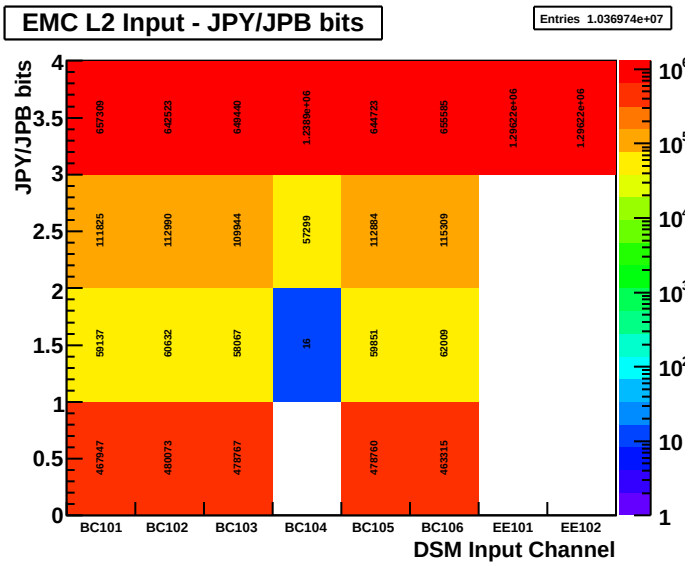

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

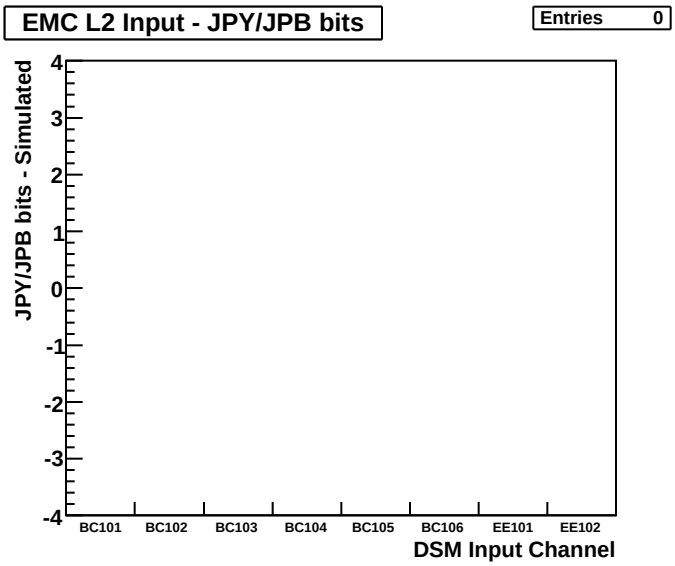

Endcap EMC L1 Input - High Tower Bits
Entries 1.555462e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

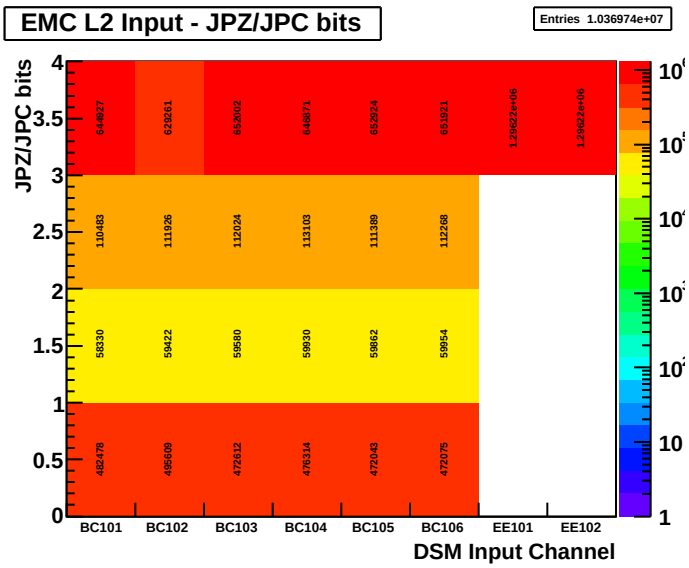

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

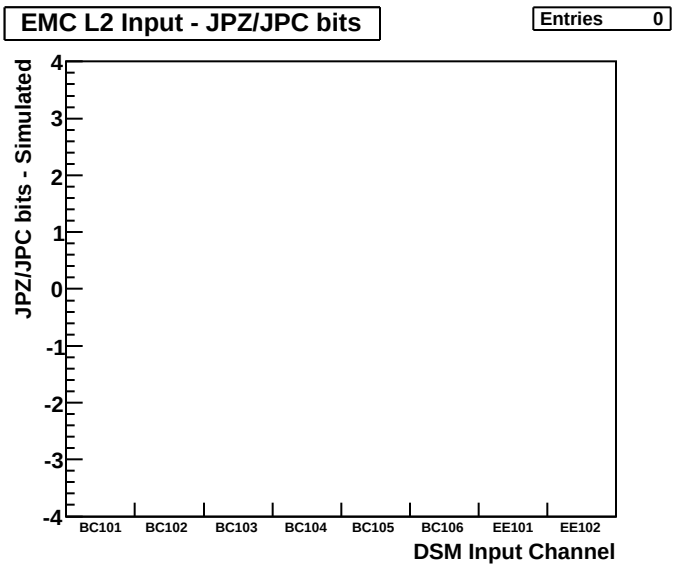

EMC L2 Input - JPX/JPA bits
Entries 1.036974e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
656800 654639 651946 1.29369e+06 646178 637903 1.29622e+06 1.29622e+06
113120 113873 111181 2531 112880 111276
60676 61168 59014 60419 58988
455622 456538 464077 476741 488051

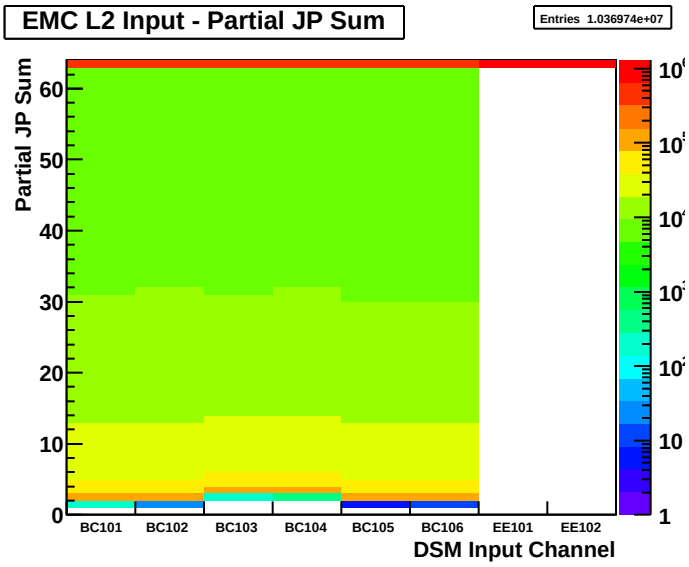

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

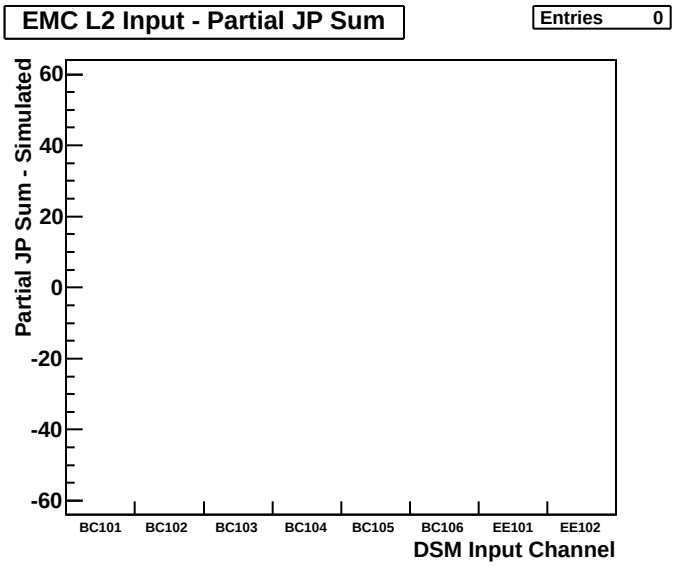

EMC L2 Input - JPY/JPB bits
Entries 1.036974e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
657309 642523 640440 1.2393e+06 644723 655585 1.29622e+06 1.29622e+06
111825 112990 109944 57299 112884 115399
59137 60632 58067 16 59851 62009
467947 480073 478767 478760 463315

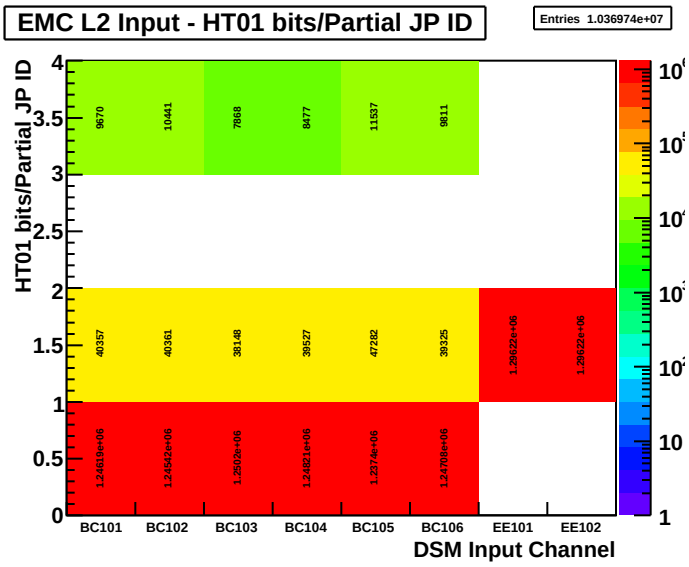

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

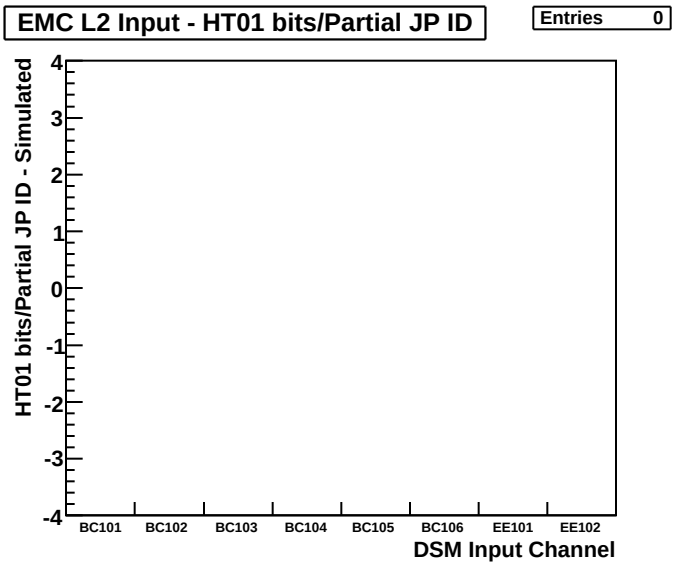

EMC L2 Input - JPZ/JPC bits
Entries 1.036974e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
644927 628261 652002 648871 652924 651921 1.29622e+06 1.29622e+06
110483 111926 112024 113103 111389 112268
58330 59422 59580 59930 59862 59954
482478 495609 472612 476314 472043 472075

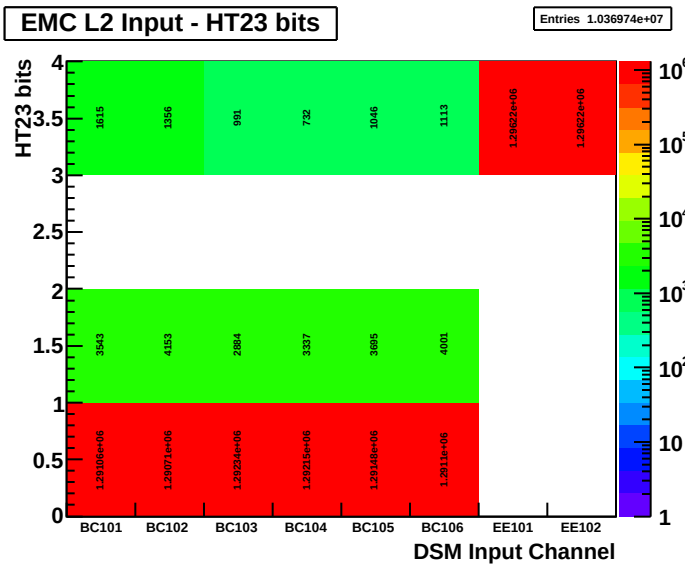

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

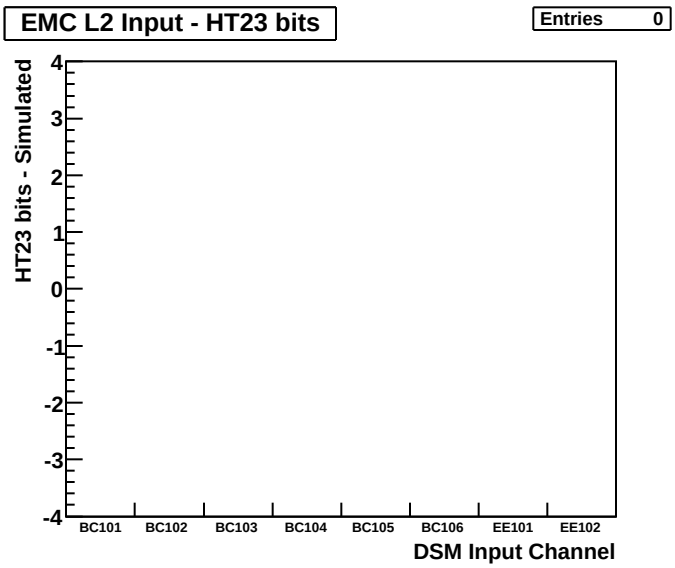

EMC L2 Input - Partial JP Sum
Entries 1.036974e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

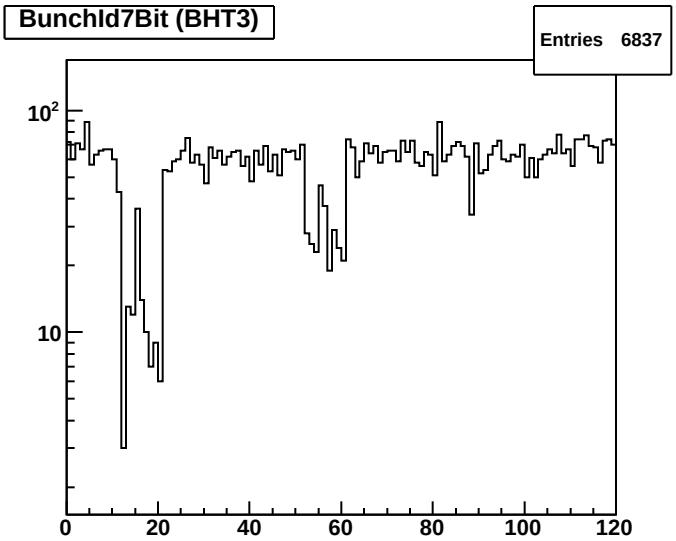

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

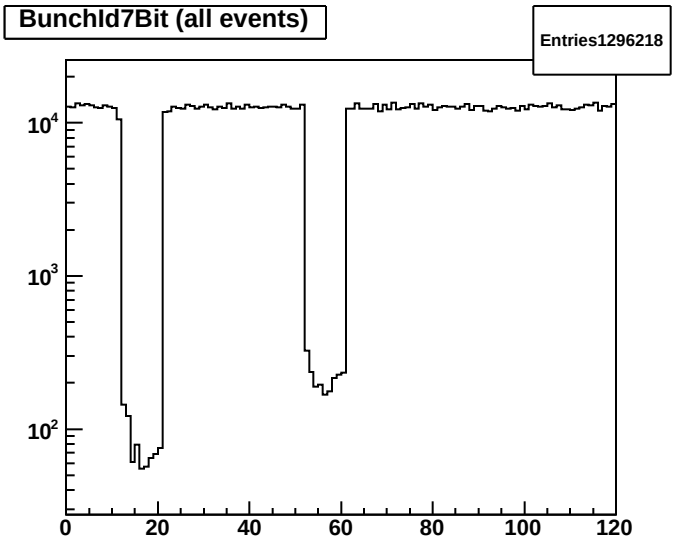

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.036974e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

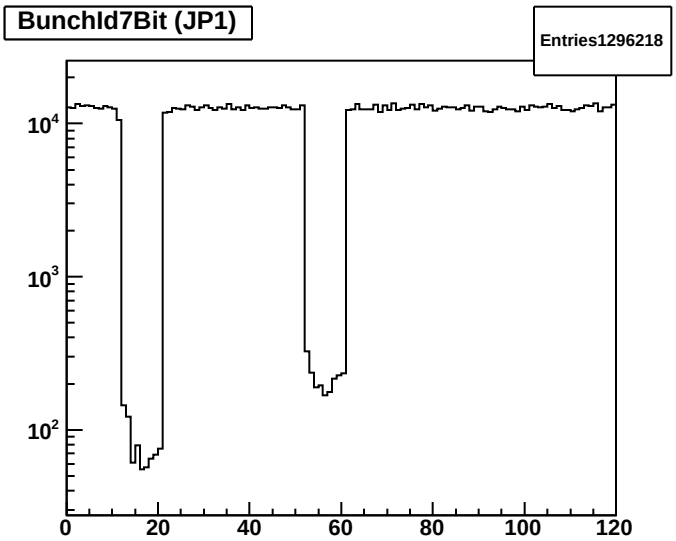

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

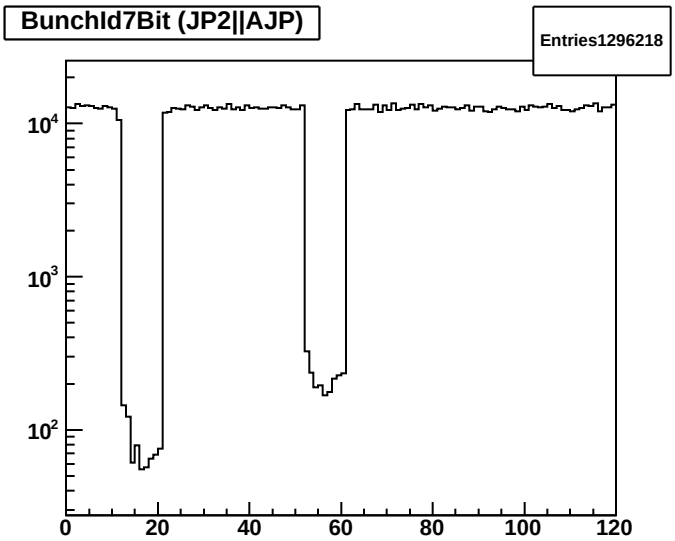

EMC L2 Input - HT23 bits
Entries 1.036974e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

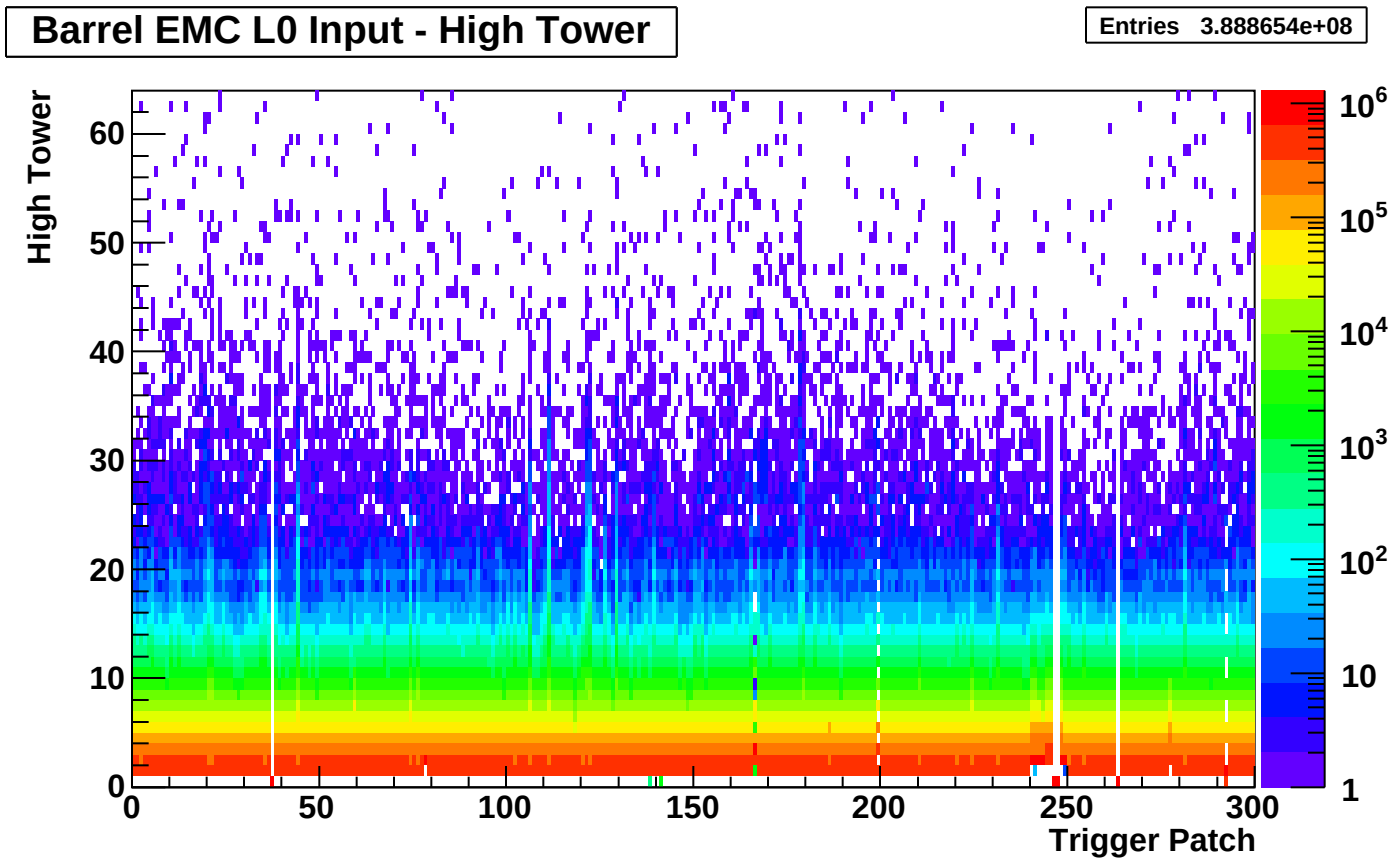

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

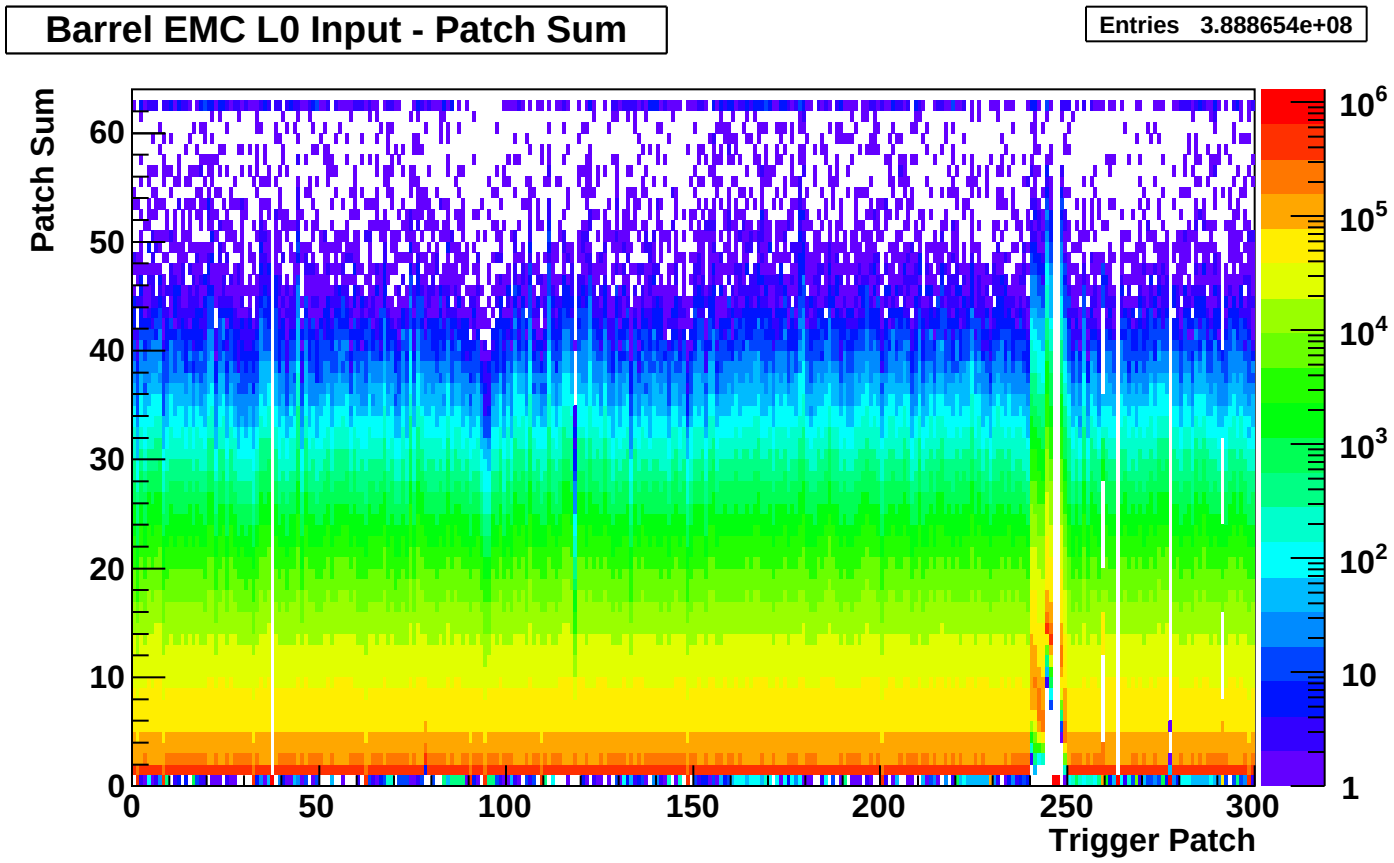

BunchId7Bit (BHT3)
Entries 6837
10^2
10
0
20
40
60
80
100
120

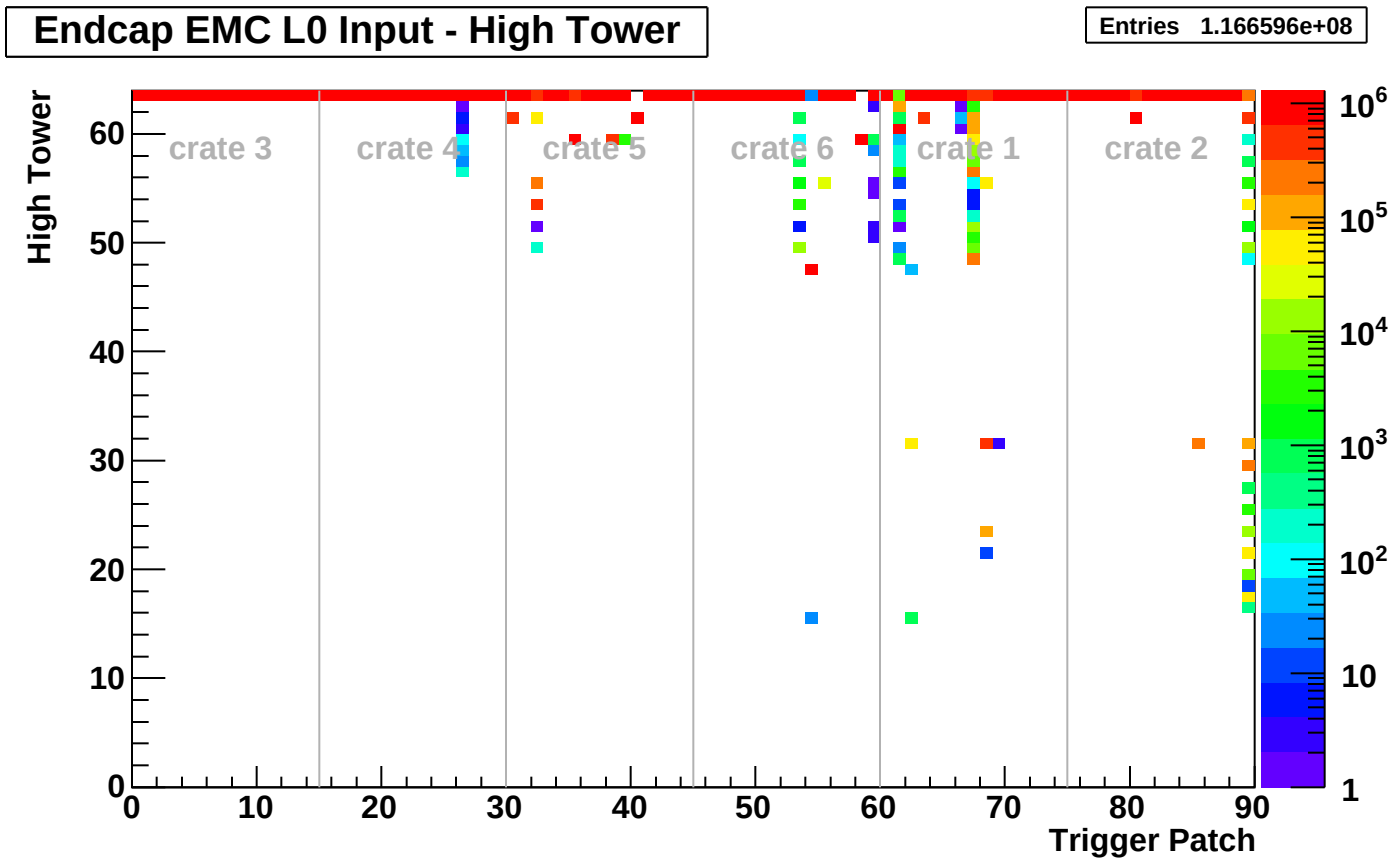

BunchId7Bit (all events)
Entries1296218
10^4
10^3
10^2
0
20
40
60
80
100
120

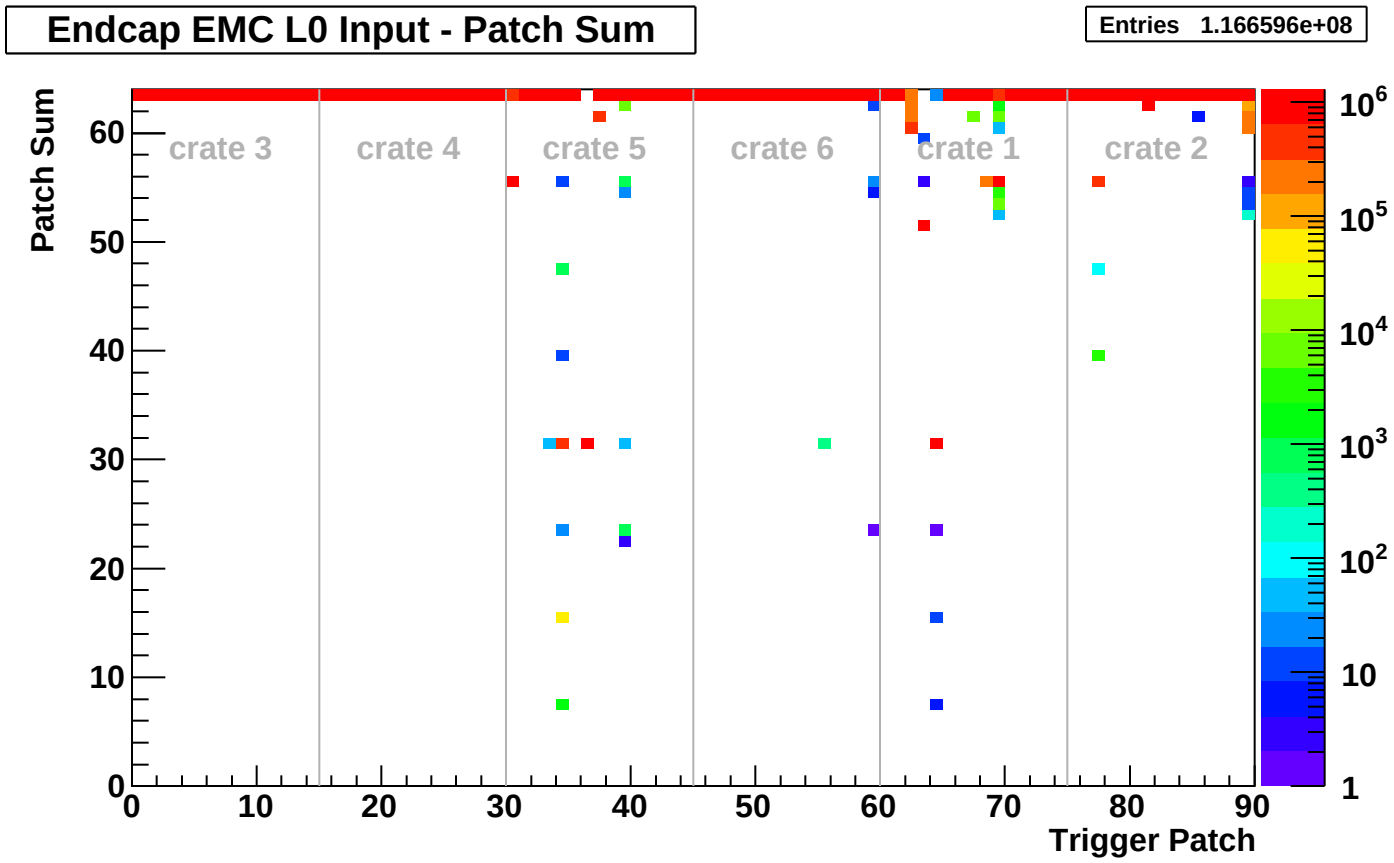

BunchId7Bit (JP1)
Entries1296218
10^4
10^3
10^2
0
20
40
60
80
100
120

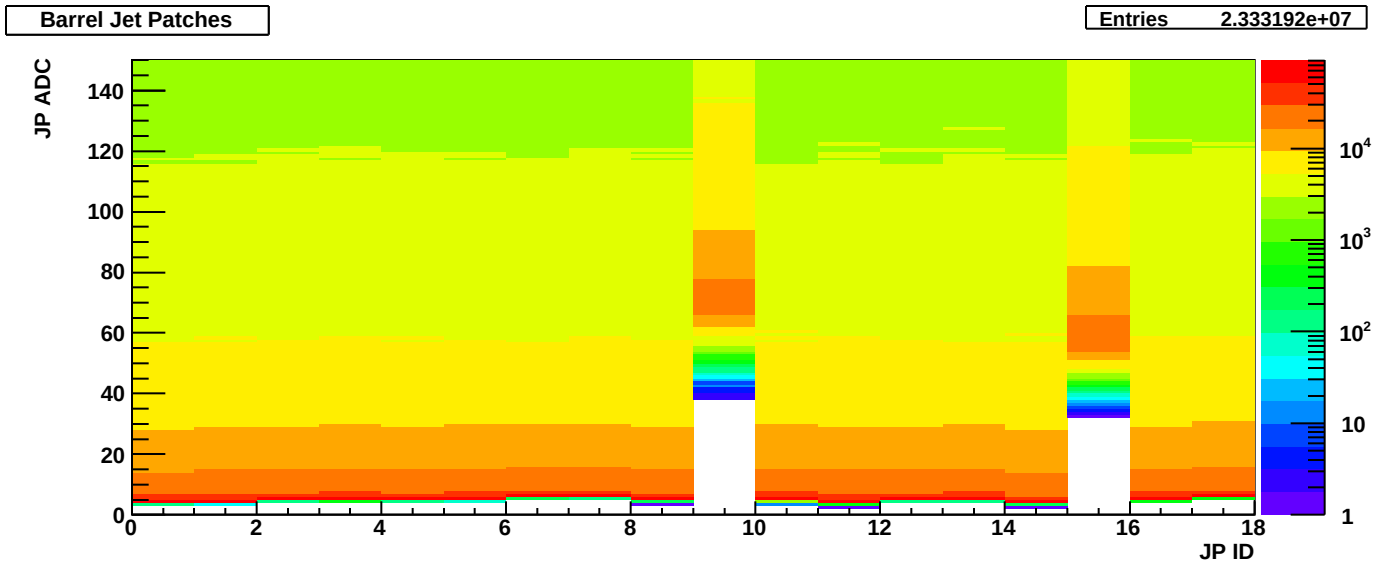

BunchId7Bit (JP2||AJP)
Entries1296218
10^4
10^3
10^2
0
20
40
60
80
100
120

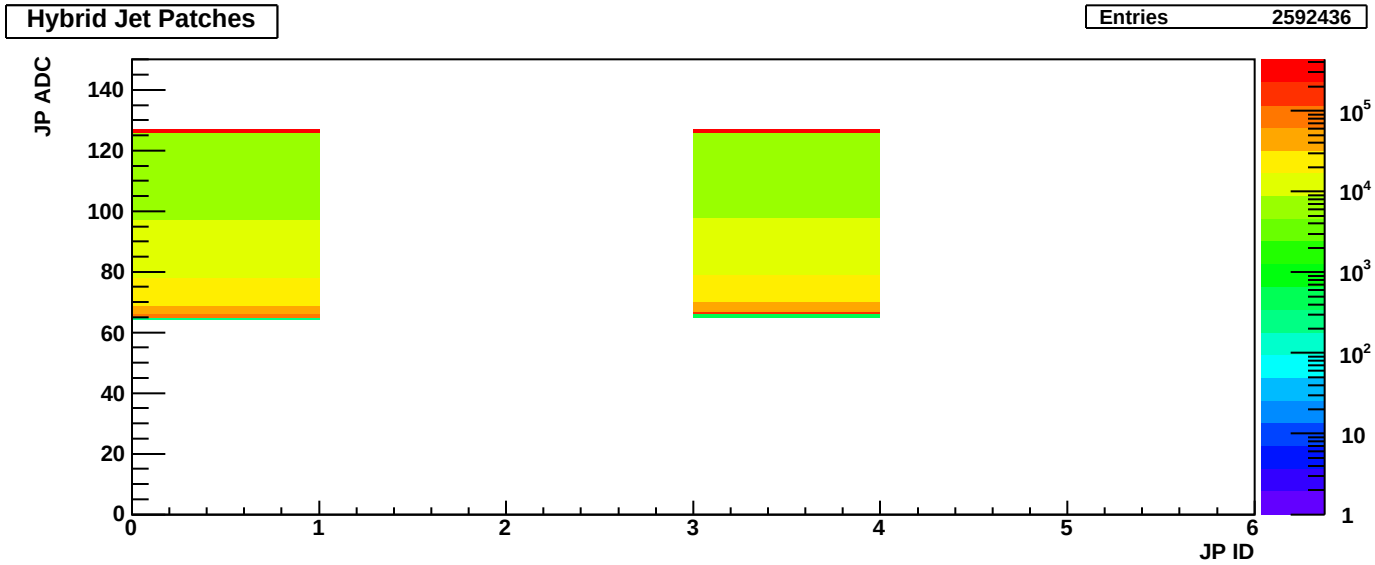

Barrel EMC L0 Input - High Tower
Entries 3.888654e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

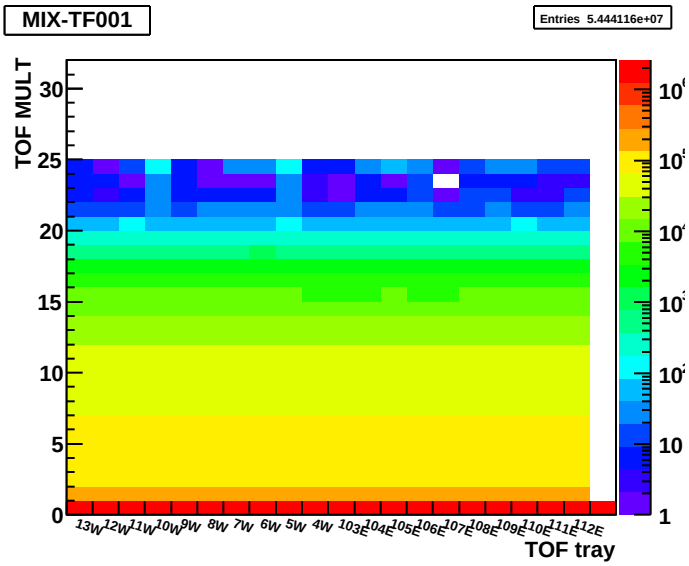

Barrel EMC L0 Input - Patch Sum
Entries 3.888654e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

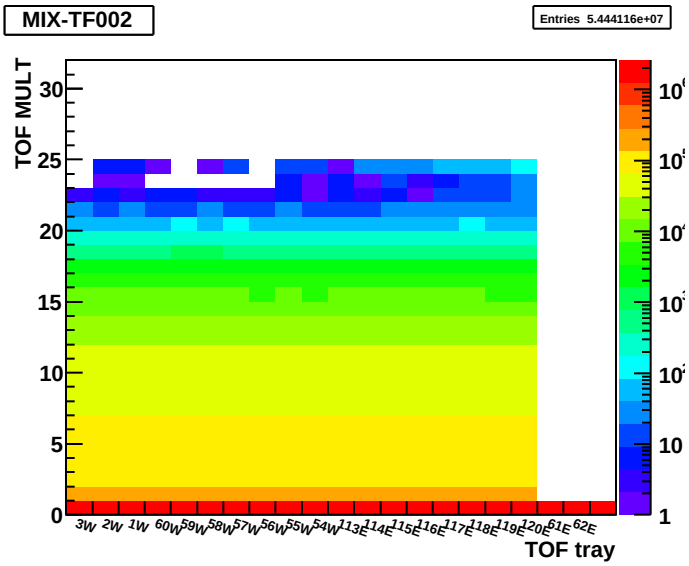

Endcap EMC L0 Input - High Tower
Entries 1.166596e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

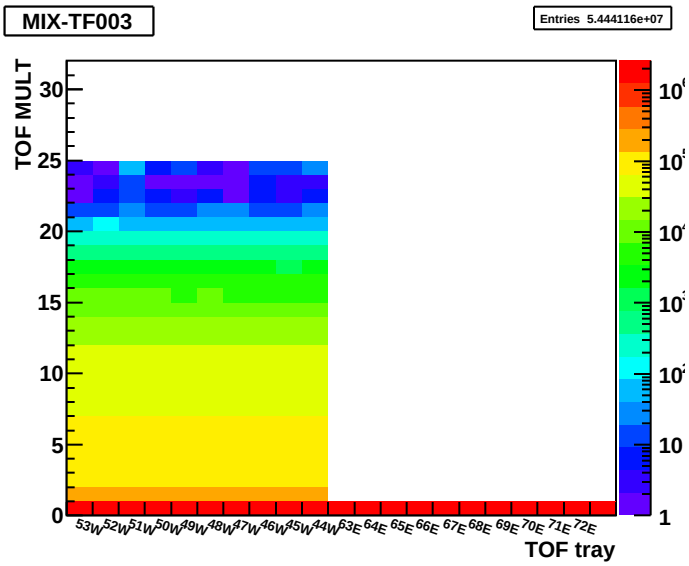

Endcap EMC L0 Input - Patch Sum
Entries 1.166596e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

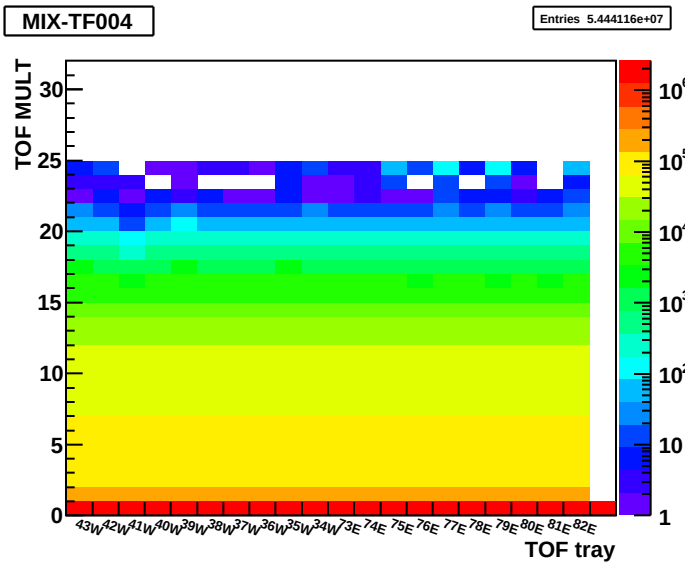

Barrel Jet Patches
Entries 2.333192e+07
JP ADC
JP ID

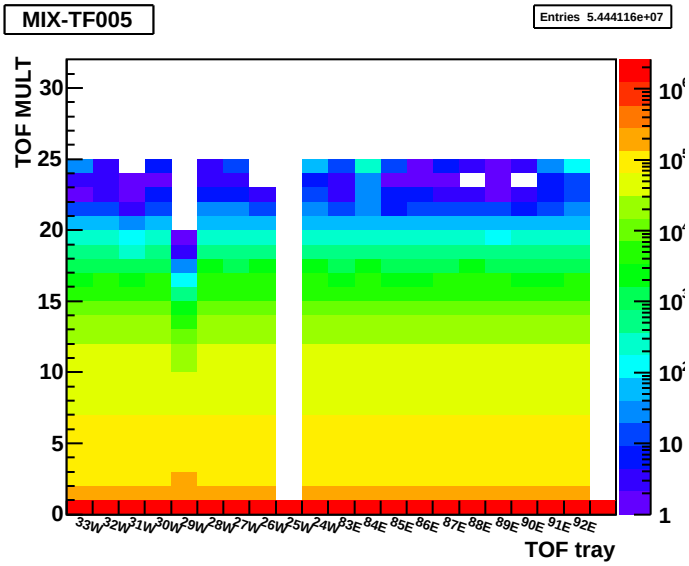

Hybrid Jet Patches
Entries 2592436
JP ADC
JP ID

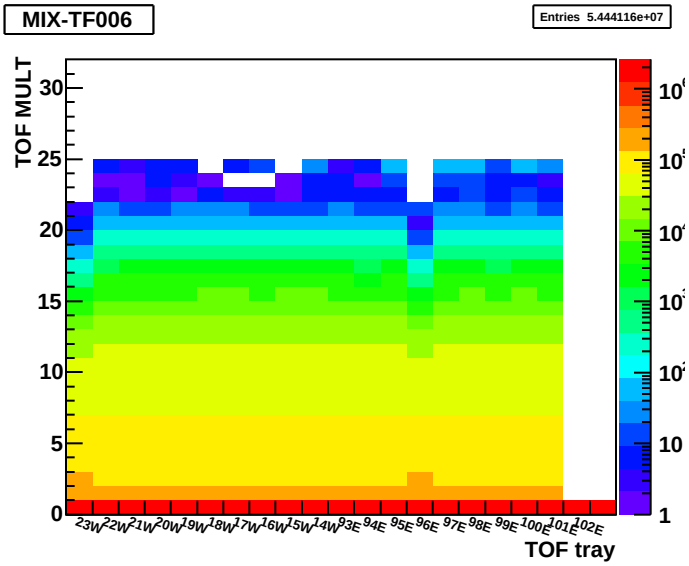

MIX-TF001
Entries 5.444116e+07
TOF MULT
TOF tray

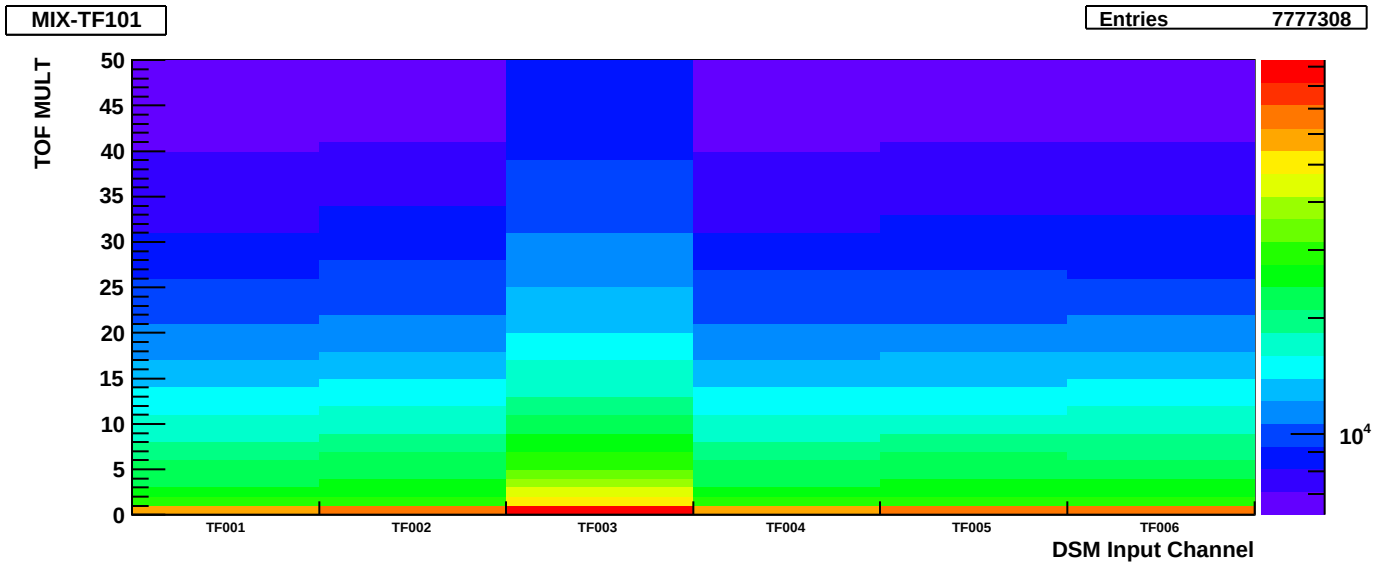

MIX-TF002
Entries 5.444116e+07
TOF MULT
TOF tray

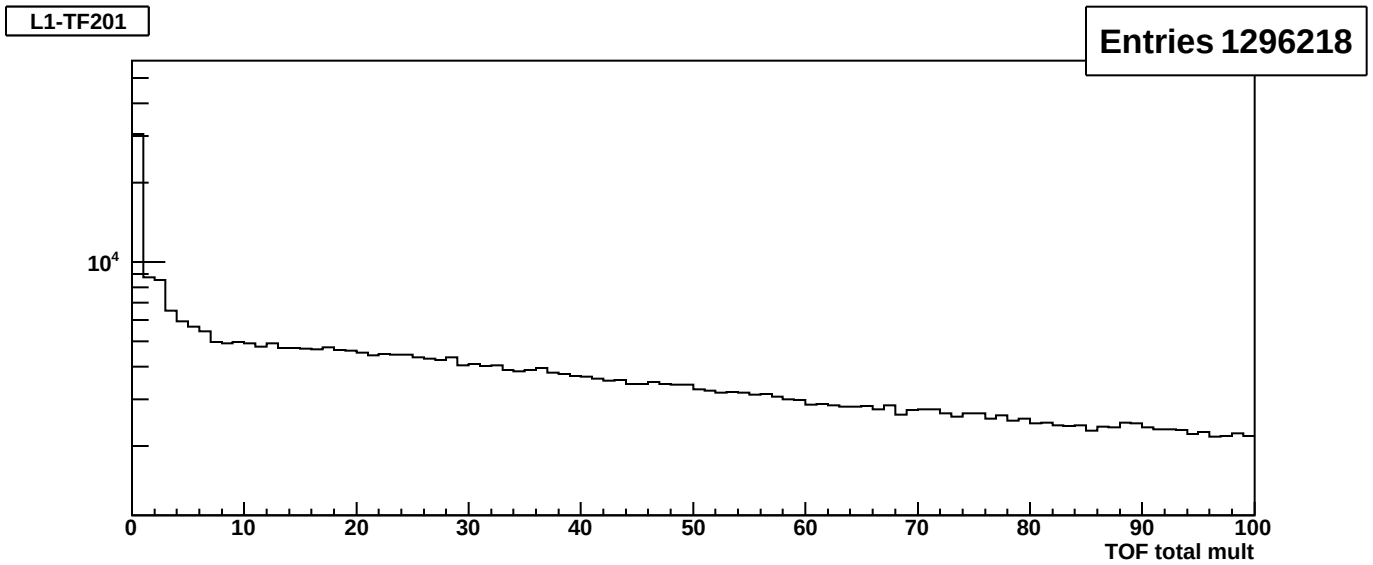

MIX-TF003
Entries 5.444116e+07
TOF MULT
TOF tray

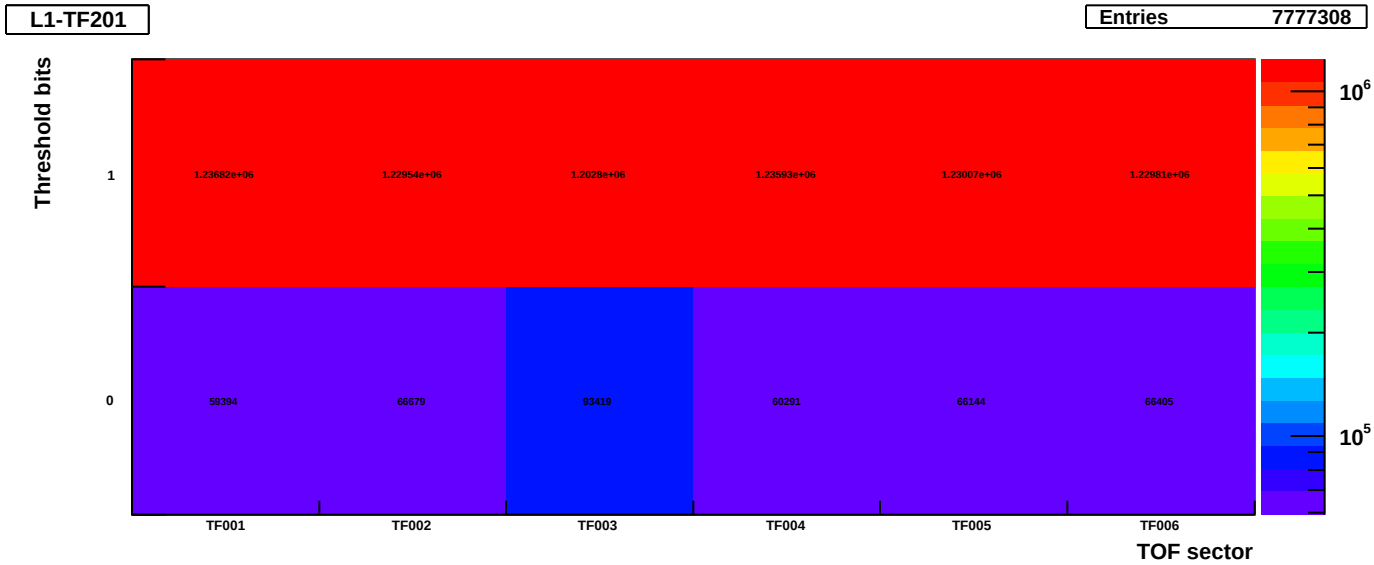

MIX-TF004
Entries 5.444116e+07
TOF MULT
TOF tray

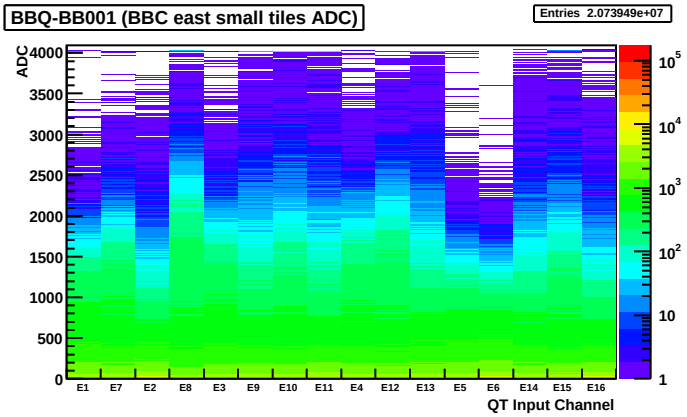

MIX-TF005
Entries 5.444116e+07
TOF MULT
TOF tray

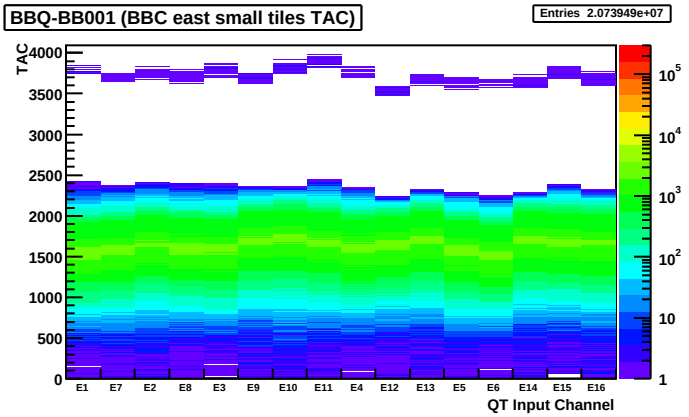

MIX-TF006
Entries 5.444116e+07
TOF MULT
TOF tray

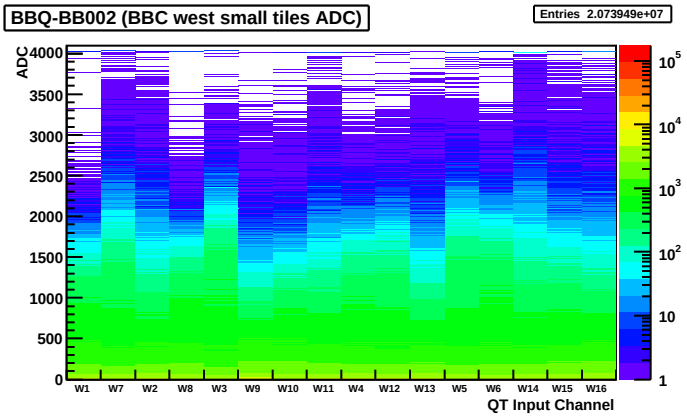

MIX-TF101
Entries 7777308
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

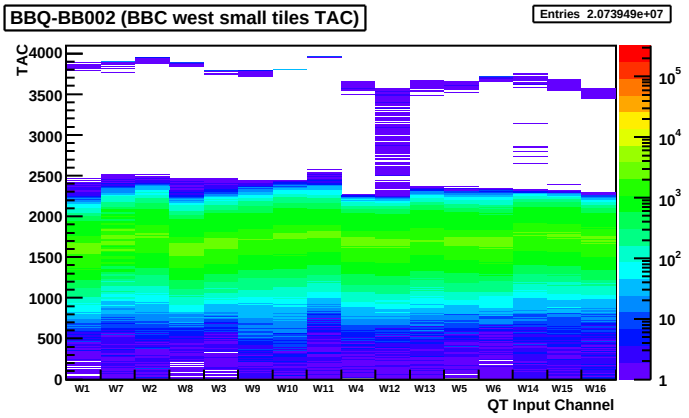

L1-TF201
Entries 1296218
10^4
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

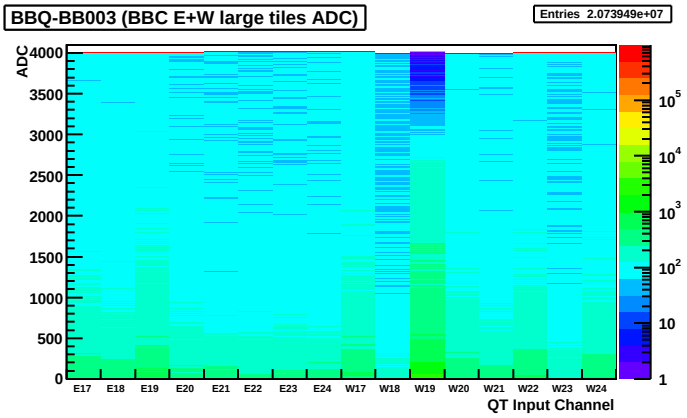

L1-TF201
Entries 7777308
Threshold bits
1
0
1.23682e+06
1.22954e+06
1.2028e+06
1.23593e+06
1.23007e+06
1.22981e+06
59394
66679
93419
60291
66144
66405
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

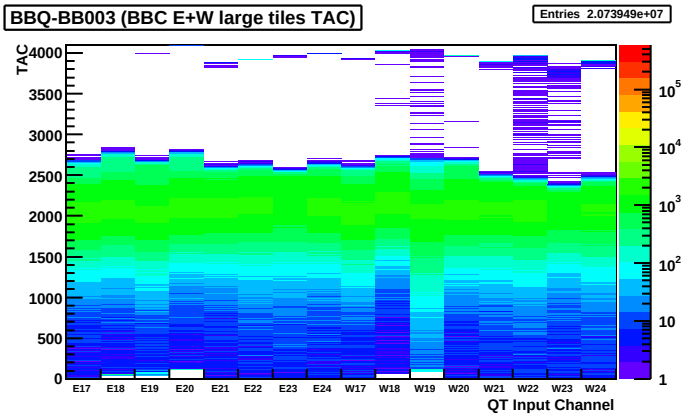

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.073949e+07
ADC
QT Input Channel

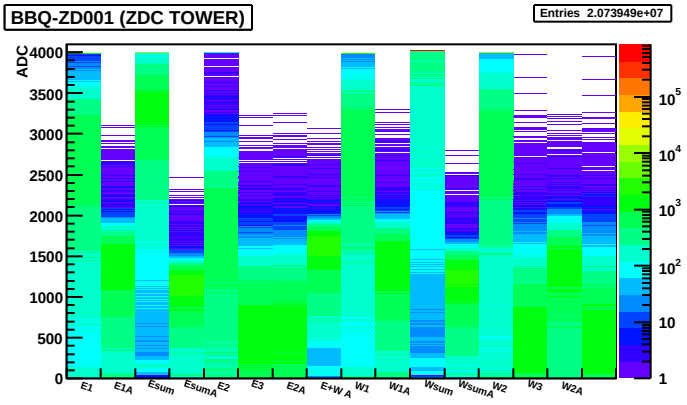

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.073949e+07
TAC
QT Input Channel

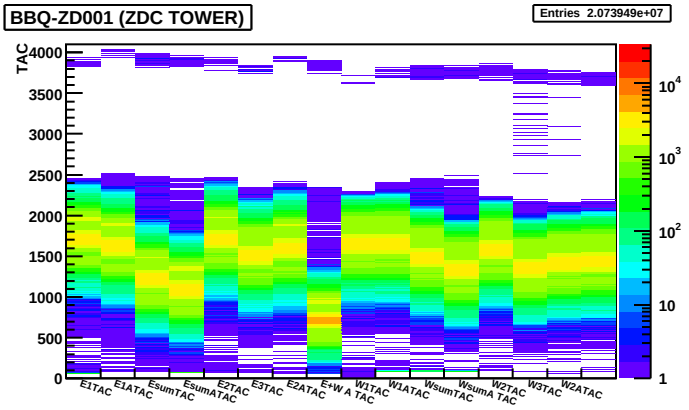

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.073949e+07
ADC
QT Input Channel

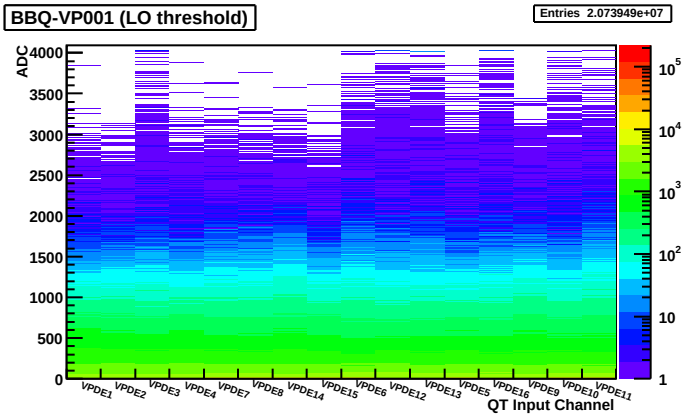

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.073949e+07
TAC
QT Input Channel

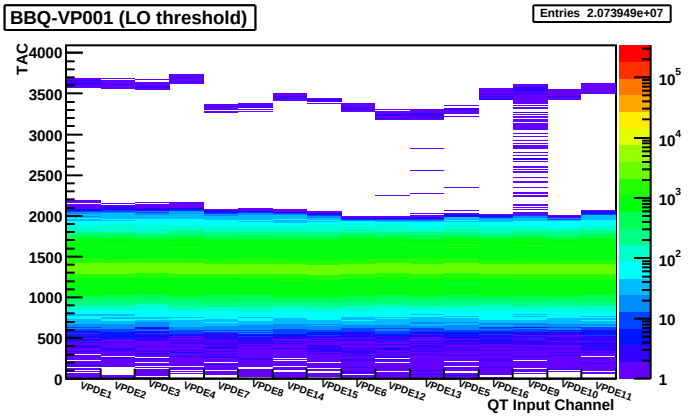

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.073949e+07
ADC
QT Input Channel

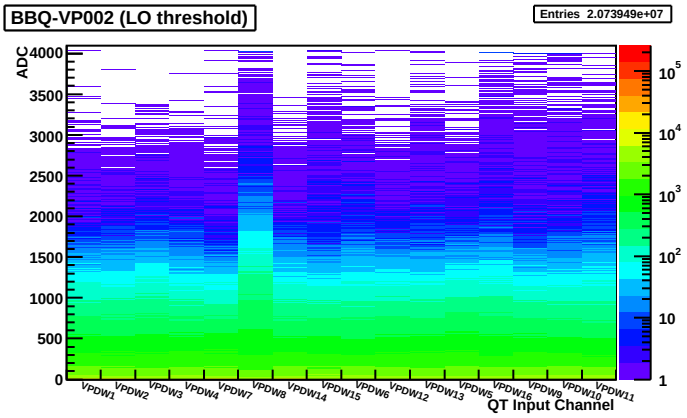

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.073949e+07
TAC
QT Input Channel

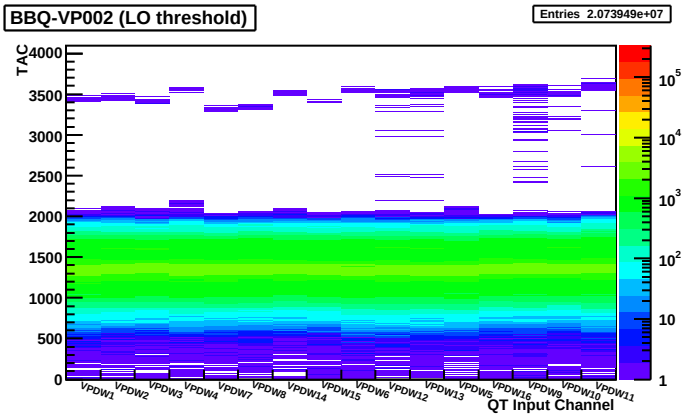

BBQ-ZD001 (ZDC TOWER)
Entries 2.073949e+07
ADC

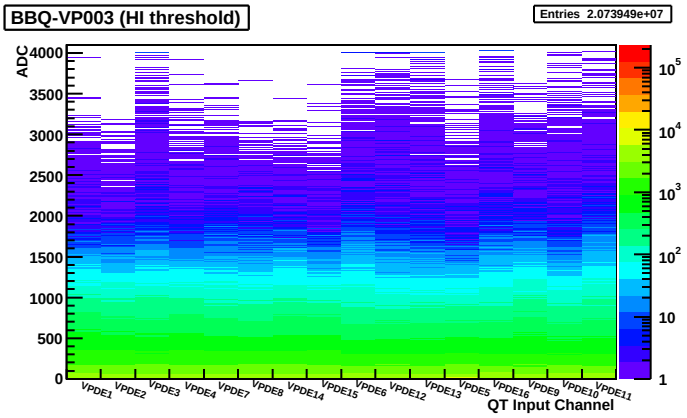

BBQ-ZD001 (ZDC TOWER)
Entries 2.073949e+07
TAC

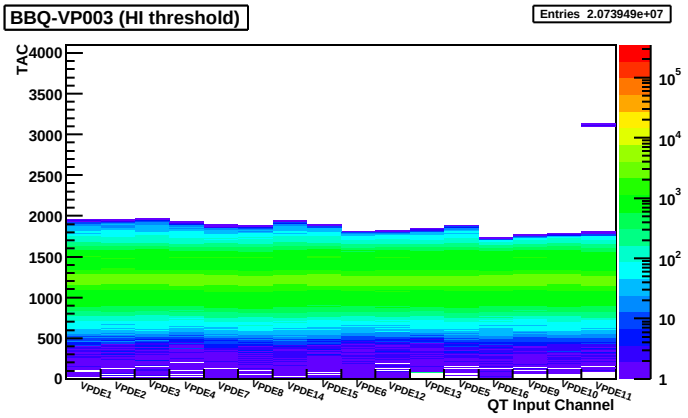

BBQ-VP001 (LO threshold)
Entries 2.073949e+07
ADC
QT Input Channel

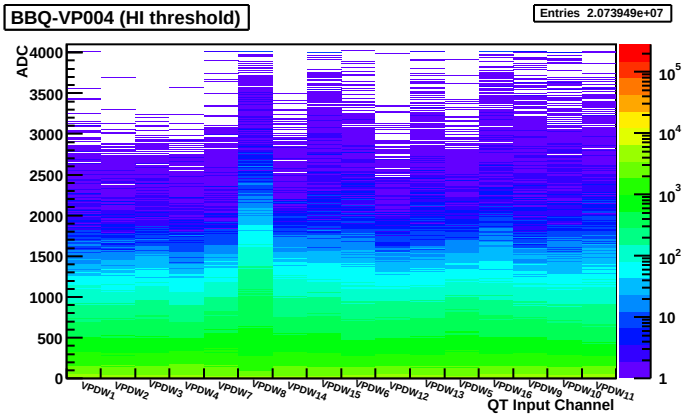

BBQ-VP001 (LO threshold)
Entries 2.073949e+07
TAC
QT Input Channel

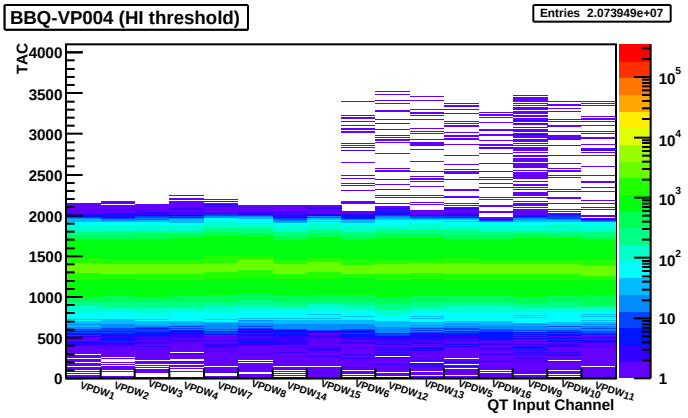

BBQ-VP002 (LO threshold)
Entries 2.073949e+07
ADC
QT Input Channel

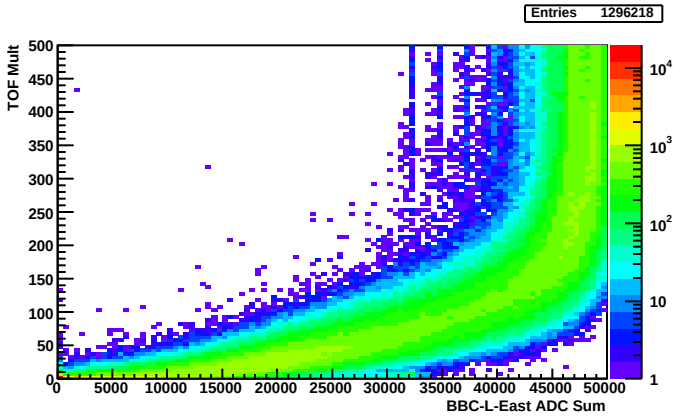

BBQ-VP002 (LO threshold)
Entries 2.073949e+07
TAC
QT Input Channel

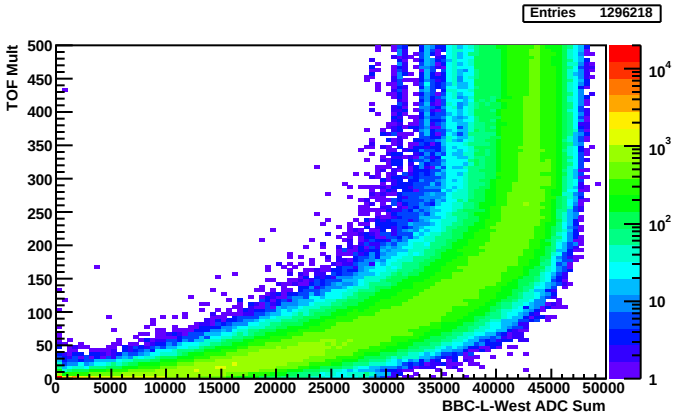

BBQ-VP003 (HI threshold)
Entries 2.073949e+07
ADC
QT Input Channel

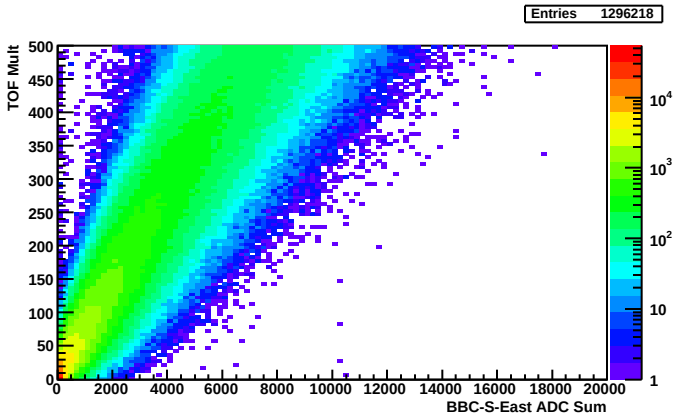

BBQ-VP003 (HI threshold)
Entries 2.073949e+07
TAC
QT Input Channel

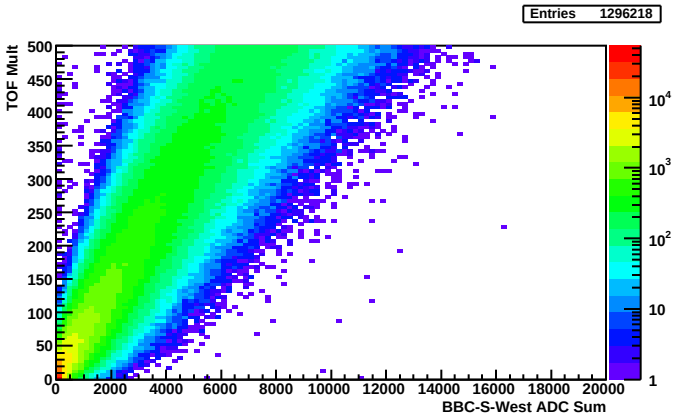

BBQ-VP004 (HI threshold)
Entries 2.073949e+07
ADC
QT Input Channel

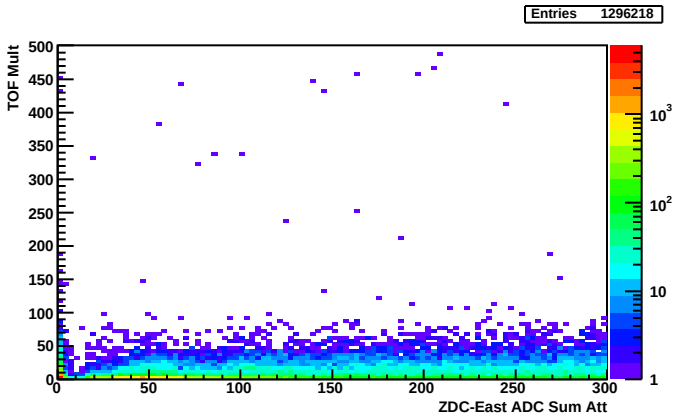

BBQ-VP004 (HI threshold)
Entries 2.073949e+07
TAC
QT Input Channel

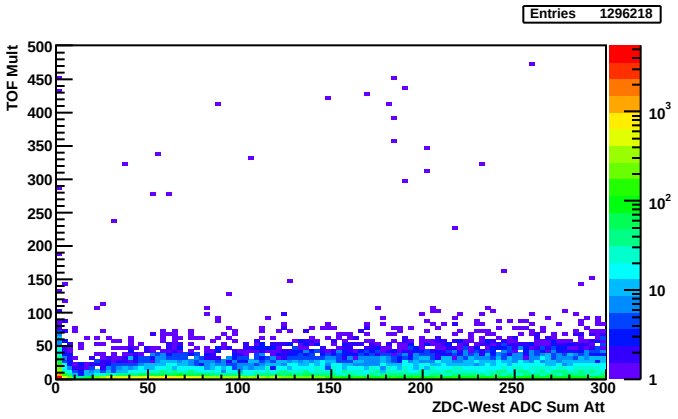

Entries 1296218
TOF Mult
BBC-L-East ADC Sum

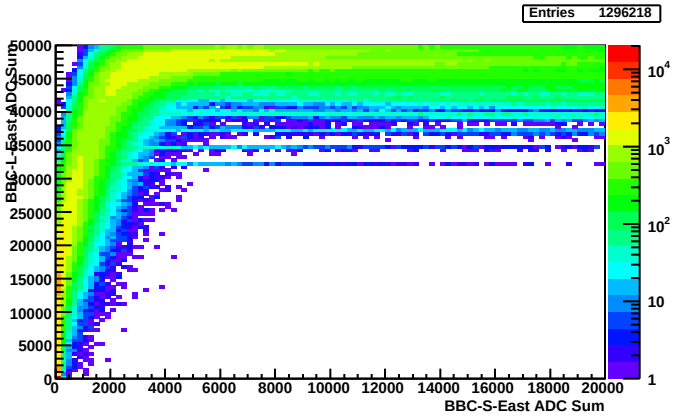

Entries 1296218
TOF Mult
BBC-L-West ADC Sum

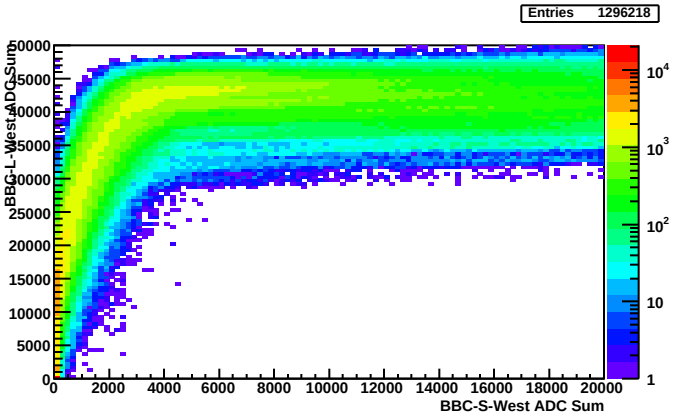

Entries 1296218
TOF Mult
BBC-S-East ADC Sum

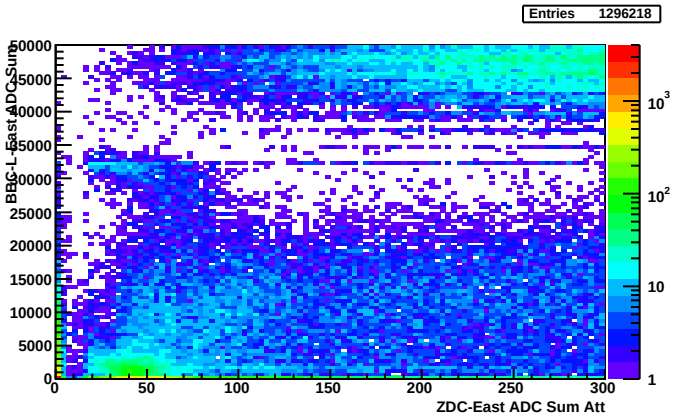

Entries 1296218
TOF Mult
BBC-S-West ADC Sum

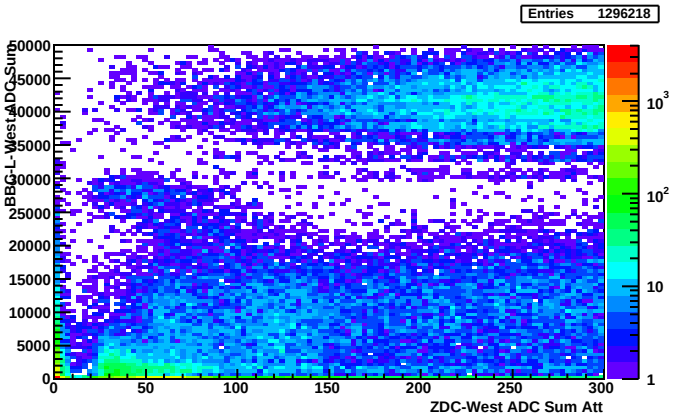

Entries 1296218
TOF Mult
ZDC-East ADC Sum Att

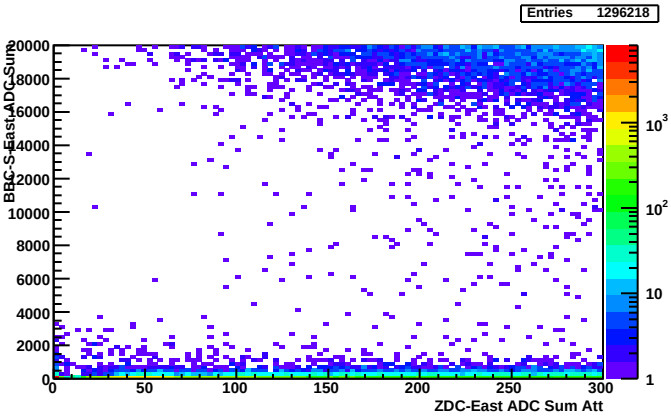

Entries 1296218
TOF Mult
ZDC-West ADC Sum Att


Entries 1296218
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1296218
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1296218
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 1296218
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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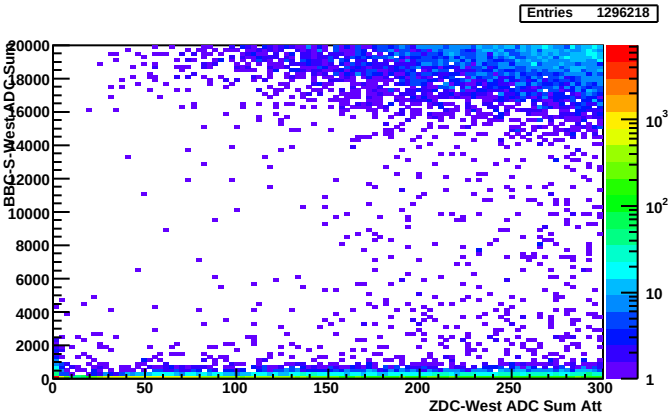
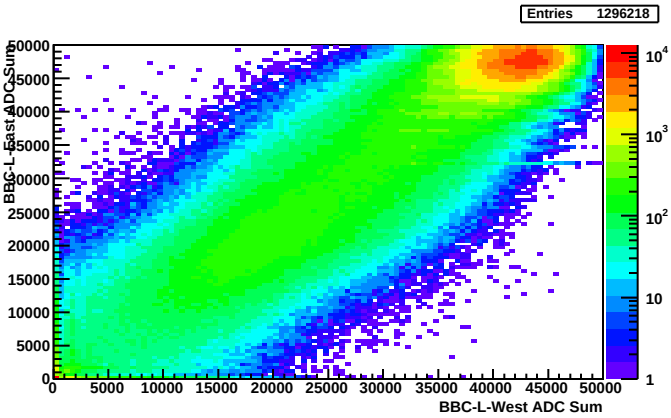
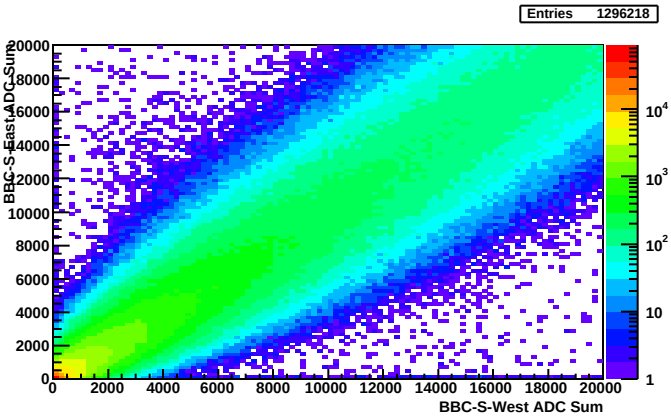
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1296218
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1296218
BBC-L-East ADC Sum
BBC-L-West ADC Sum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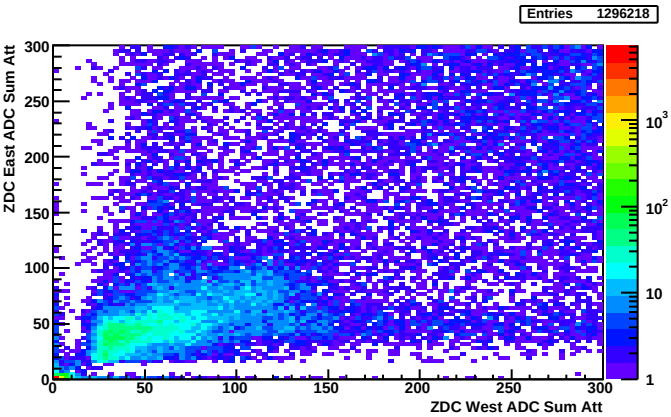
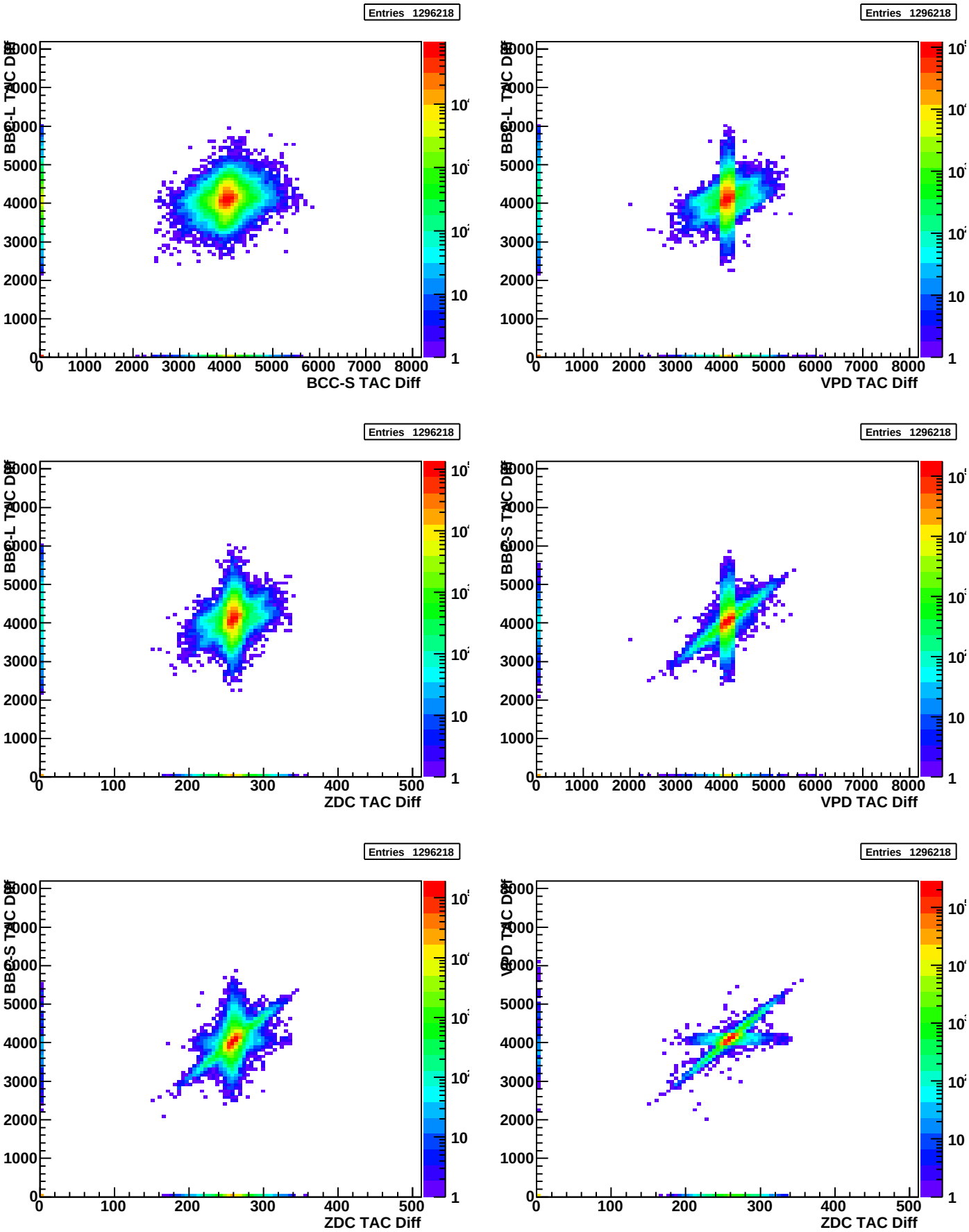
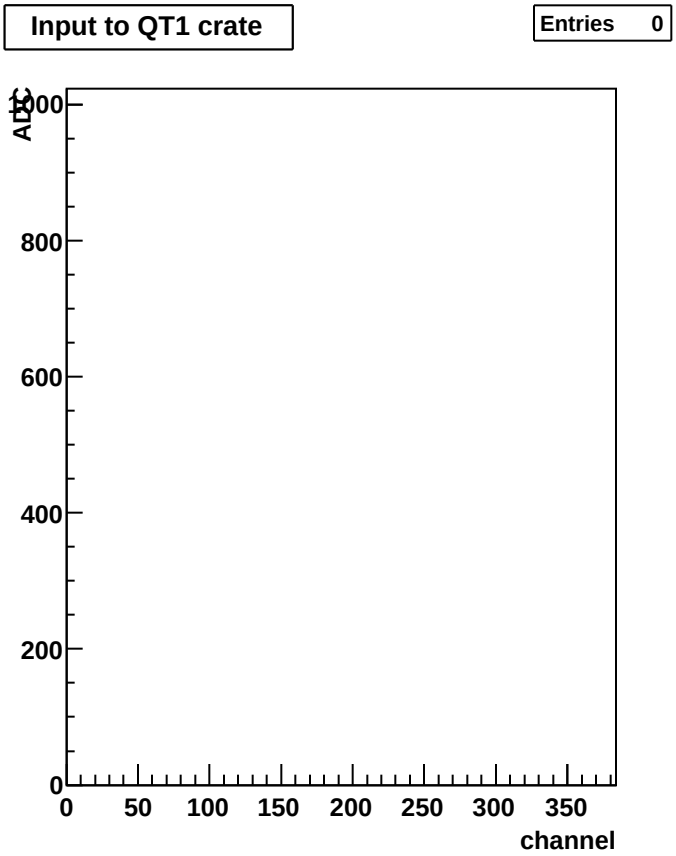
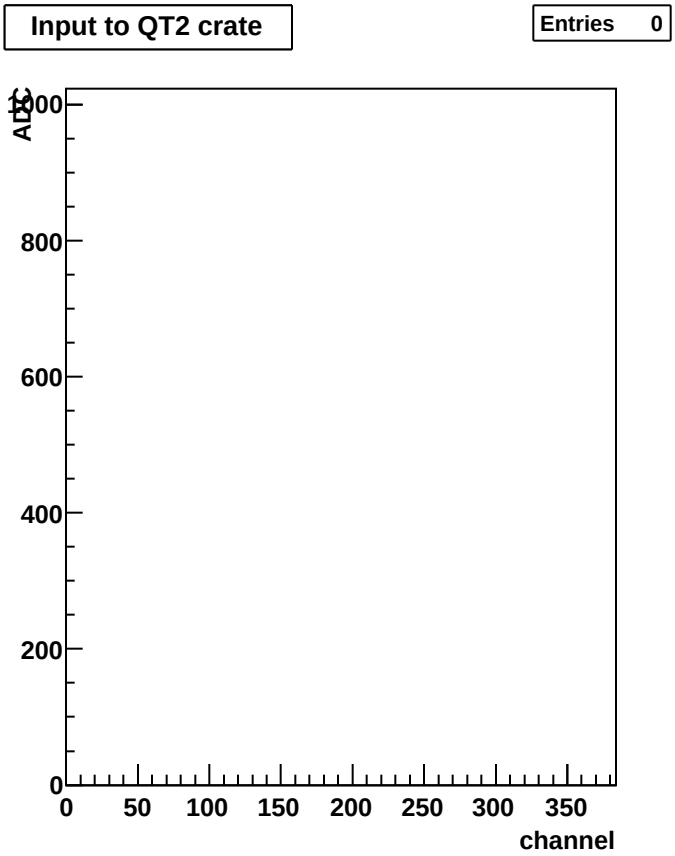
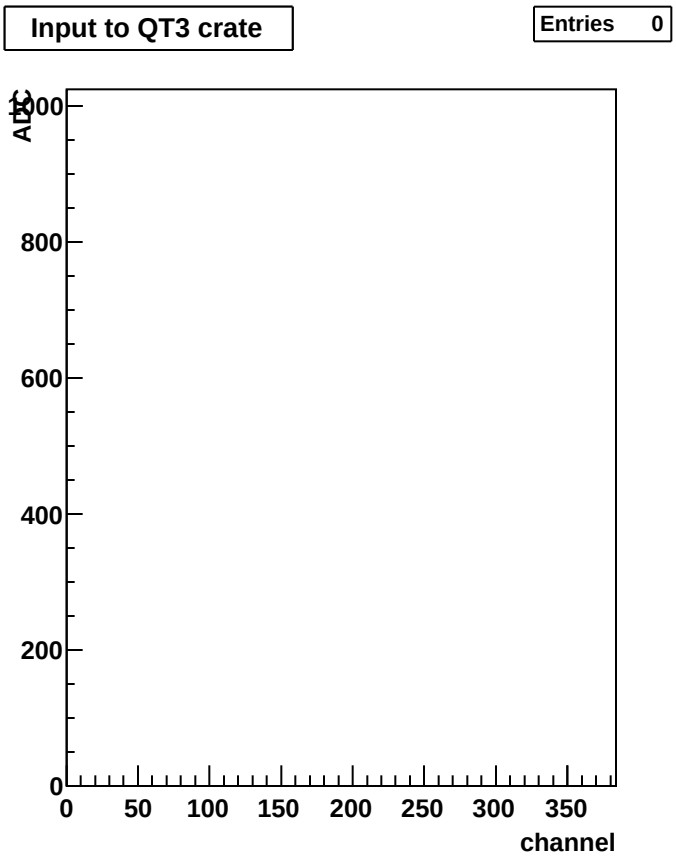
BBC-S-East ADC Sum
BBC-S-West ADC Sum

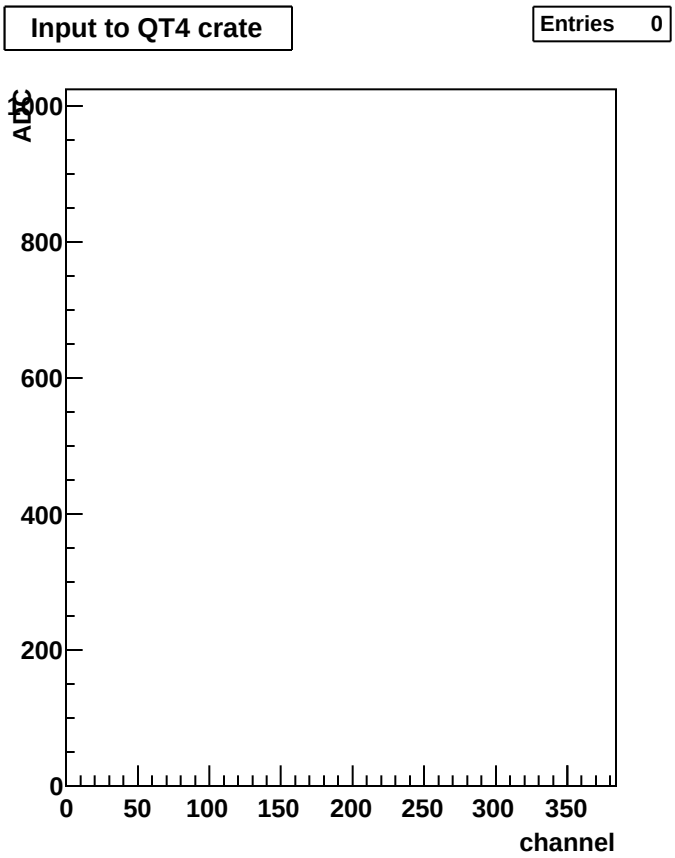

Entries 1296218
ZDC East ADC Sum Att
ZDC West ADC Sum Att

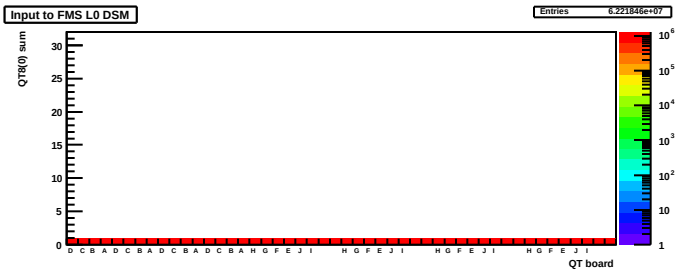

Entries 1296218
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1296218
BBC-L TAC Diff
VPD TAC Diff
Entries 1296218
BBC-L TAC Diff
ZDC TAC Diff
Entries 1296218
BBC-S TAC Diff
VPD TAC Diff
Entries 1296218
BBC-S TAC Diff
ZDC TAC Diff
Entries 1296218
VPD TAC Diff
ZDC TAC Diff

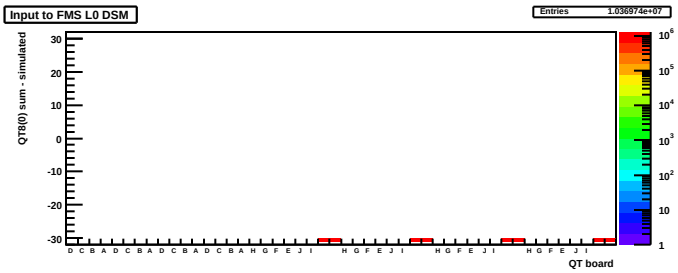

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

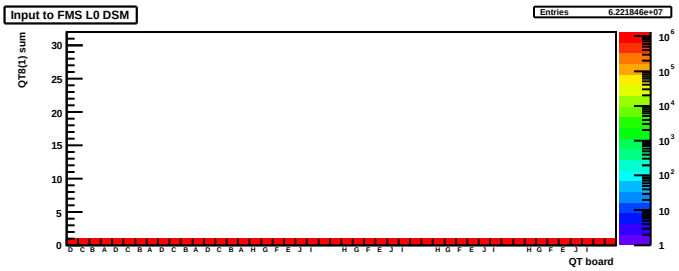

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

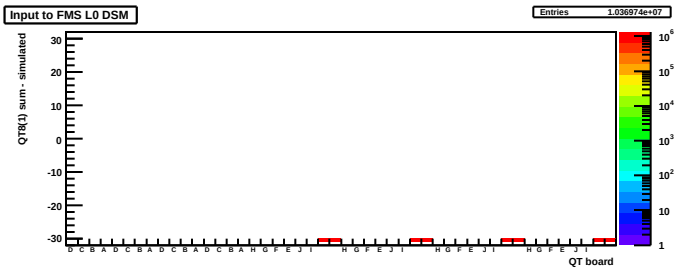

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

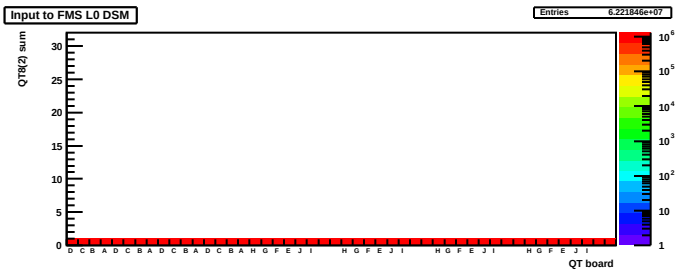

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

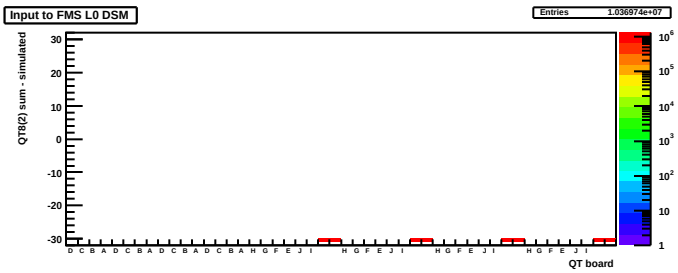

Input to FMS L0 DSM
Entries 6.221846e+07
QT8(0) sum
QT board

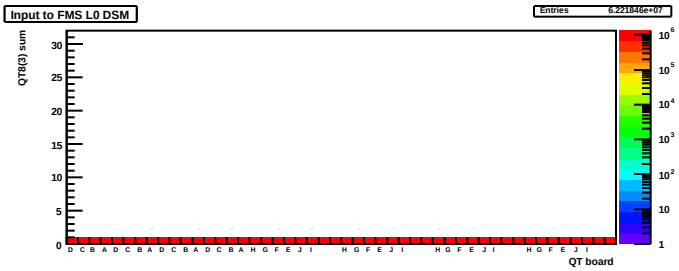

Input to FMS L0 DSM
Entries 1.036974e+07
QT8(0) sum - simulated
QT board

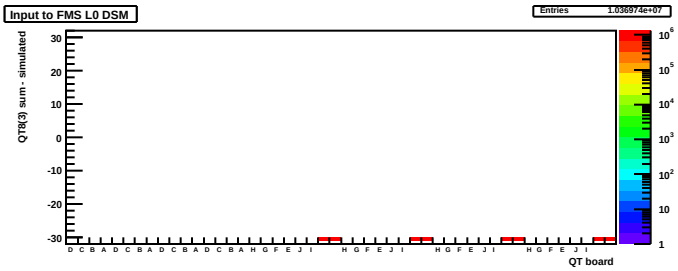

Input to FMS L0 DSM
Entries 6.221846e+07
QT8(1) sum
QT board

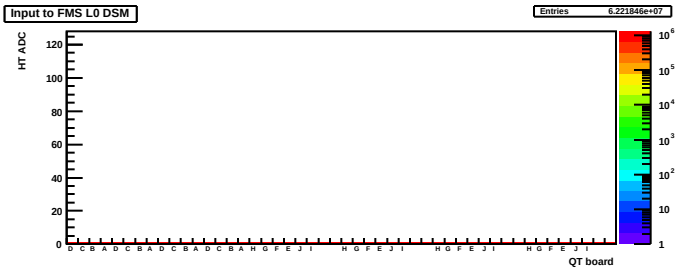

Input to FMS L0 DSM
Entries 1.036974e+07
QT8(1) sum - simulated
QT board

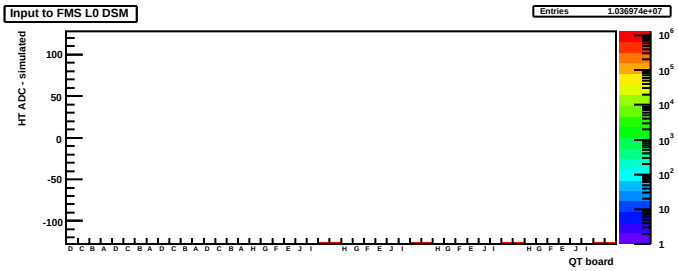

Input to FMS L0 DSM
Entries 6.221846e+07
QT8(2) sum
QT board

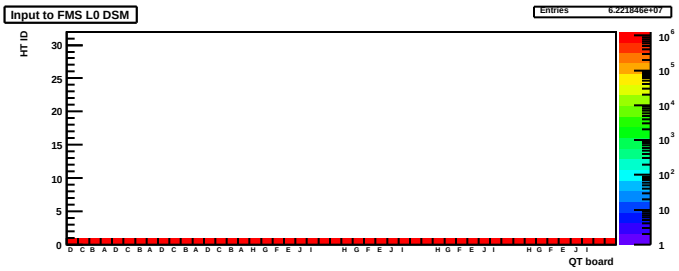

Input to FMS L0 DSM
Entries 1.036974e+07
QT8(2) sum - simulated
QT board

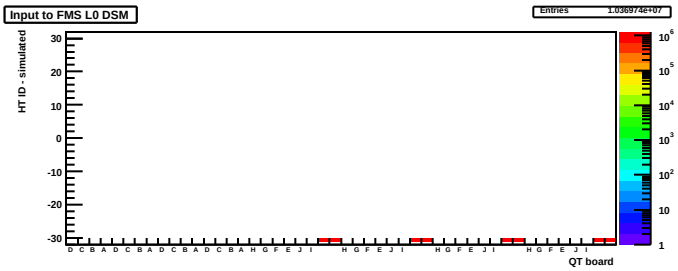

Input to FMS L0 DSM
Entries 6.221846e+07
QT8(3) sum
QT board

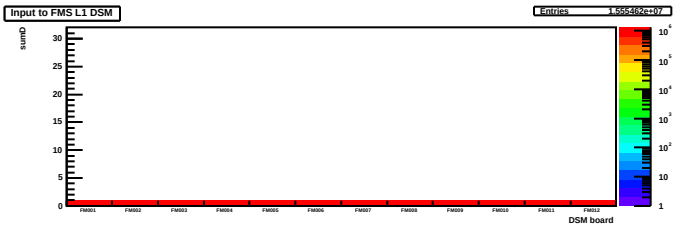

Input to FMS L0 DSM
Entries 1.036974e+07
QT8(3) sum - simulated
QT board

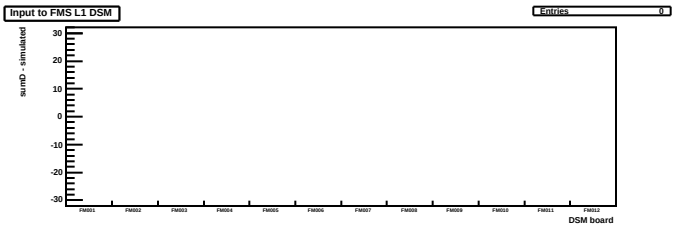

Input to FMS L0 DSM
Entries 6.221846e+07
HT ADC
QT board

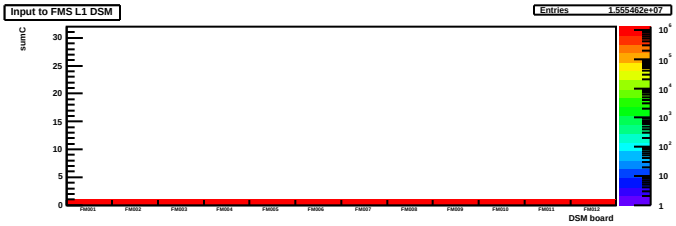

Input to FMS L0 DSM
Entries 1.036974e+07
HT ADC - simulated
QT board

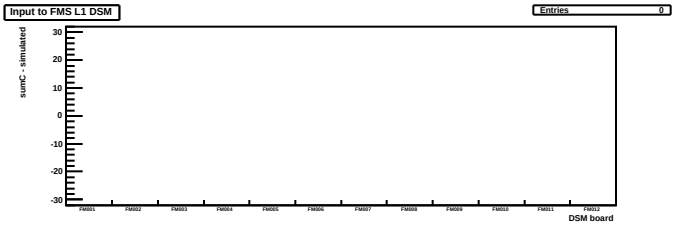

Input to FMS L0 DSM
Entries 6.221846e+07
HT ID
QT board

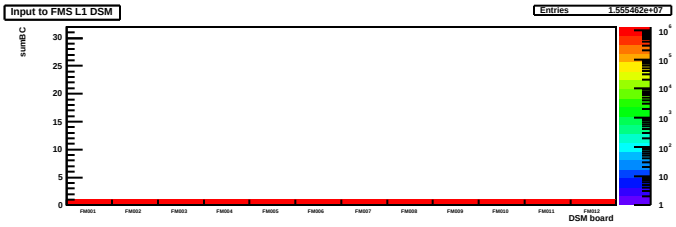

Input to FMS L0 DSM
Entries 1.036974e+07
HT ID - simulated
QT board

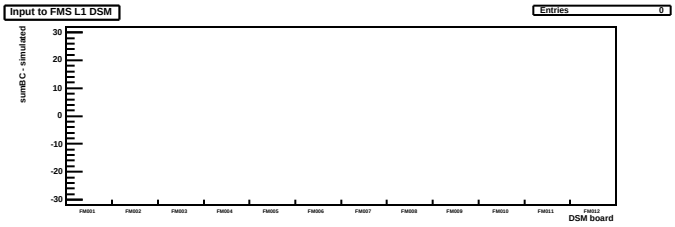

Input to FMS L1 DSM
Entries 1.555462e+07
sumD
DSM board

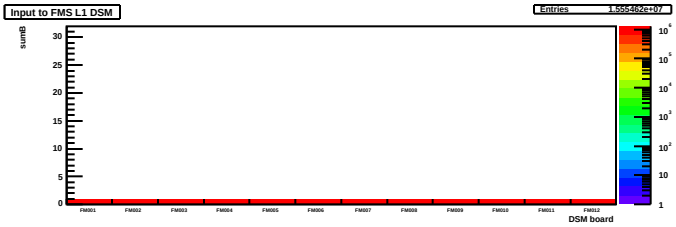

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

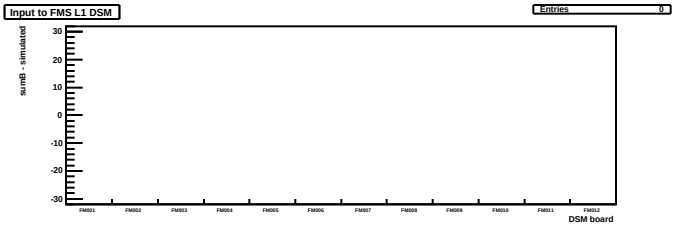

Input to FMS L1 DSM
Entries 1.555462e+07
sumC
DSM board

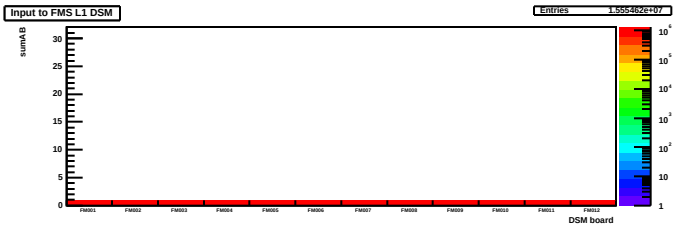

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

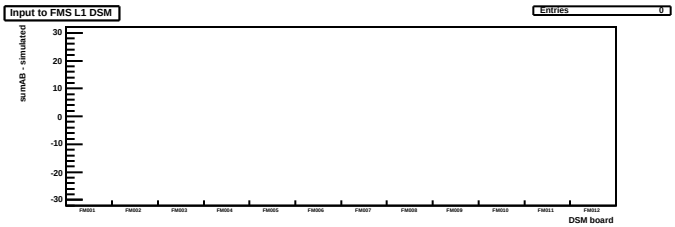

Input to FMS L1 DSM
Entries 1.555462e+07
sumBC
DSM board

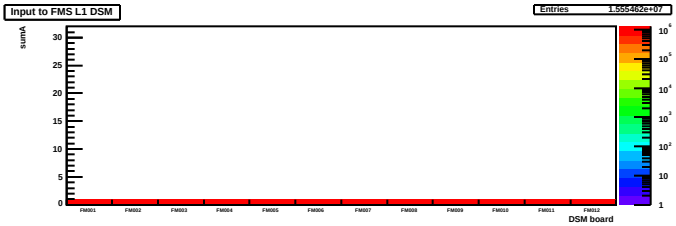

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

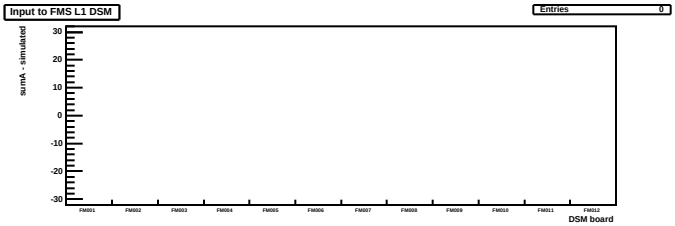

Input to FMS L1 DSM
Entries 1.555462e+07
sumB
DSM board

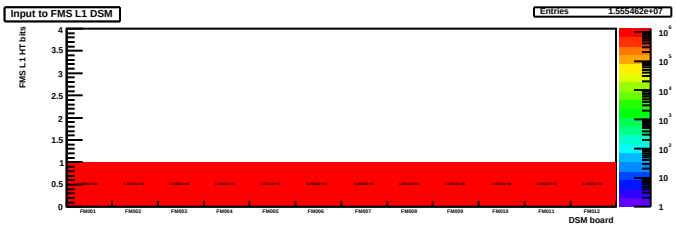

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

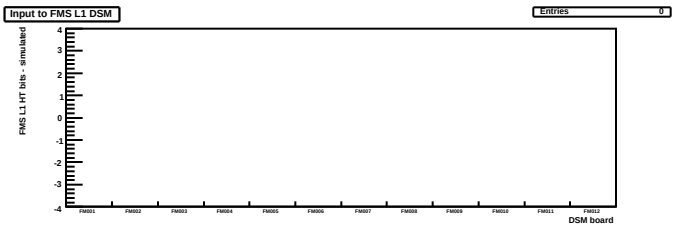

Input to FMS L1 DSM
Entries 1.555462e+07
sumAB
DSM board

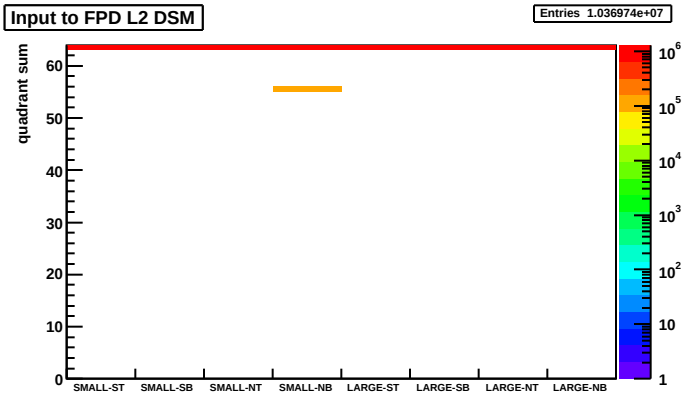

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

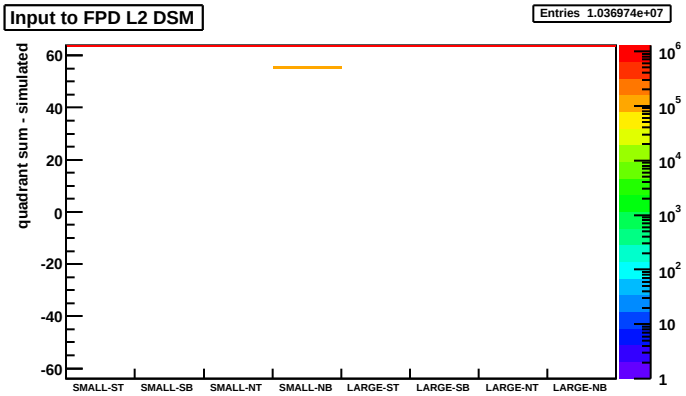

Input to FMS L1 DSM
Entries 1.555462e+07
sumA
DSM board

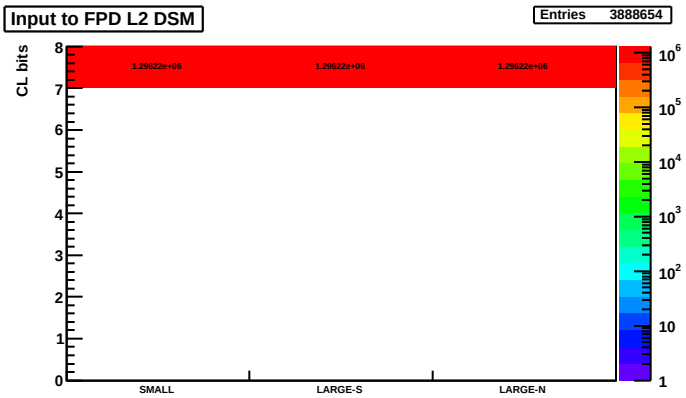

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

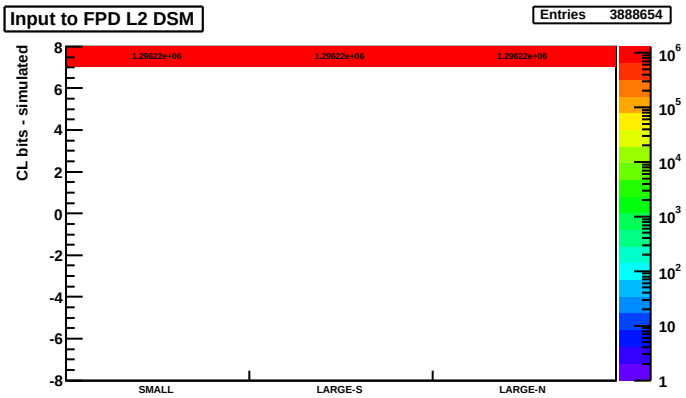

Input to FMS L1 DSM
Entries 1.555462e+07
FMS L1 HT bits
DSM board

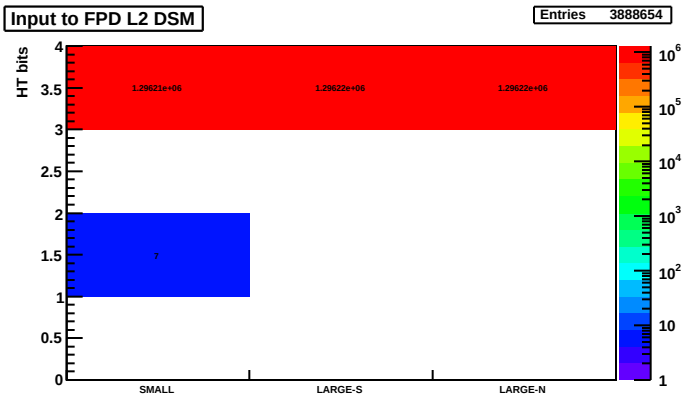

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

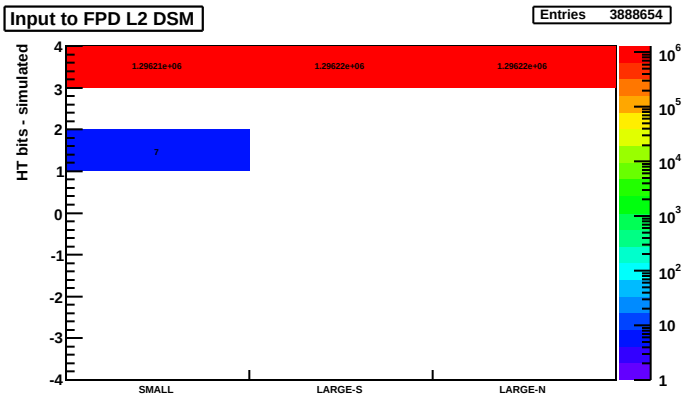

Input to FPD L2 DSM
Entries 1.036974e+07
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

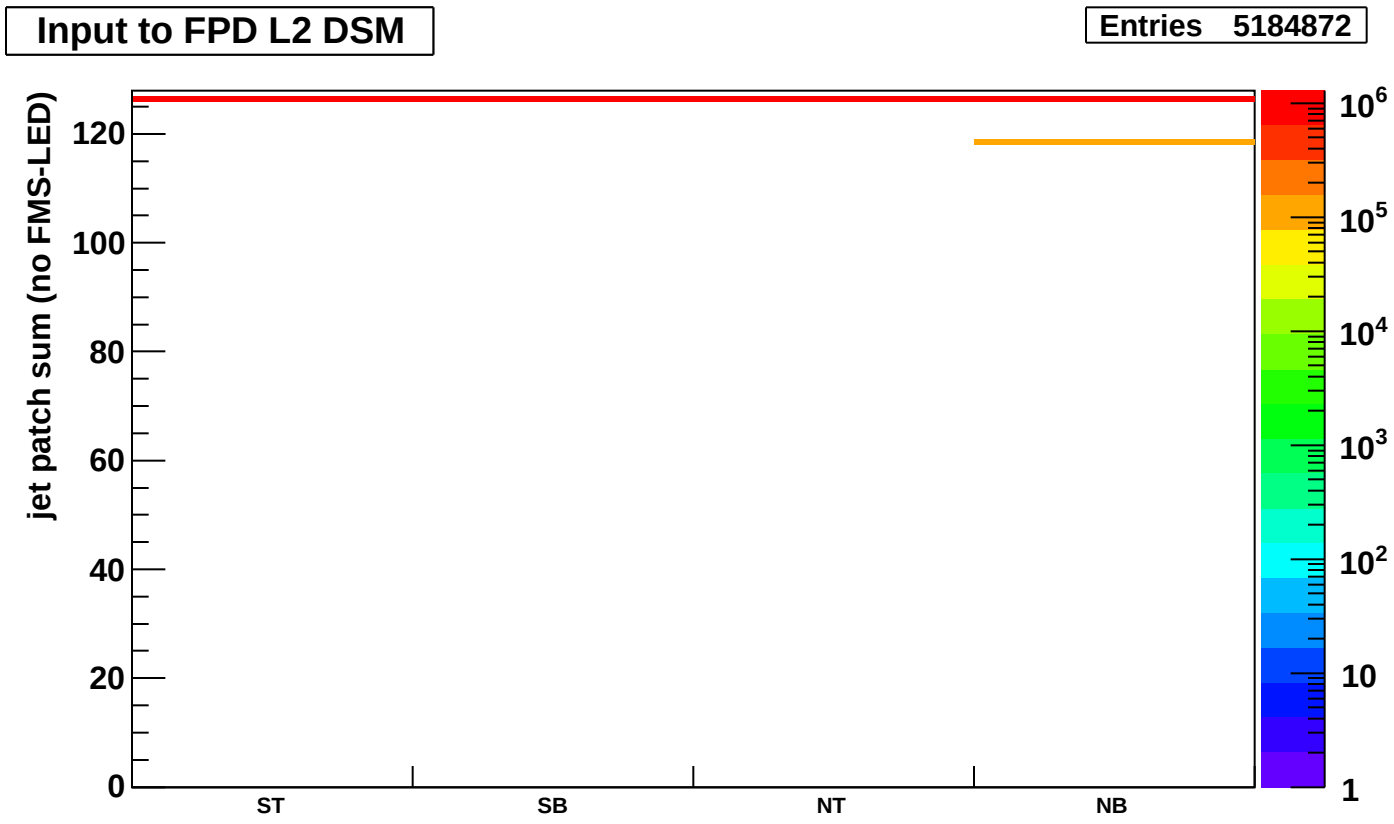

Input to FPD L2 DSM
Entries 1.036974e+07
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

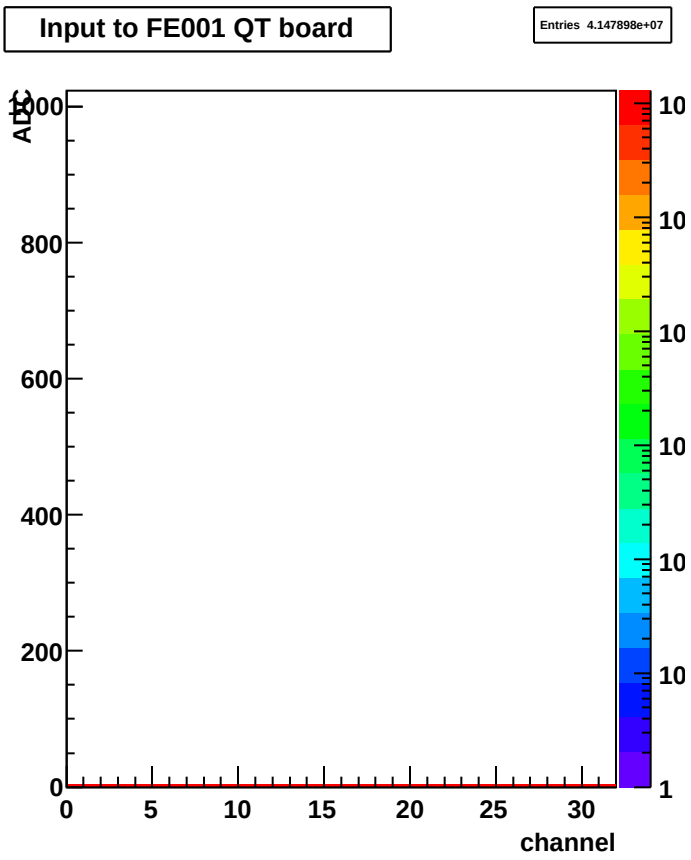

Input to FPD L2 DSM
Entries 3888654
CL bits
1.29622e+06
1.29622e+06
1.29622e+06
SMALL
LARGE-S
LARGE-N

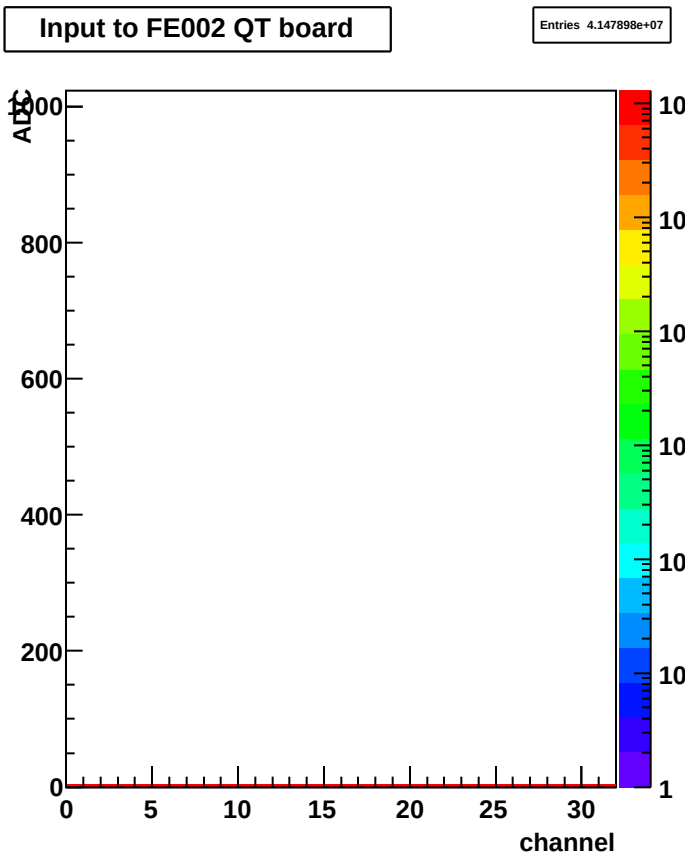

Input to FPD L2 DSM
Entries 3888654
CL bits - simulated
1.29622e+06
1.29622e+06
1.29622e+06
SMALL
LARGE-S
LARGE-N

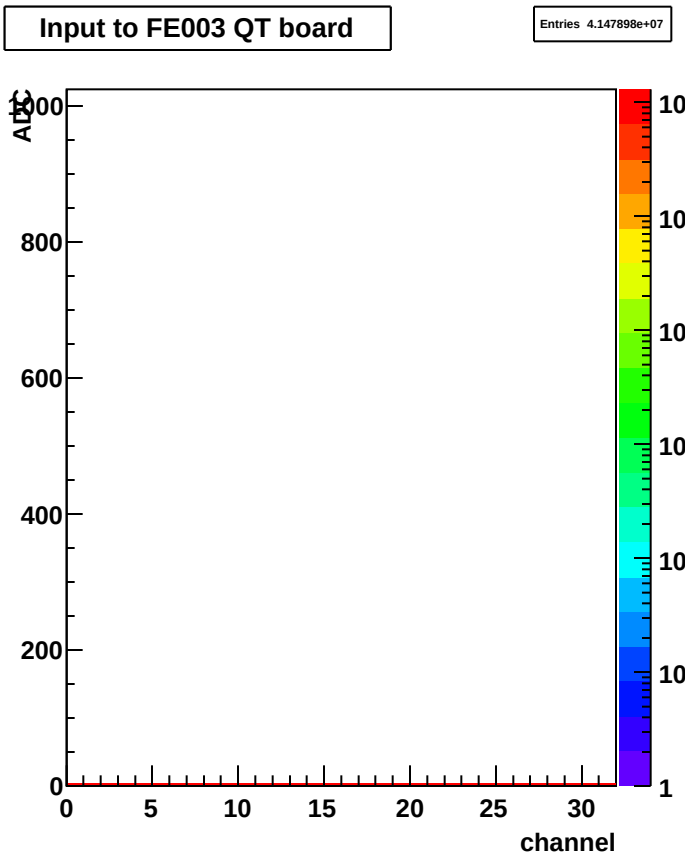

Input to FPD L2 DSM
Entries 3888654
HT bits
1.29621e+06
1.29622e+06
1.29622e+06
7
SMALL
LARGE-S
LARGE-N

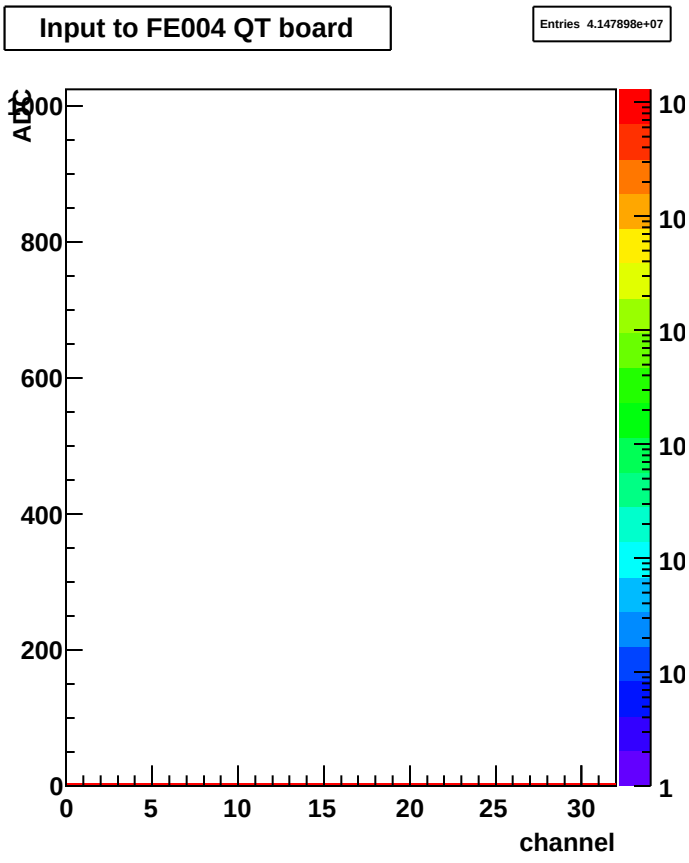

Input to FPD L2 DSM
Entries 3888654
HT bits - simulated
1.29621e+06
1.29622e+06
1.29622e+06
7
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 5184872
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

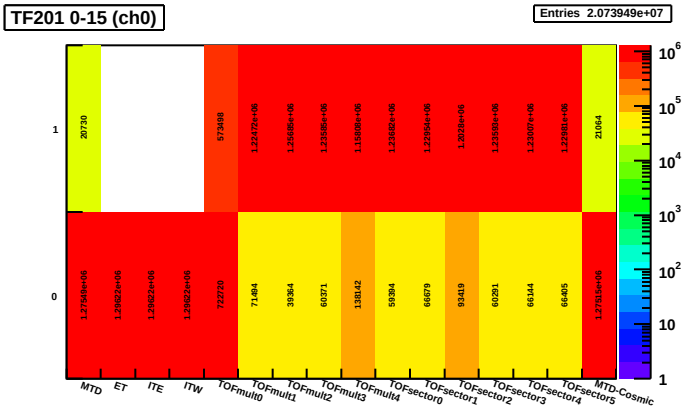

Input to FE001 QT board
Entries 4.147898e+07
ADC
channel


Input to FE002 QT board
Entries 4.147898e+07
ADC
channel

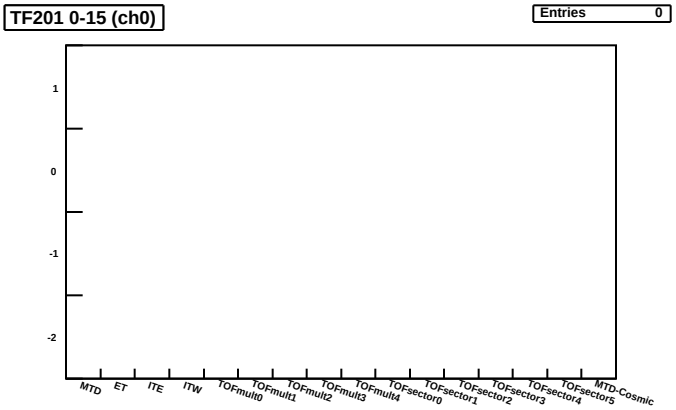

Input to FE003 QT board
Entries 4.147898e+07
ADC
channel


Input to FE004 QT board
Entries 4.147898e+07
ADC
channel

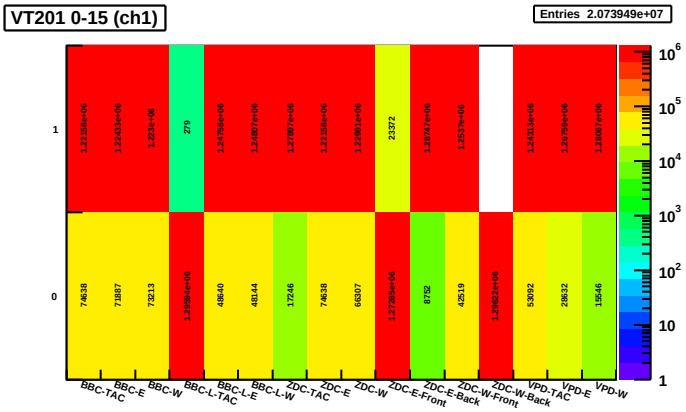
TF201 0-15 (ch0)



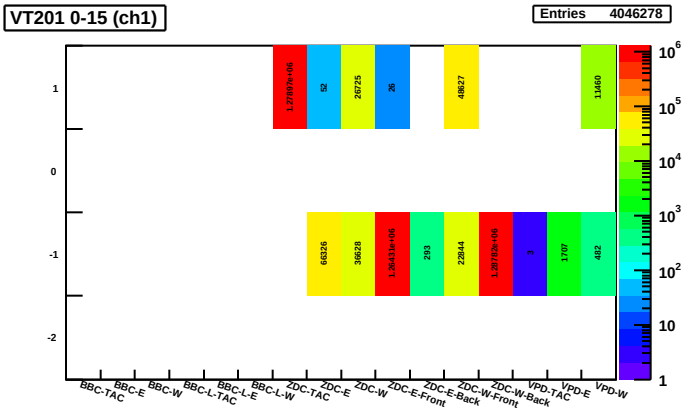
TF201 0-15 (ch0)



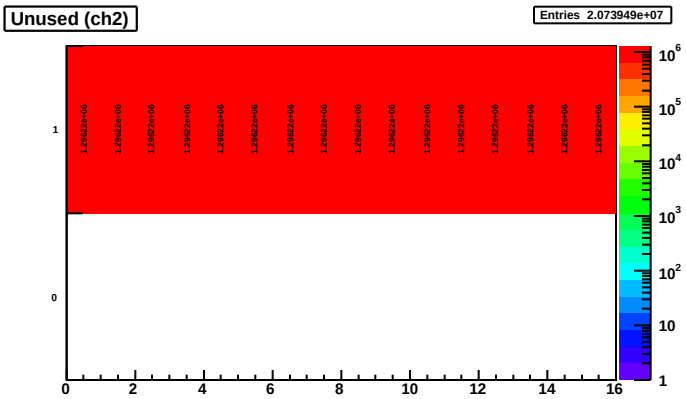
VT201 0-15 (ch1)

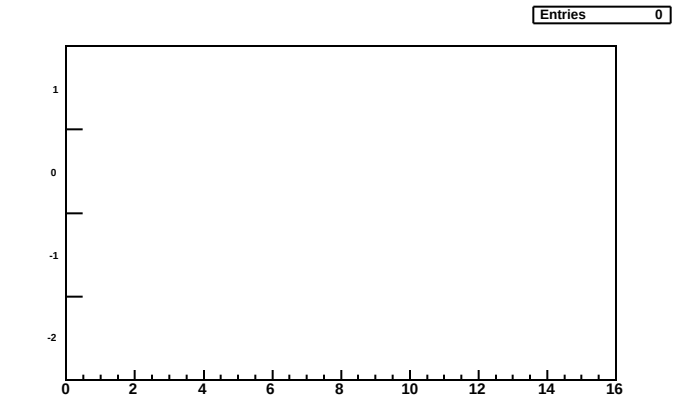


VT201 0-15 (ch1)

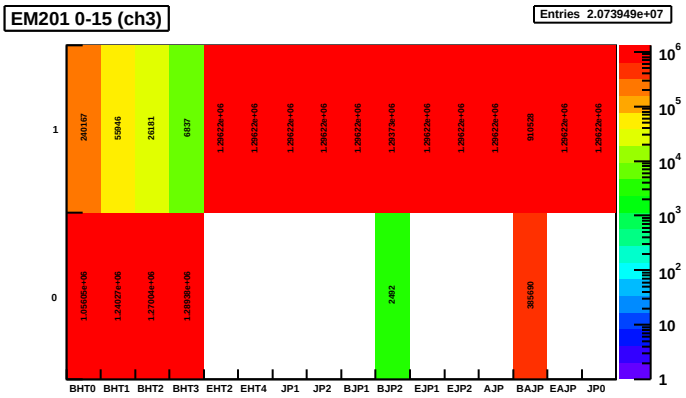


Unused (ch2)

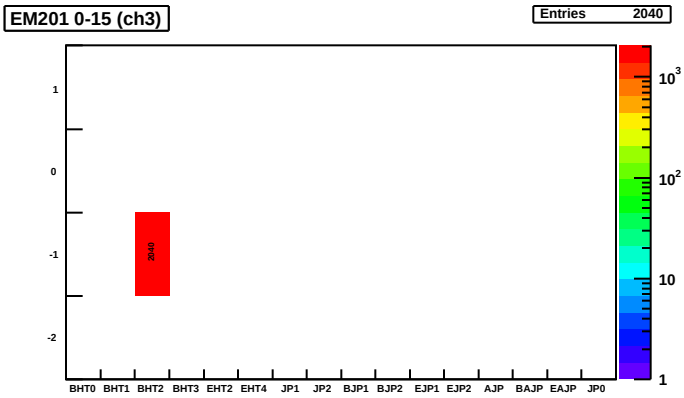




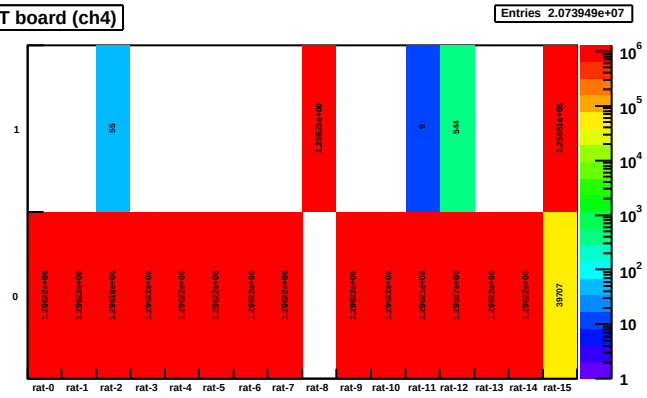
EM201 0-15 (ch3)

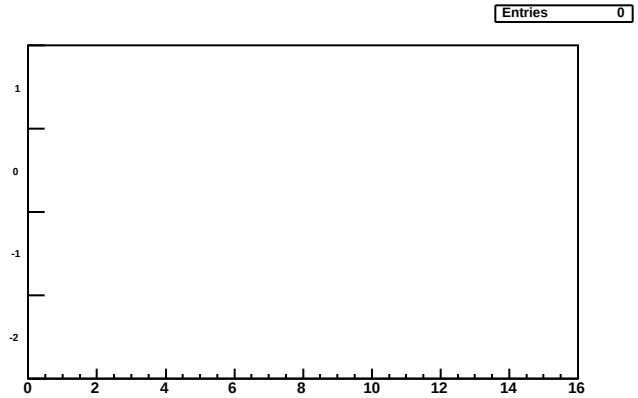


EM201 0-15 (ch3)

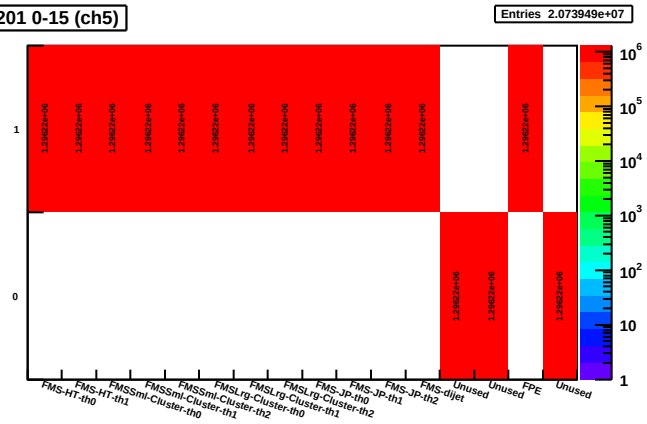


RAT board (ch4)

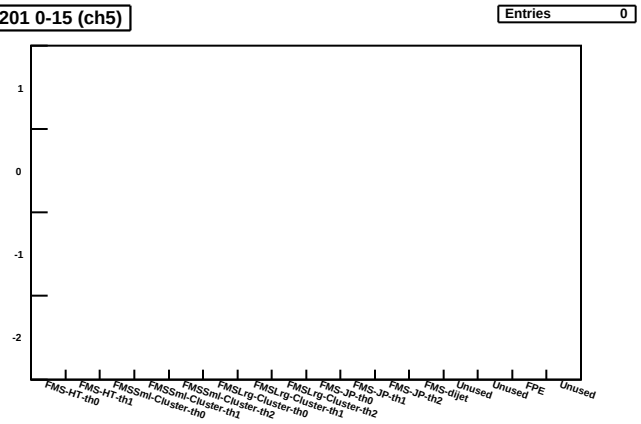




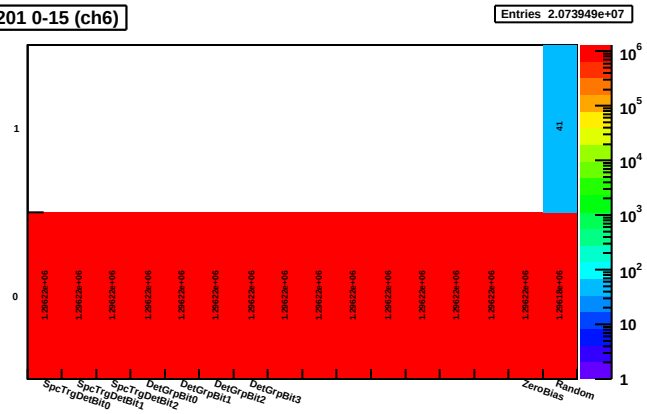
FP201 0-15 (ch5)

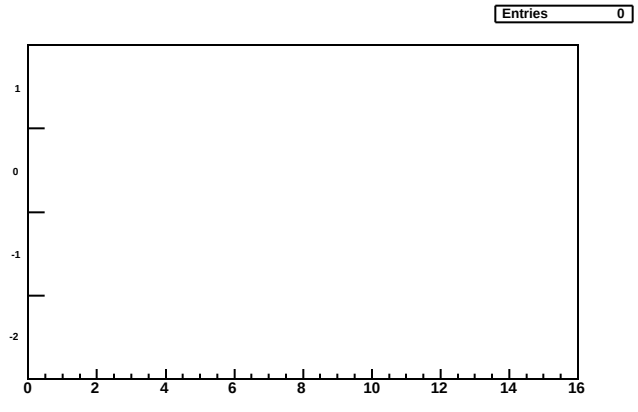


FP201 0-15 (ch5)

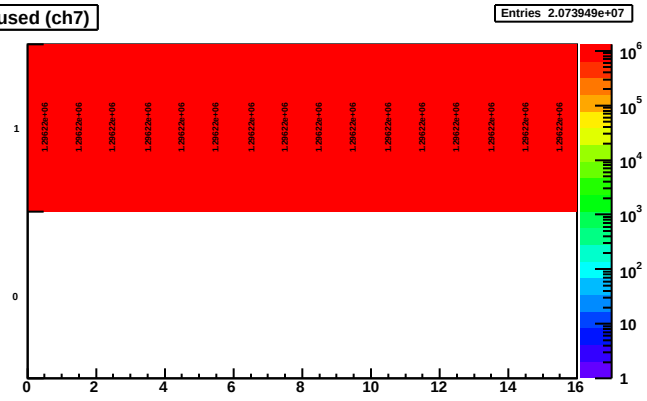


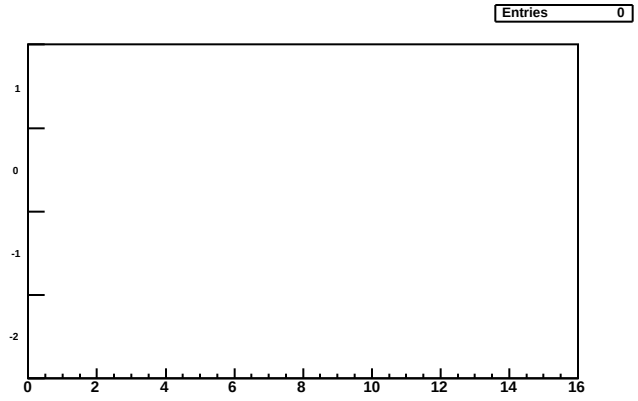
ST201 0-15 (ch6)

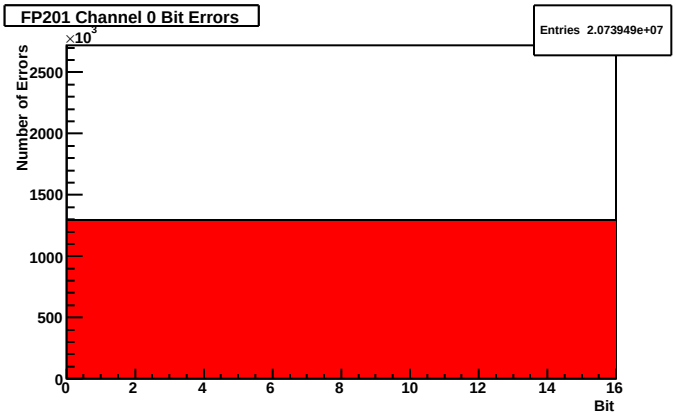


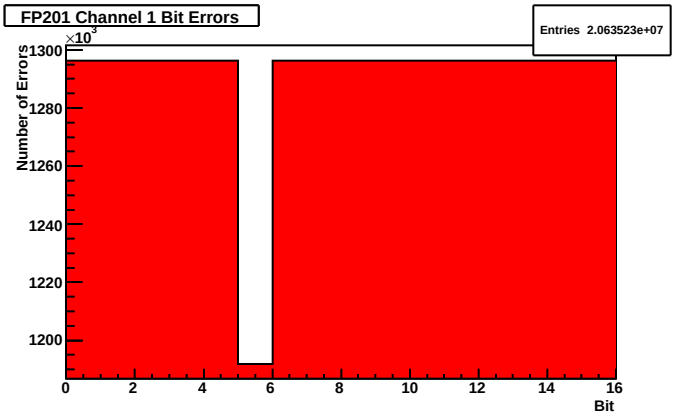


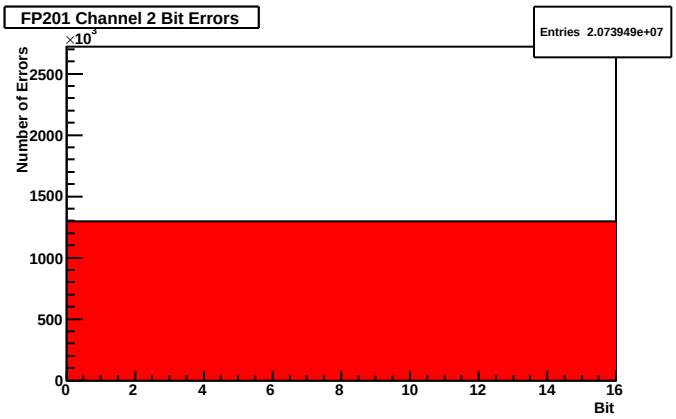
Unused (ch7)

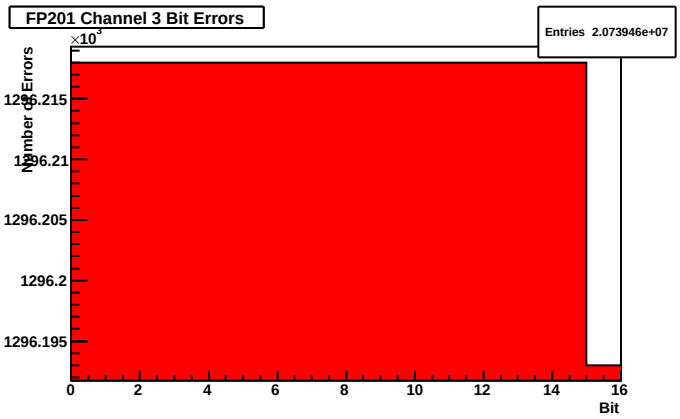


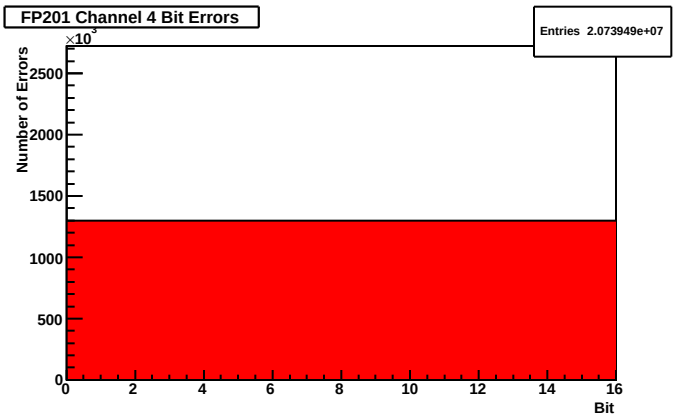


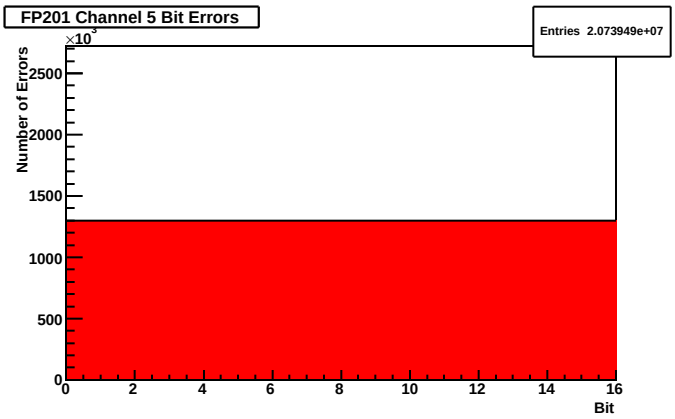

FP201 Channel 0 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

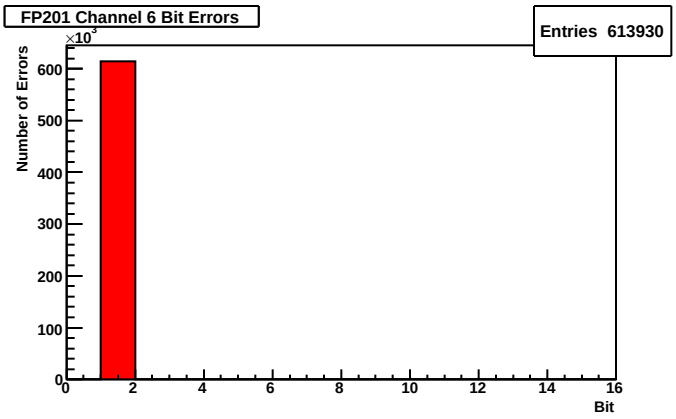

FP201 Channel 1 Bit Errors
Entries 2.063523e+07
Number of Errors
Bit

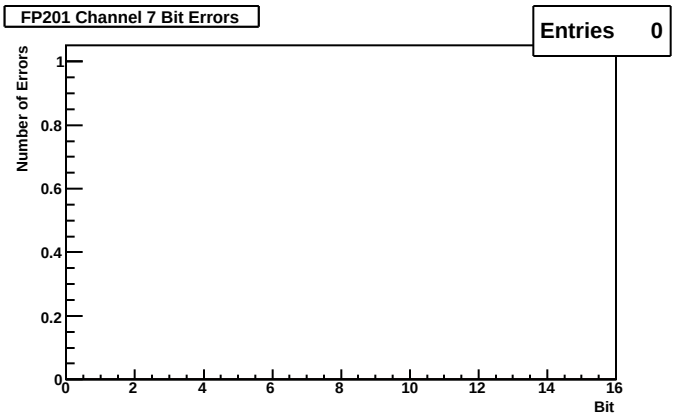

FP201 Channel 2 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

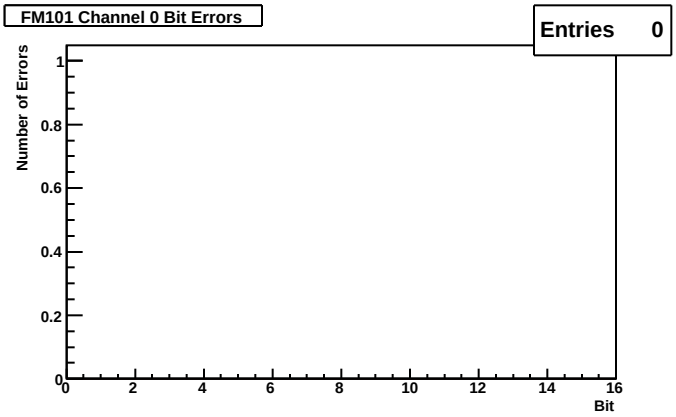

FP201 Channel 3 Bit Errors
Entries 2.073946e+07
Number of Errors
Bit

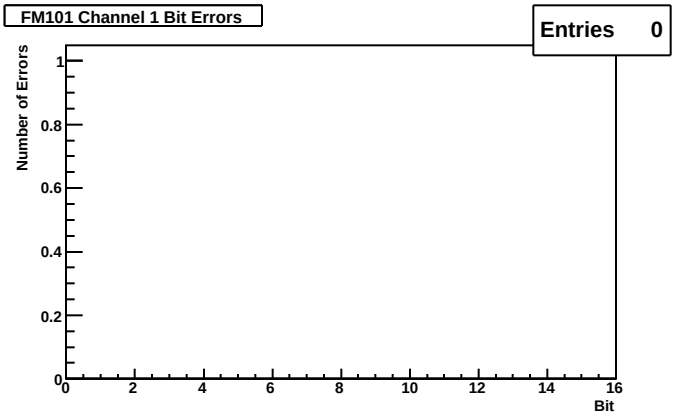

FP201 Channel 4 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

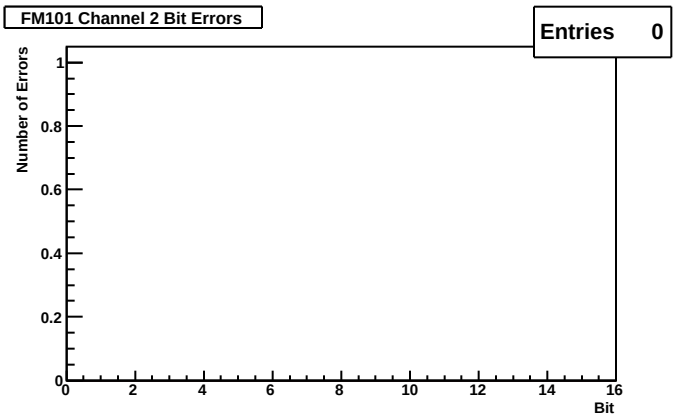

FP201 Channel 5 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

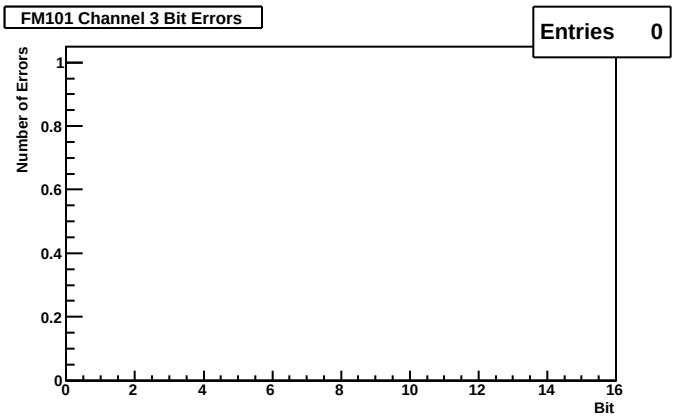

FP201 Channel 6 Bit Errors
Entries 613930
Number of Errors
Bit

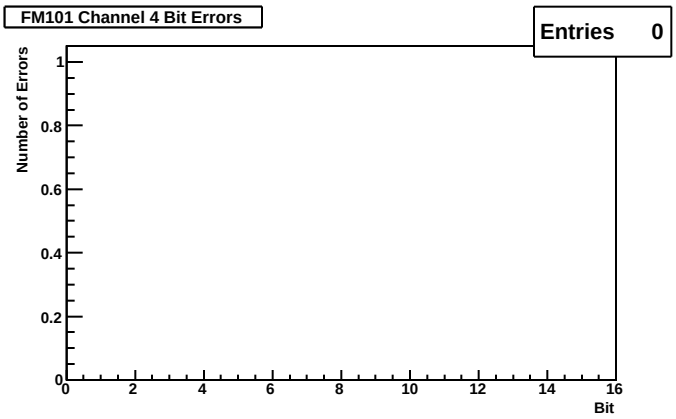

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

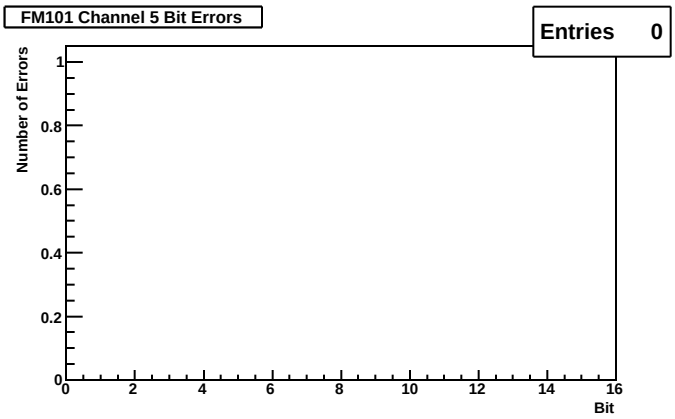

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

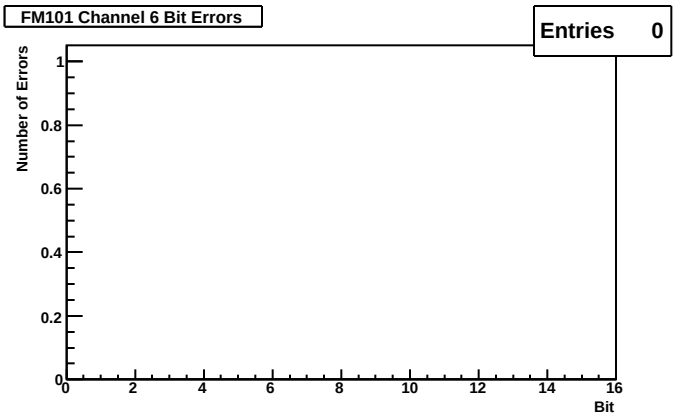

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

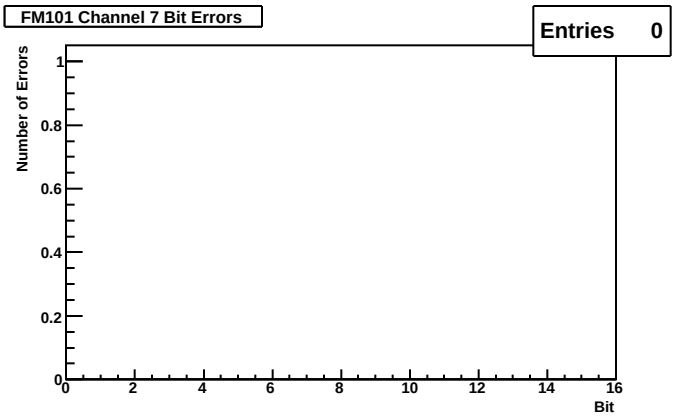

FM101 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

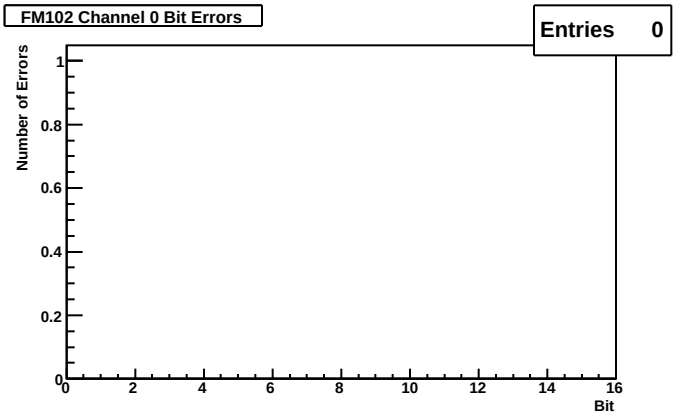

FM101 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

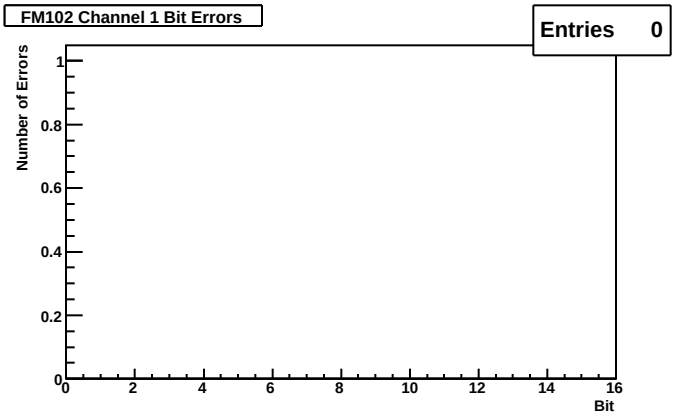

FM101 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

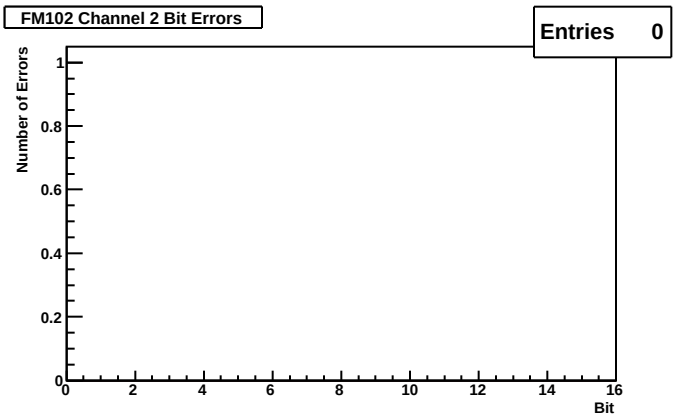

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

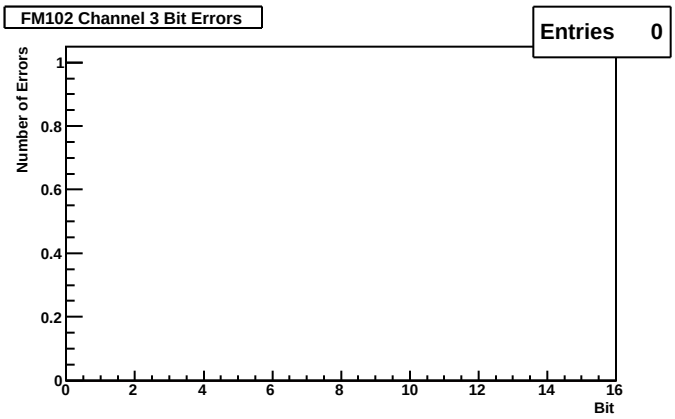

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

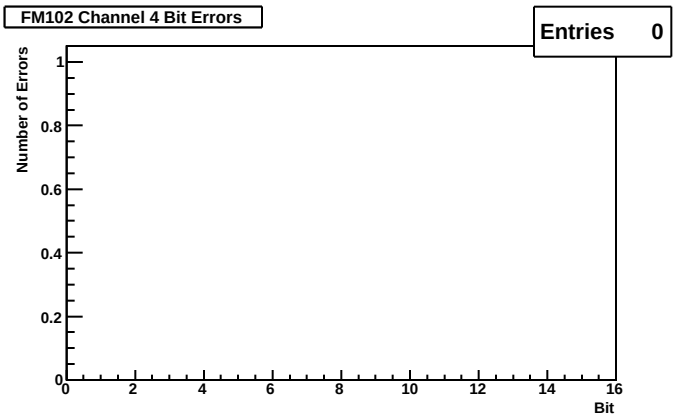

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

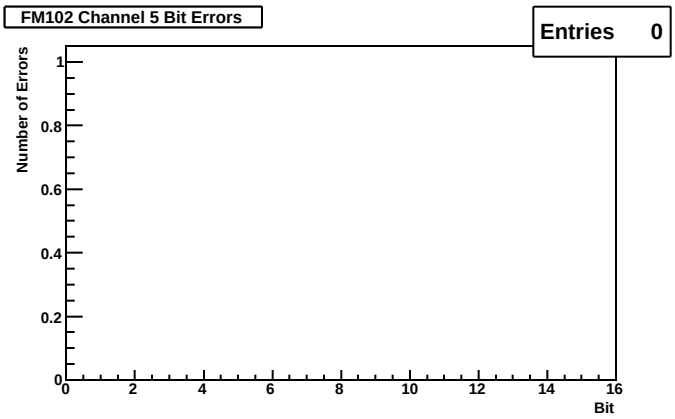

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

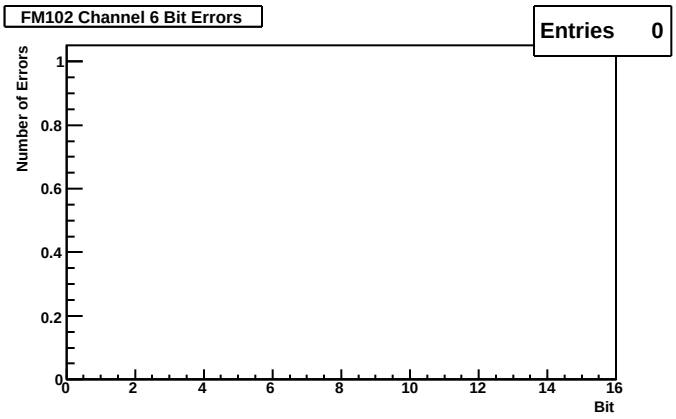

FM102 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

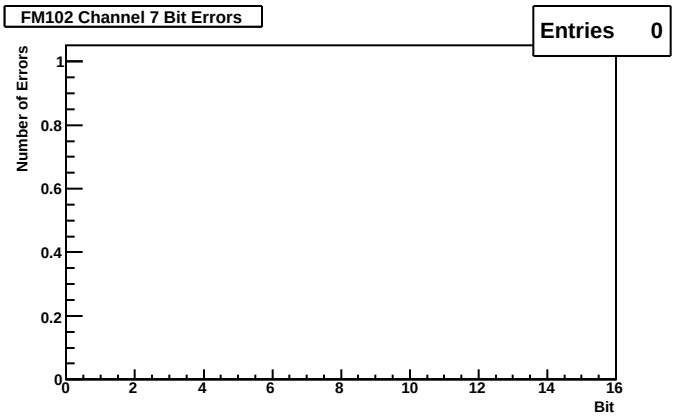

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

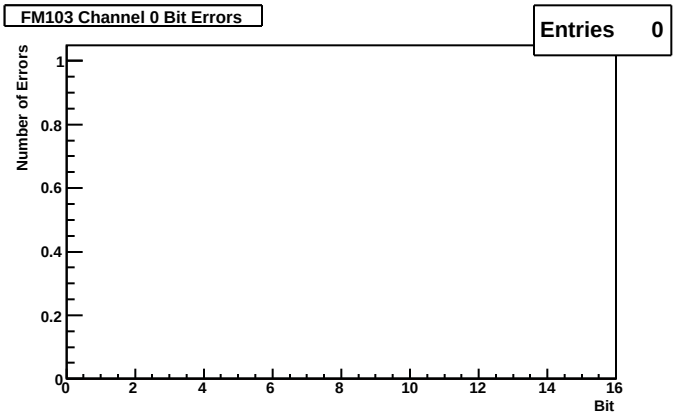

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

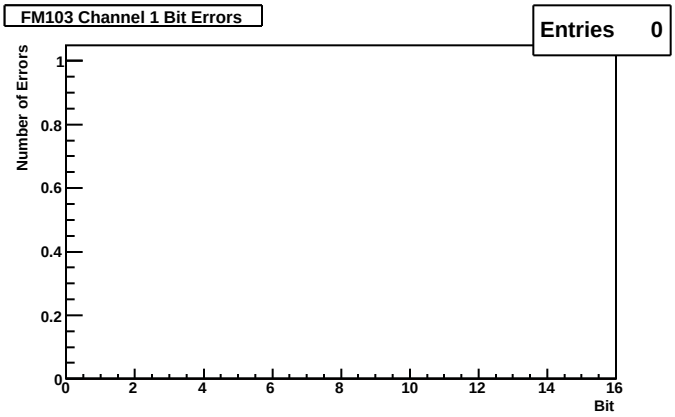

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

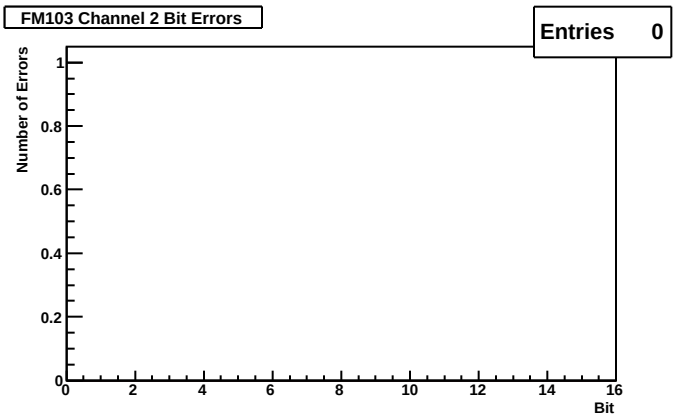

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

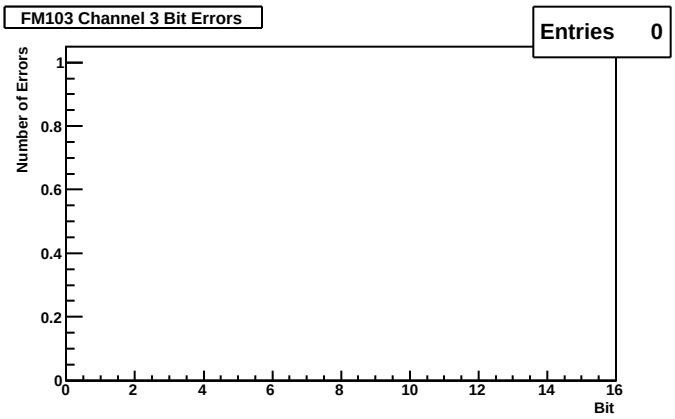

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

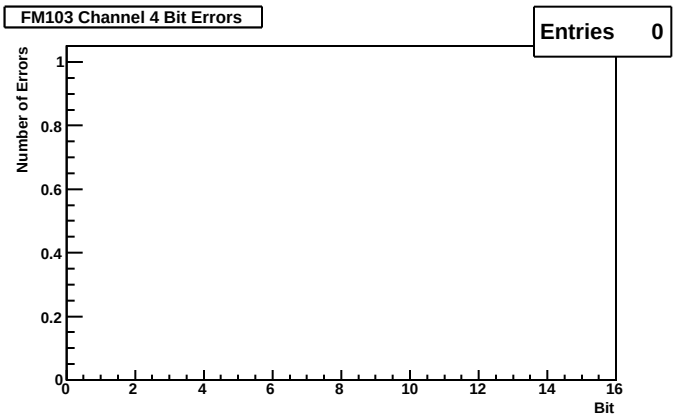

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

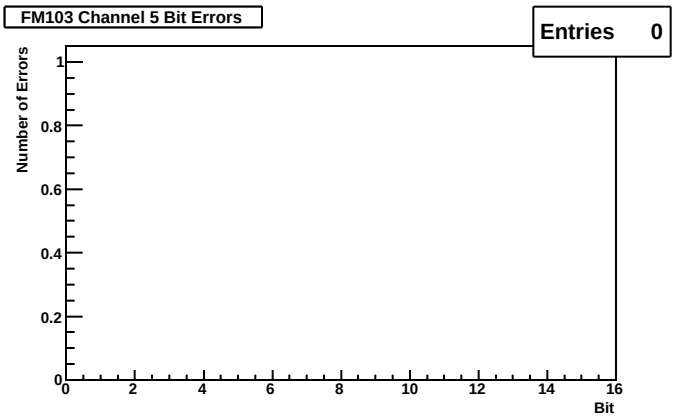

FM103 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

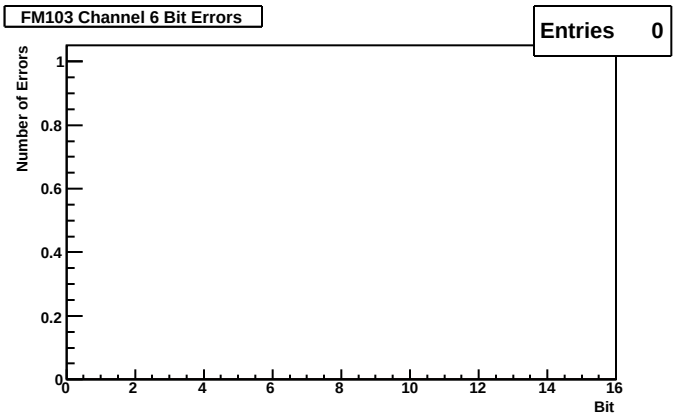

FM103 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

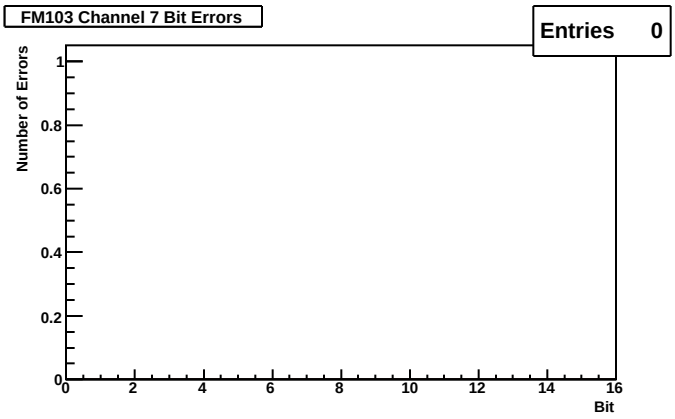

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

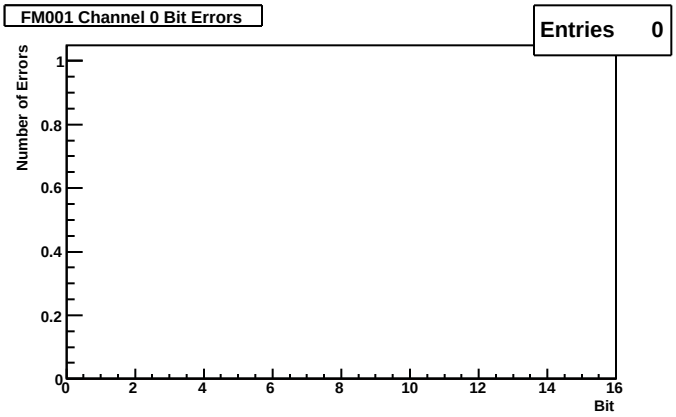

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

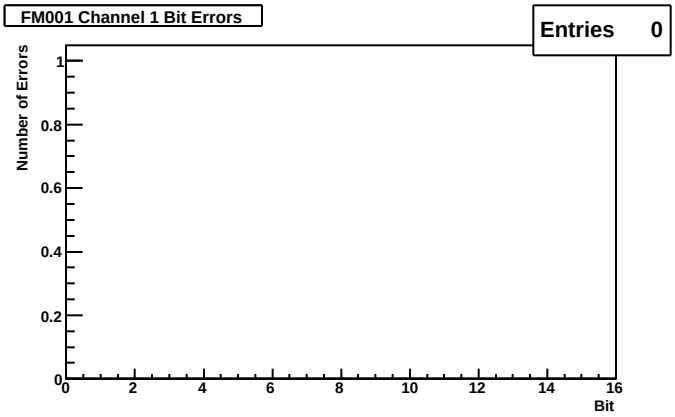

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

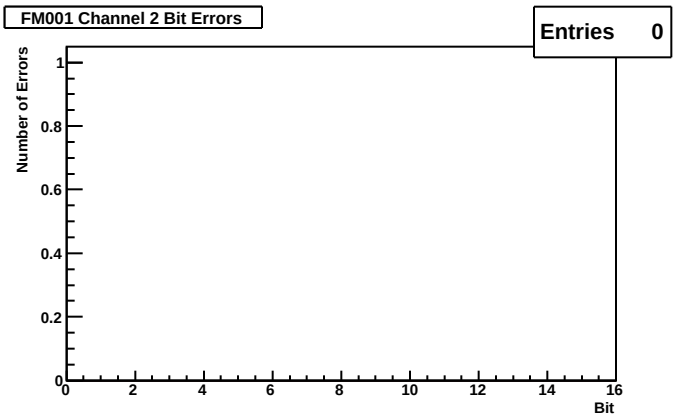

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

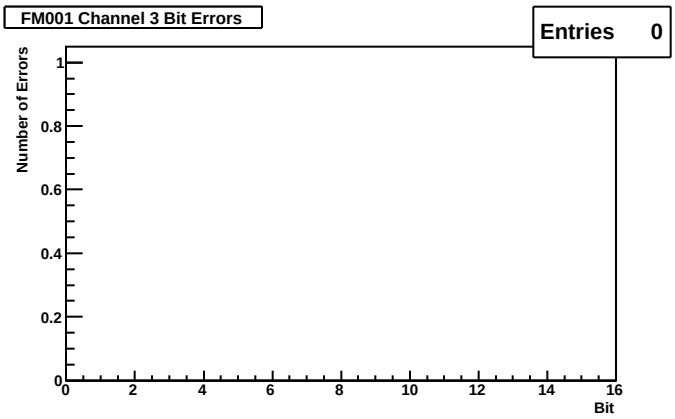

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

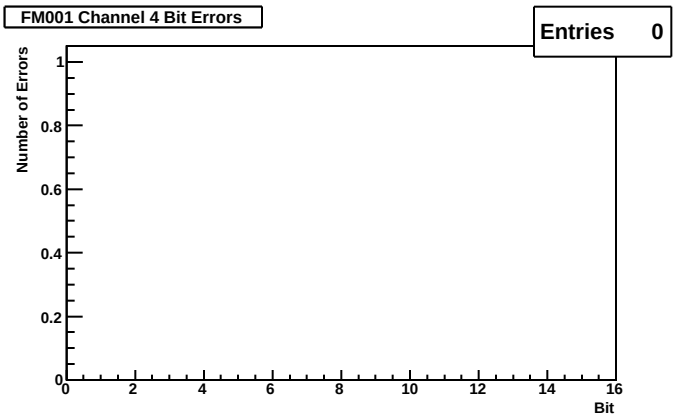

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

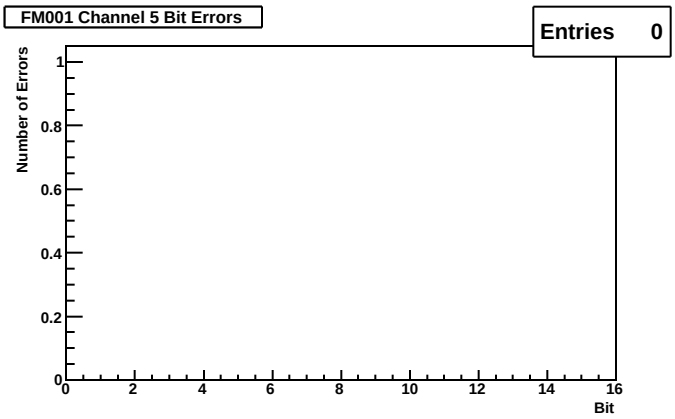

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

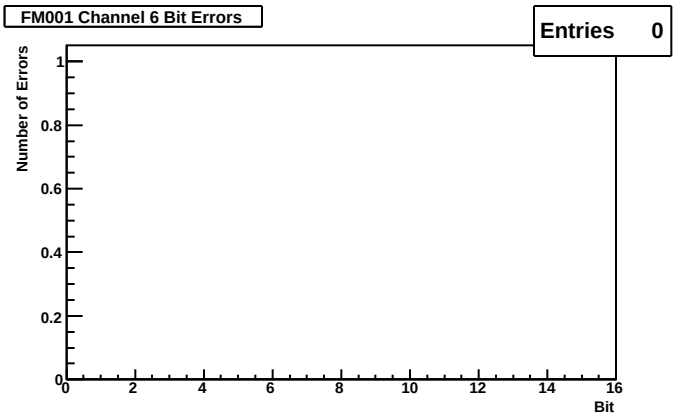

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

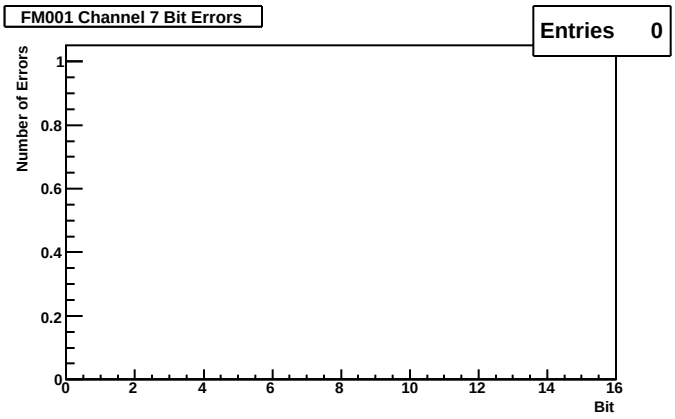

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

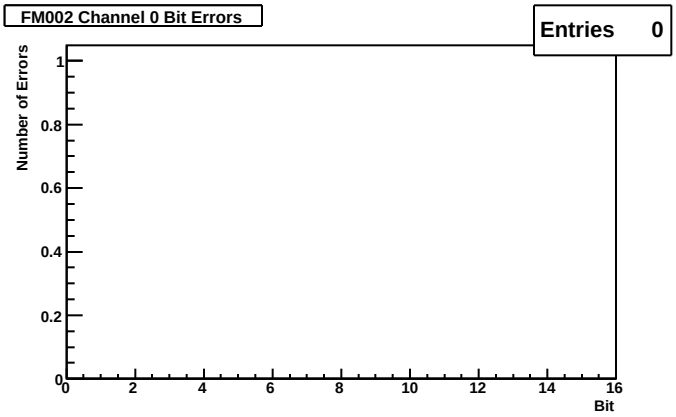

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

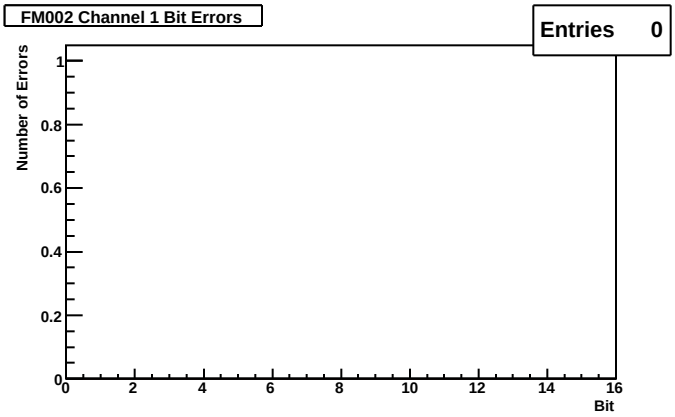

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

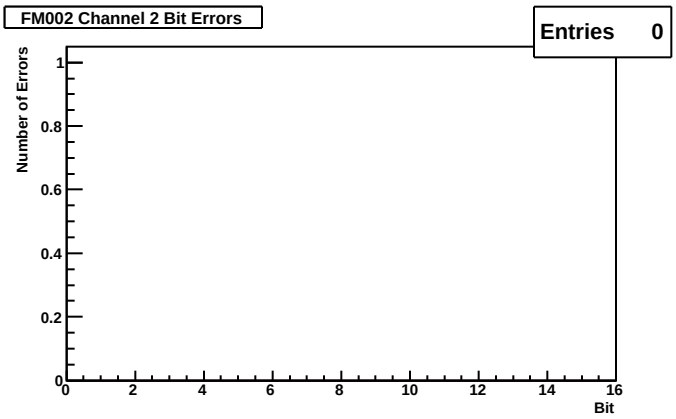

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

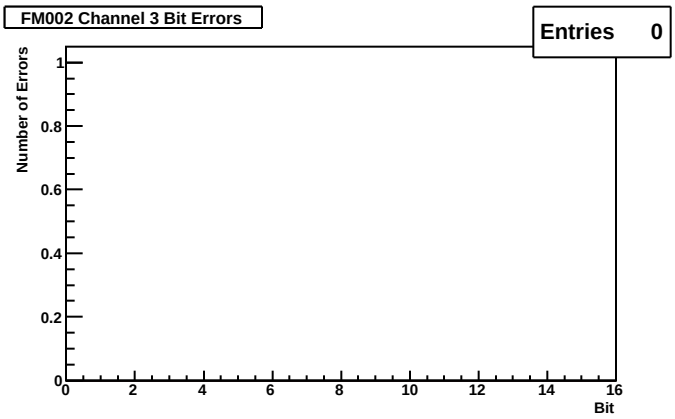

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

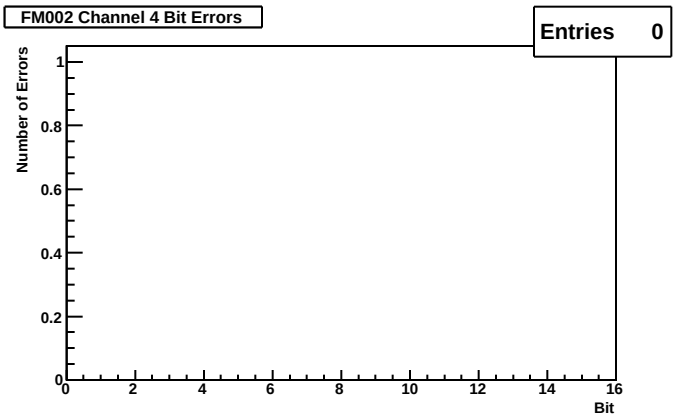

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

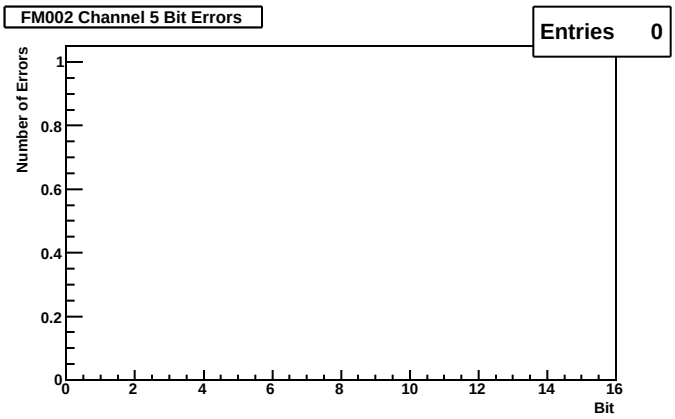

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

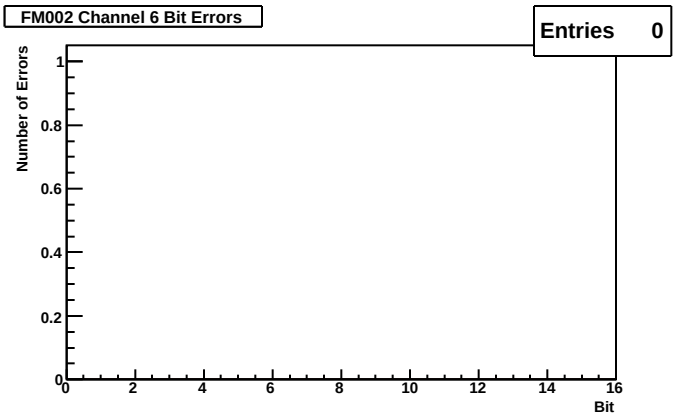

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

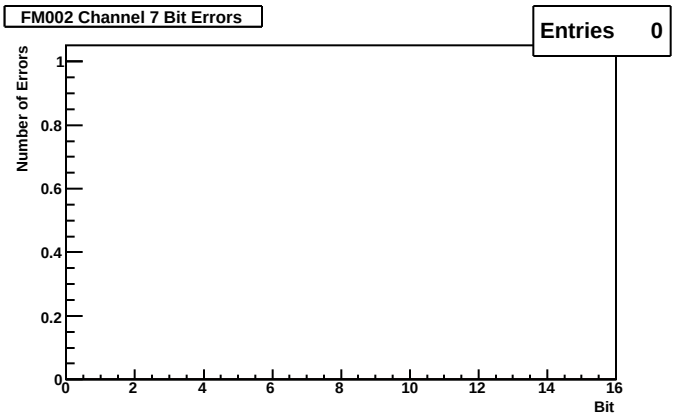

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

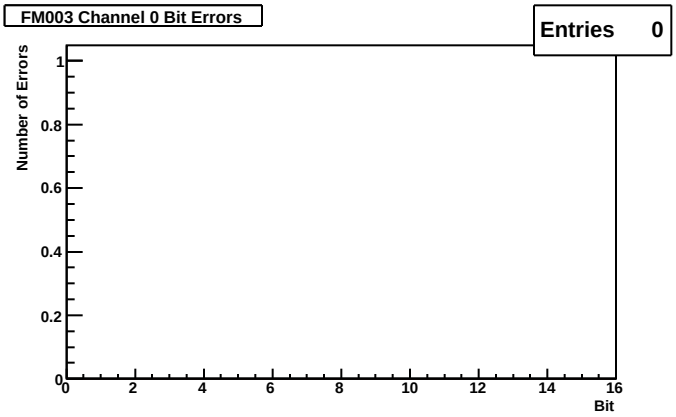

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

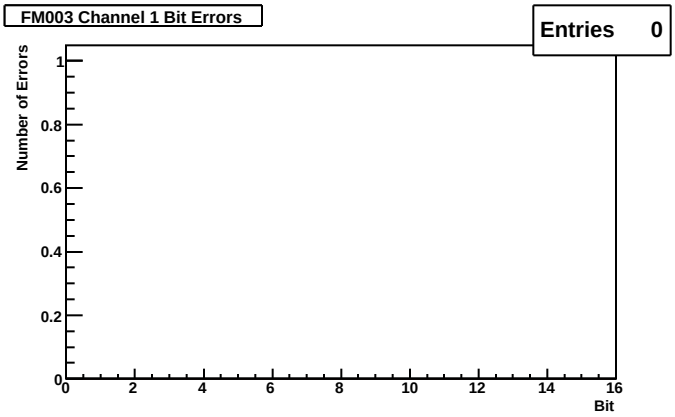

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

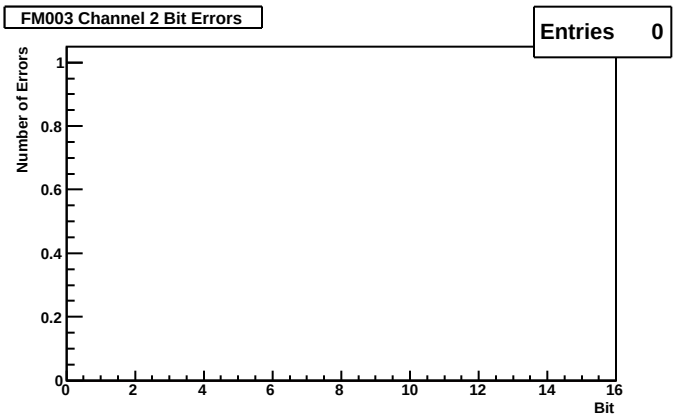

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

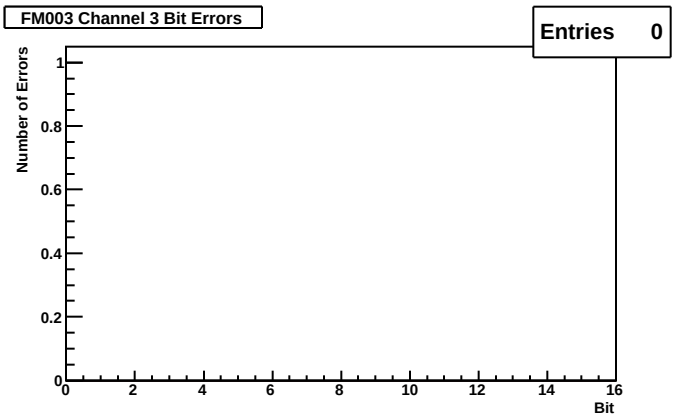

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

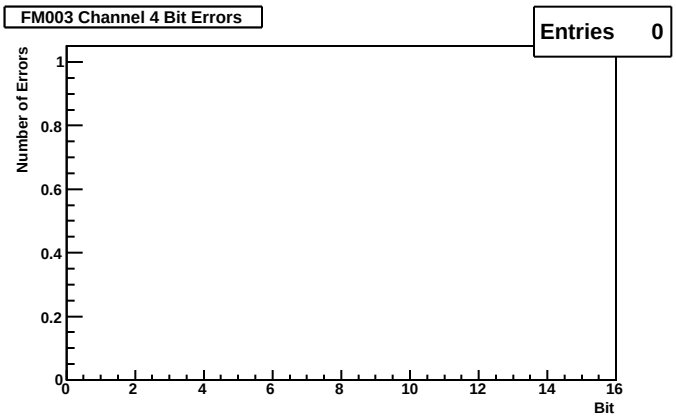

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

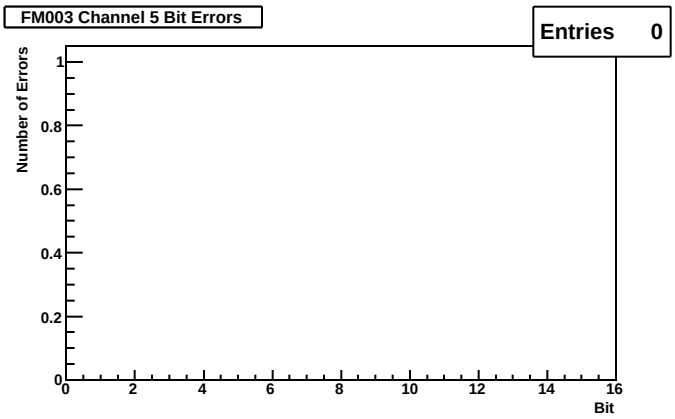

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

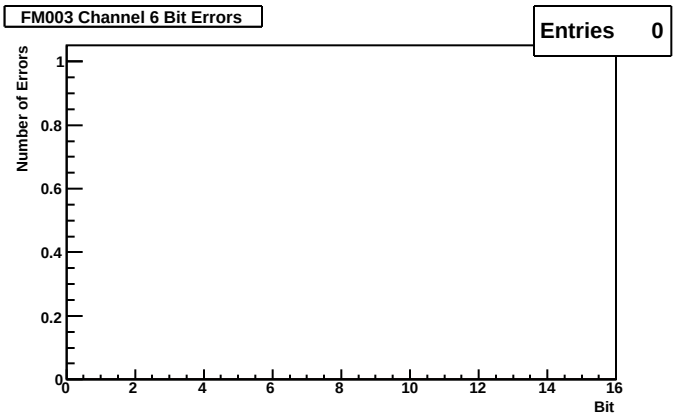

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

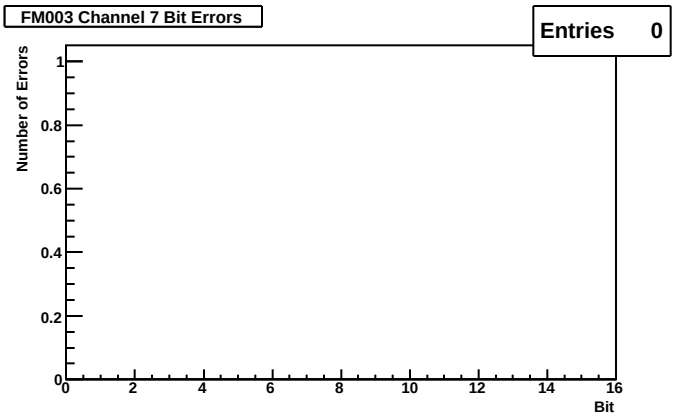

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

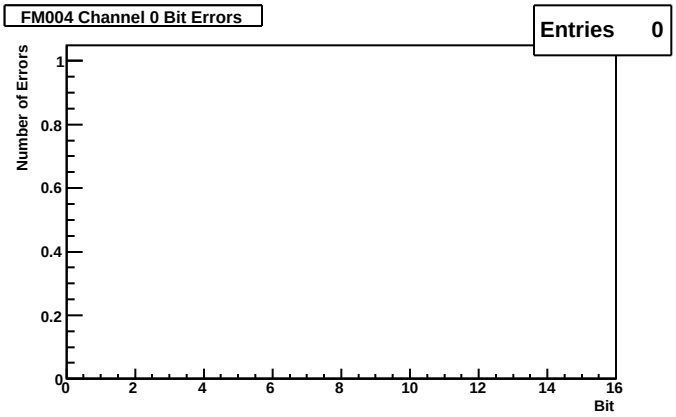

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

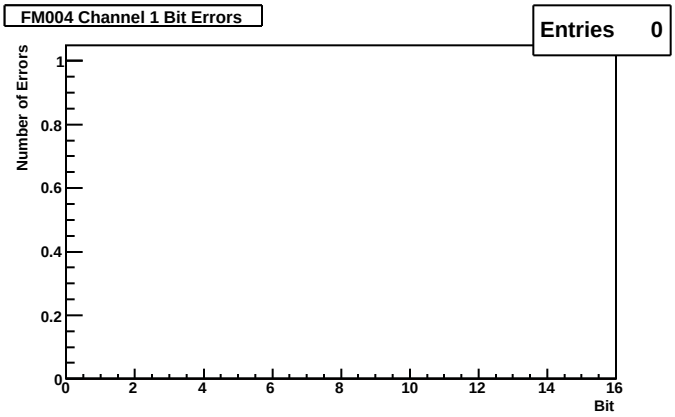

FM003 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

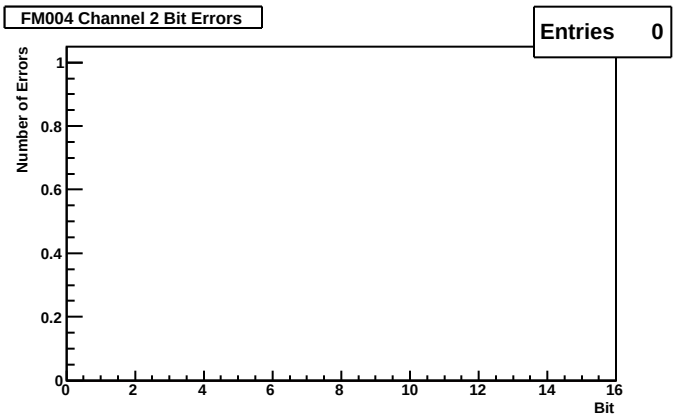

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

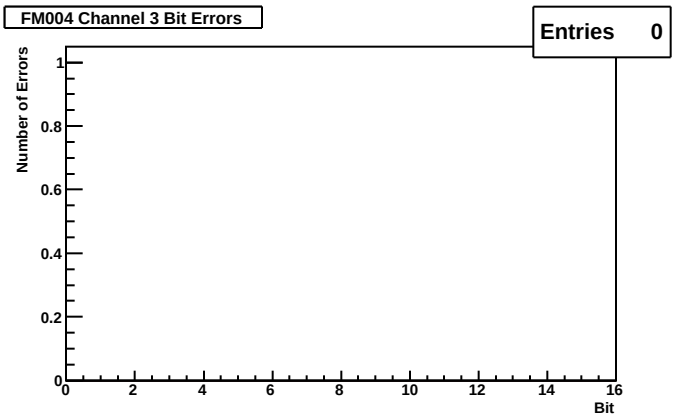

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

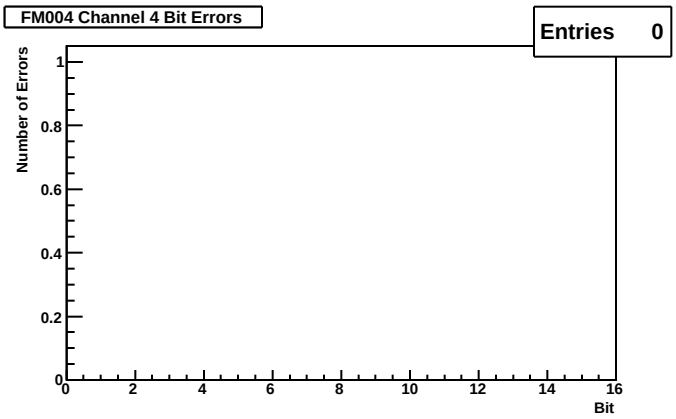

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

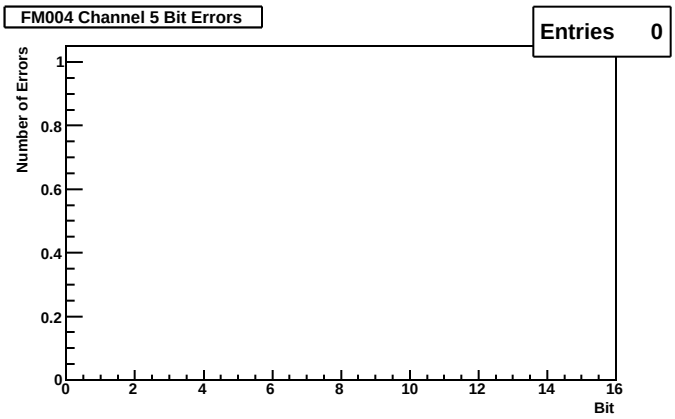

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

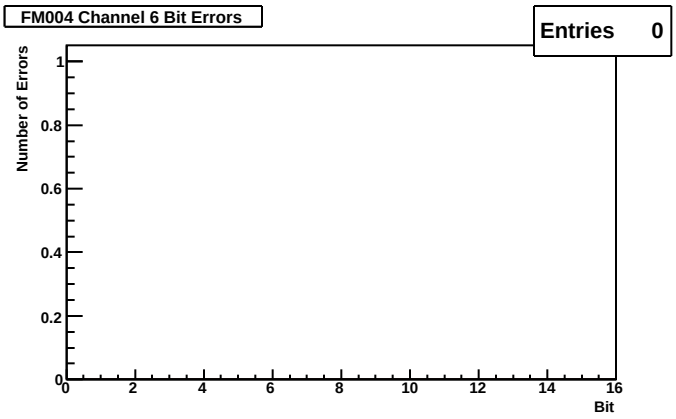

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

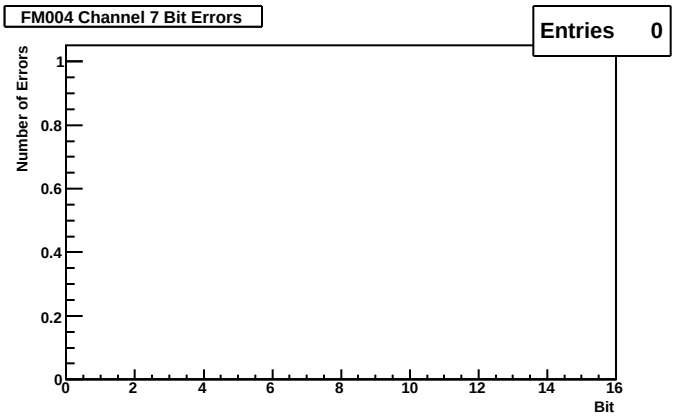

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

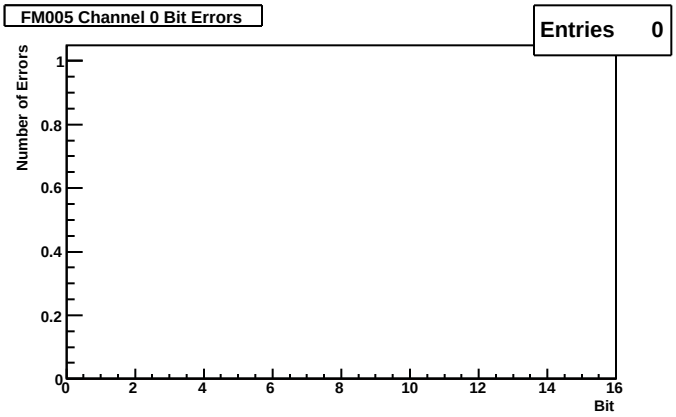

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

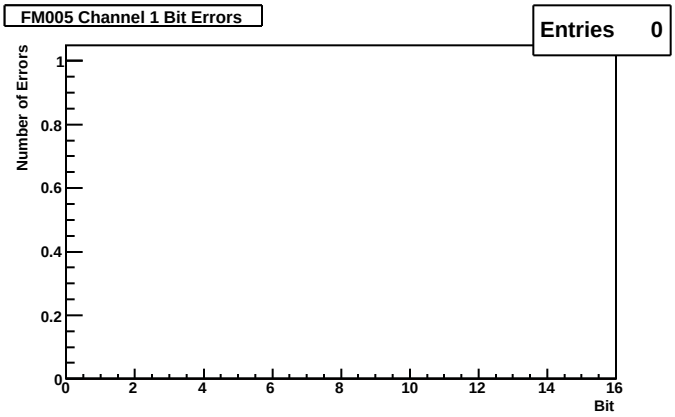

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

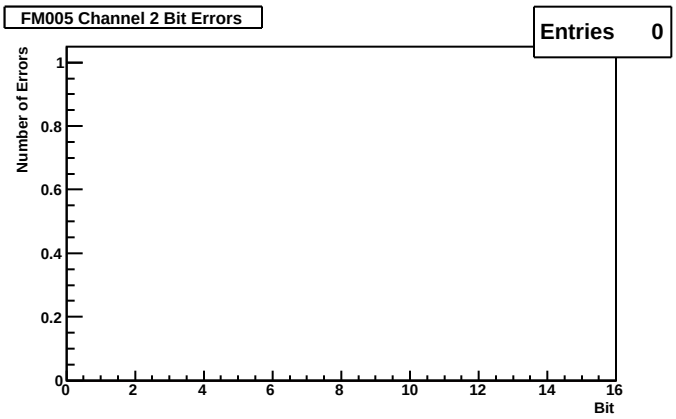

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

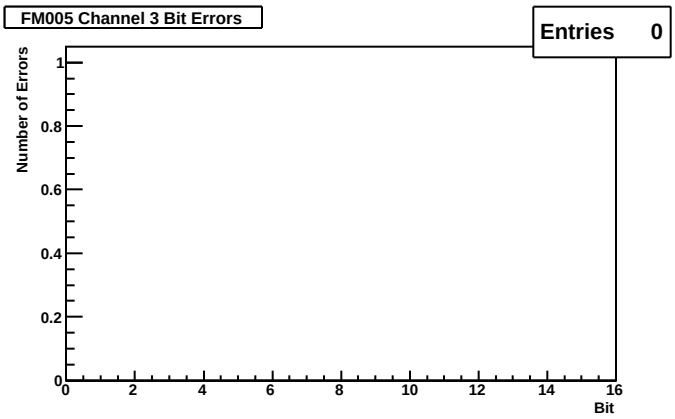

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

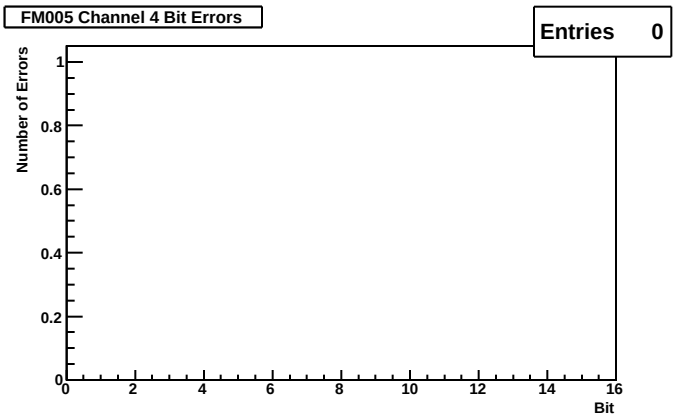

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

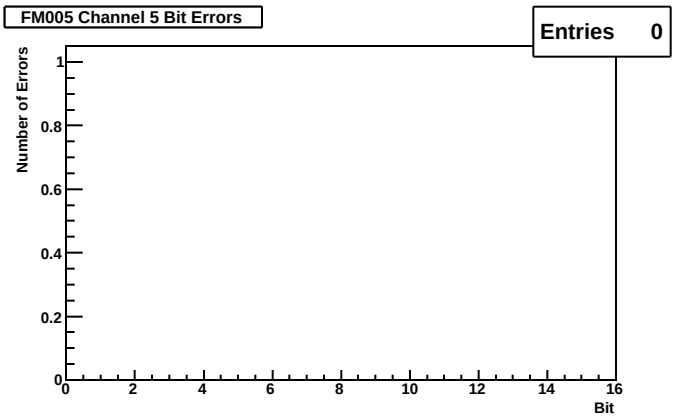

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

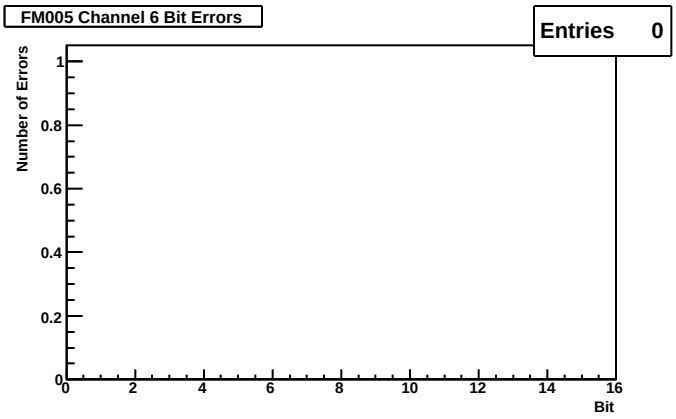

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

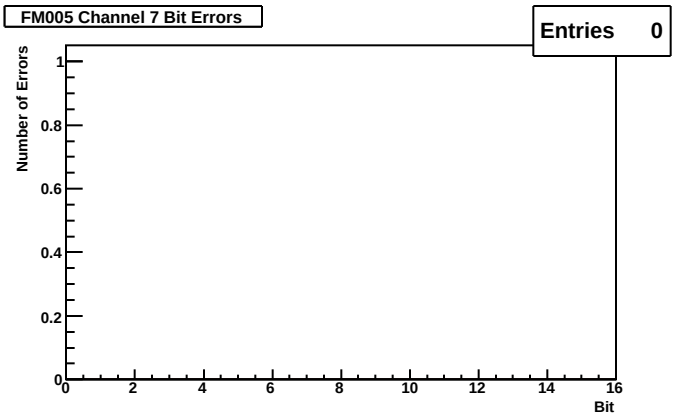

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

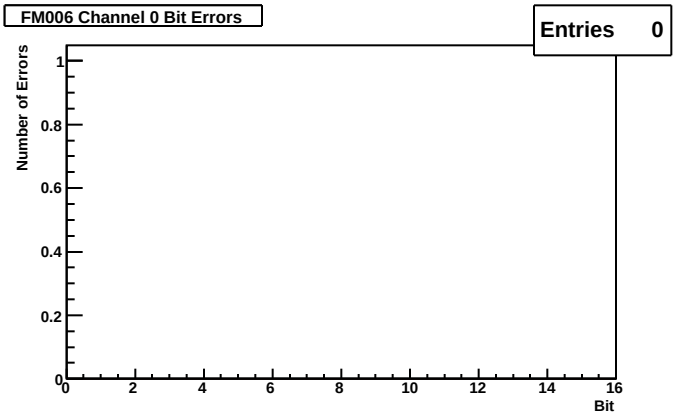

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

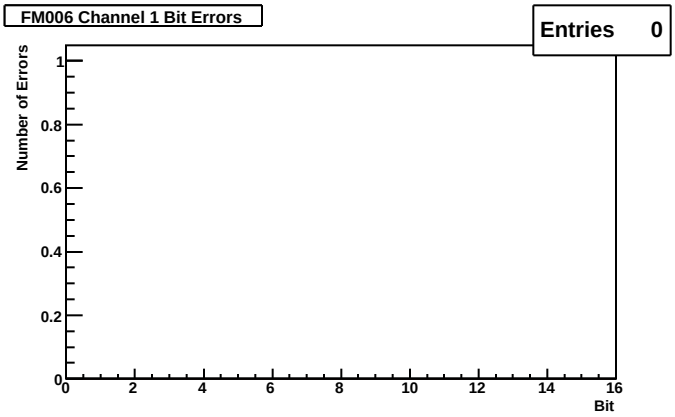

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

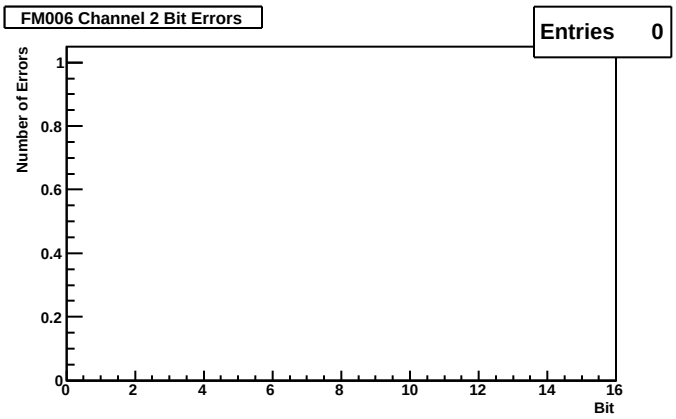

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

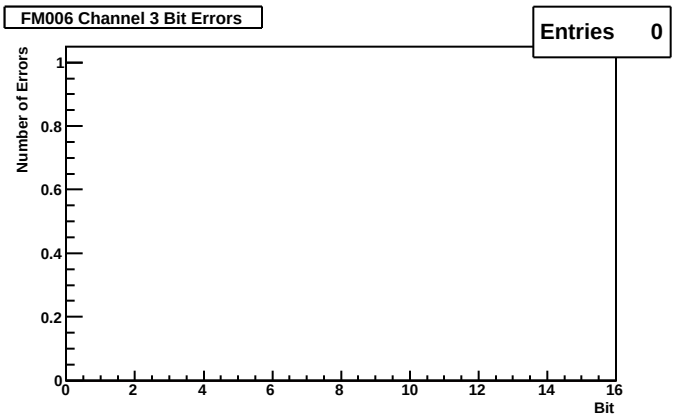

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

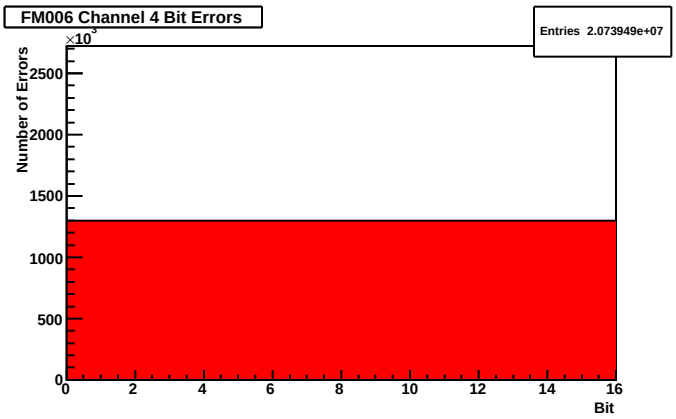

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

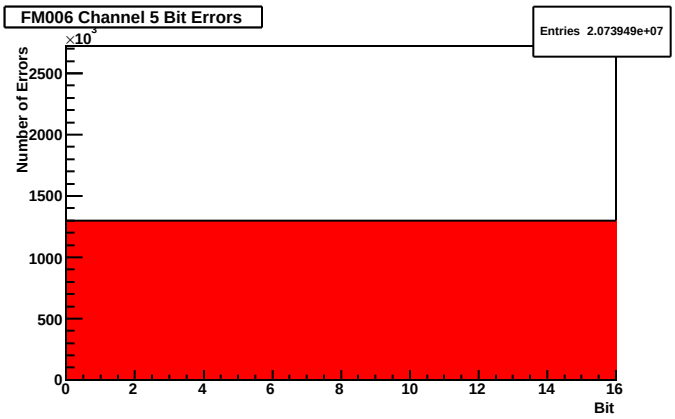

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

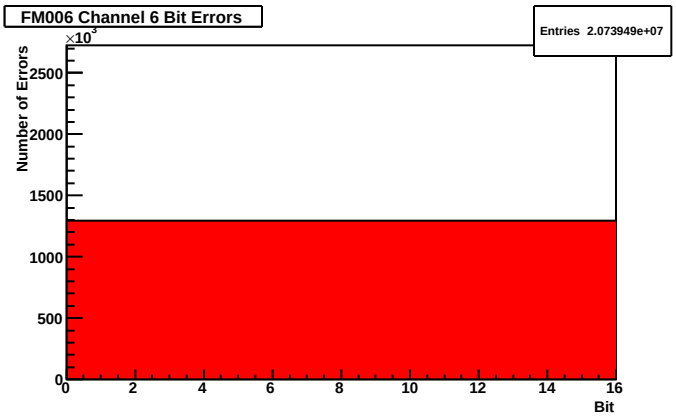

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

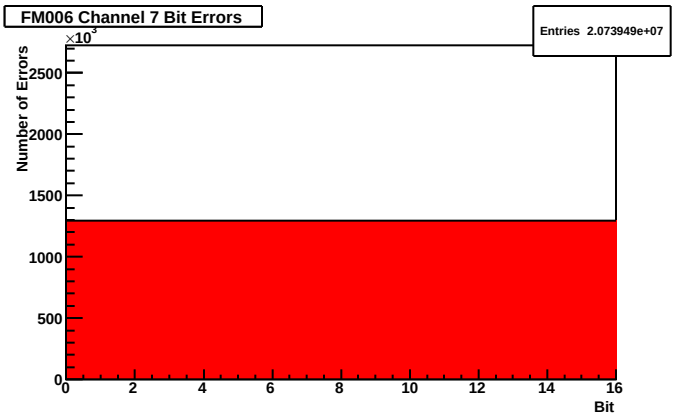

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

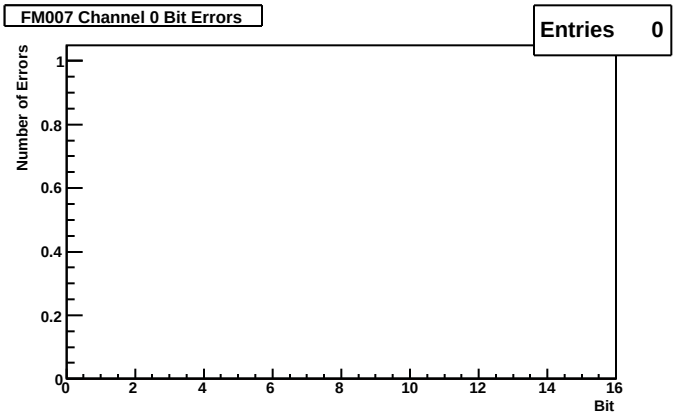

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

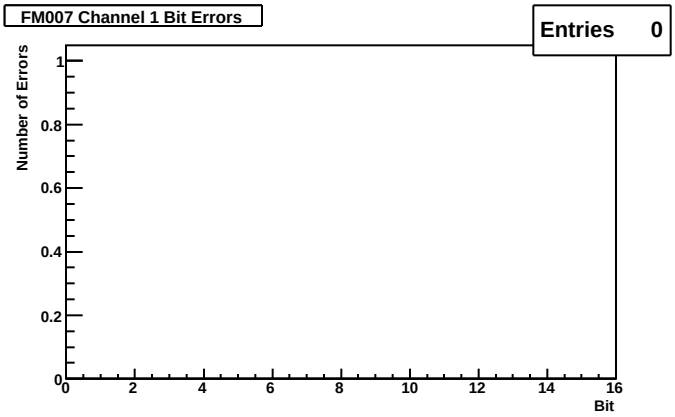

FM006 Channel 4 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

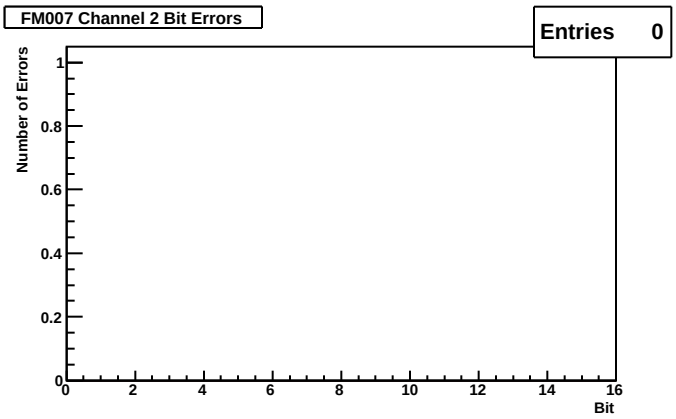

FM006 Channel 5 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

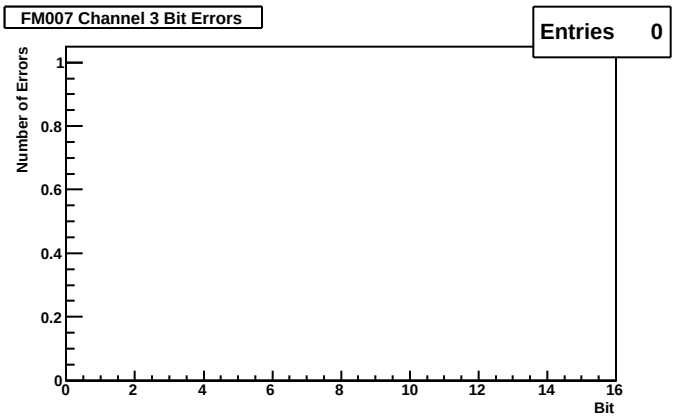

FM006 Channel 6 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

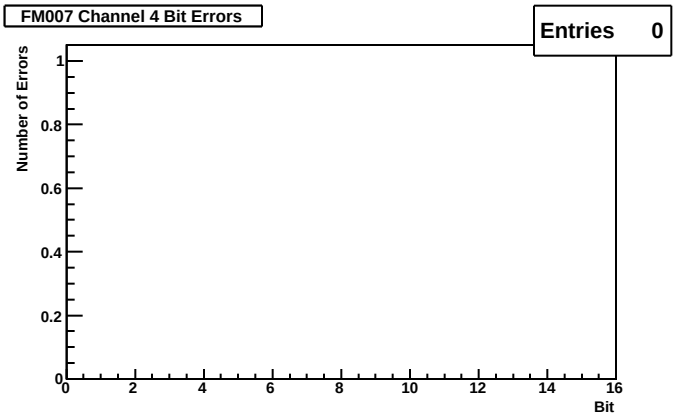

FM006 Channel 7 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

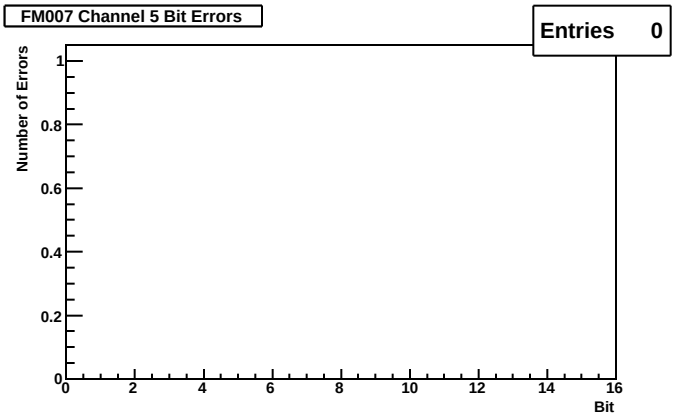

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

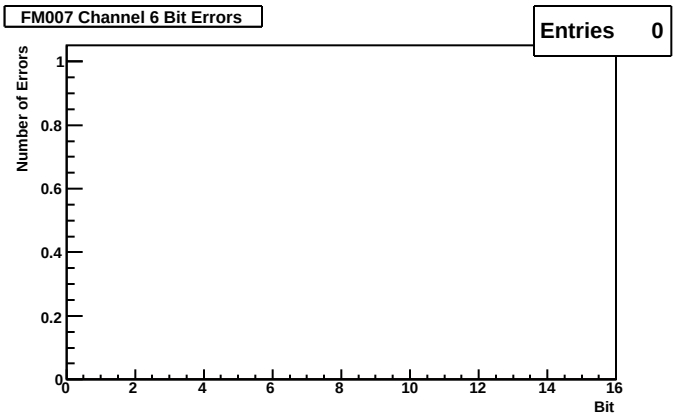

FM007 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

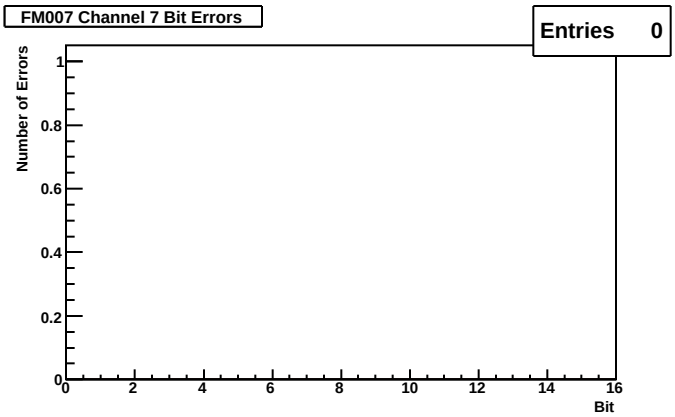

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

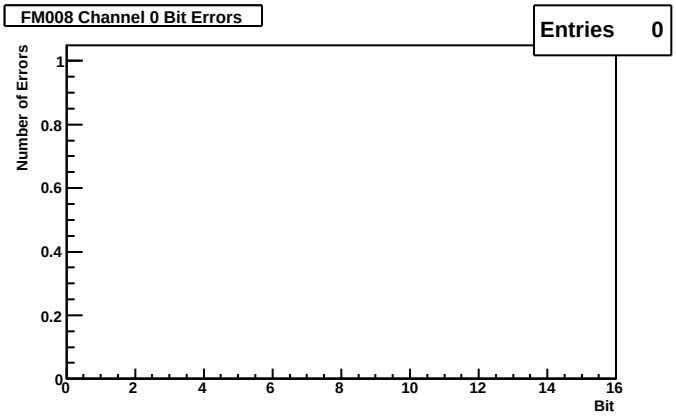

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

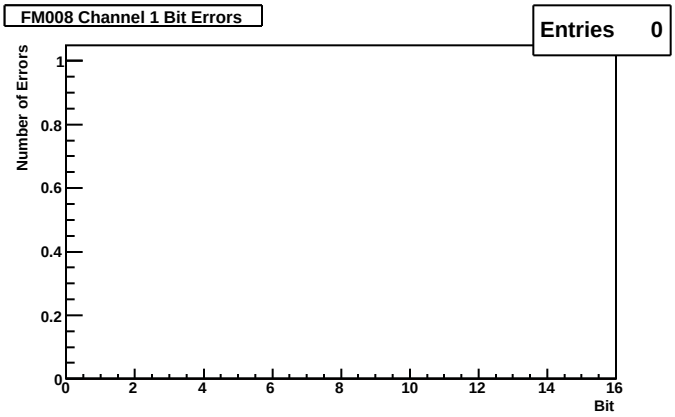

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

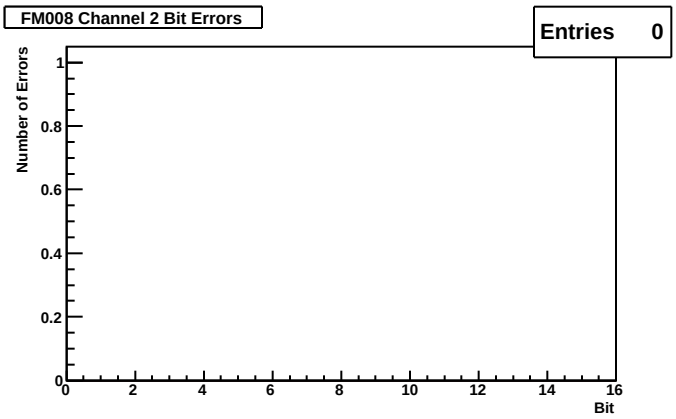

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

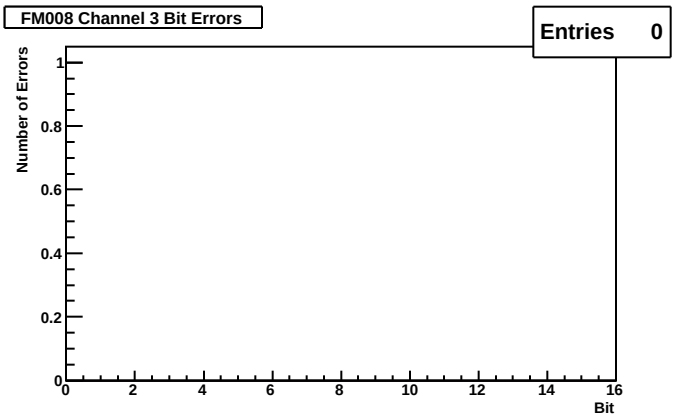

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

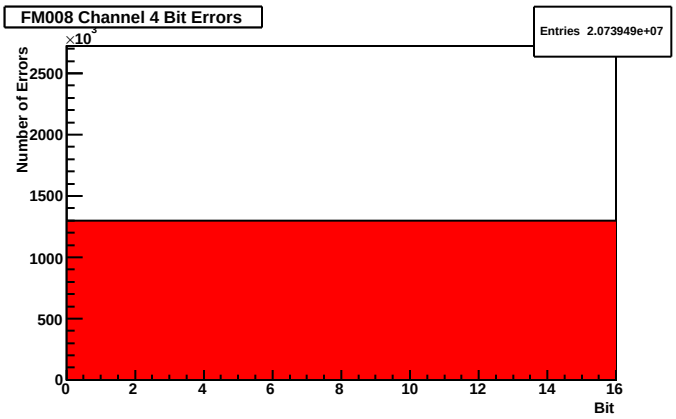

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

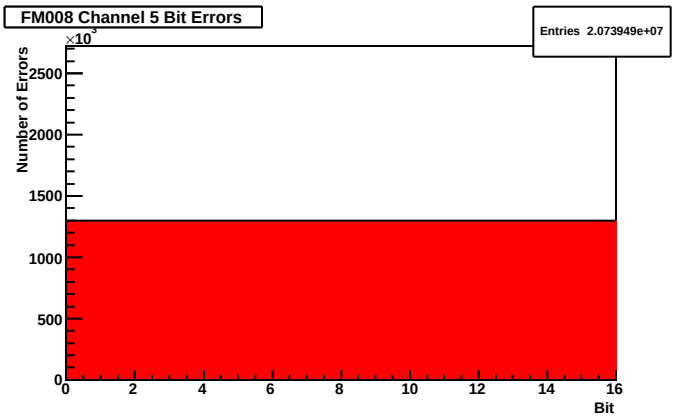

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

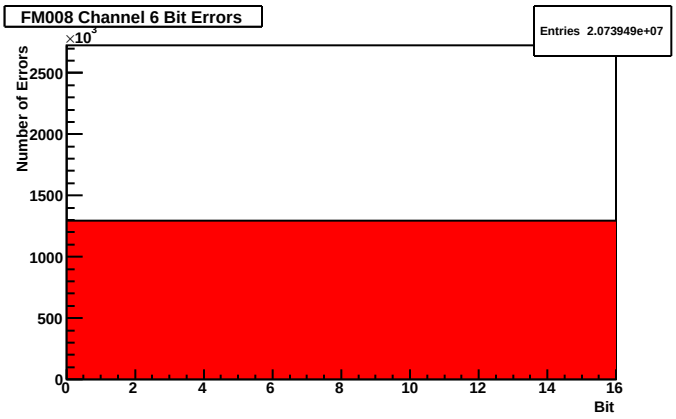

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

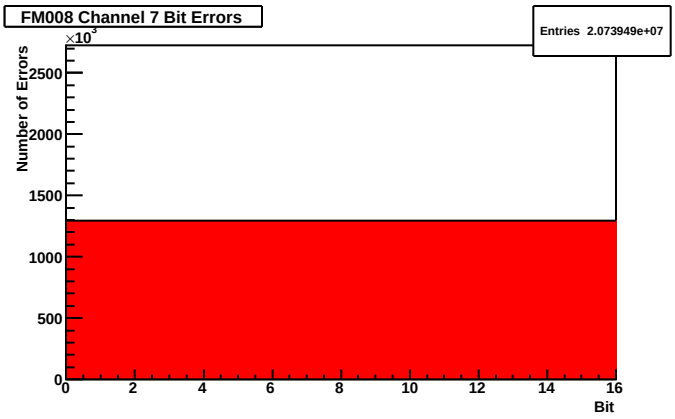

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

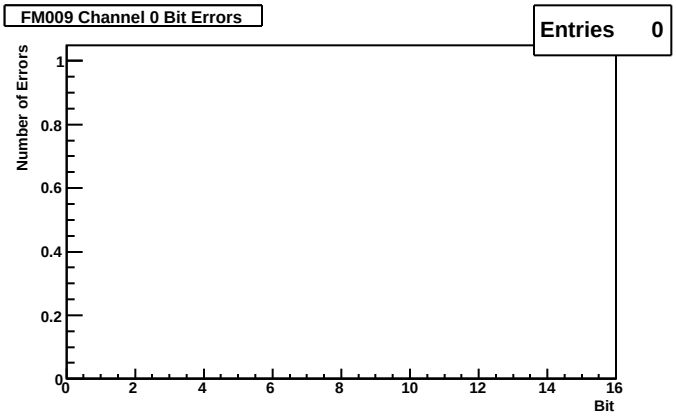

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

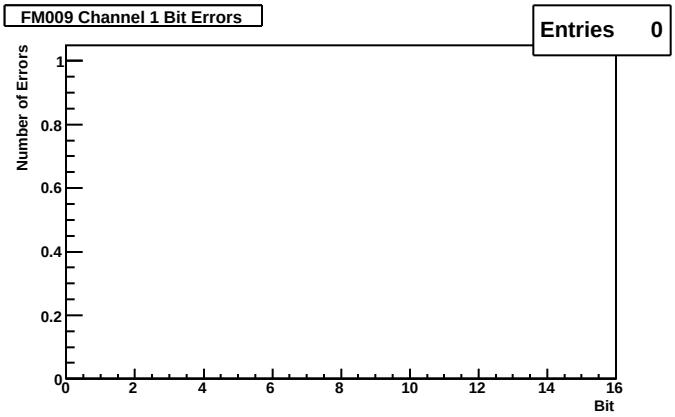

FM008 Channel 4 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

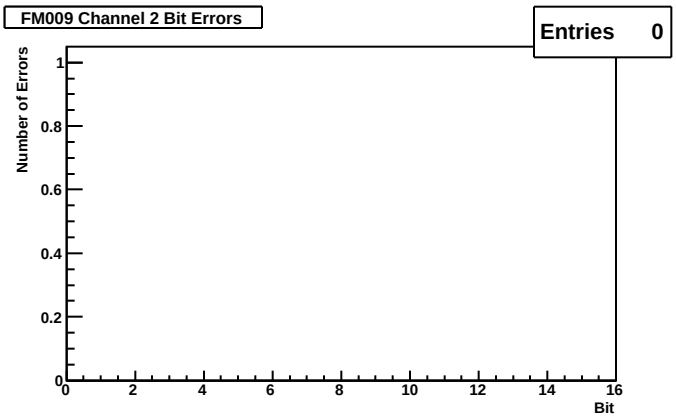

FM008 Channel 5 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

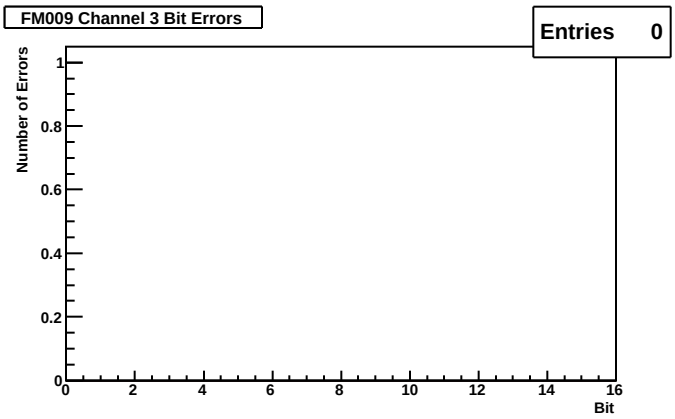

FM008 Channel 6 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

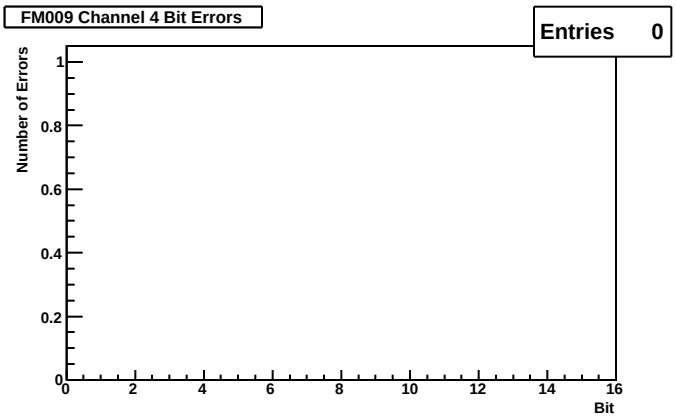

FM008 Channel 7 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

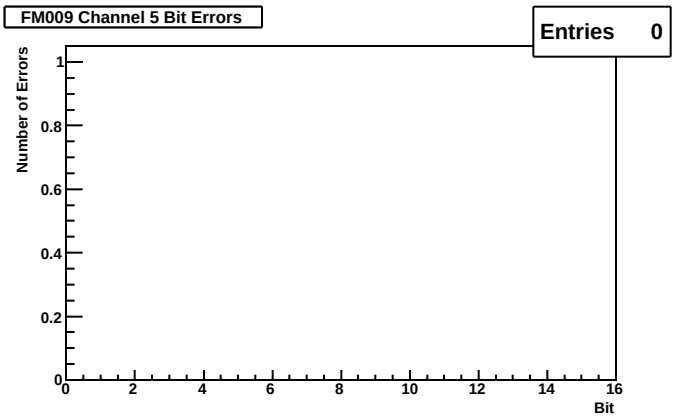

FM009 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

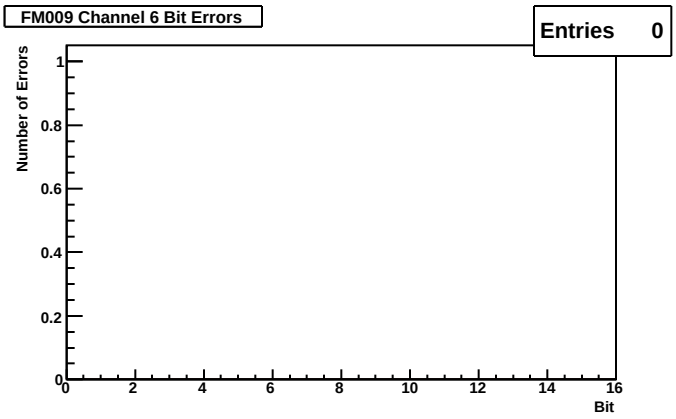

FM009 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

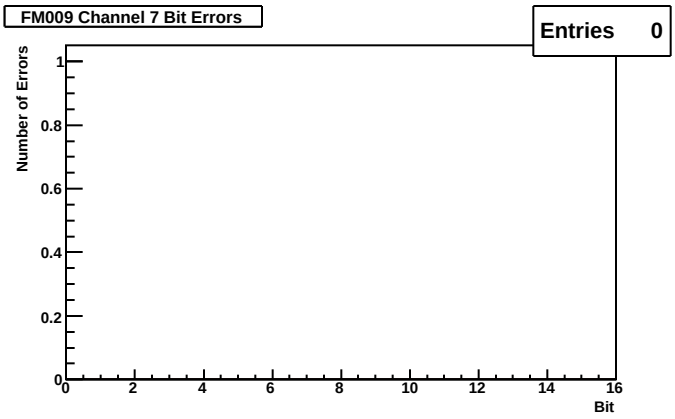

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

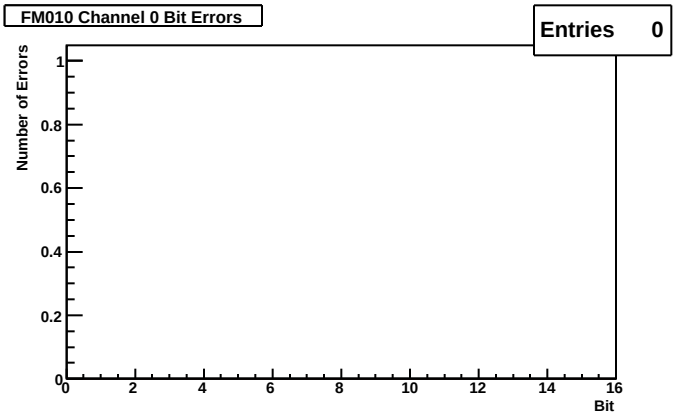

FM009 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

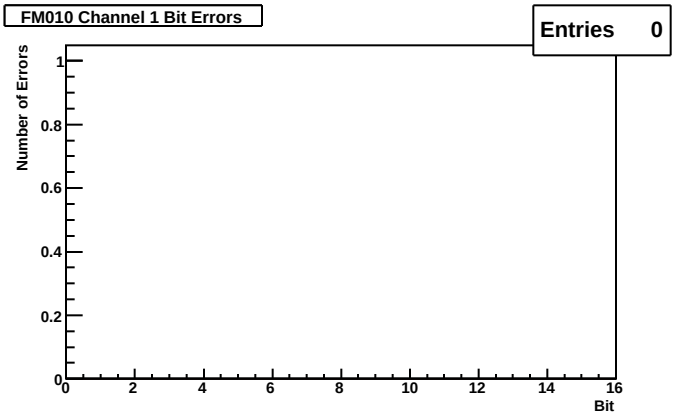

FM009 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

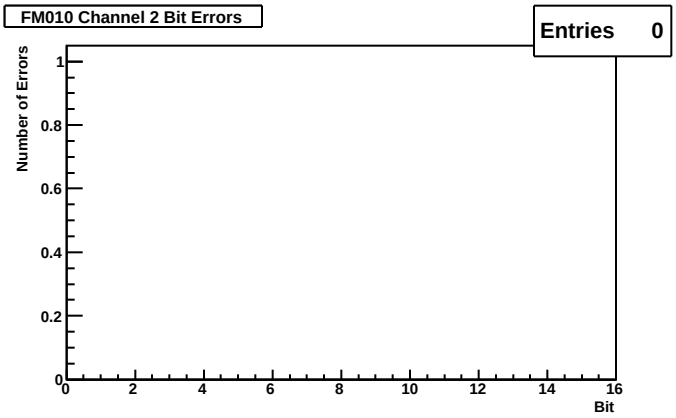

FM009 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

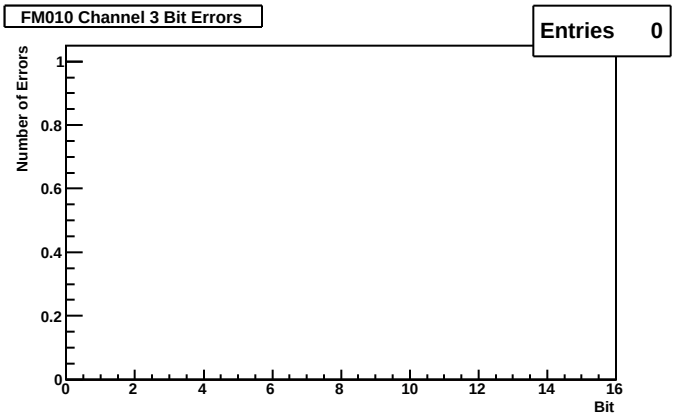

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

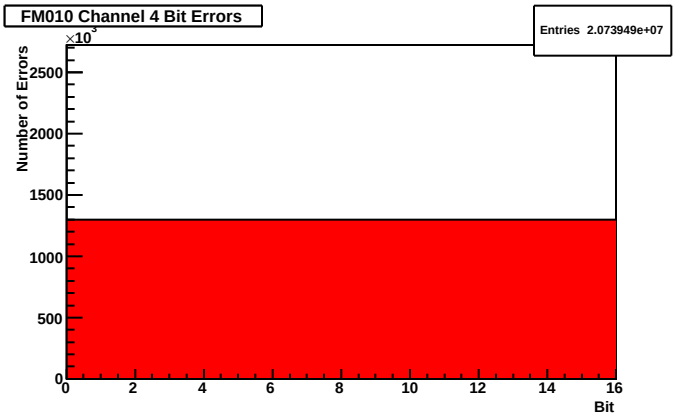

FM009 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

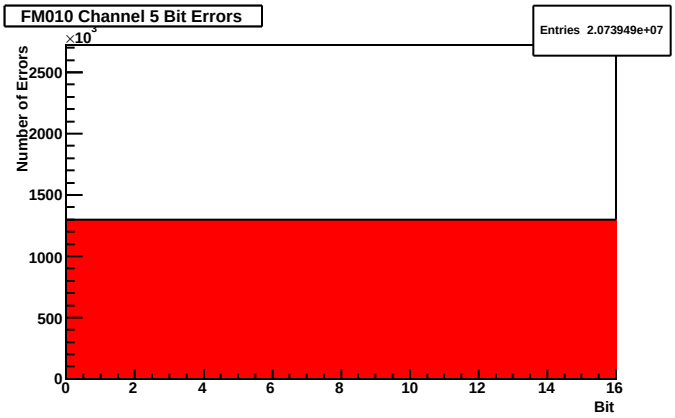

FM010 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

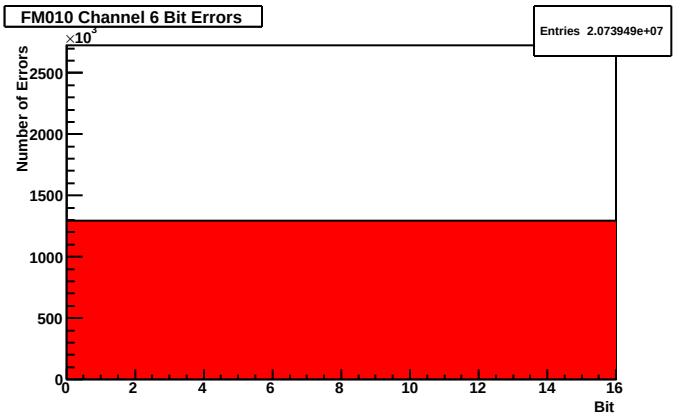

FM010 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

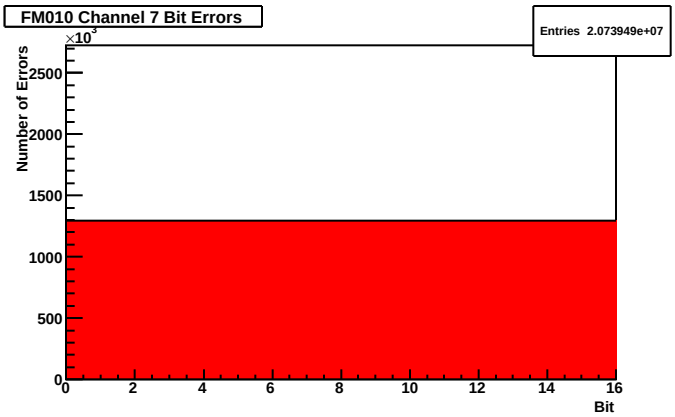

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

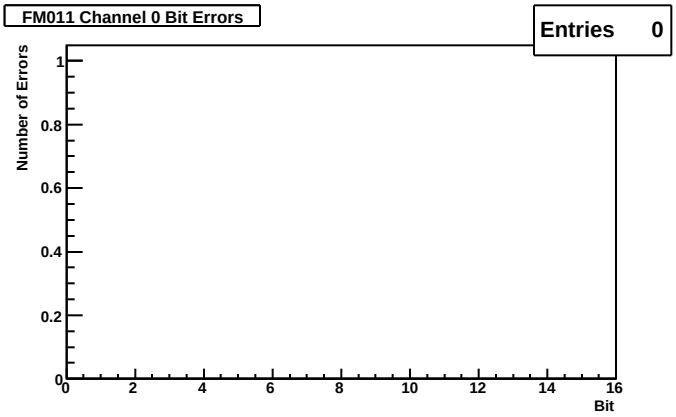

FM010 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

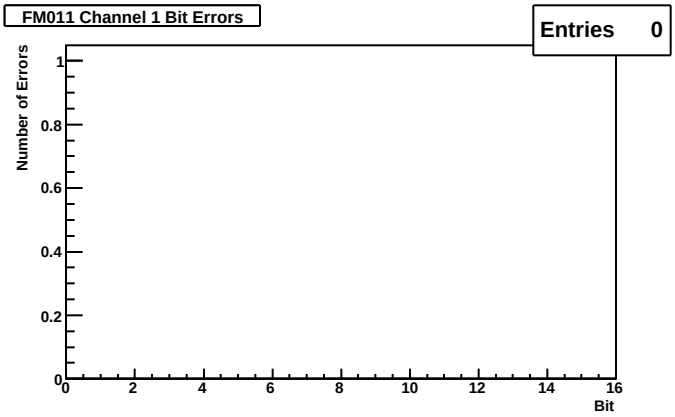

FM010 Channel 4 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

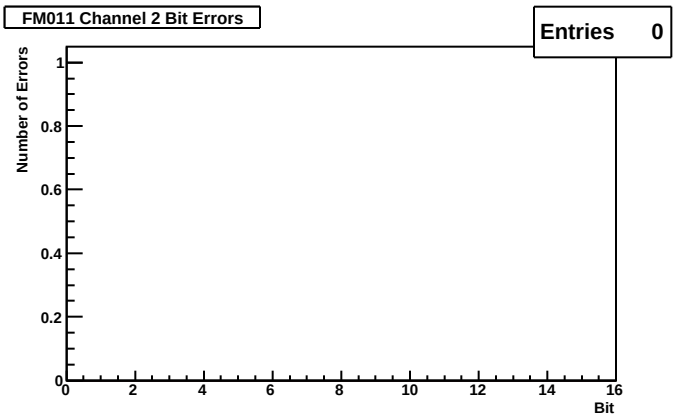

FM010 Channel 5 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit

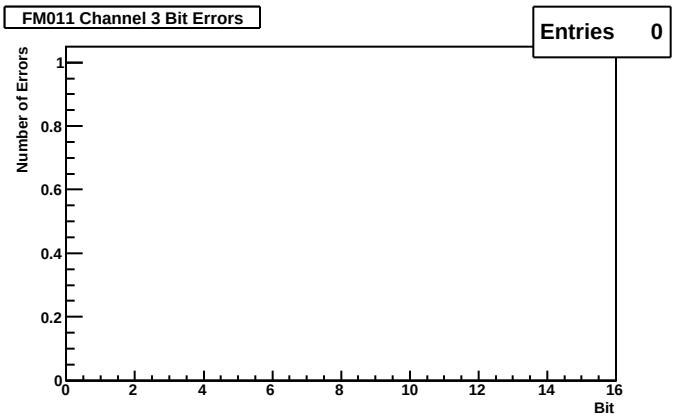

FM010 Channel 6 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit


FM010 Channel 7 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit


FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit


FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit


FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit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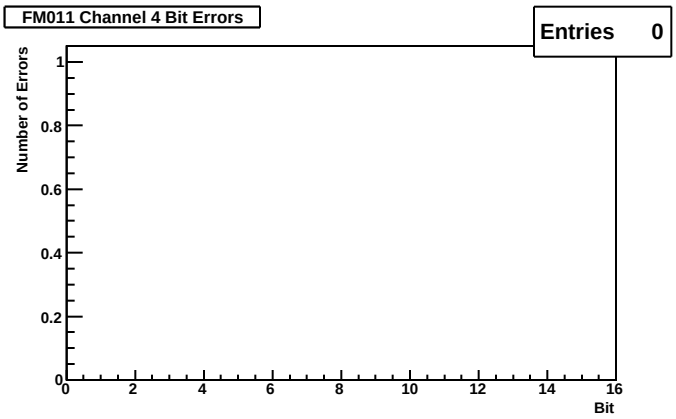
Entries 0
Number of Errors
Bit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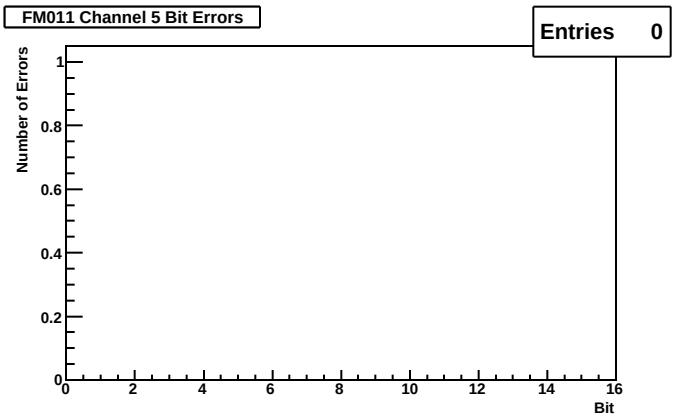
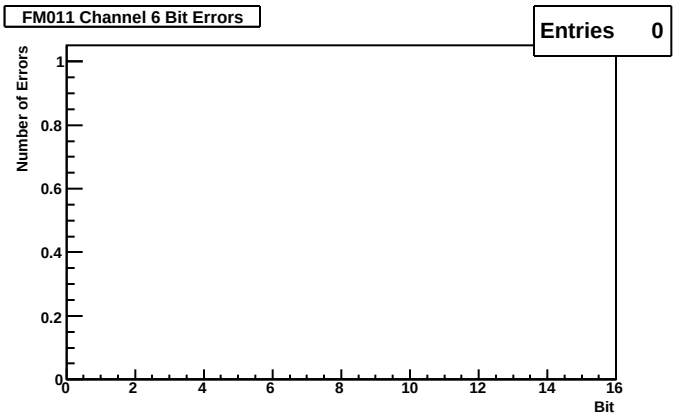
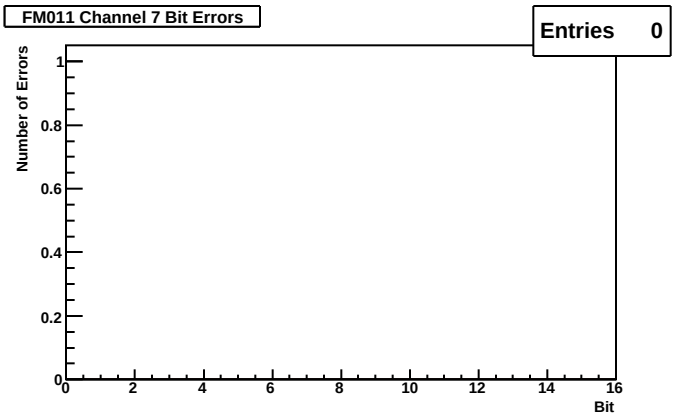
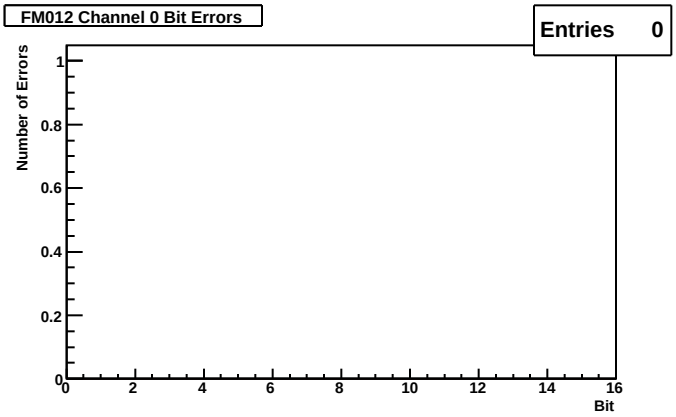
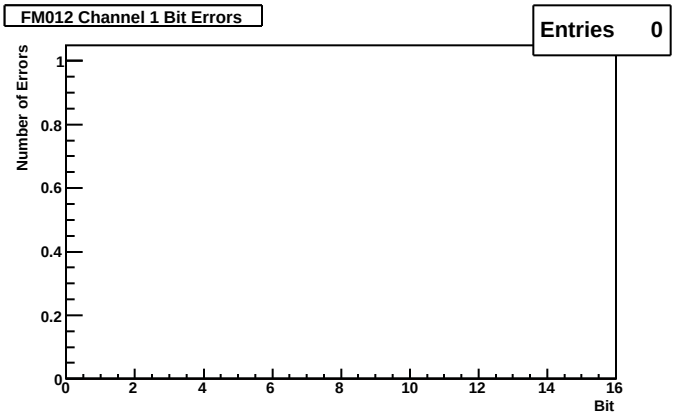
Entries 0
Number of Errors
Bit

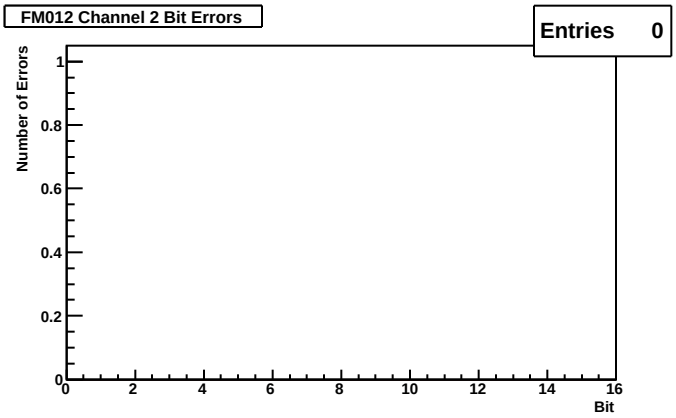

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

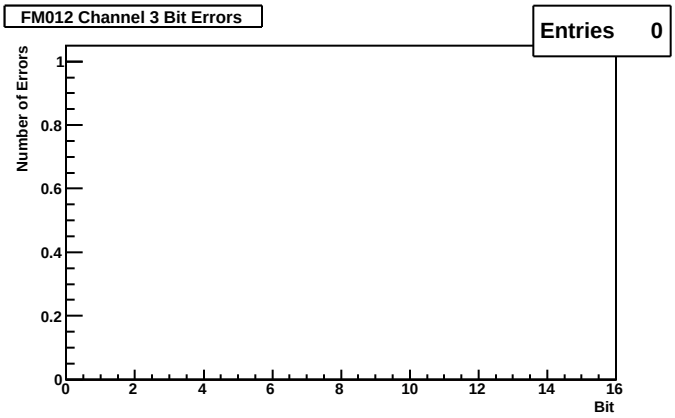

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

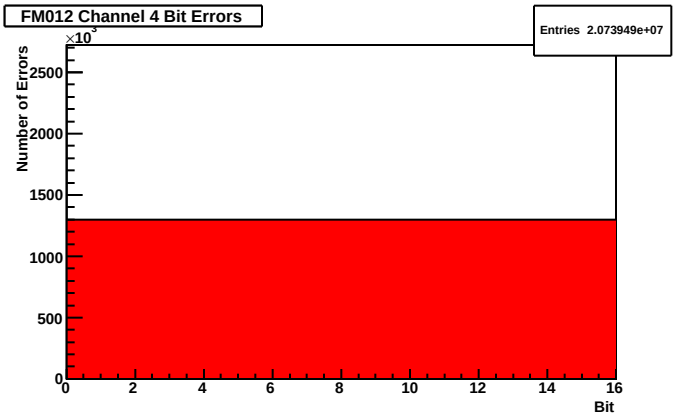

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

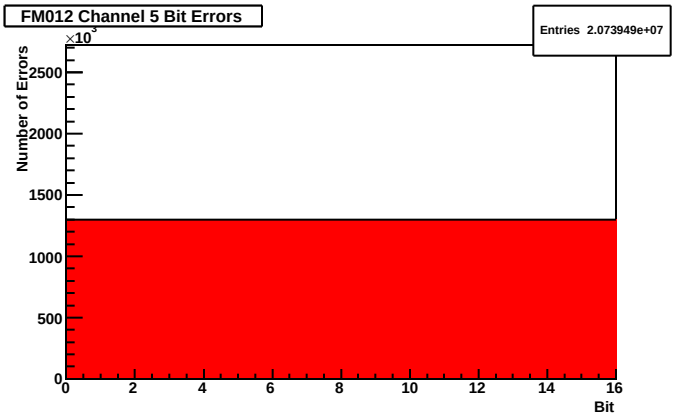

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

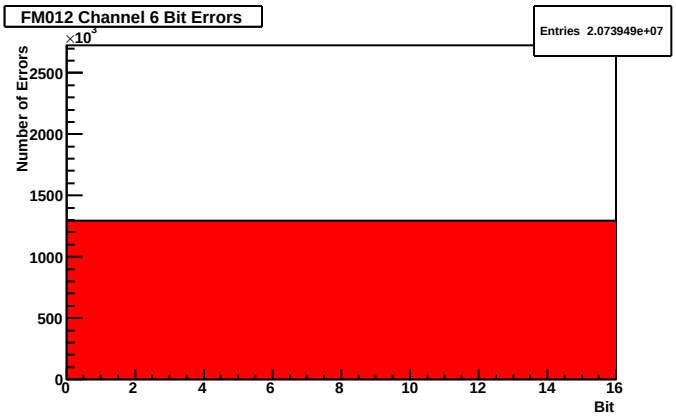

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

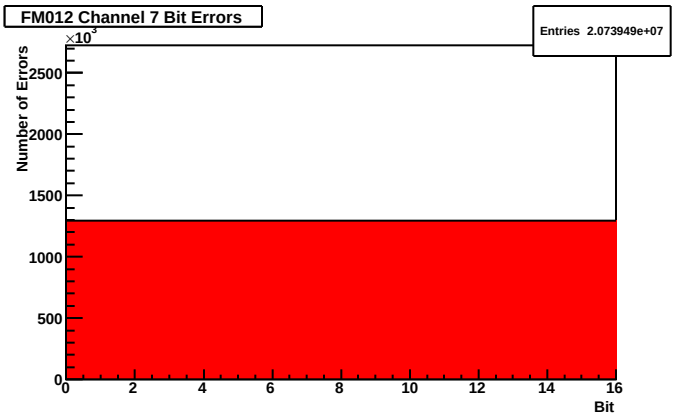

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 2.073949e+07
Number of Errors
Bit