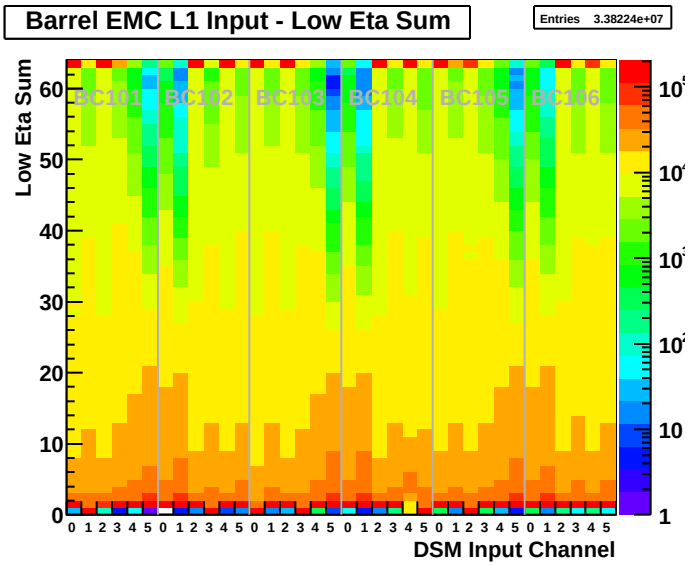
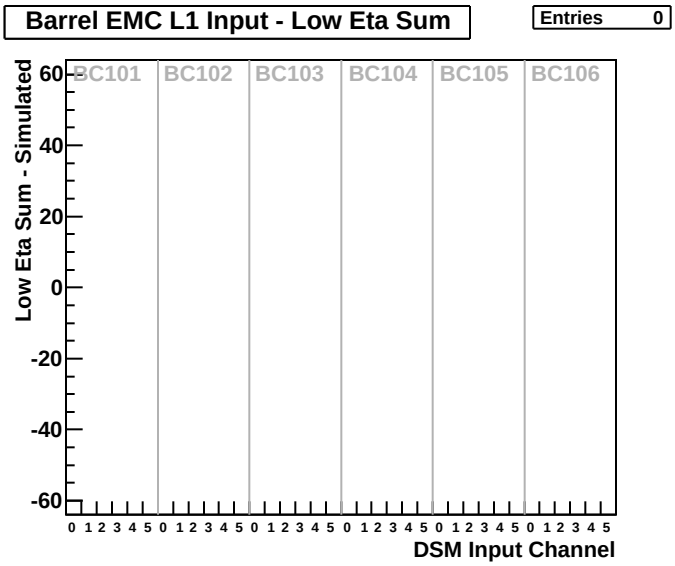
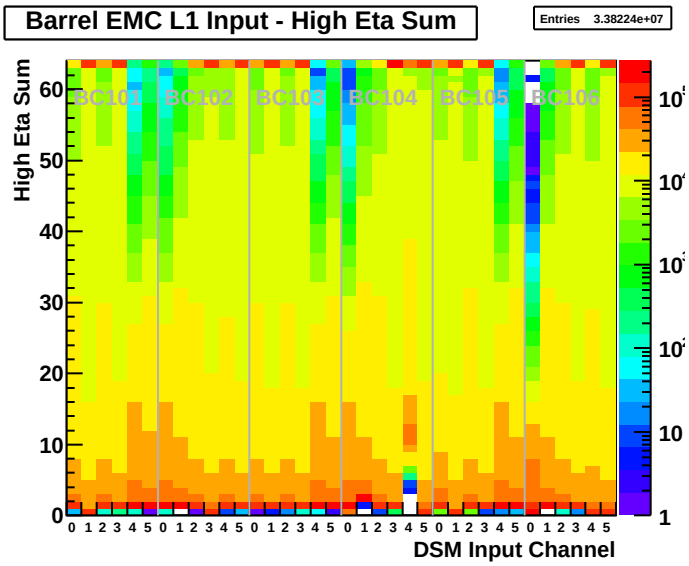
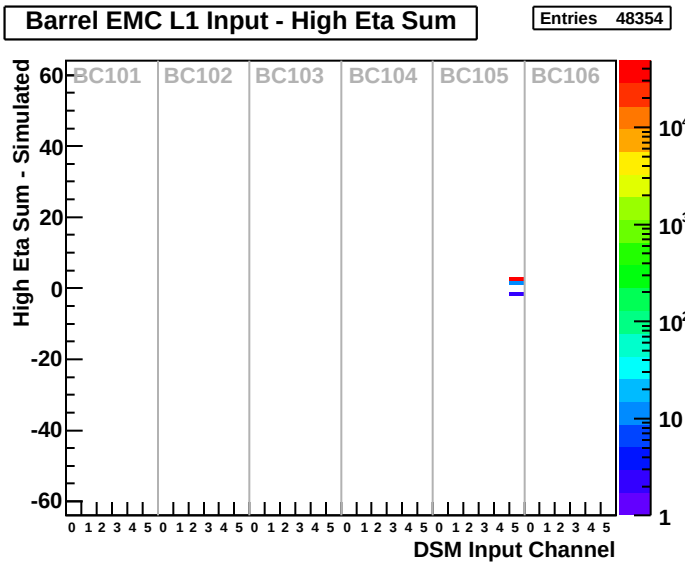
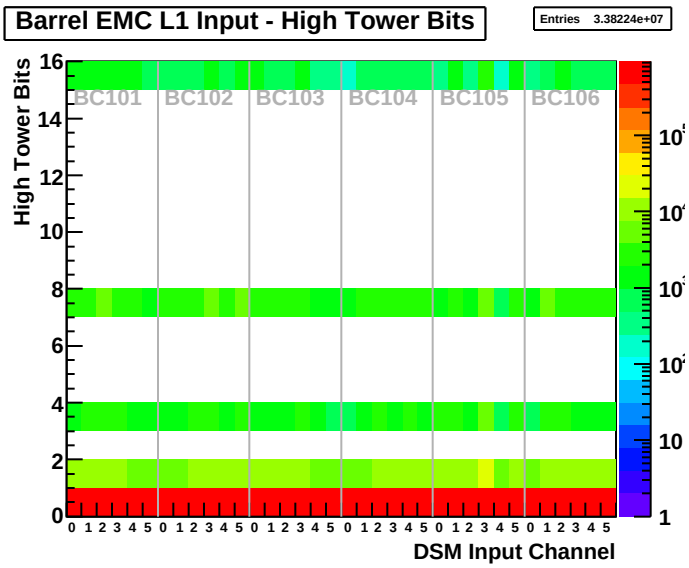
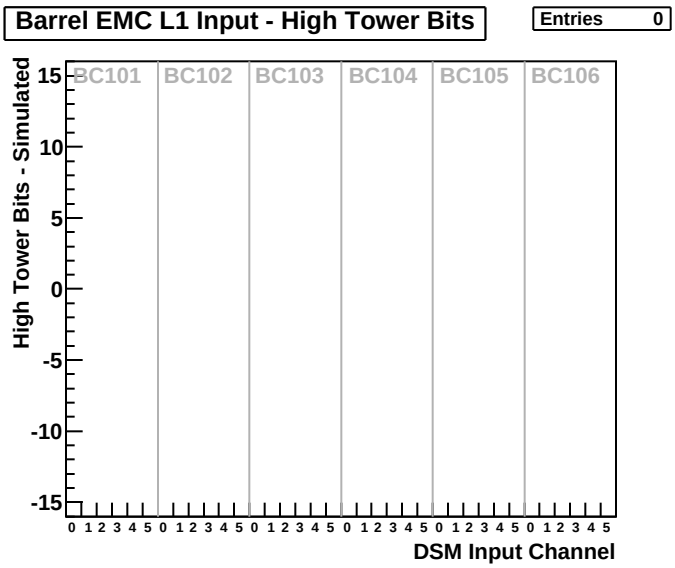

Barrel EMC L1 Input - Low Eta Sum
Entries 3.38224e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

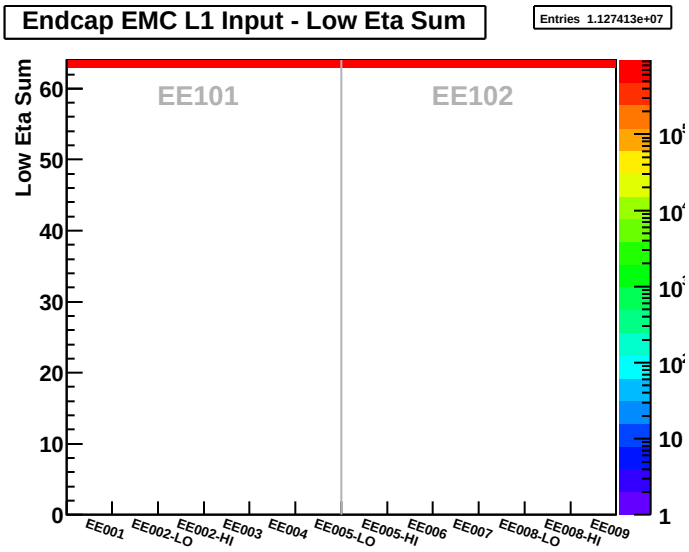

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

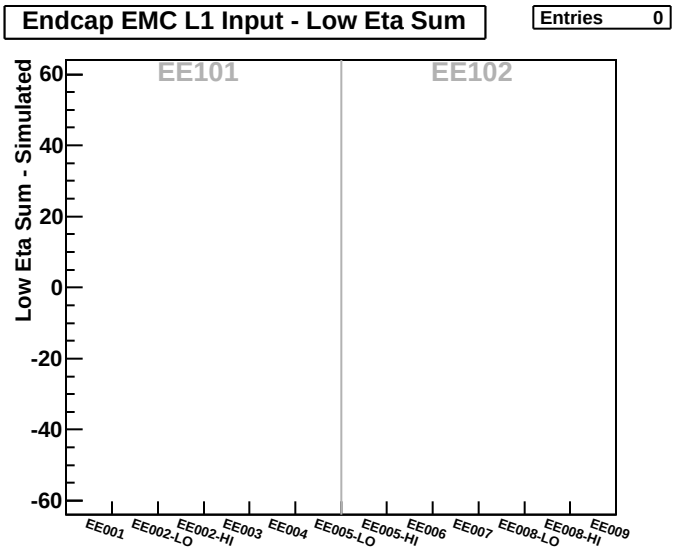

Barrel EMC L1 Input - High Eta Sum
Entries 3.38224e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

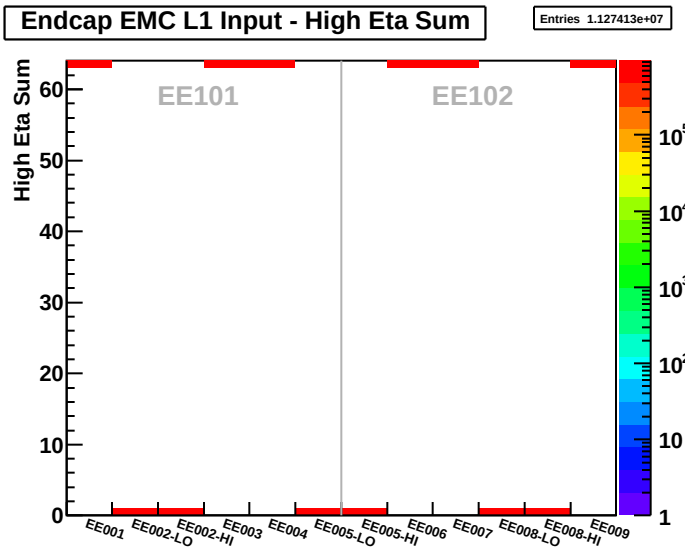

Barrel EMC L1 Input - High Eta Sum
Entries 48354
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

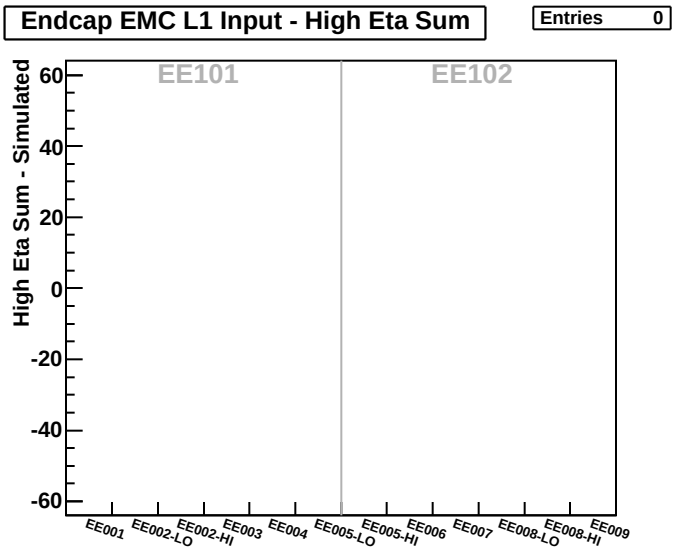

Barrel EMC L1 Input - High Tower Bits
Entries 3.38224e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

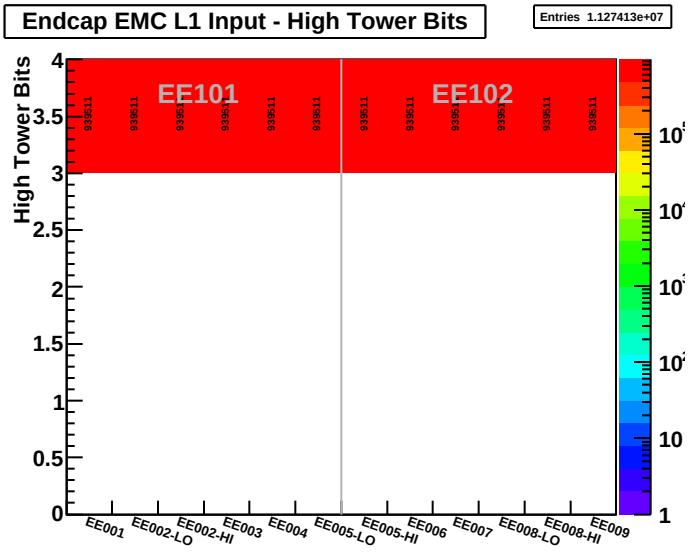

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

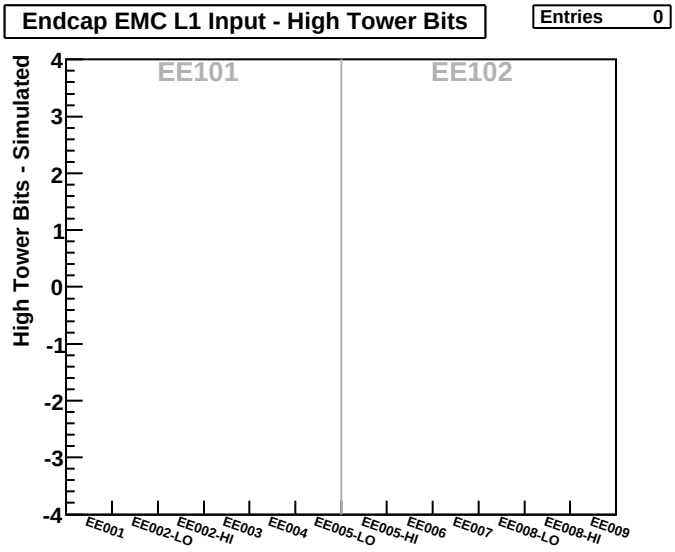

Endcap EMC L1 Input - Low Eta Sum
Entries 1.127413e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

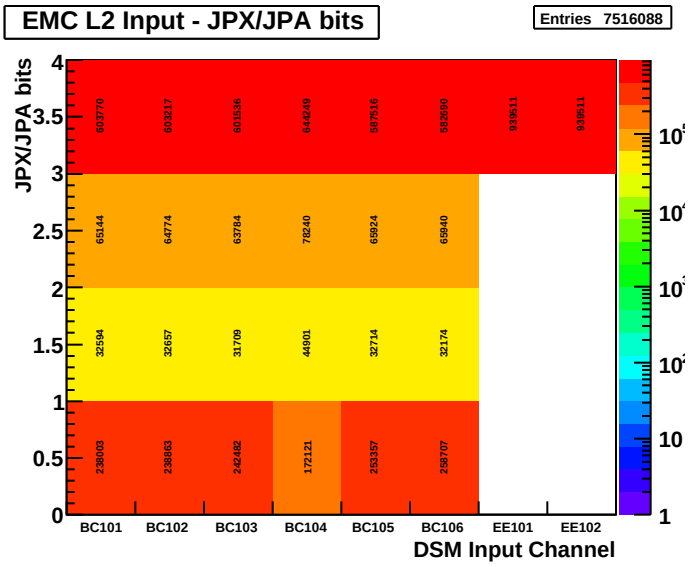

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

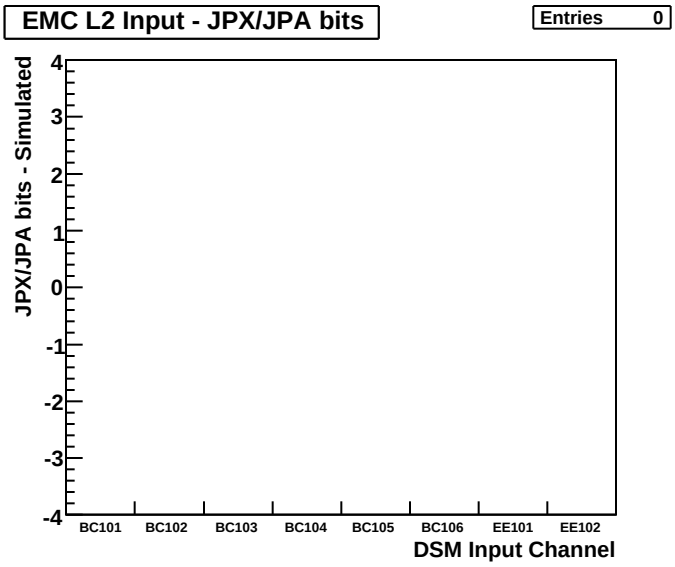

Endcap EMC L1 Input - High Eta Sum
Entries 1.127413e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

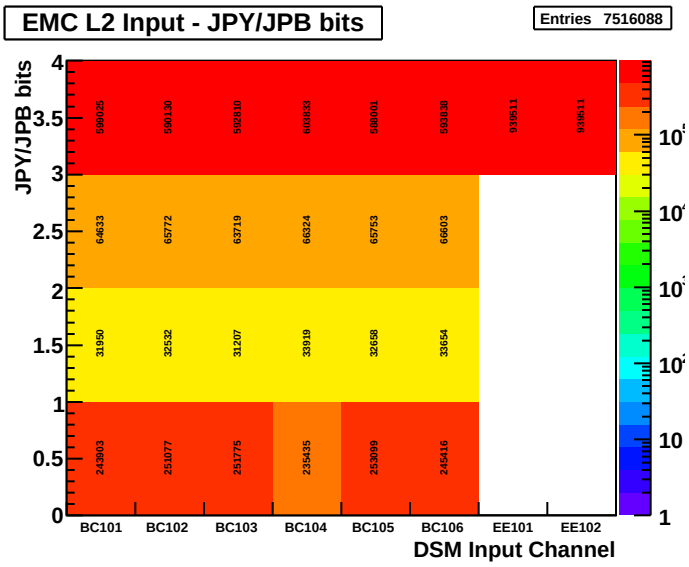

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

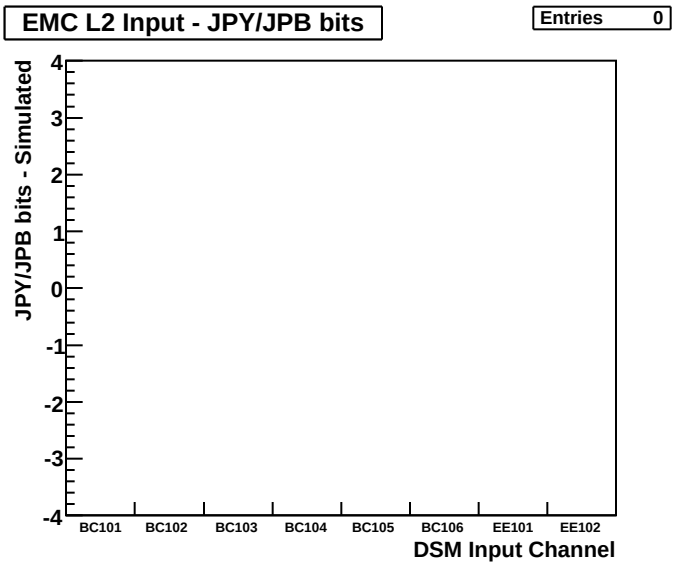

Endcap EMC L1 Input - High Tower Bits
Entries 1.127413e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

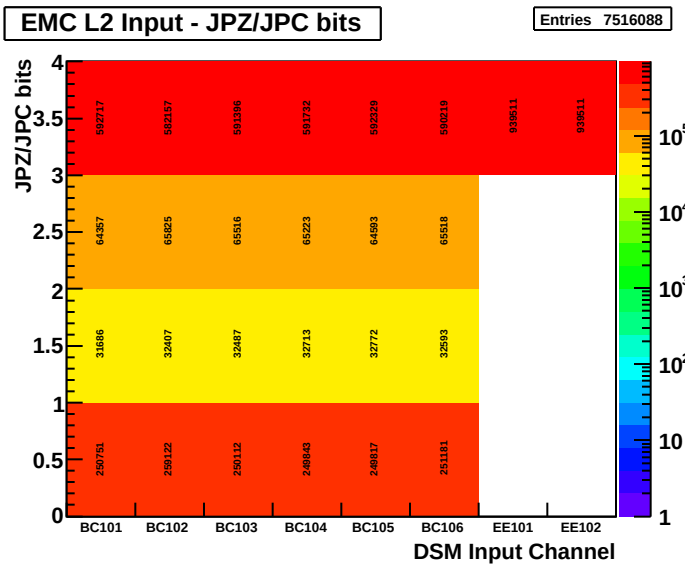

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

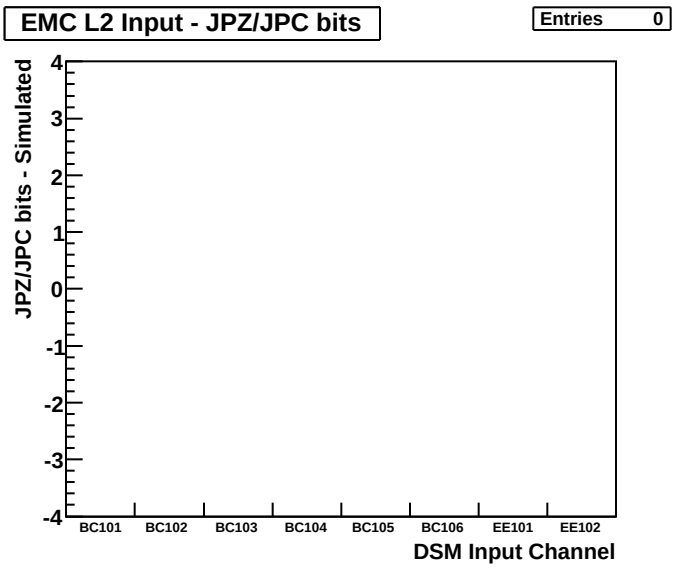

EMC L2 Input - JPX/JPA bits
Entries 7516088
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
603770 603217 601536 644249 597516 592890 939511 939511
65144 64774 63784 78240 65924 65940
32594 32657 31709 44901 32714 32174
238003 238863 242482 172121 253357 258707

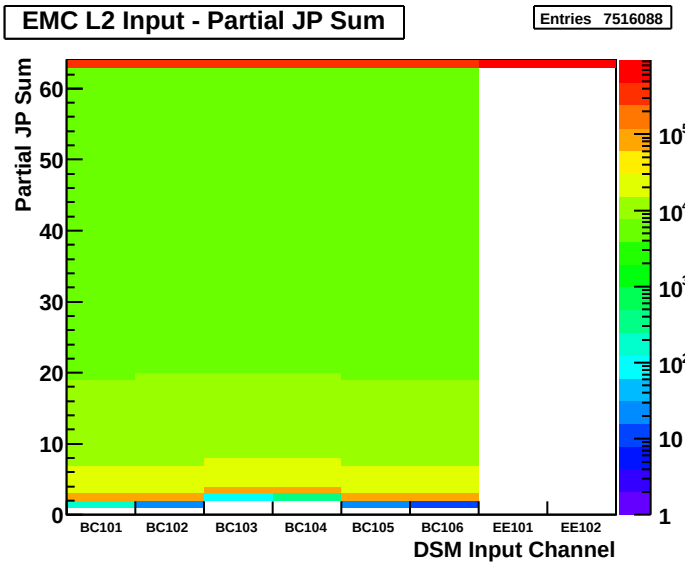

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

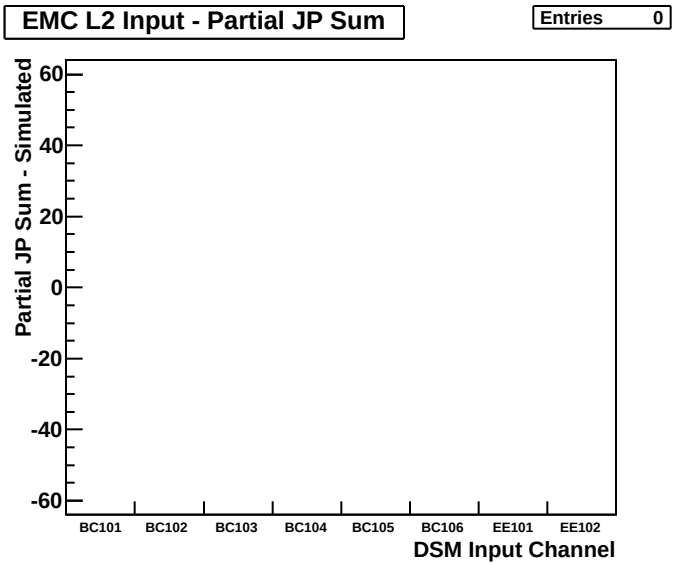

EMC L2 Input - JPY/JPB bits
Entries 7516088
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
598025 590130 592810 603833 598001 593838 939511 939511
64633 65772 63719 66324 65753 66603
31950 32532 31207 33919 32658 33654
243803 251077 251775 235435 253099 245416

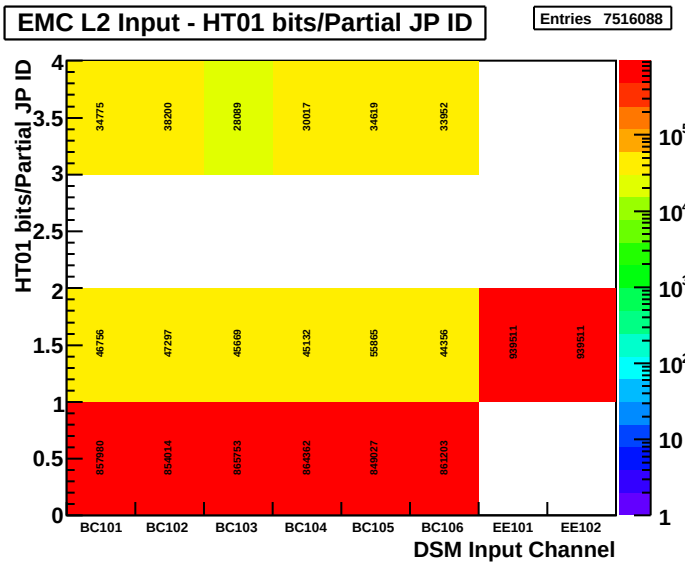

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

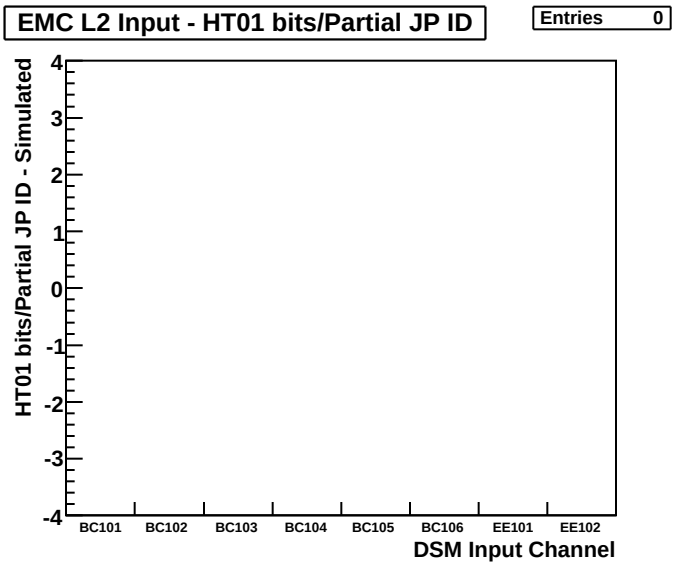

EMC L2 Input - JPZ/JPC bits
Entries 7516088
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
592717 582157 591396 591732 592329 590219 939511 939511
64357 65825 65516 65223 64593 65518
31686 32407 32487 32713 32772 32593
250751 259122 259112 249843 249817 251181

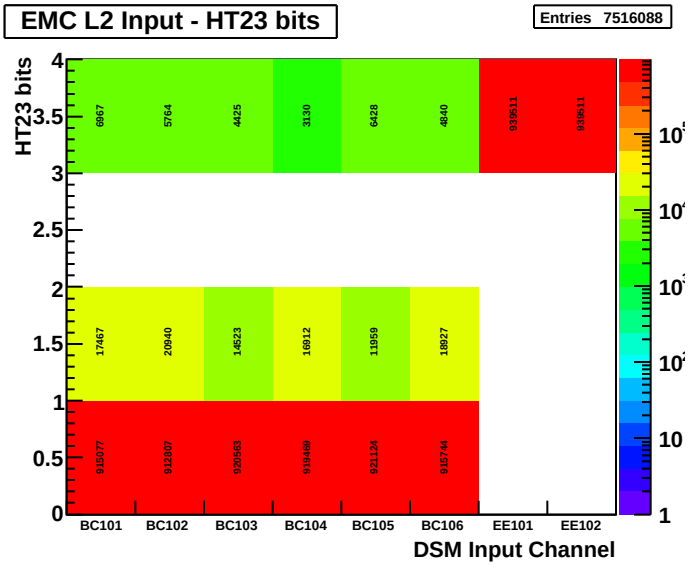

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

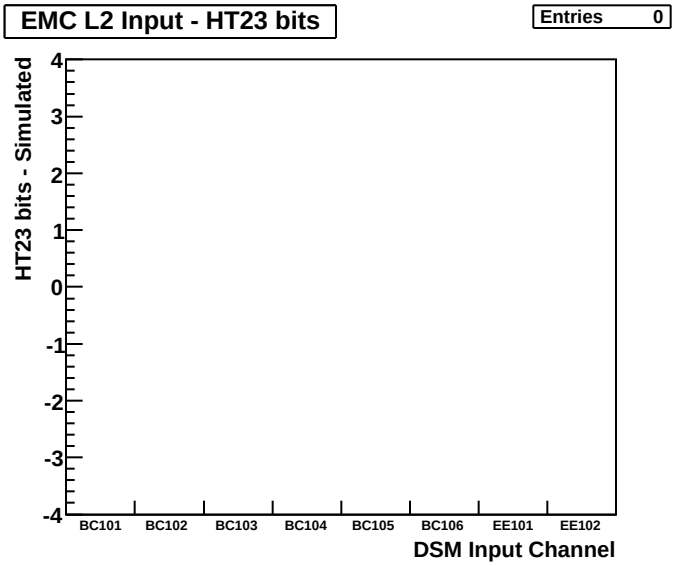

EMC L2 Input - Partial JP Sum
Entries 7516088
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

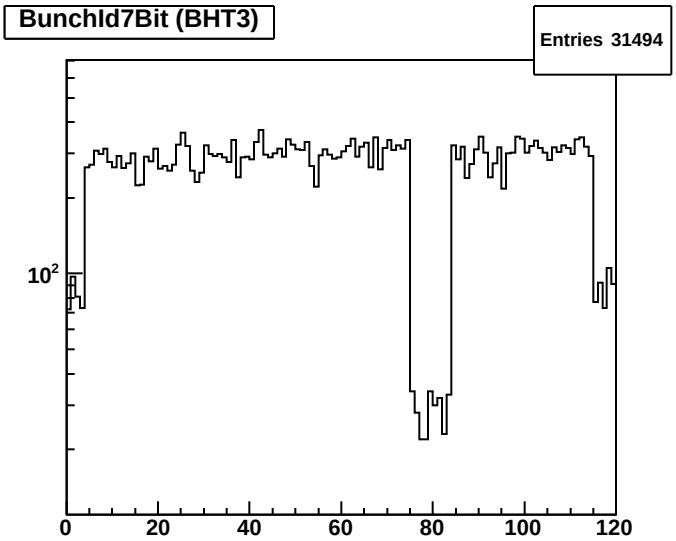

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

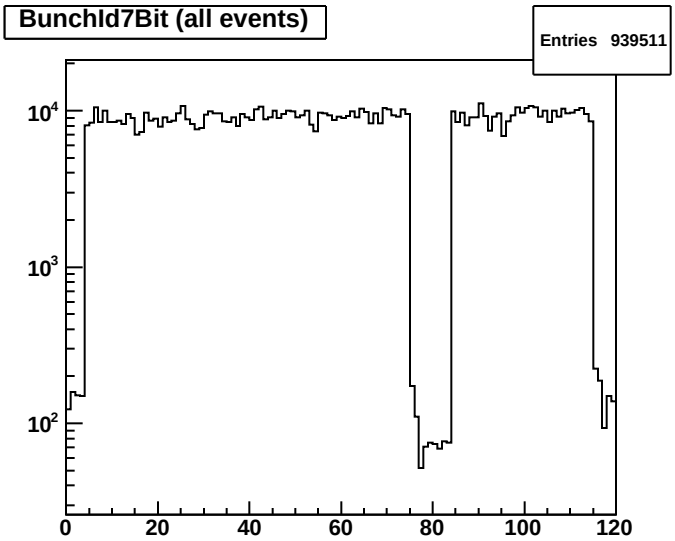

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7516088
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

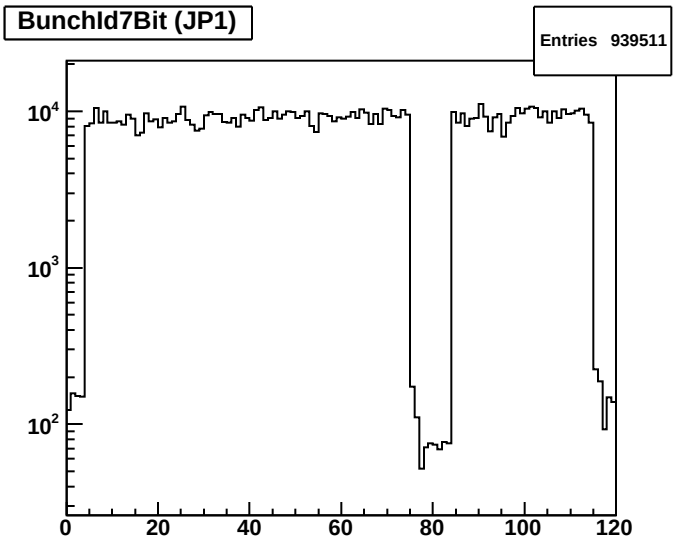

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

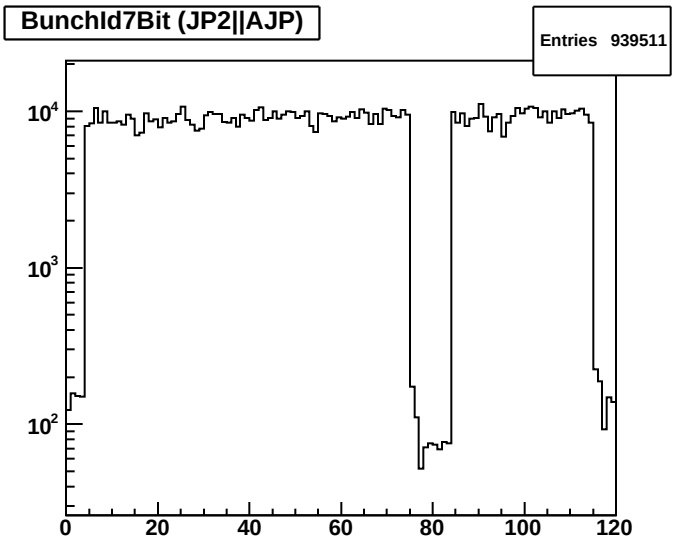

EMC L2 Input - HT23 bits
Entries 7516088
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

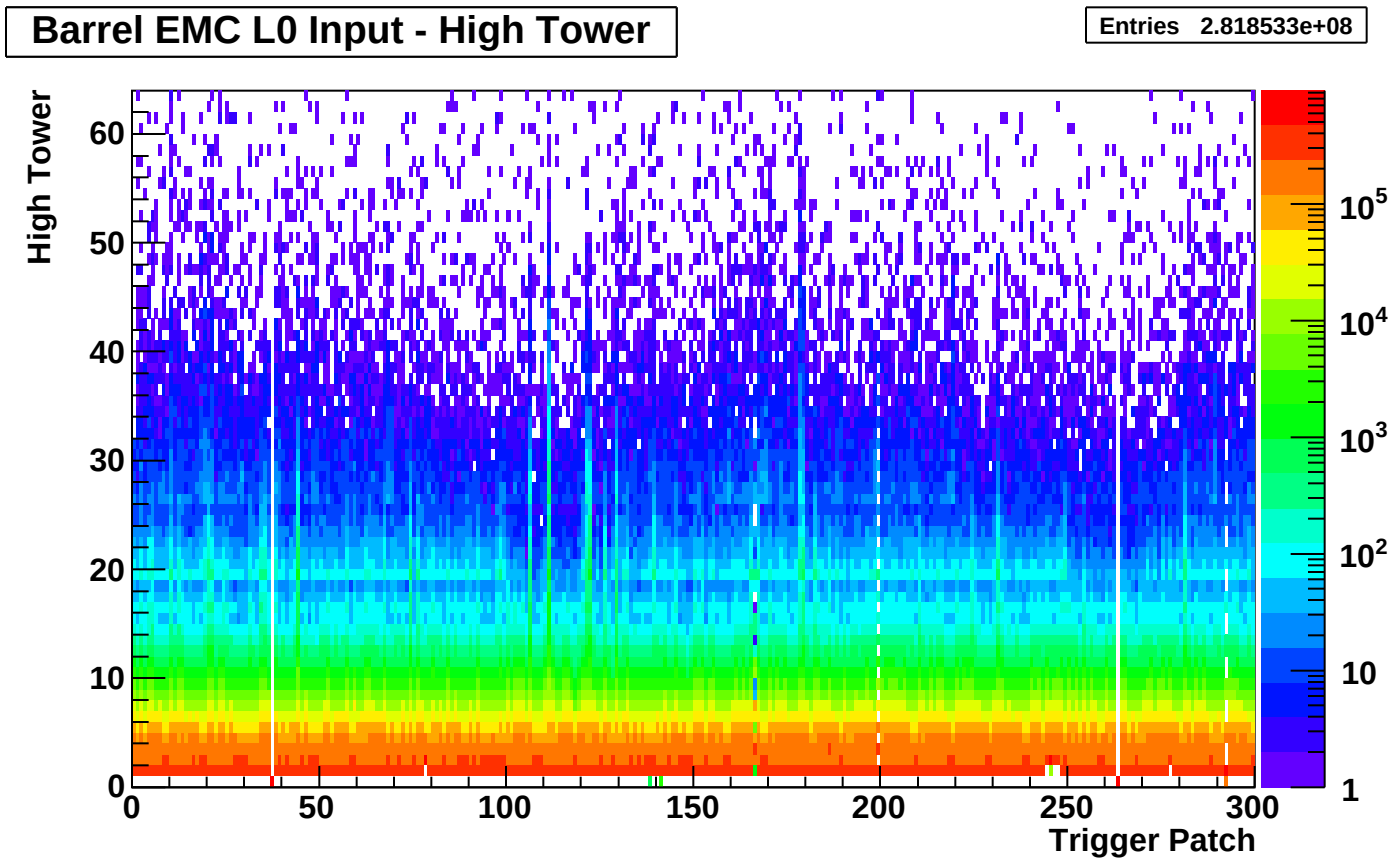

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

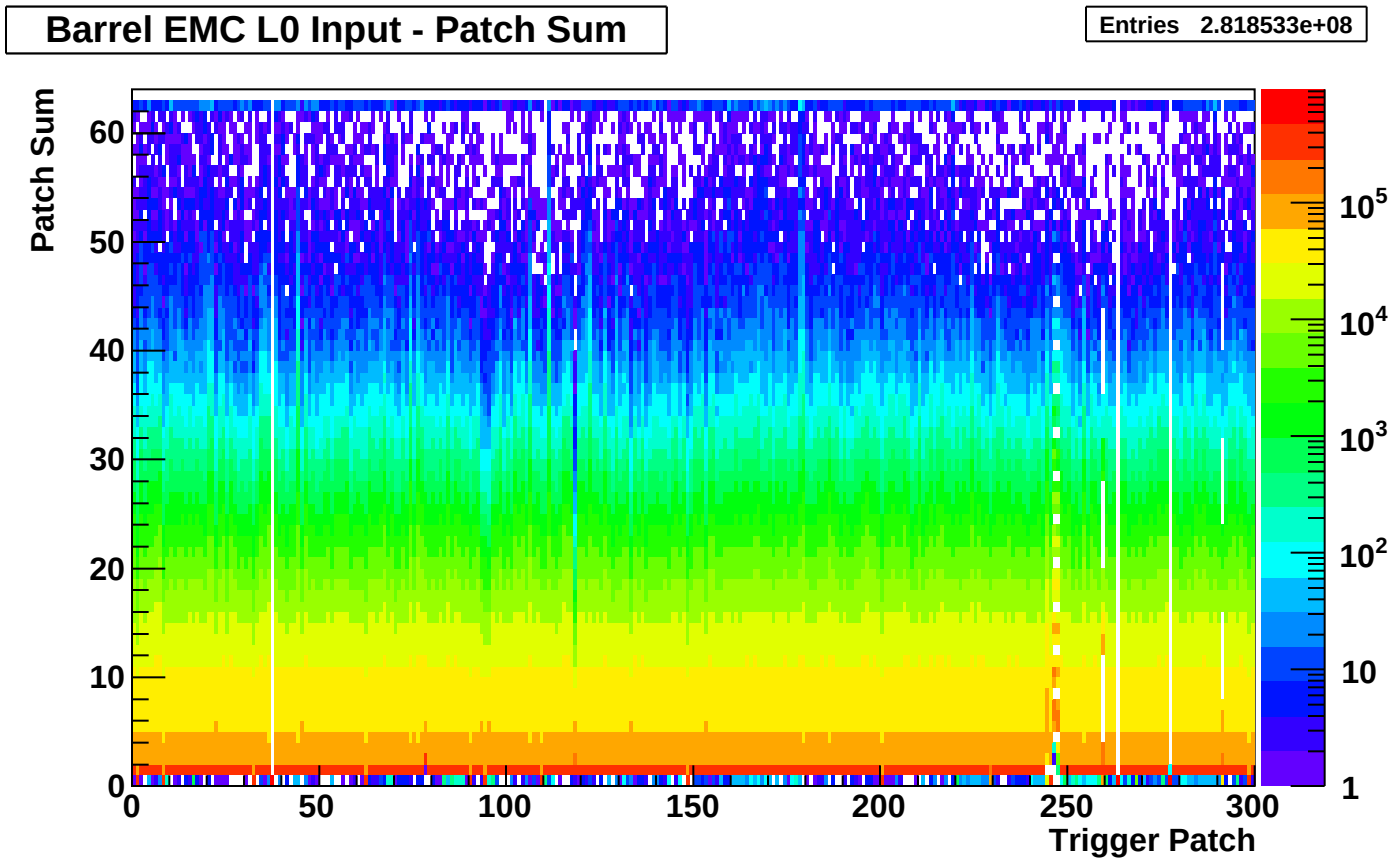

BunchId7Bit (BHT3)
Entries 31494
10^2
0
20
40
60
80
100
120

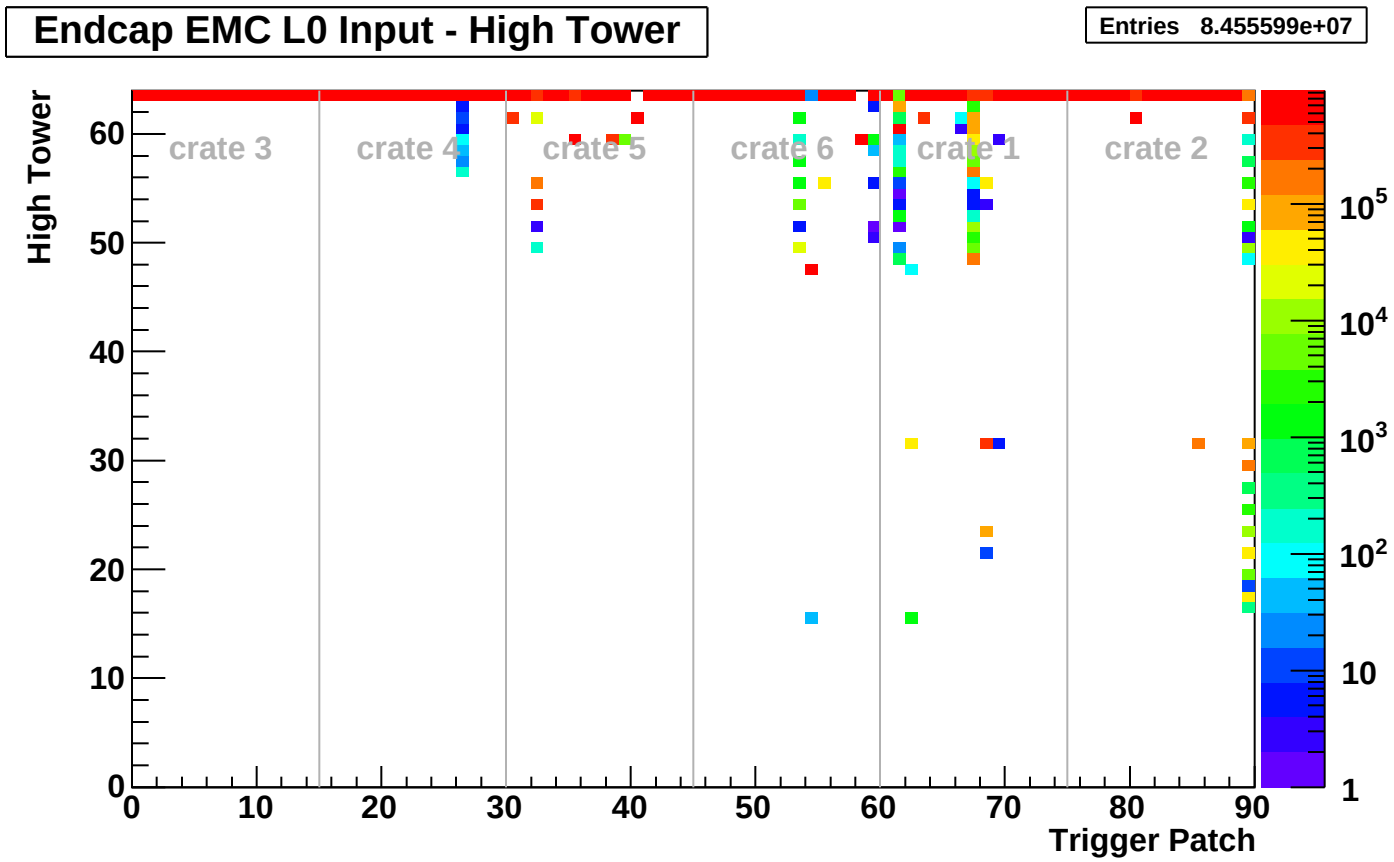

BunchId7Bit (all events)
Entries 939511
10^4
10^3
10^2
0
20
40
60
80
100
120

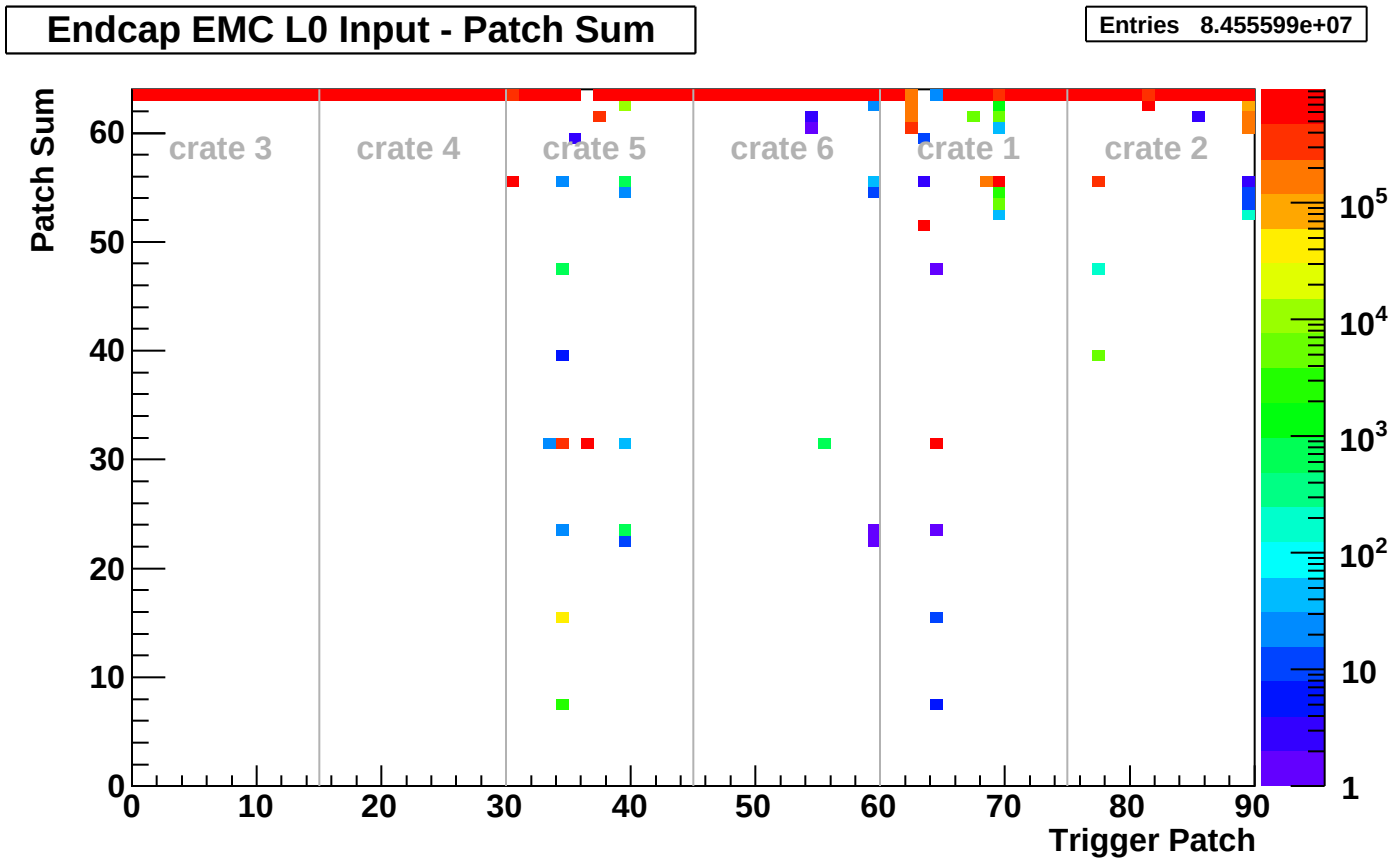

BunchId7Bit (JP1)
Entries 939511
10^4
10^3
10^2
0
20
40
60
80
100
120

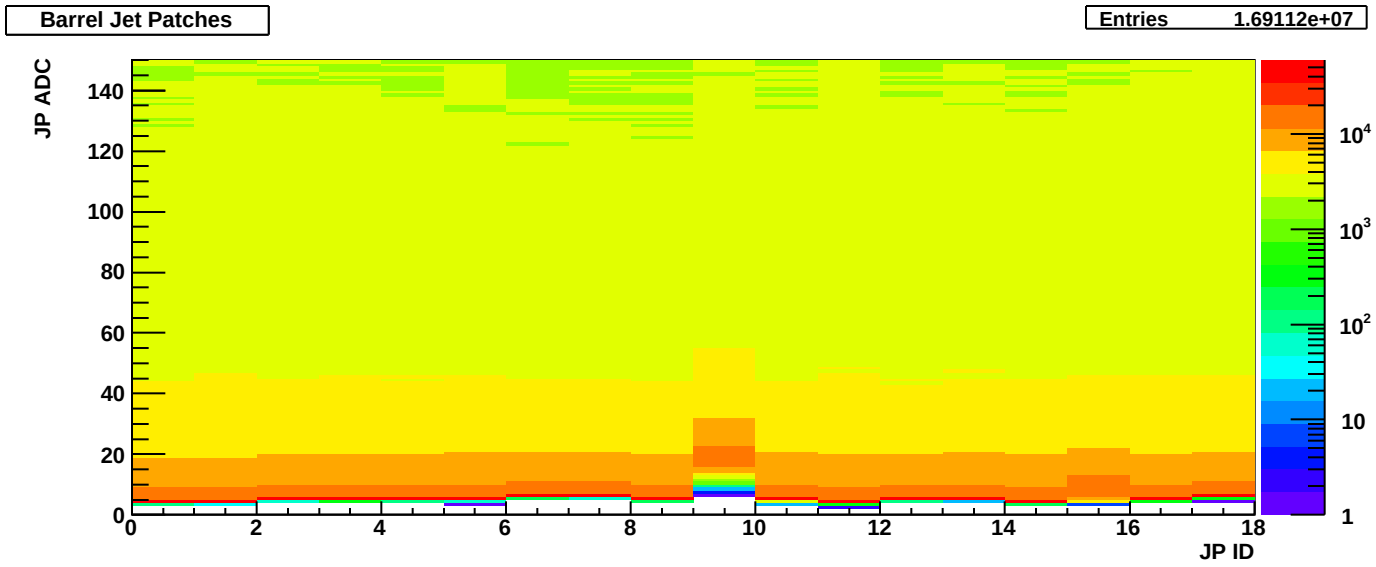

BunchId7Bit (JP2||AJP)
Entries 939511
10^4
10^3
10^2
0
20
40
60
80
100
120

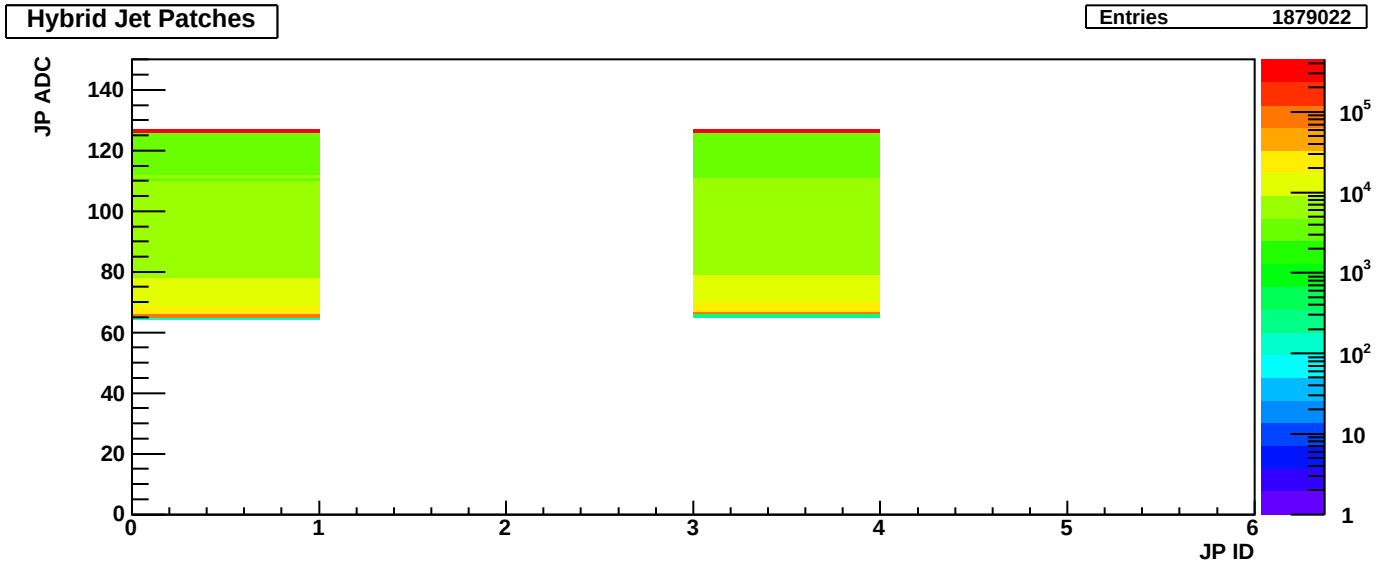

Barrel EMC L0 Input - High Tower
Entries 2.818533e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

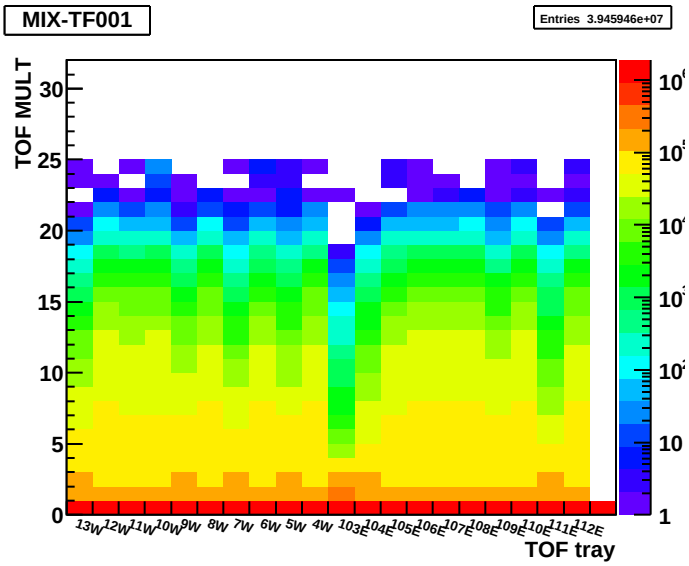

Barrel EMC L0 Input - Patch Sum
Entries 2.818533e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

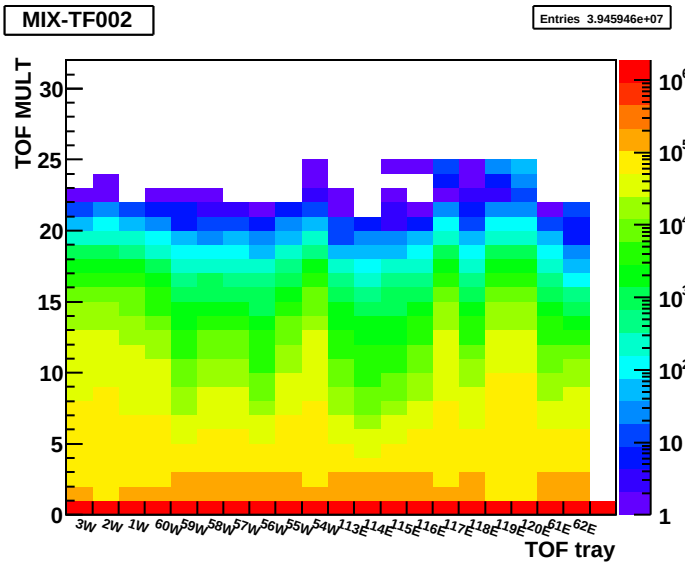

Endcap EMC L0 Input - High Tower
Entries 8.455599e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

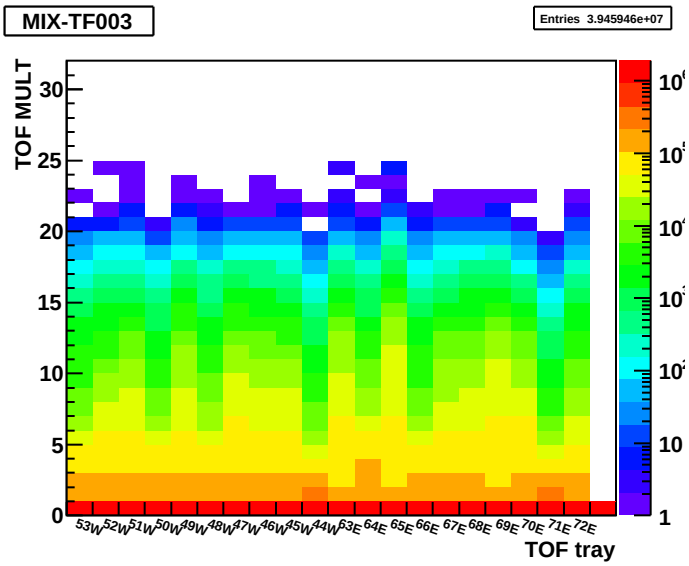

Endcap EMC L0 Input - Patch Sum
Entries 8.455599e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

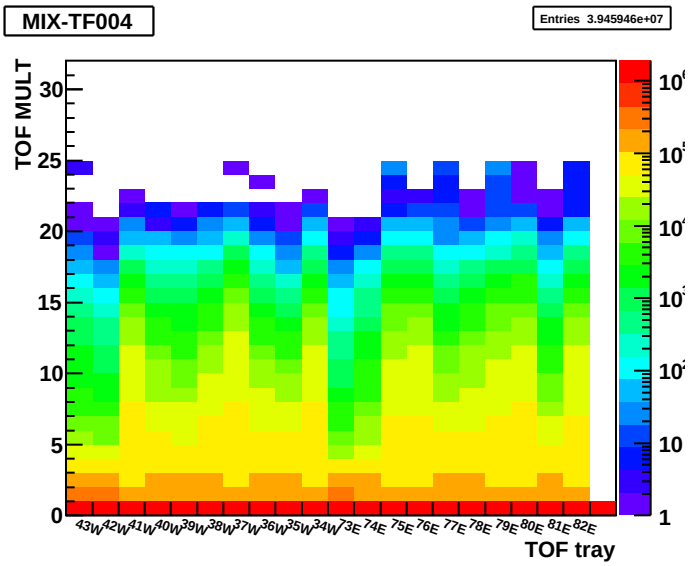

Barrel Jet Patches
Entries 1.69112e+07
JP ADC
JP ID

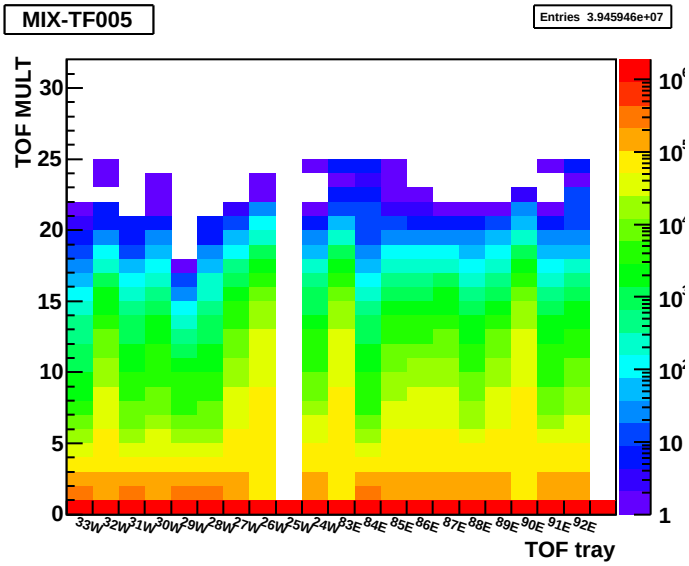

Hybrid Jet Patches
Entries 1879022
JP ADC
JP ID

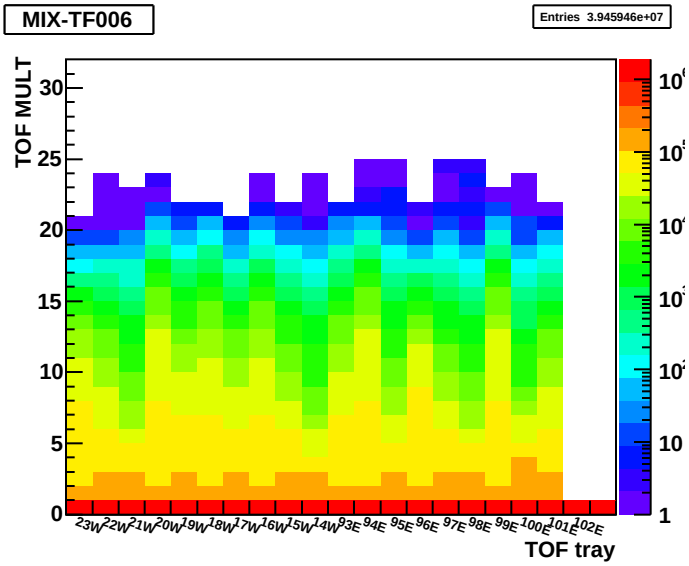

MIX-TF001
Entries 3.945946e+07
TOF MULT
TOF tray

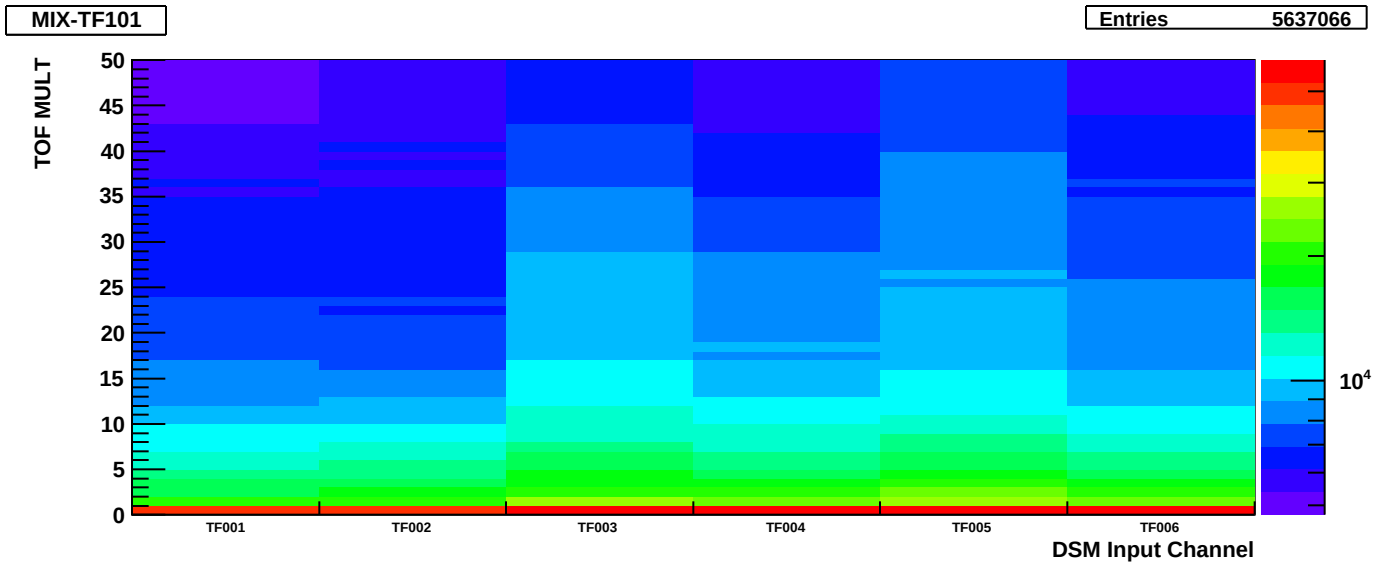

MIX-TF002
Entries 3.945946e+07
TOF MULT
TOF tray

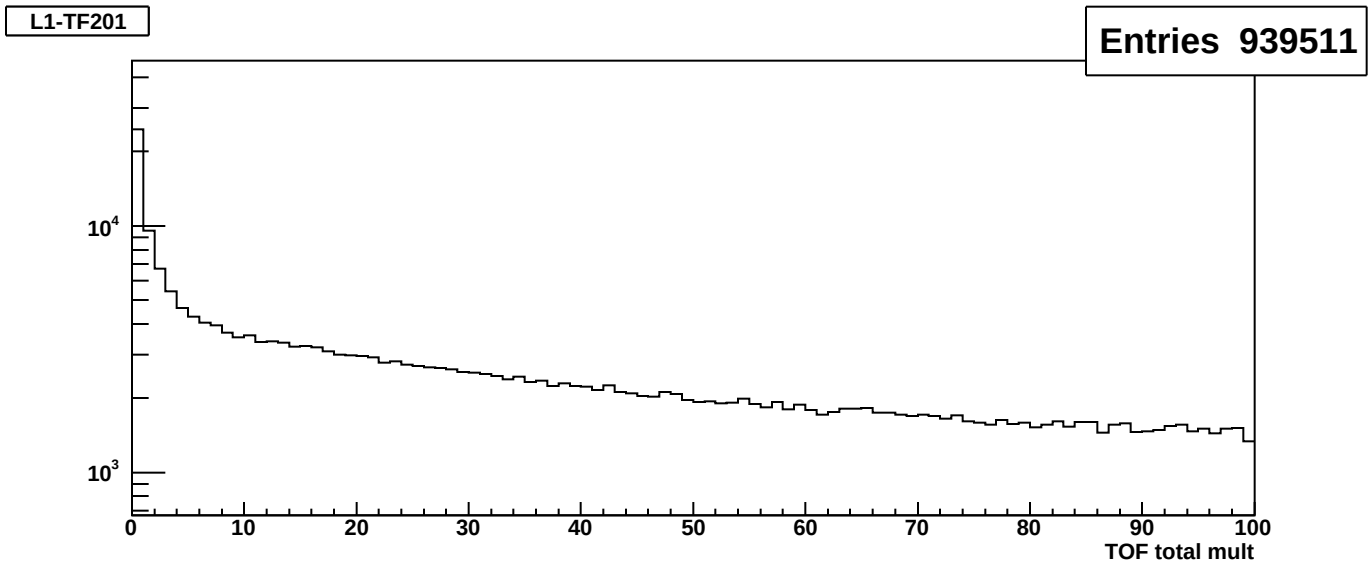

MIX-TF003
Entries 3.945946e+07
TOF MULT
TOF tray

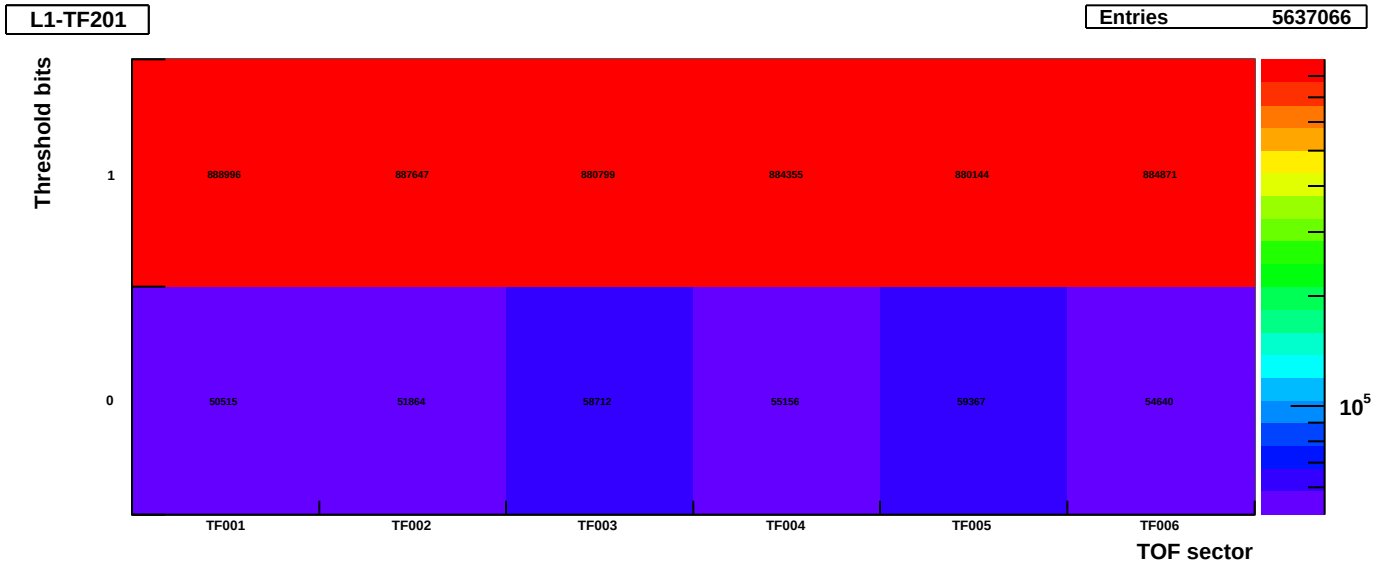

MIX-TF004
Entries 3.945946e+07
TOF MULT
TOF tray

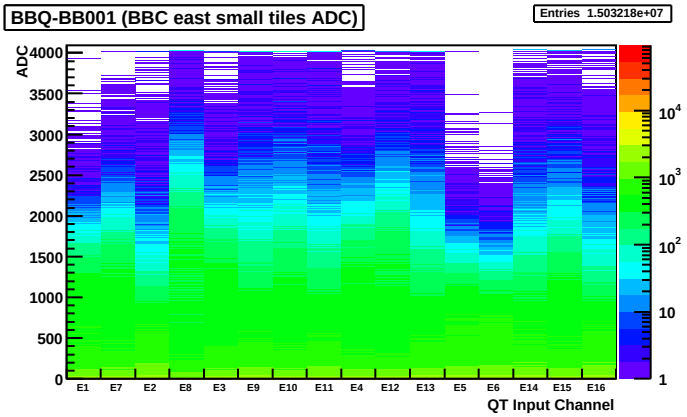

MIX-TF005
Entries 3.945946e+07
TOF MULT
TOF tray

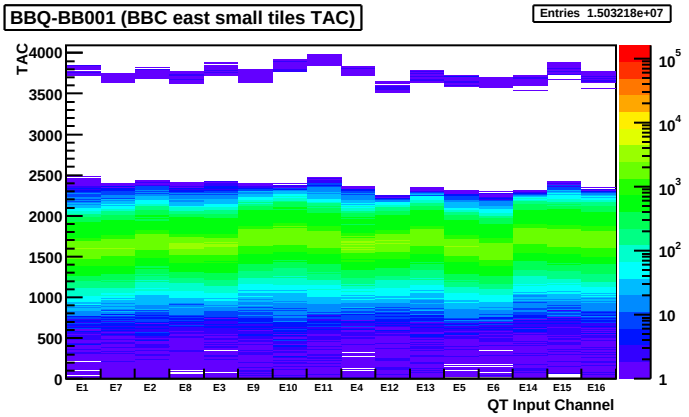

MIX-TF006
Entries 3.945946e+07
TOF MULT
TOF tray

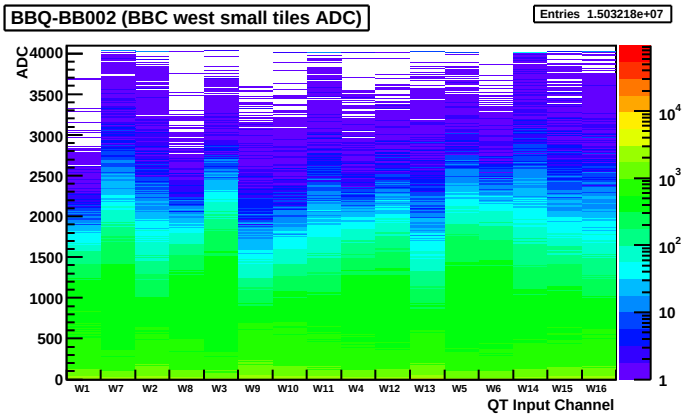

MIX-TF101
Entries 5637066
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

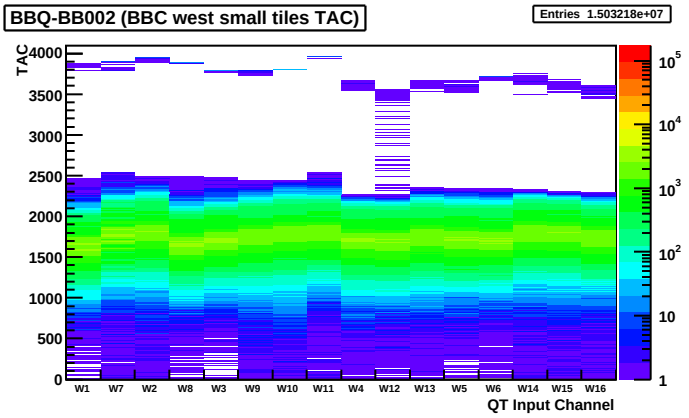

L1-TF201
Entries 939511
10^4
10^3
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

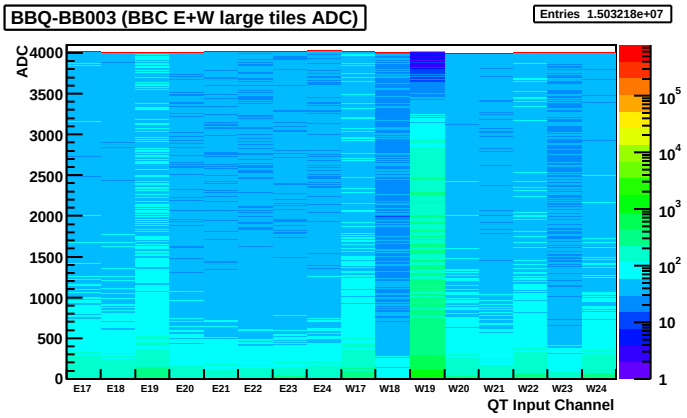

L1-TF201
Entries 5637066
Threshold bits
1
0
888996
887647
880799
884355
880144
884871
50515
51864
58712
55156
59367
54640
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

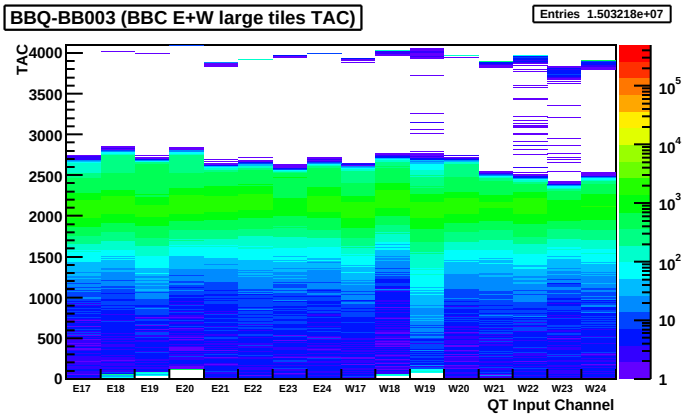

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.503218e+07
ADC
QT Input Channel

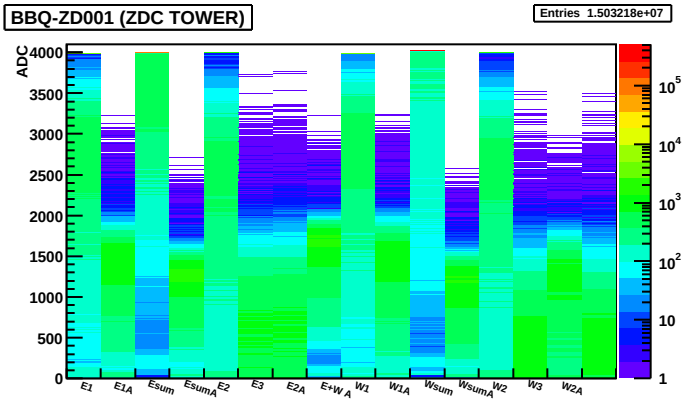

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.503218e+07
TAC
QT Input Channel

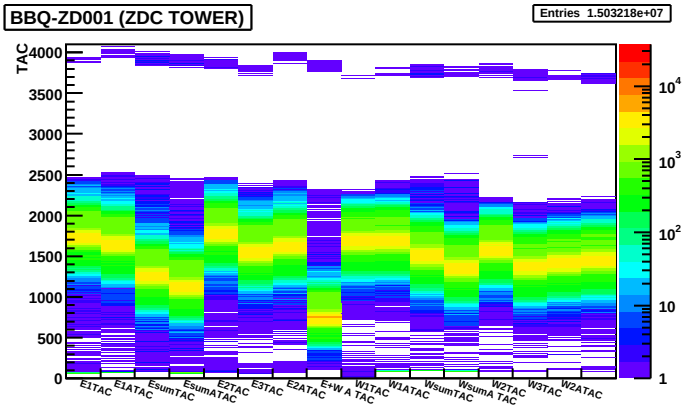

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.503218e+07
ADC
QT Input Channel

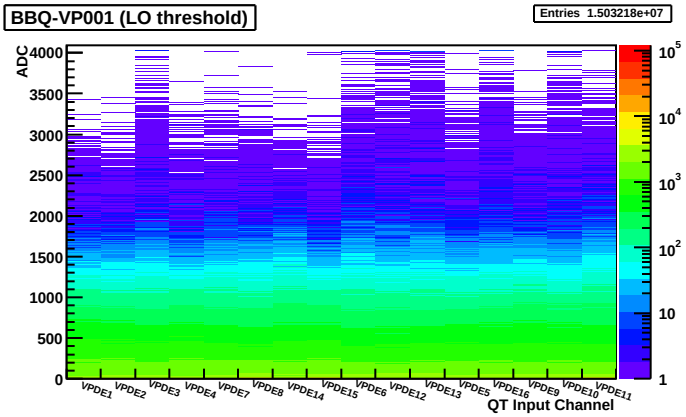

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.503218e+07
TAC
QT Input Channel

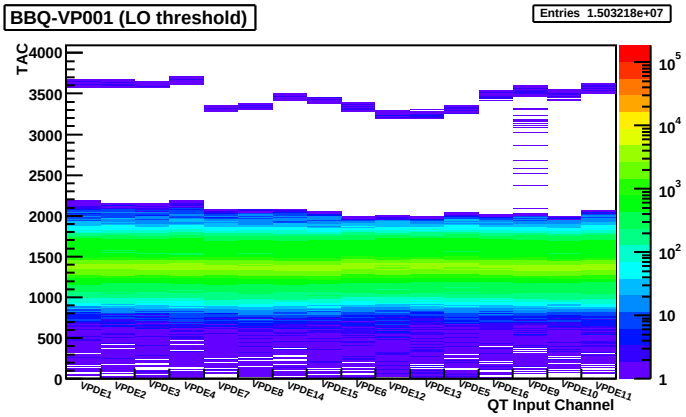

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.503218e+07
ADC
QT Input Channel

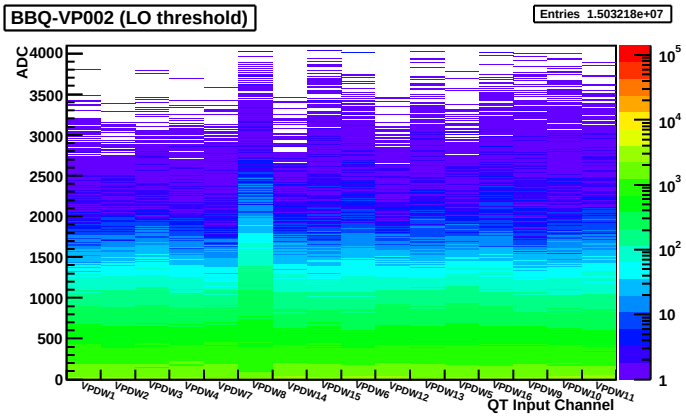

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.503218e+07
TAC
QT Input Channel

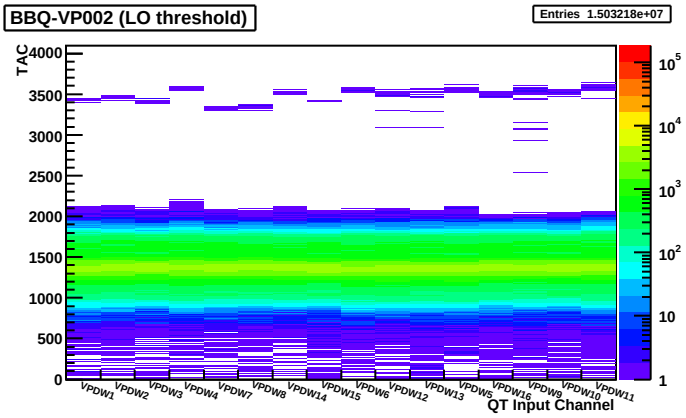

BBQ-ZD001 (ZDC TOWER)
Entries 1.503218e+07
ADC

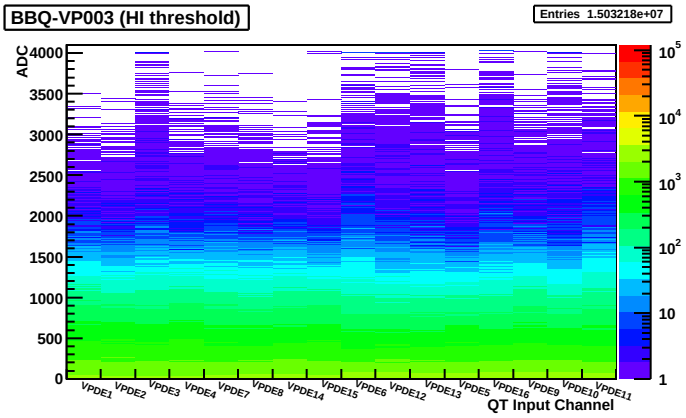

BBQ-ZD001 (ZDC TOWER)
Entries 1.503218e+07
TAC

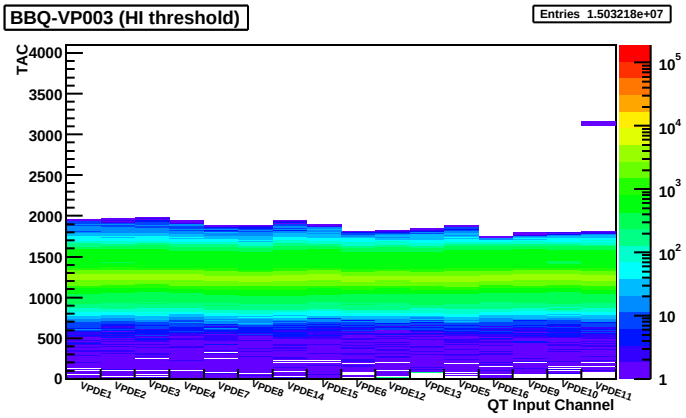

BBQ-VP001 (LO threshold)
Entries 1.503218e+07
ADC
QT Input Channel

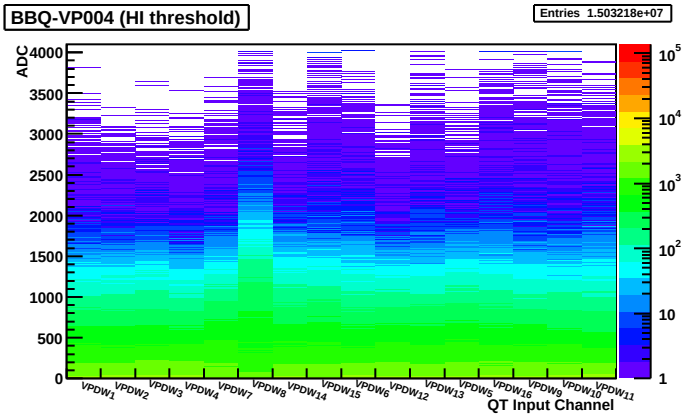

BBQ-VP001 (LO threshold)
Entries 1.503218e+07
TAC
QT Input Channel

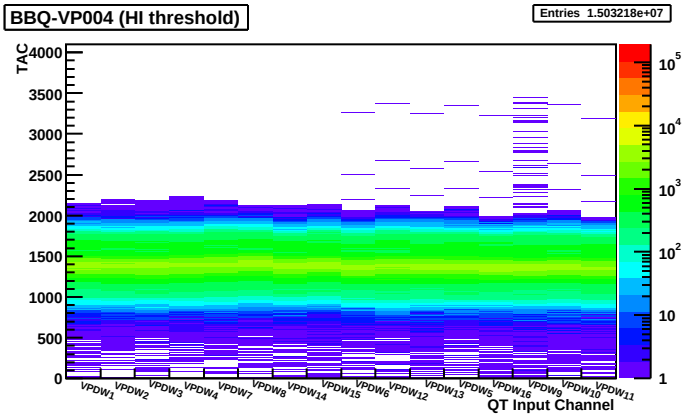

BBQ-VP002 (LO threshold)
Entries 1.503218e+07
ADC
QT Input Channel

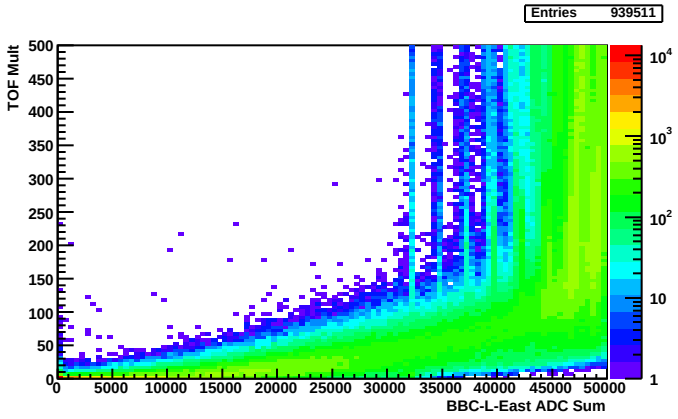

BBQ-VP002 (LO threshold)
Entries 1.503218e+07
TAC
QT Input Channel

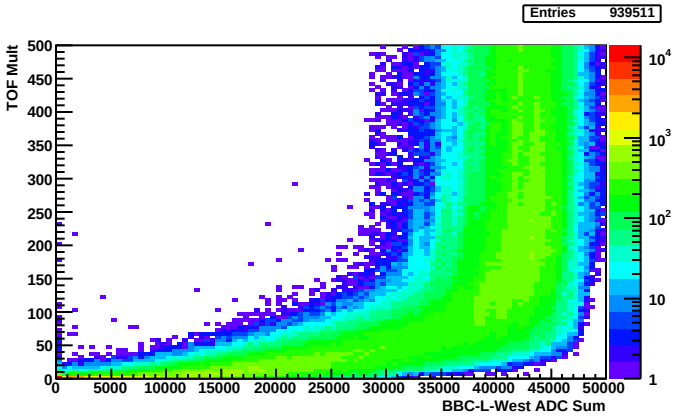

BBQ-VP003 (HI threshold)
Entries 1.503218e+07
ADC
QT Input Channel

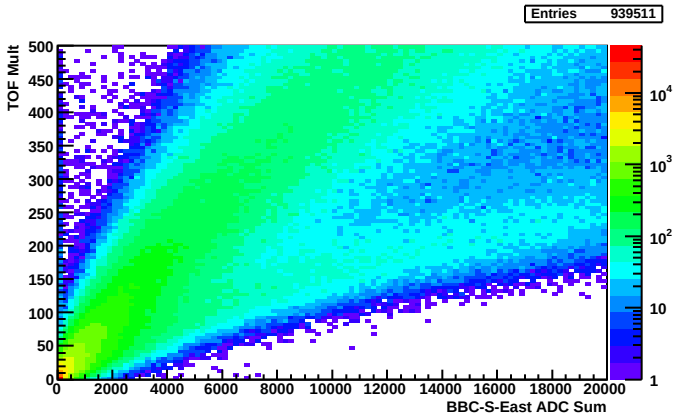

BBQ-VP003 (HI threshold)
Entries 1.503218e+07
TAC
QT Input Channel

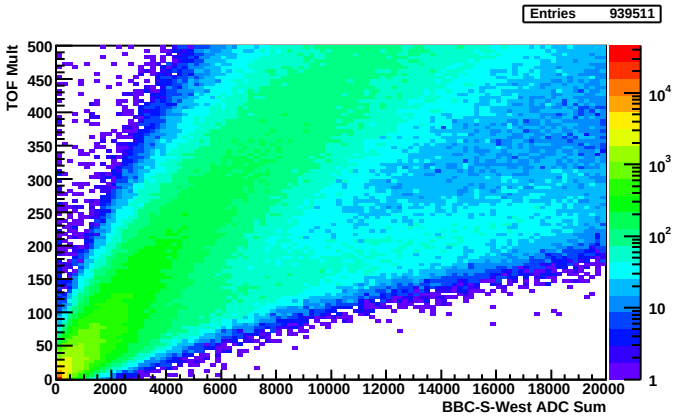

BBQ-VP004 (HI threshold)
Entries 1.503218e+07
ADC
QT Input Channel

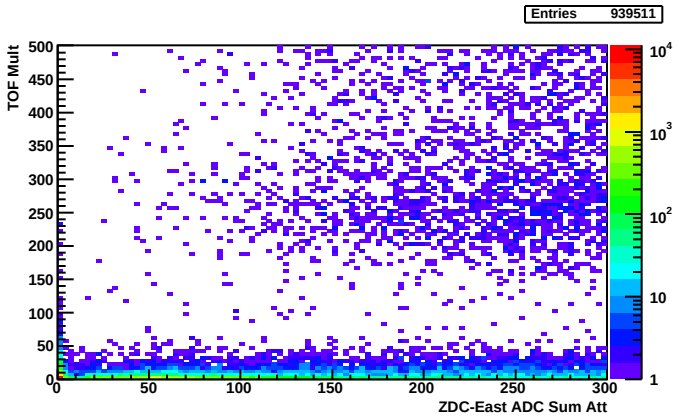

BBQ-VP004 (HI threshold)
Entries 1.503218e+07
TAC
QT Input Channel

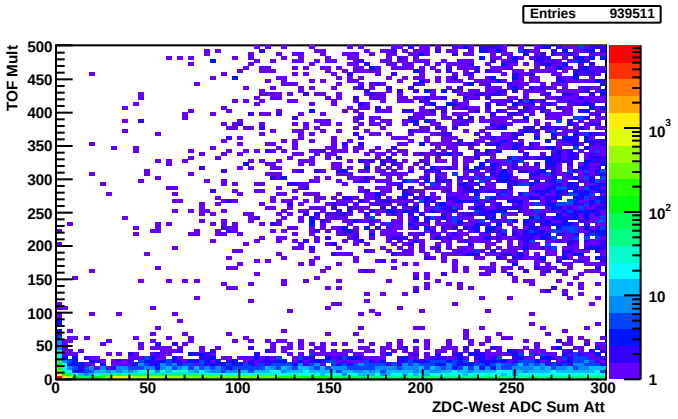

Entries 939511
TOF Mult
BBC-L-East ADC Sum

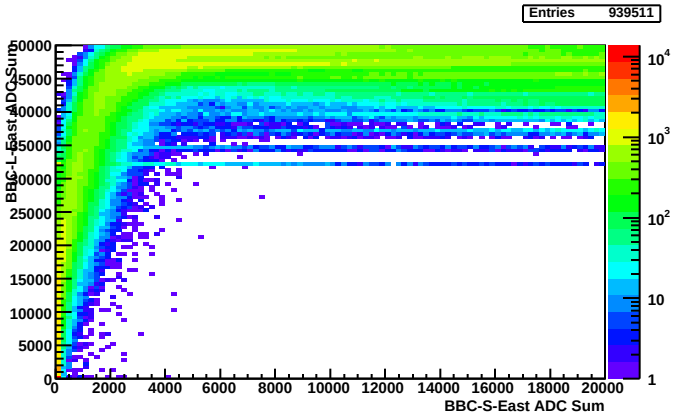

Entries 939511
TOF Mult
BBC-L-West ADC Sum

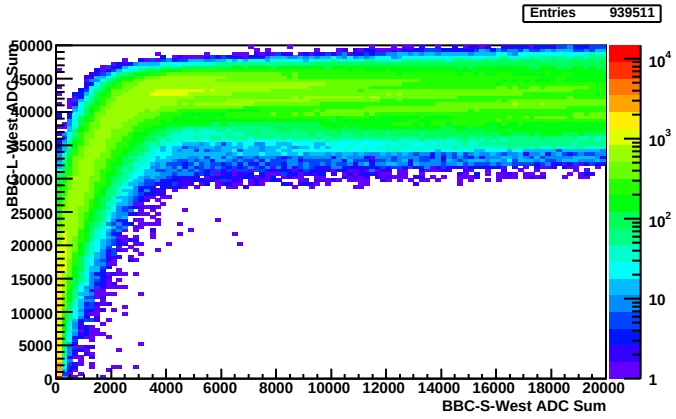

Entries 939511
TOF Mult
BBC-S-East ADC Sum

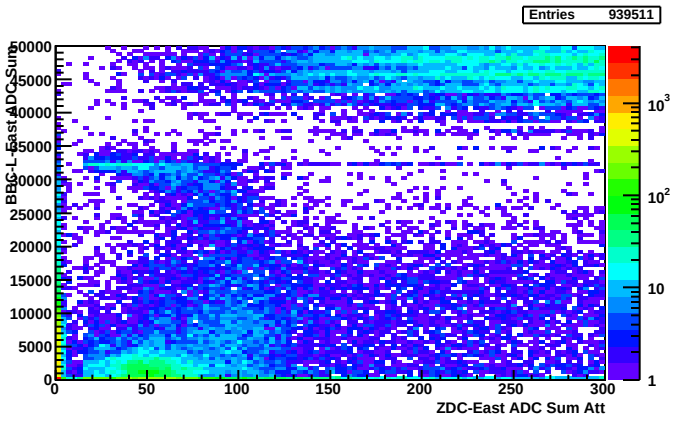

Entries 939511
TOF Mult
BBC-S-West ADC Sum

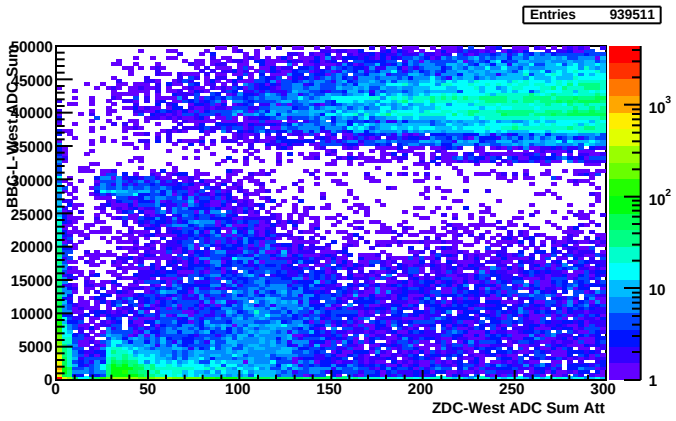

Entries 939511
TOF Mult
ZDC-East ADC Sum Att

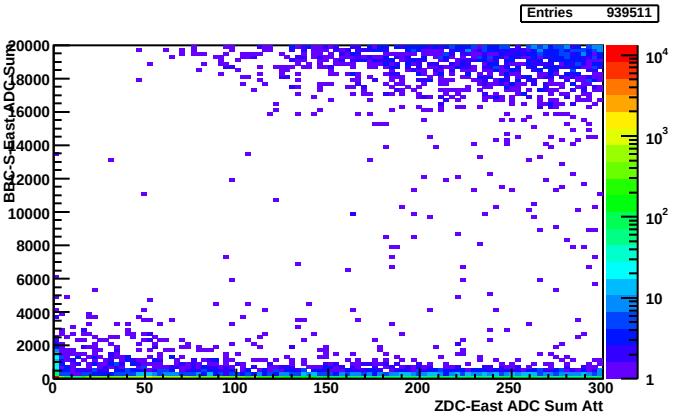

Entries 939511
TOF Mult
ZDC-West ADC Sum Att

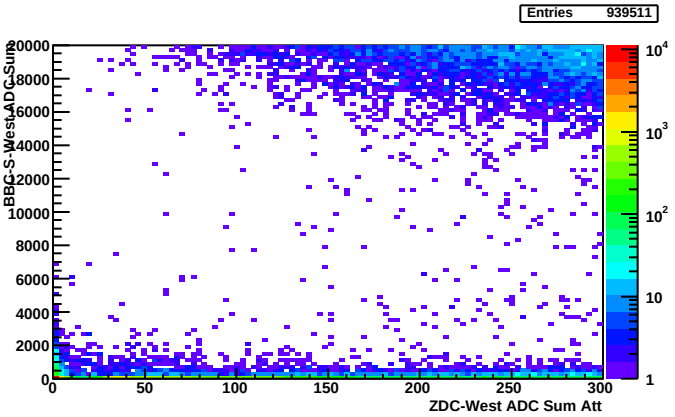

Entries 939511
BBC-L-East ADC Sum
BBC-S-East ADC Sum

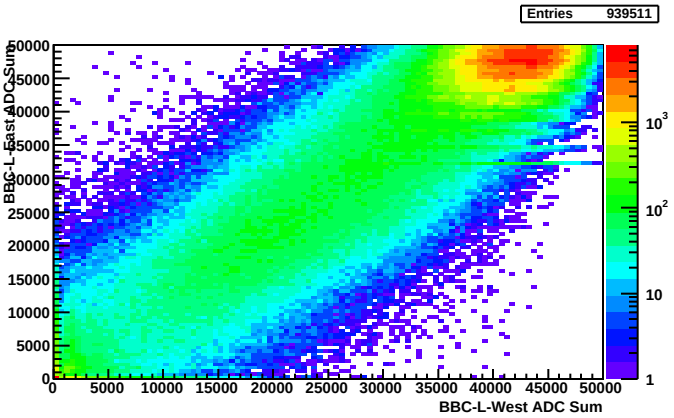

Entries 939511
BBC-L-West ADC Sum
BBC-S-West ADC Sum

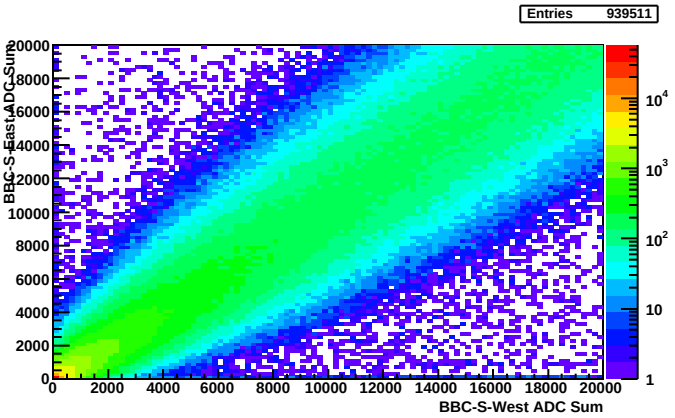

Entries 939511
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

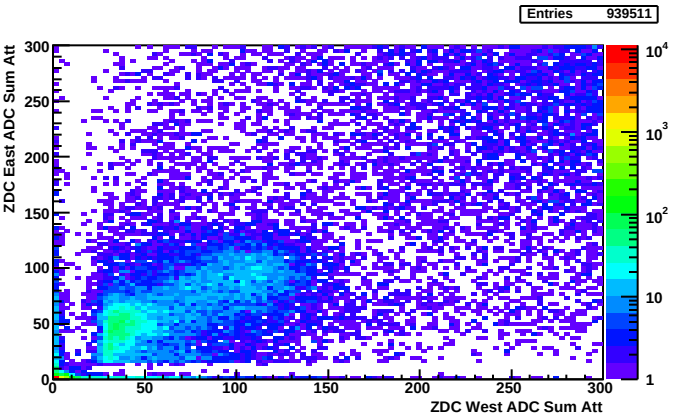

Entries 939511
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

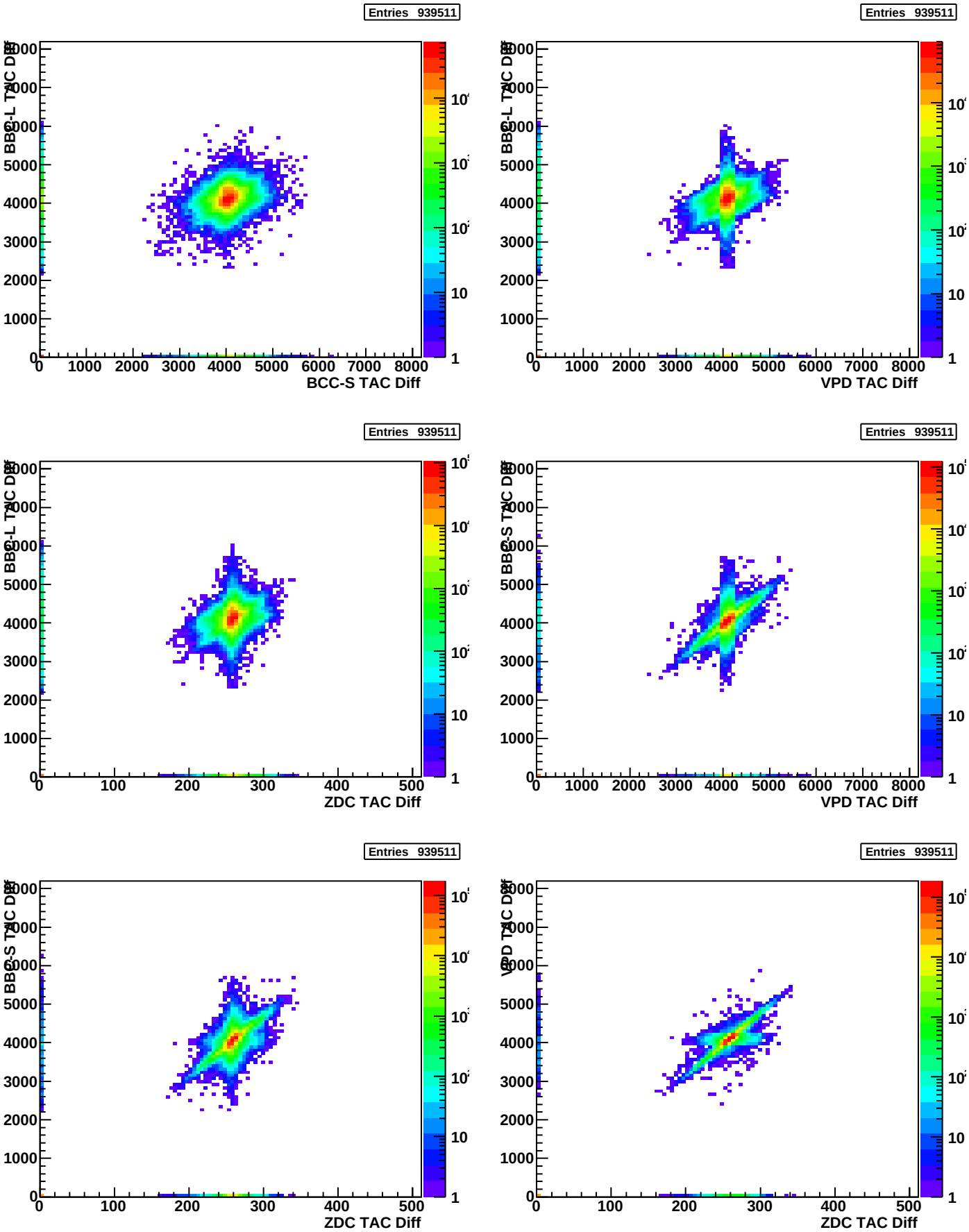

Entries 939511
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

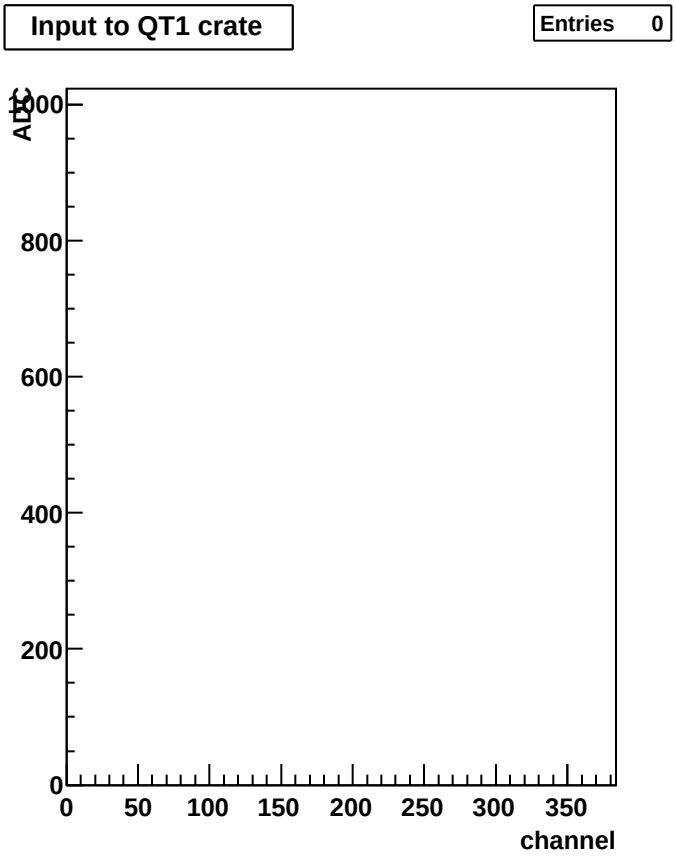

Entries 939511
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

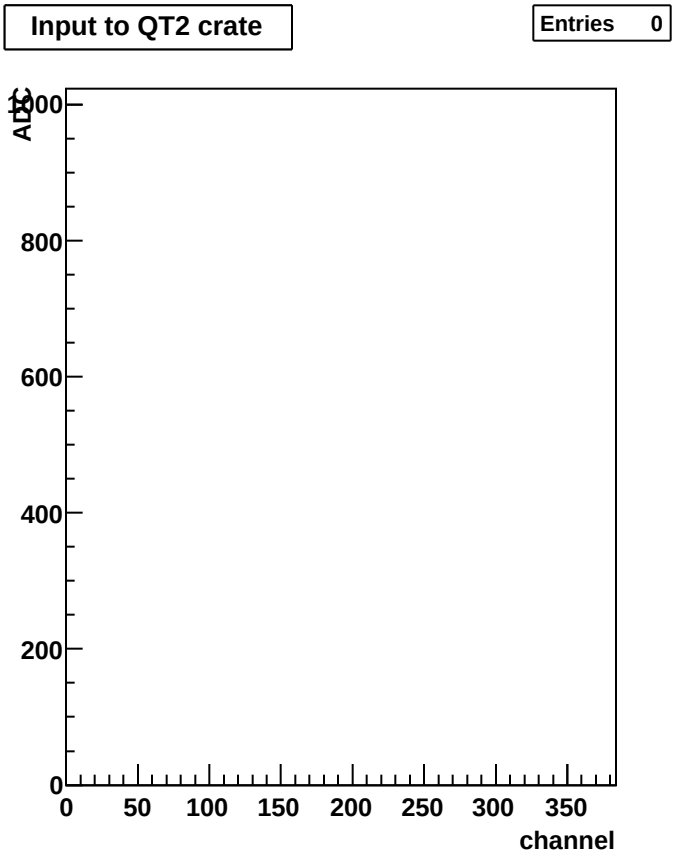

Entries 939511
BBC-L-East ADC Sum
BBC-L-West ADC Sum

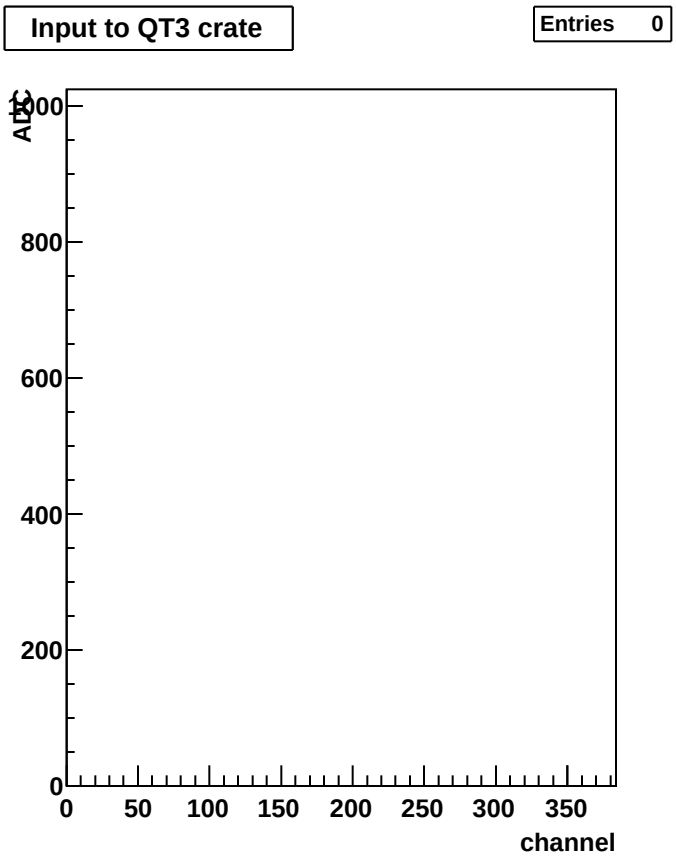

Entries 939511
BBC-S-East ADC Sum
BBC-S-West ADC Sum

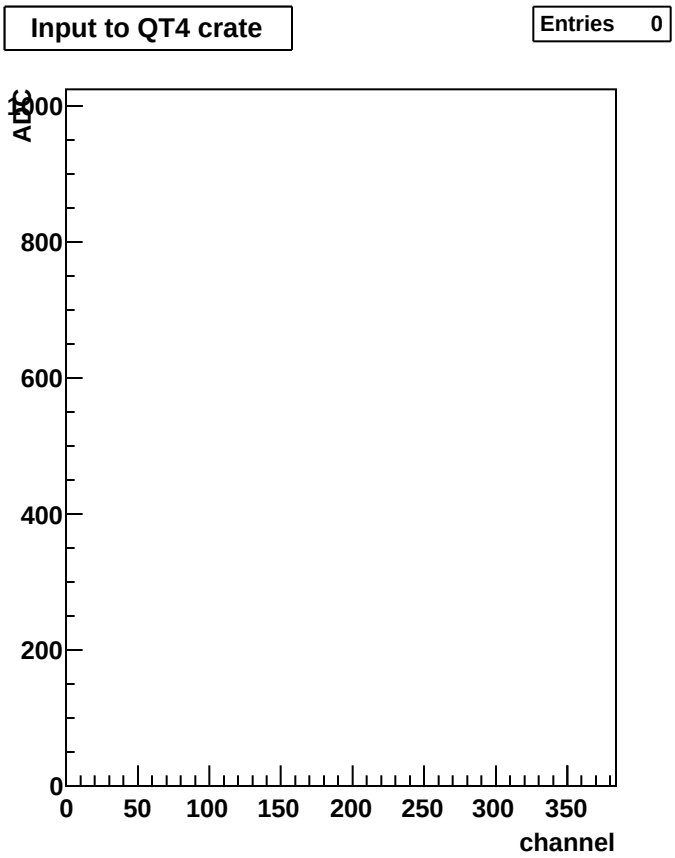

Entries 939511
ZDC East ADC Sum Att
ZDC West ADC Sum Att

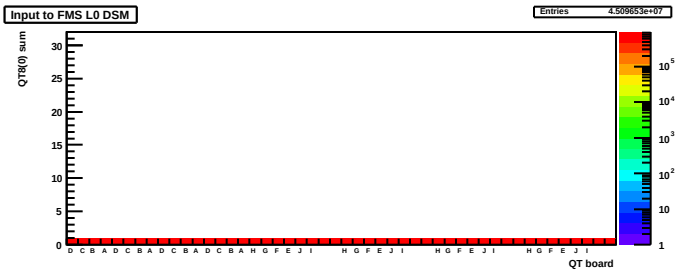

Entries 939511
BBC-L TAC Diff
BCC-S TAC Diff
Entries 939511
BBC-L TAC Diff
VPD TAC Diff
Entries 939511
BBC-L TAC Diff
ZDC TAC Diff
Entries 939511
BBC-S TAC Diff
VPD TAC Diff
Entries 939511
BBC-S TAC Diff
ZDC TAC Diff
Entries 939511
VPD TAC Diff
ZDC TAC Diff

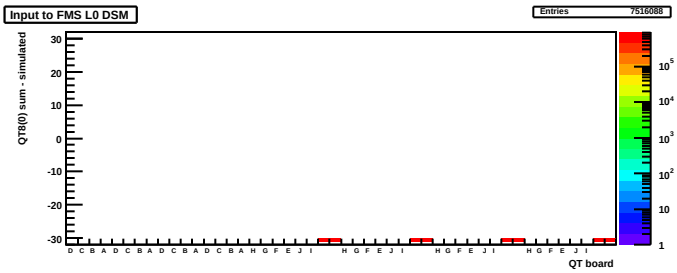

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

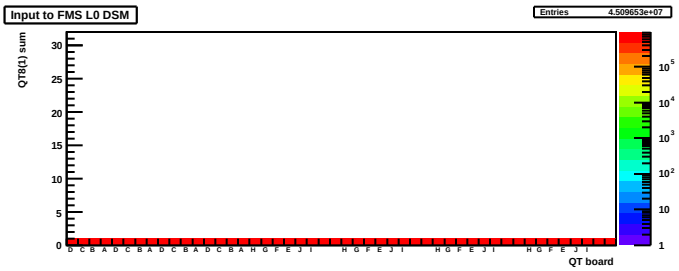

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

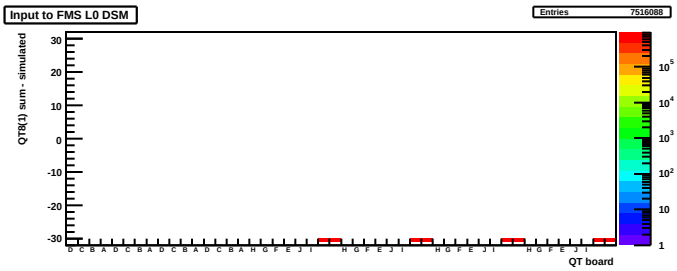

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

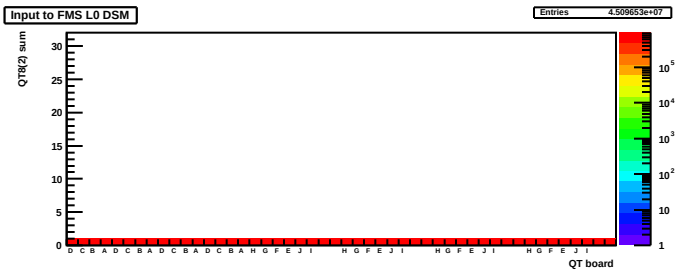

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

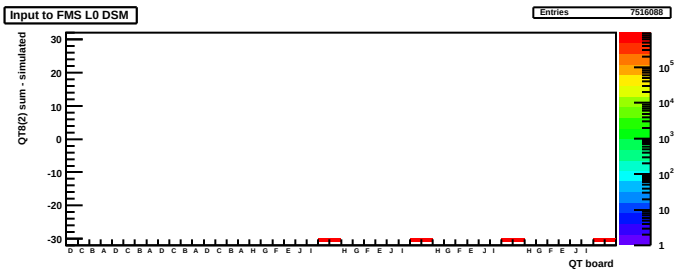

Input to FMS L0 DSM
Entries 4.509653e+07
QT8(0) sum
QT board

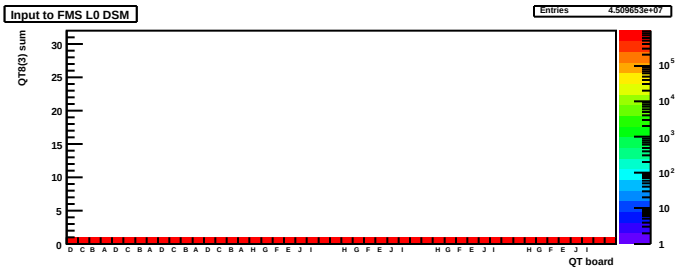

Input to FMS L0 DSM
Entries 7516088
QT8(0) sum - simulated
QT board

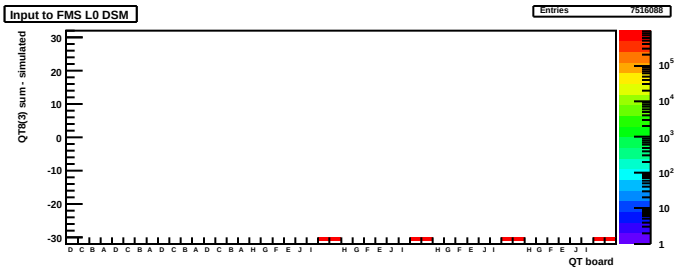

Input to FMS L0 DSM
Entries 4.509653e+07
QT8(1) sum
QT board

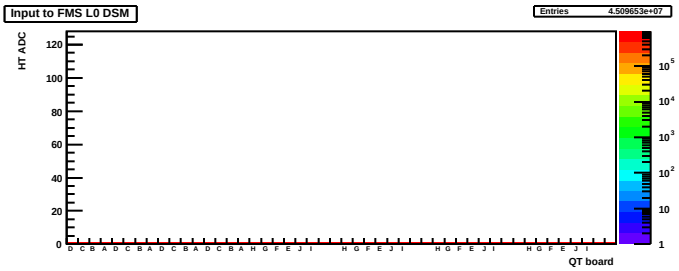

Input to FMS L0 DSM
Entries 7516088
QT8(1) sum - simulated
QT board

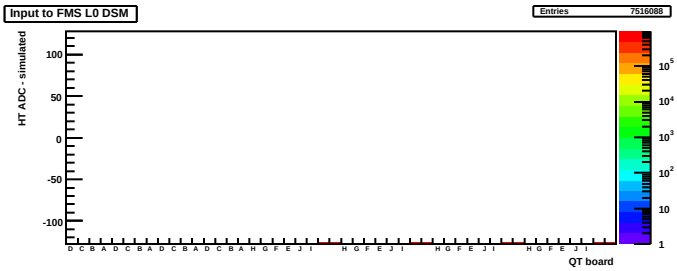

Input to FMS L0 DSM
Entries 4.509653e+07
QT8(2) sum
QT board

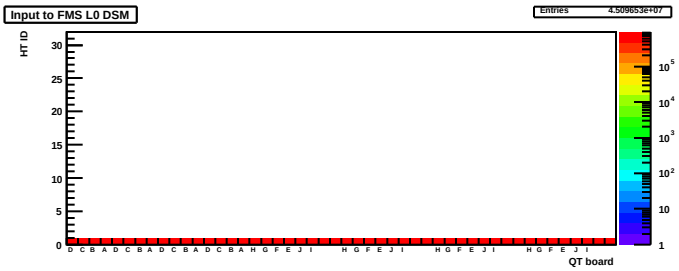

Input to FMS L0 DSM
Entries 7516088
QT8(2) sum - simulated
QT board

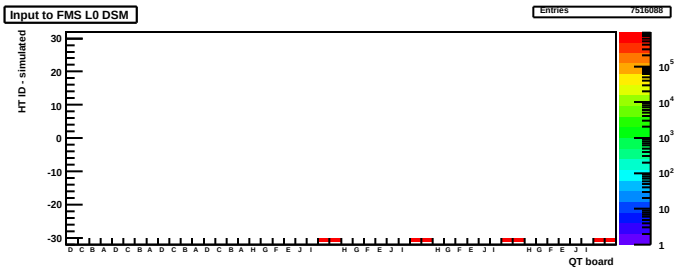

Input to FMS L0 DSM
Entries 4.509653e+07
QT8(3) sum
QT board

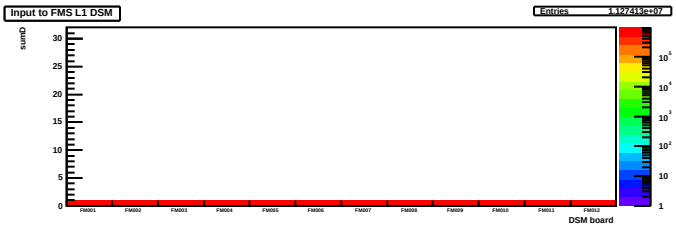

Input to FMS L0 DSM
Entries 7516088
QT8(3) sum - simulated
QT board

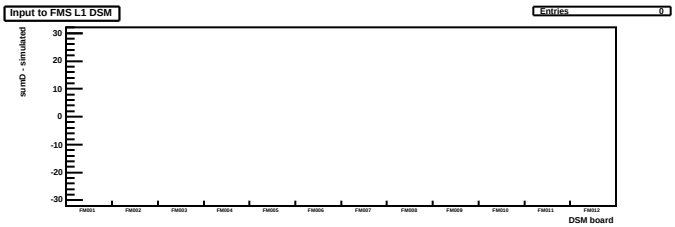

Input to FMS L0 DSM
Entries 4.509653e+07
HT ADC
QT board

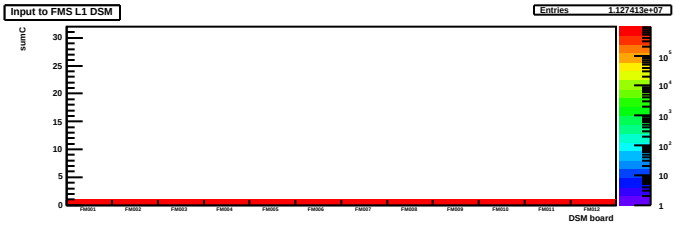

Input to FMS L0 DSM
Entries 7516088
HT ADC - simulated
QT board

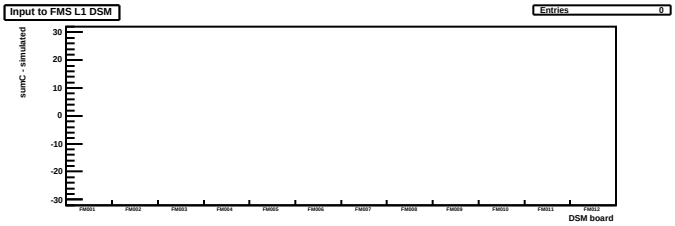

Input to FMS L0 DSM
Entries 4.509653e+07
HT ID
QT board

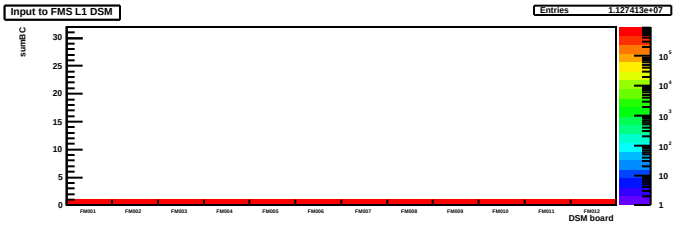

Input to FMS L0 DSM
Entries 7516088
HT ID - simulated
QT board

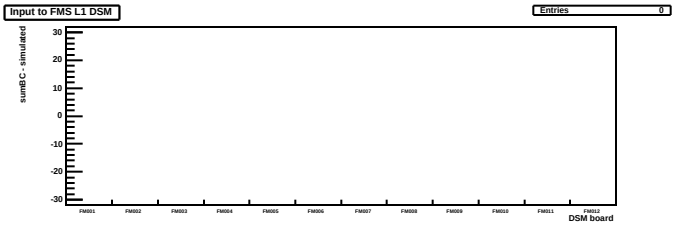

Input to FMS L1 DSM
Entries 1.127413e+07
sumD
DSM board

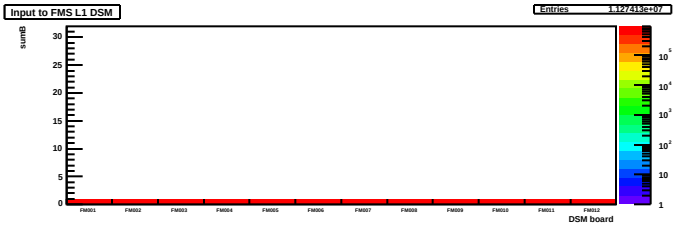

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

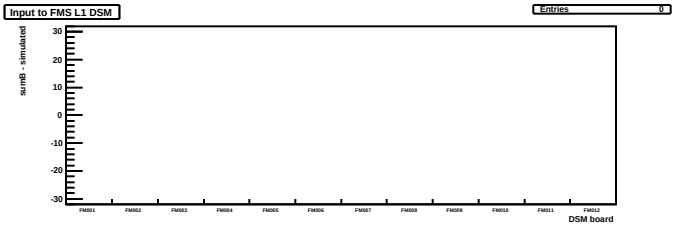

Input to FMS L1 DSM
Entries 1.127413e+07
sumC
DSM board

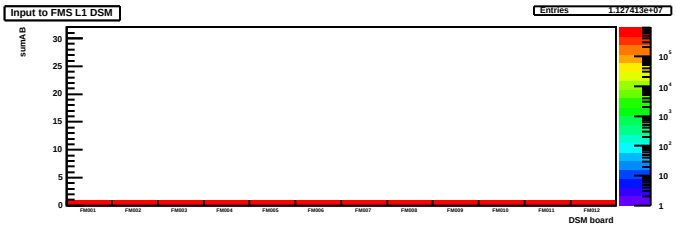

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

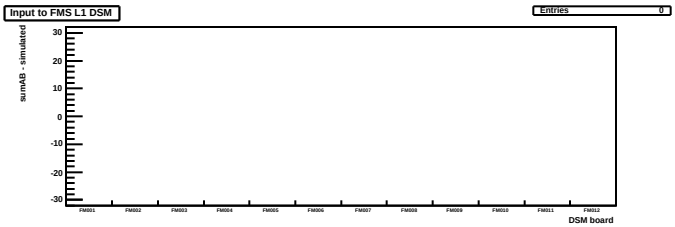

Input to FMS L1 DSM
Entries 1.127413e+07
sumBC
DSM board

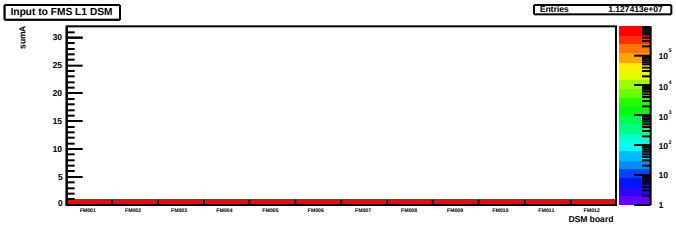

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

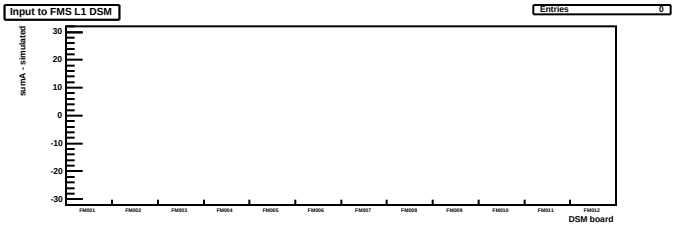

Input to FMS L1 DSM
Entries 1.127413e+07
sumB
DSM board

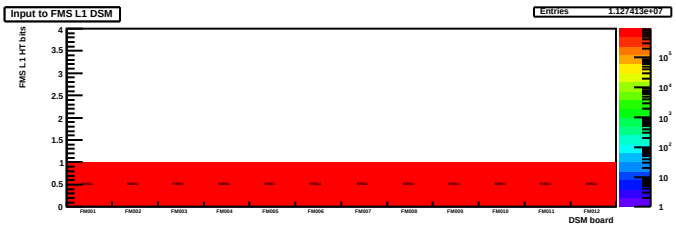

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

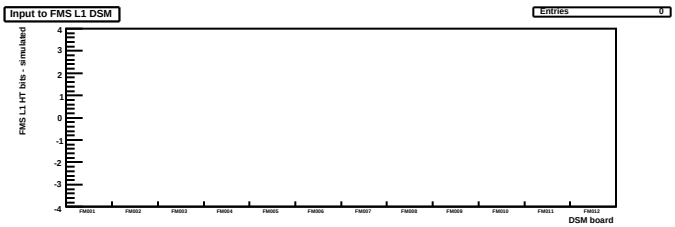

Input to FMS L1 DSM
Entries 1.127413e+07
sumAB
DSM board

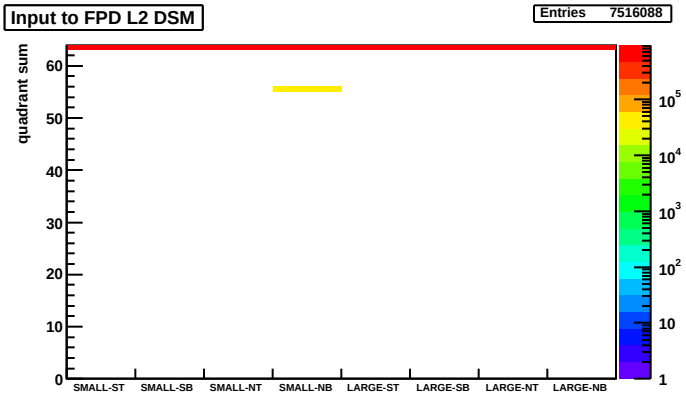

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

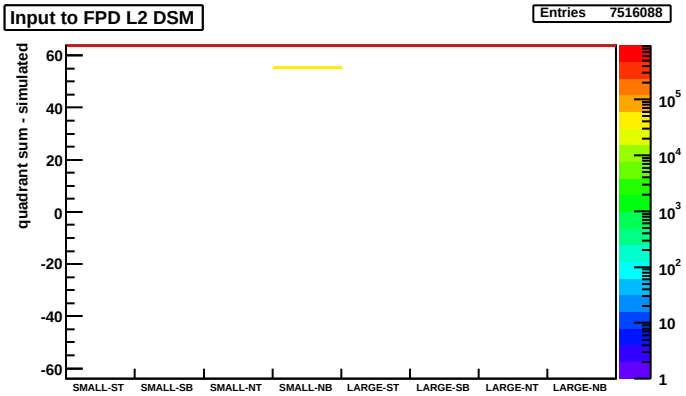

Input to FMS L1 DSM
Entries 1.127413e+07
sumA
DSM board

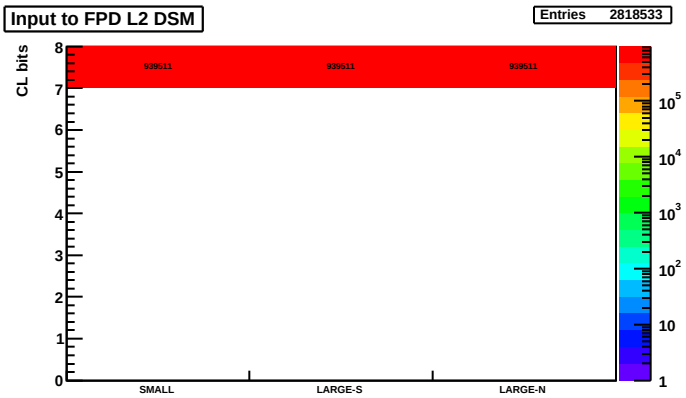

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

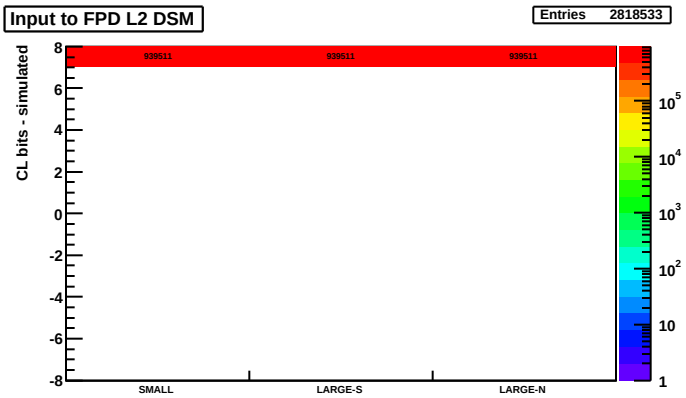

Input to FMS L1 DSM
Entries 1.127413e+07
FMS L1 HT bits
DSM board

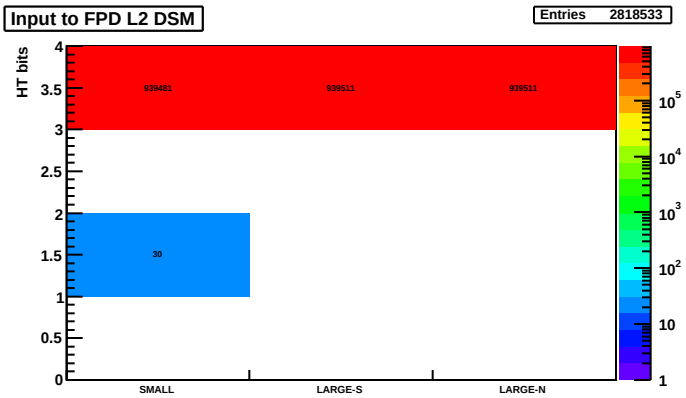

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

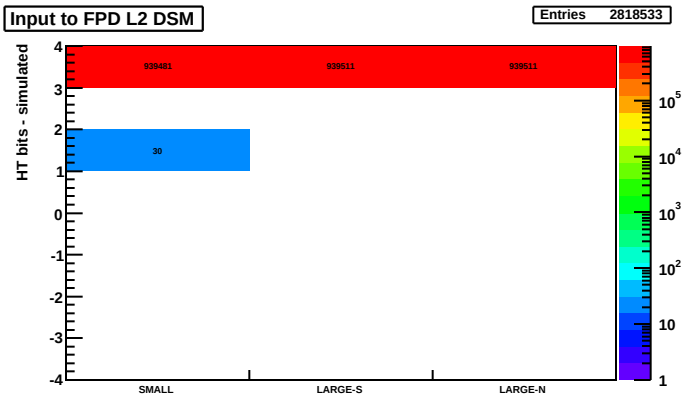

Input to FPD L2 DSM
Entries 7516088
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

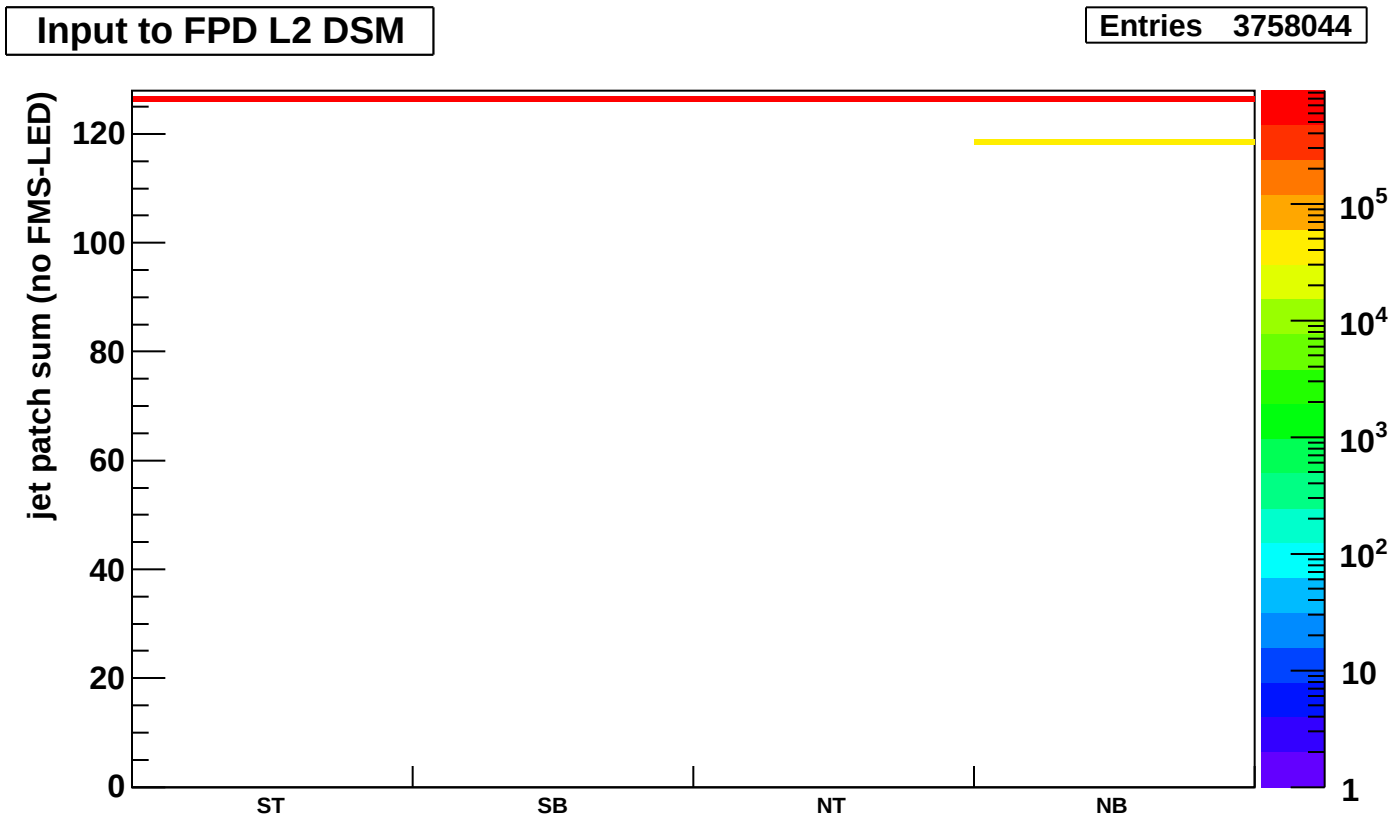

Input to FPD L2 DSM
Entries 7516088
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

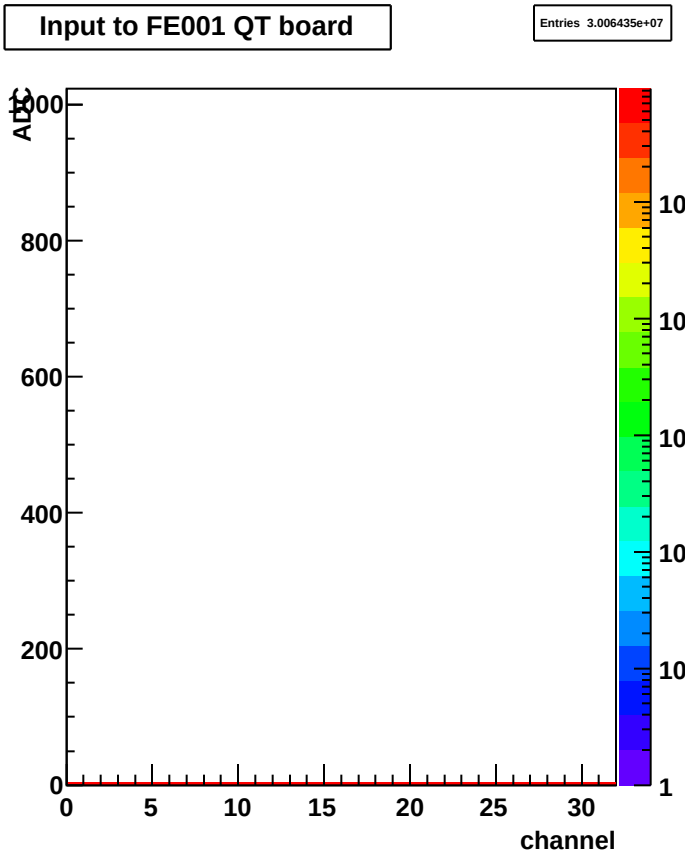

Input to FPD L2 DSM
Entries 2818533
CL bits
939511
939511
939511
SMALL
LARGE-S
LARGE-N

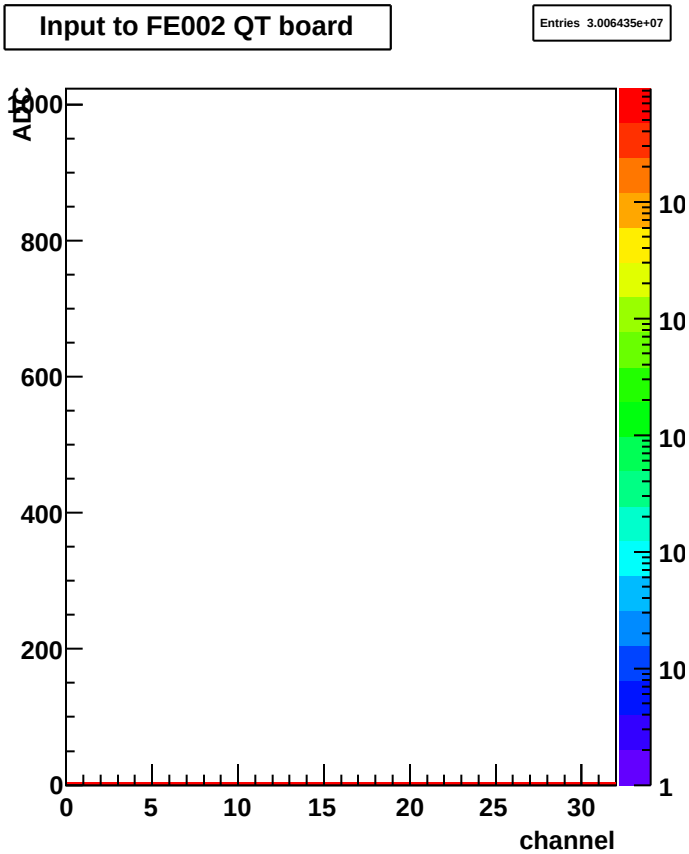

Input to FPD L2 DSM
Entries 2818533
CL bits - simulated
939511
939511
939511
SMALL
LARGE-S
LARGE-N

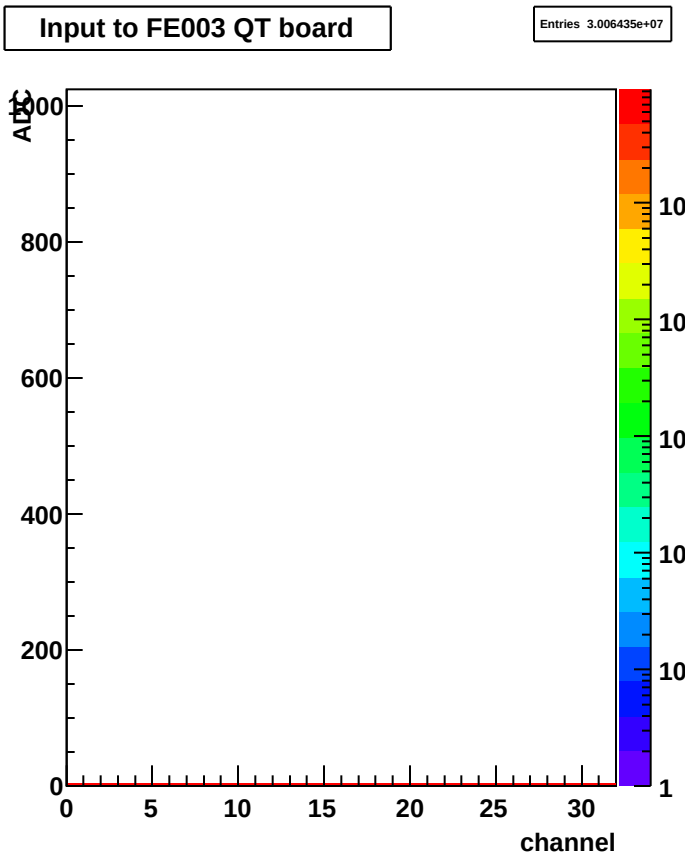

Input to FPD L2 DSM
Entries 2818533
HT bits
939481
939511
939511
30
SMALL
LARGE-S
LARGE-N

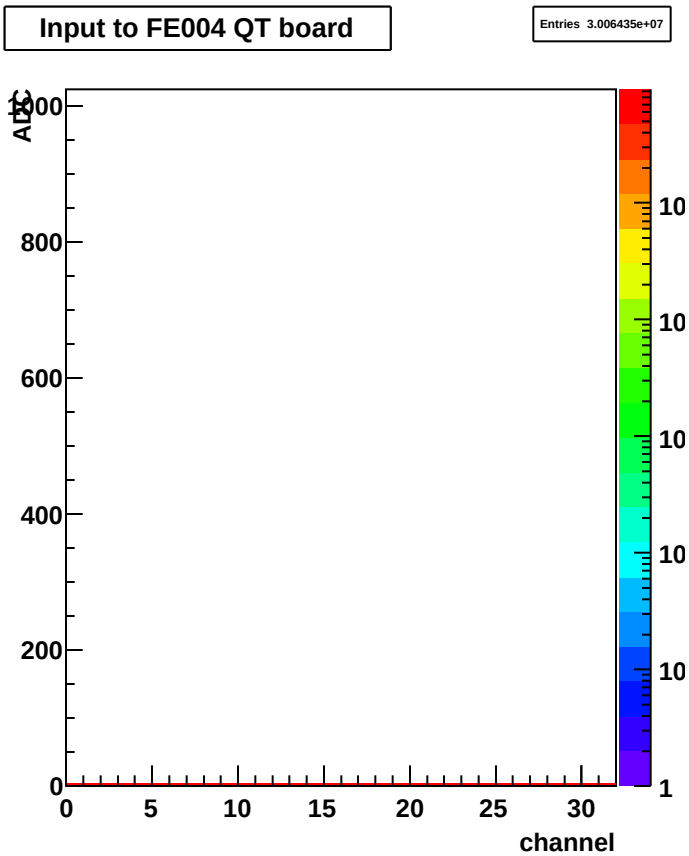

Input to FPD L2 DSM
Entries 2818533
HT bits - simulated
939481
939511
939511
30
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3758044
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

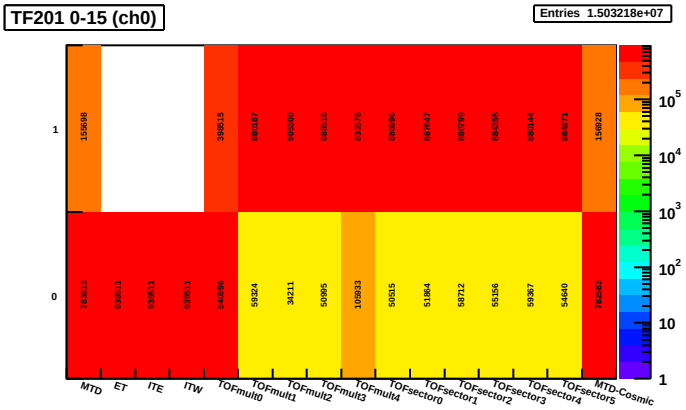

Input to FE001 QT board
Entries 3.006435e+07
ADC
channel


Input to FE002 QT board
Entries 3.006435e+07
ADC
channel

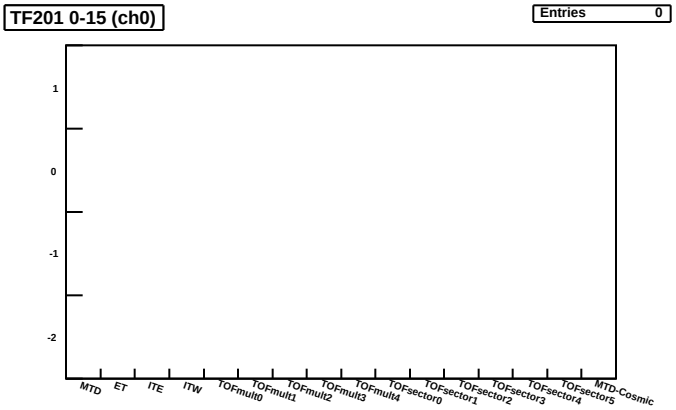

Input to FE003 QT board
Entries 3.006435e+07
ADC
channel


Input to FE004 QT board
Entries 3.006435e+07
ADC
channel

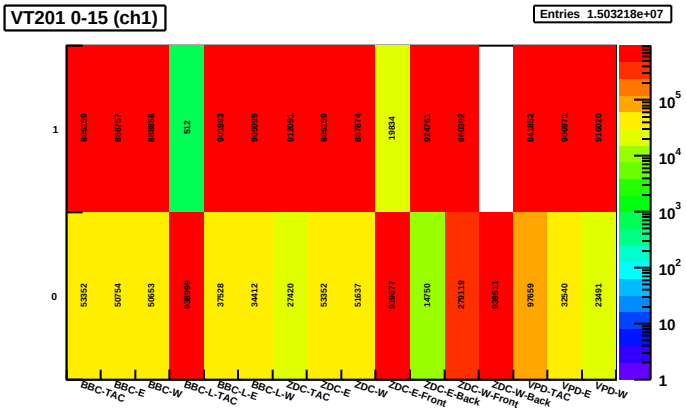
TF201 0-15 (ch0)



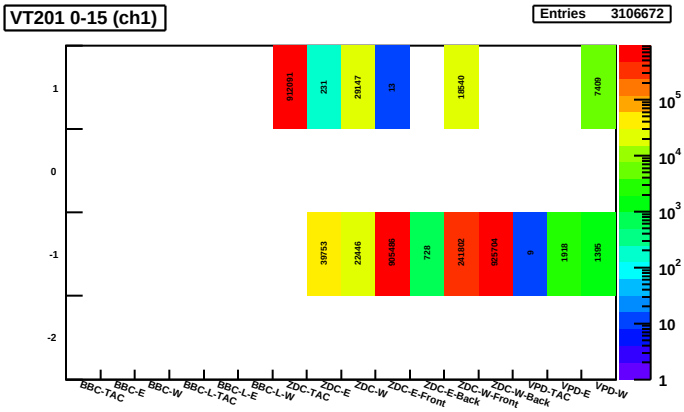
TF201 0-15 (ch0)



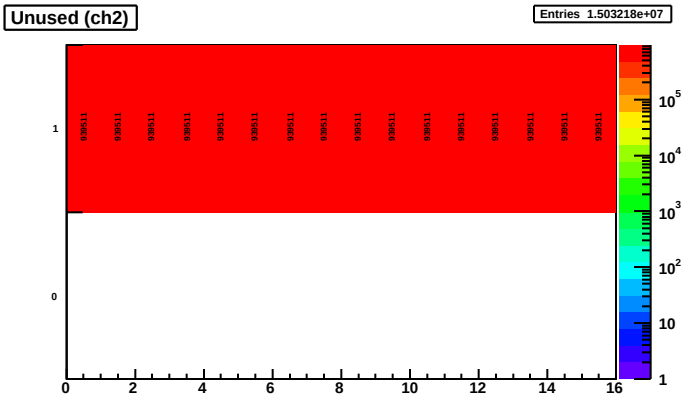
VT201 0-15 (ch1)

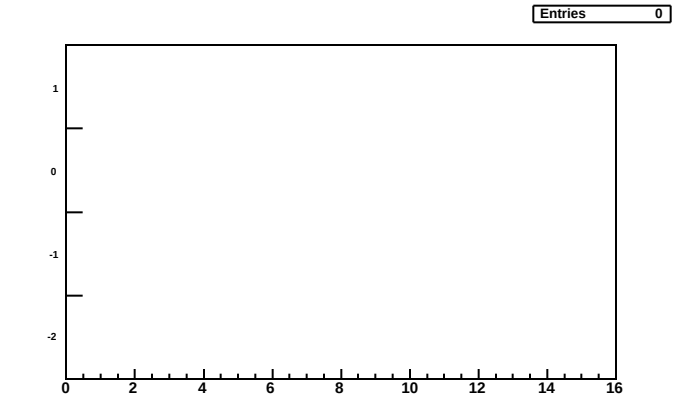


VT201 0-15 (ch1)

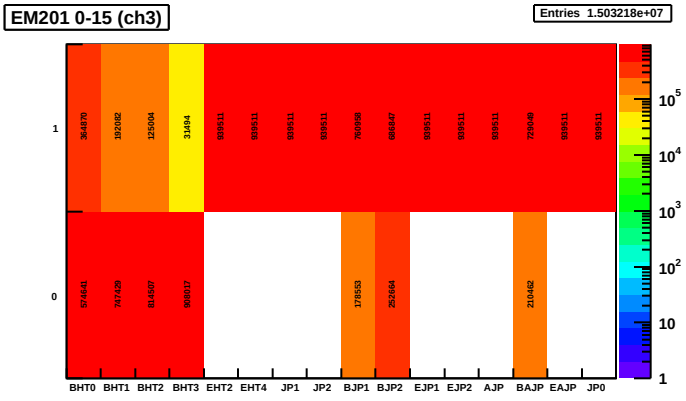


Unused (ch2)

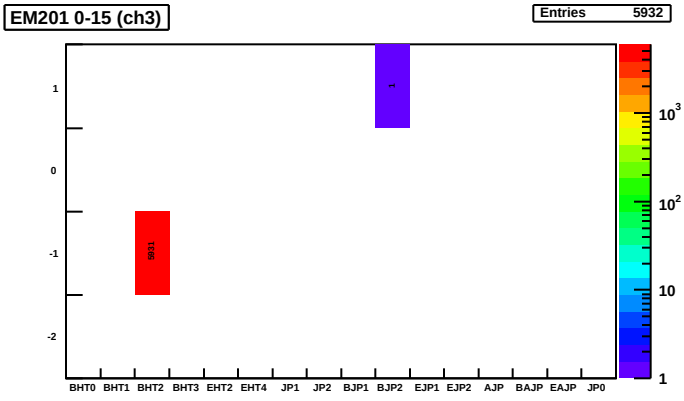


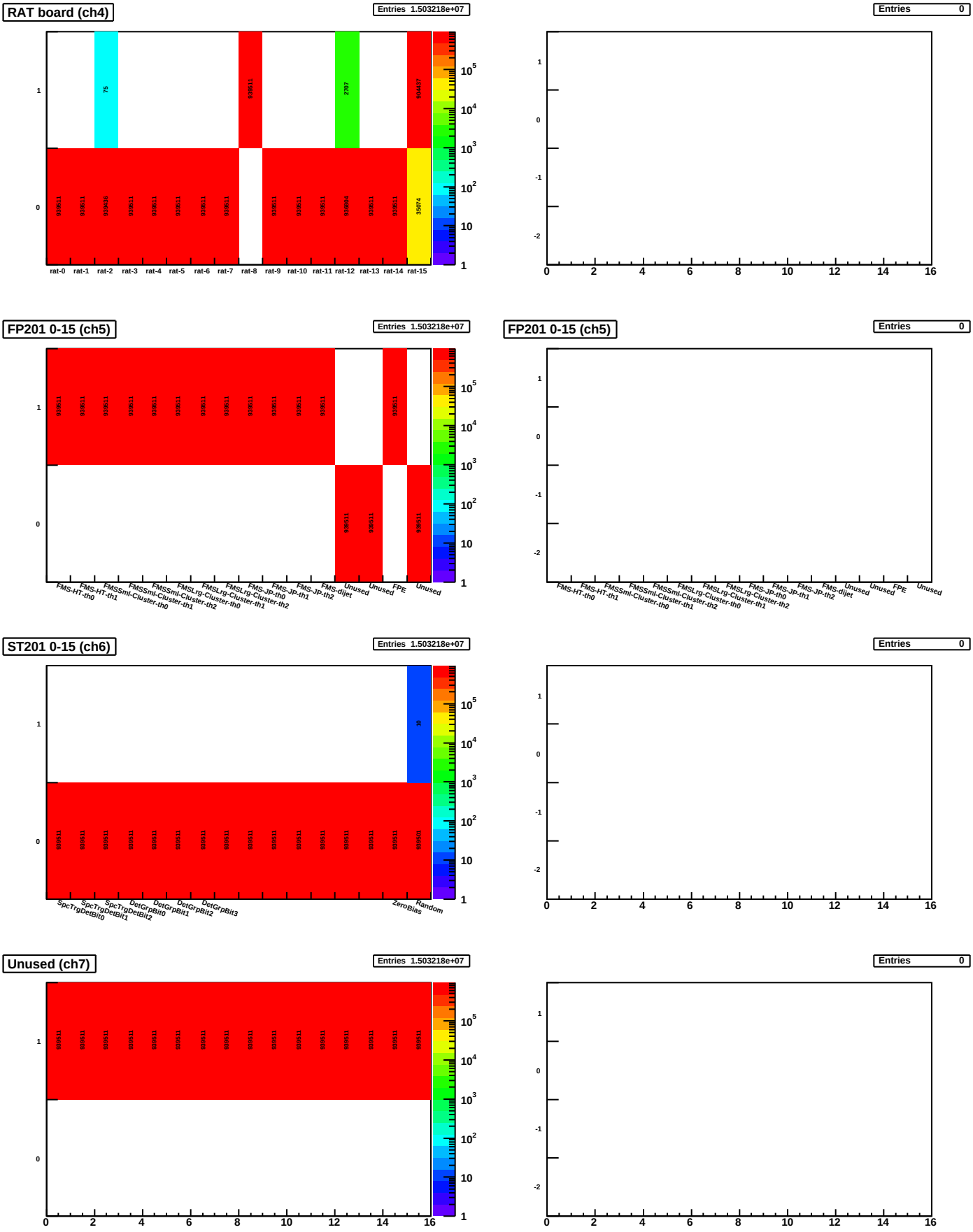


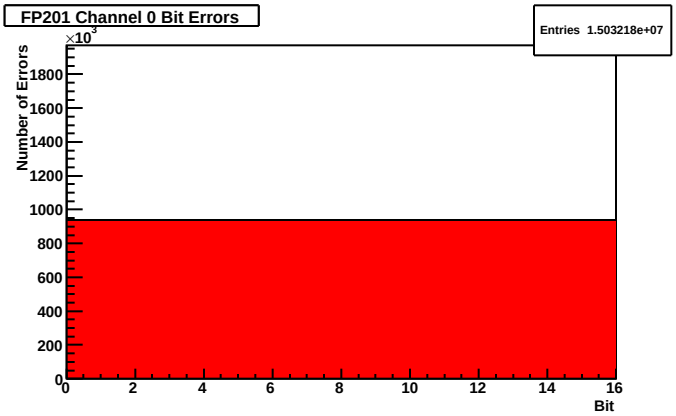
EM201 0-15 (ch3)

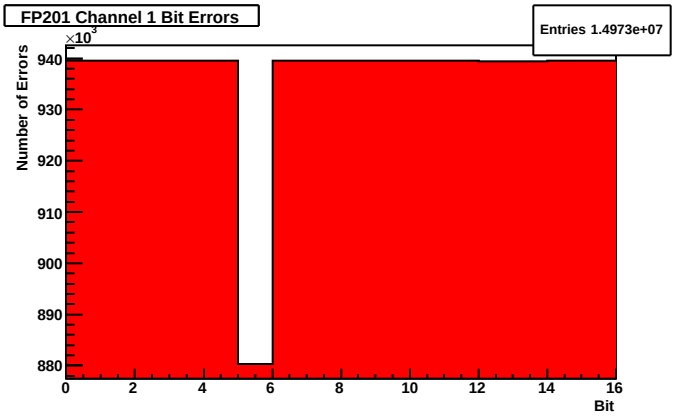


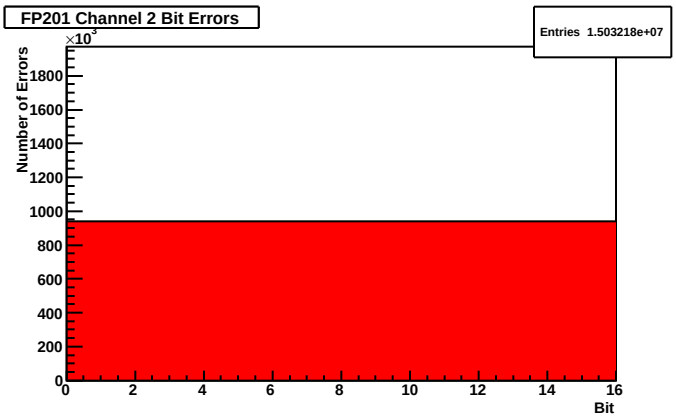
EM201 0-15 (ch3)

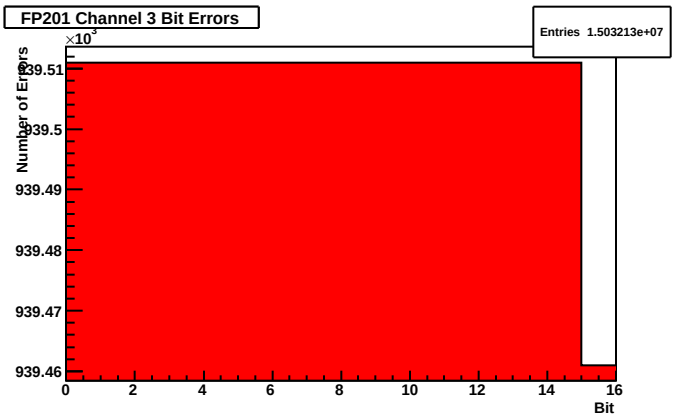


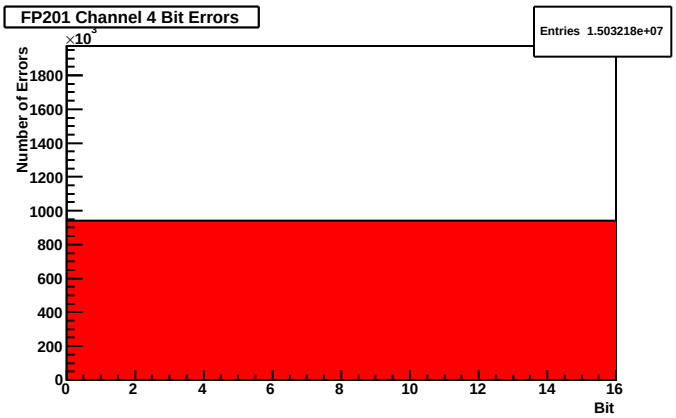

RAT board (ch4)
Entries 1.503218e+07
Entries 0
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15
FP201 0-15 (ch5)
Entries 1.503218e+07
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused
ST201 0-15 (ch6)
Entries 1.503218e+07
Entries 0
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random
Unused (ch7)
Entries 1.503218e+07
Entries 0

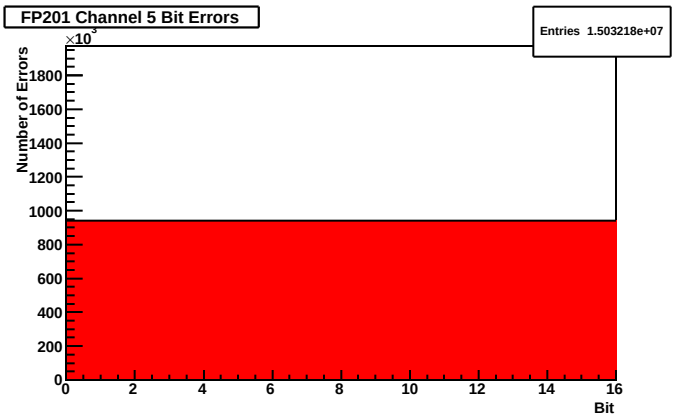

FP201 Channel 0 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

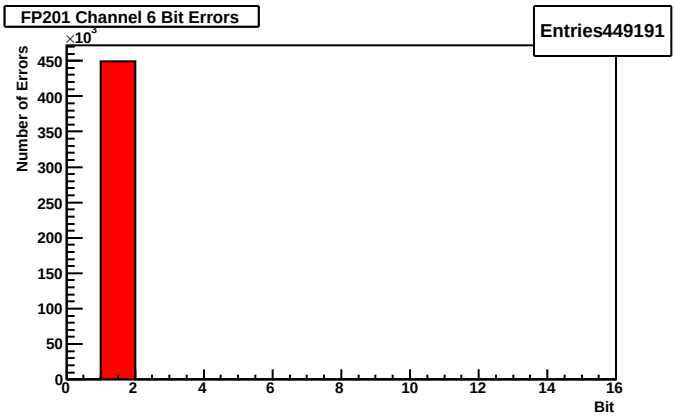

FP201 Channel 1 Bit Errors
Entries 1.4973e+07
Number of Errors
Bit

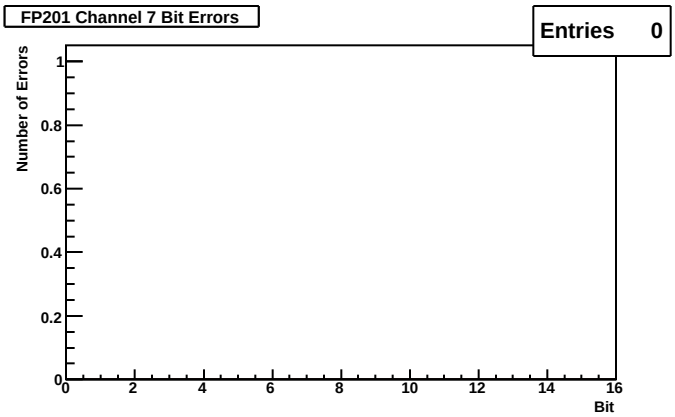

FP201 Channel 2 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

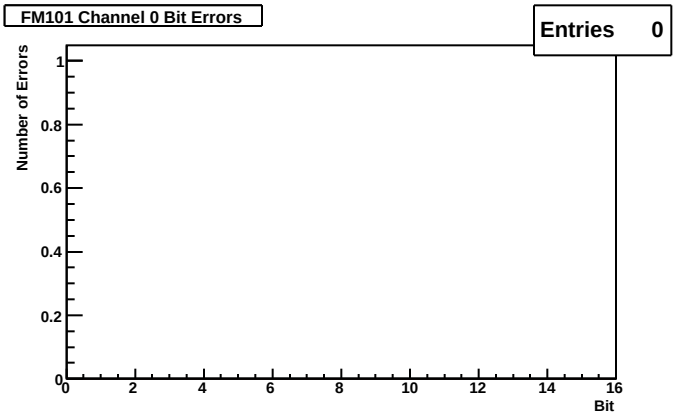

FP201 Channel 3 Bit Errors
Entries 1.503213e+07
Number of Errors
Bit

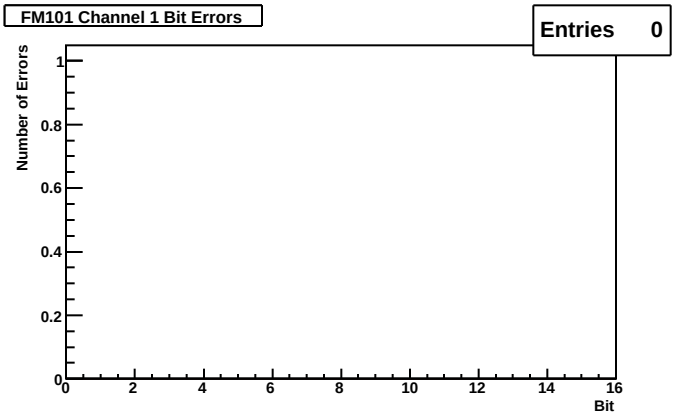

FP201 Channel 4 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

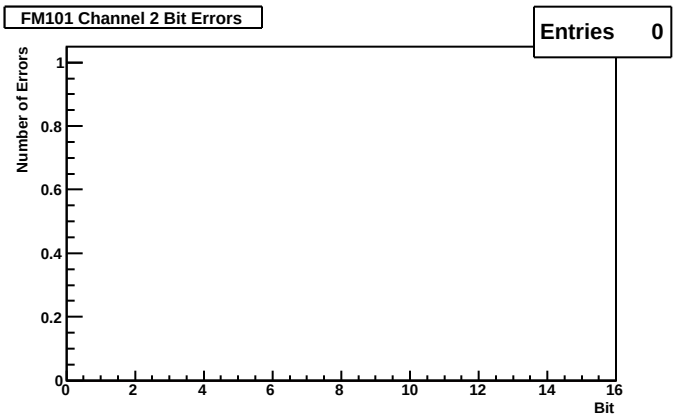

FP201 Channel 5 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

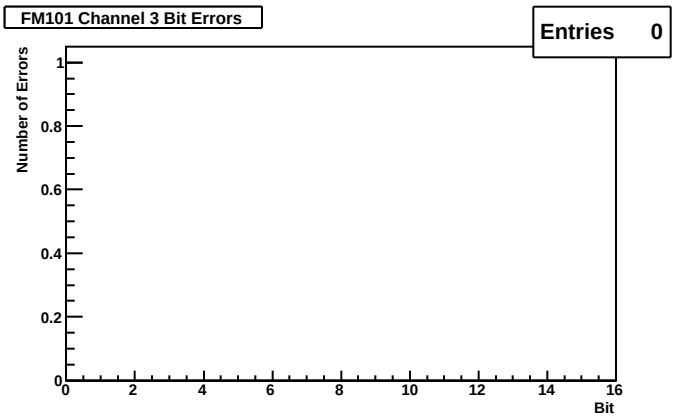

FP201 Channel 6 Bit Errors
Entries449191
Number of Errors
Bit

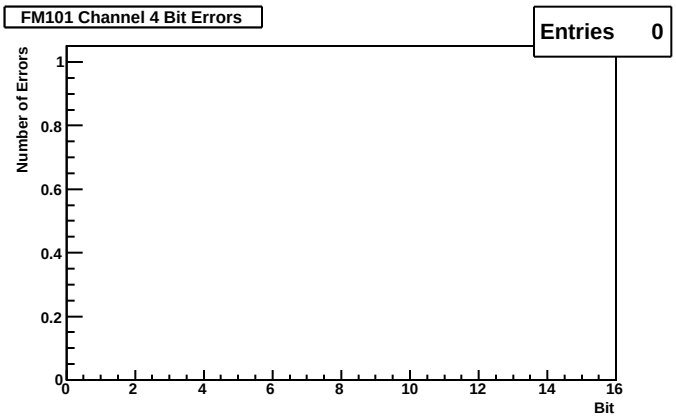

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

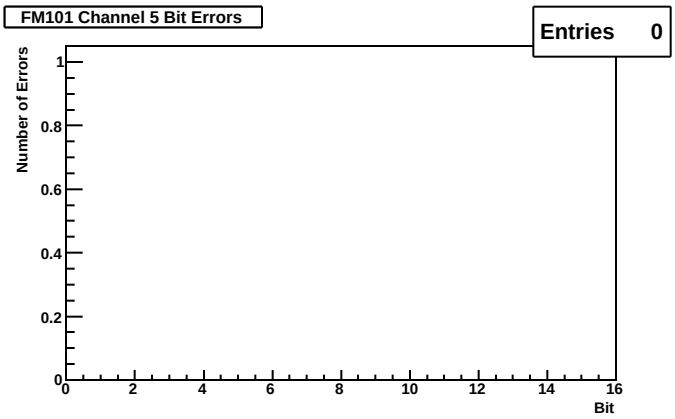

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

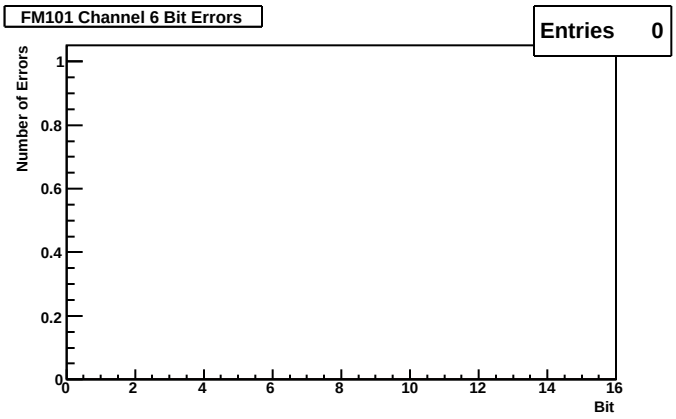

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

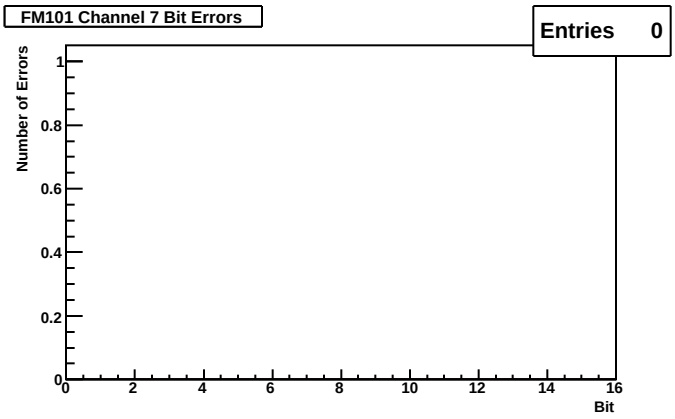

FM101 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

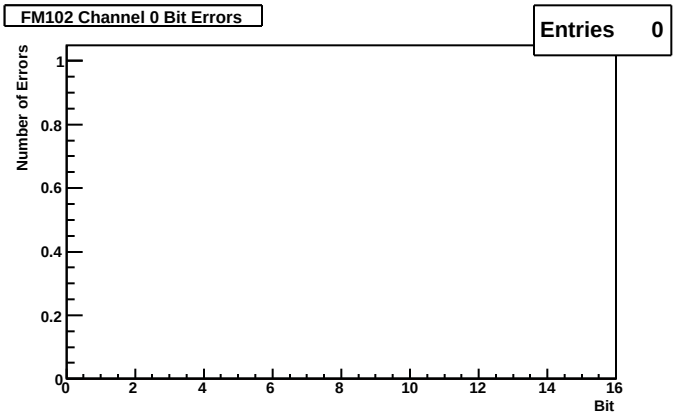

FM101 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

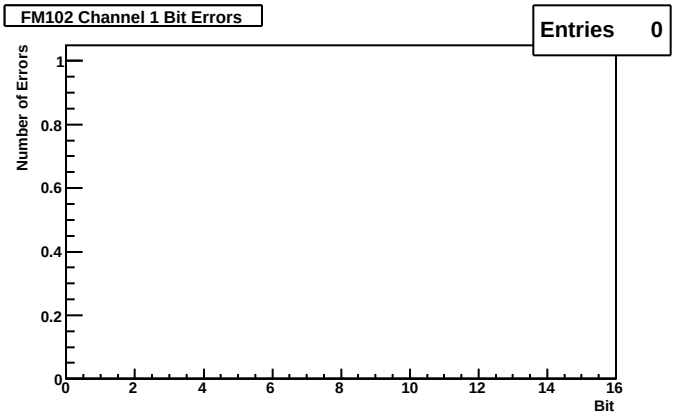

FM101 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

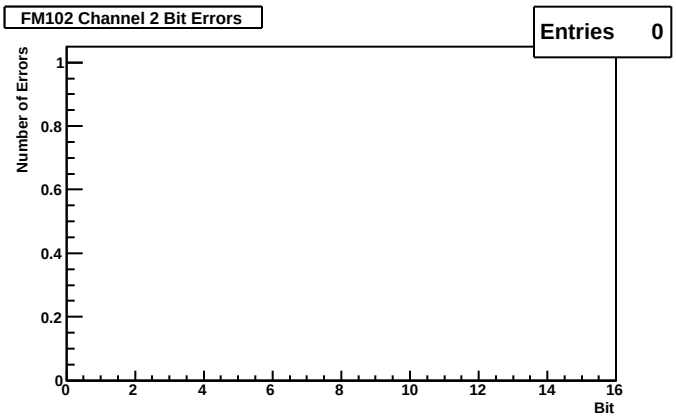

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

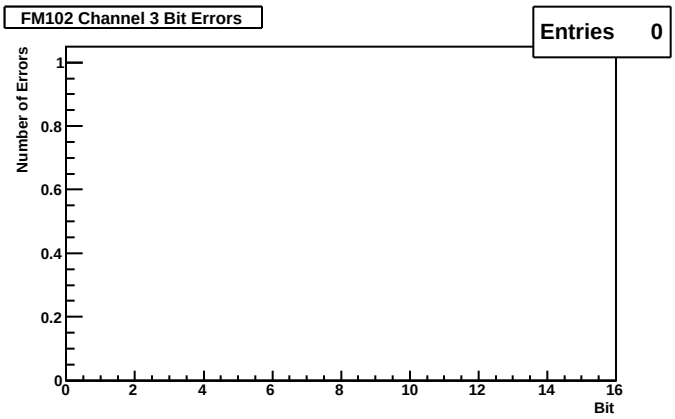

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

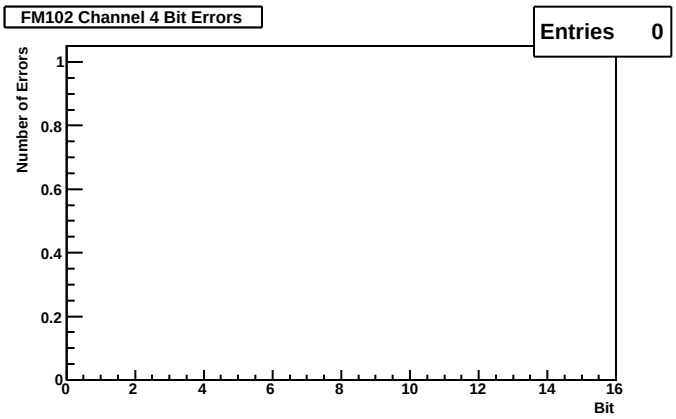

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

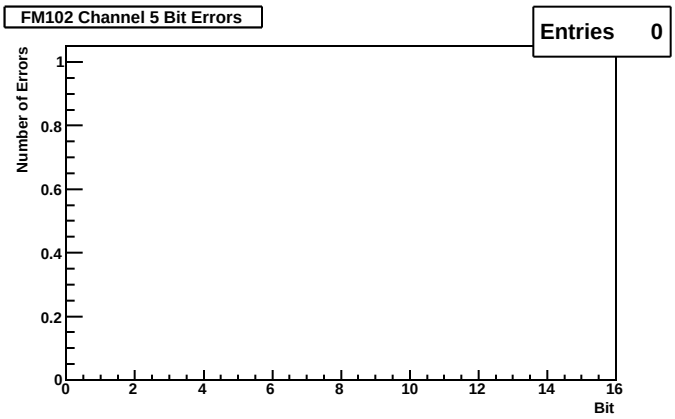

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

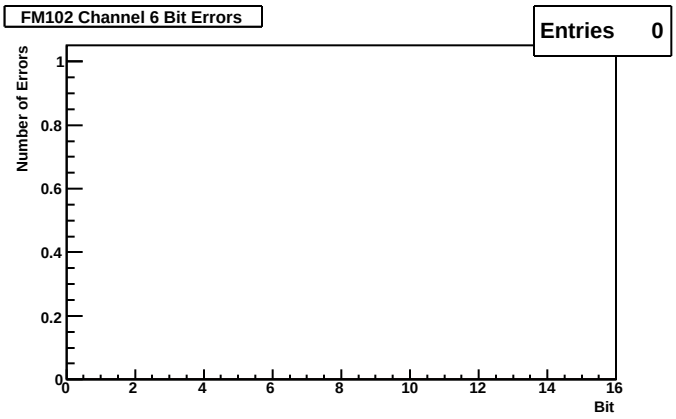

FM102 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

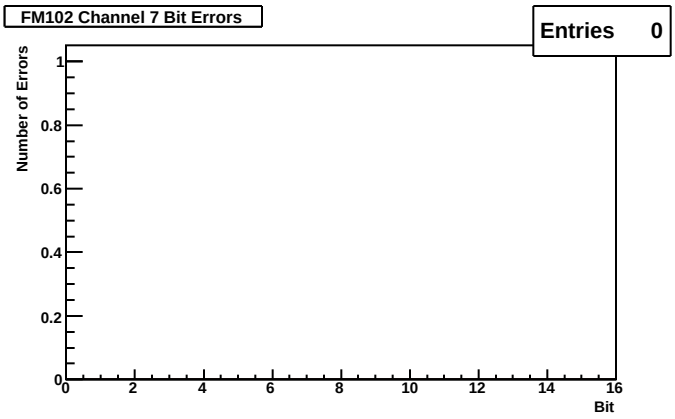

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

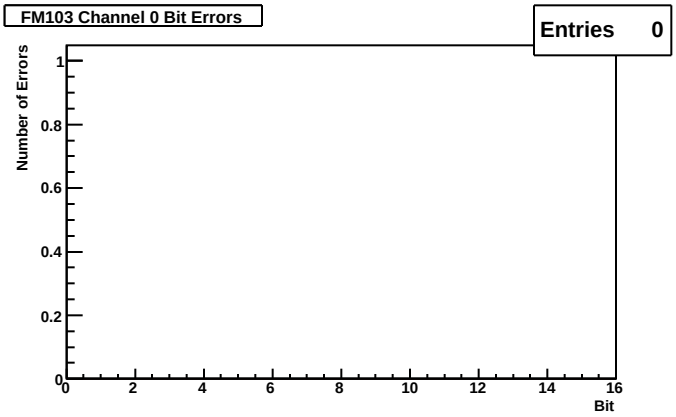

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

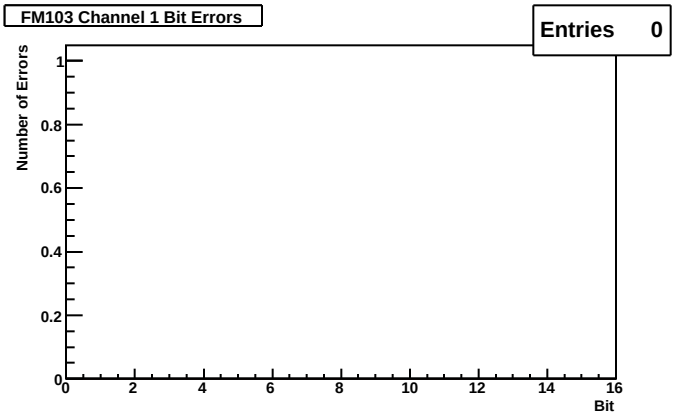

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

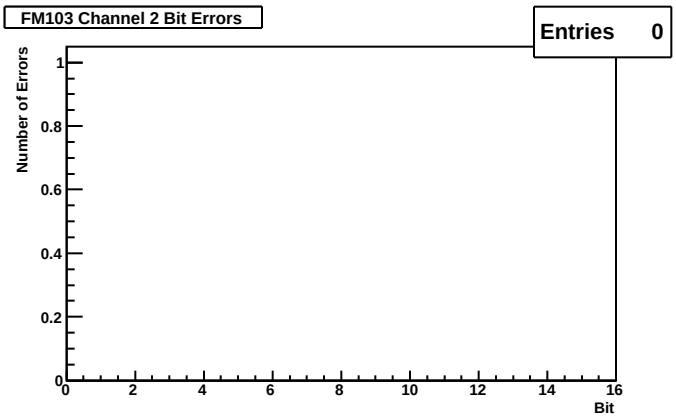

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

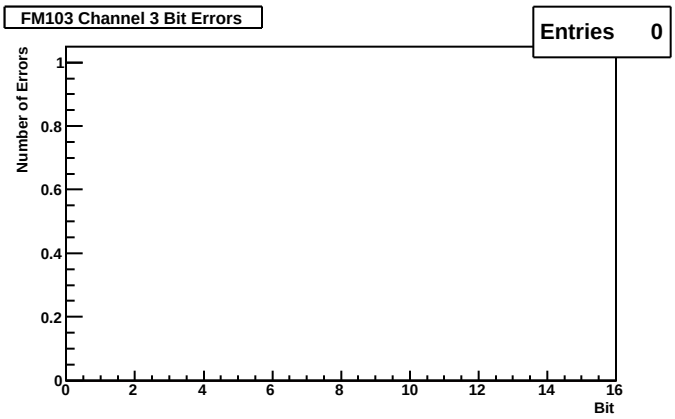

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

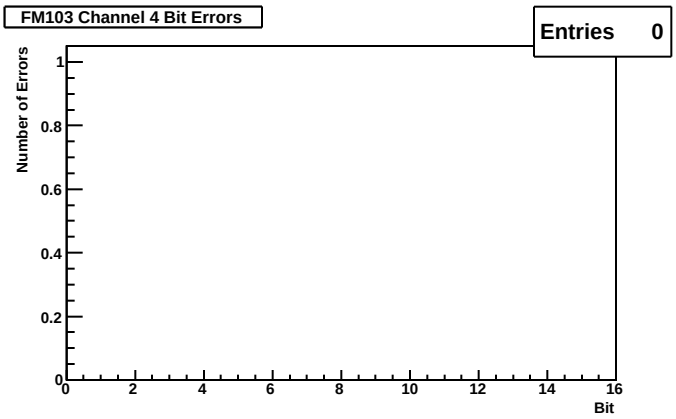

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

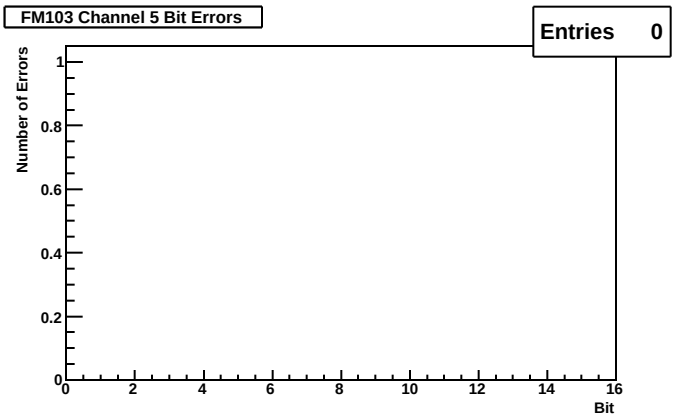

FM103 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

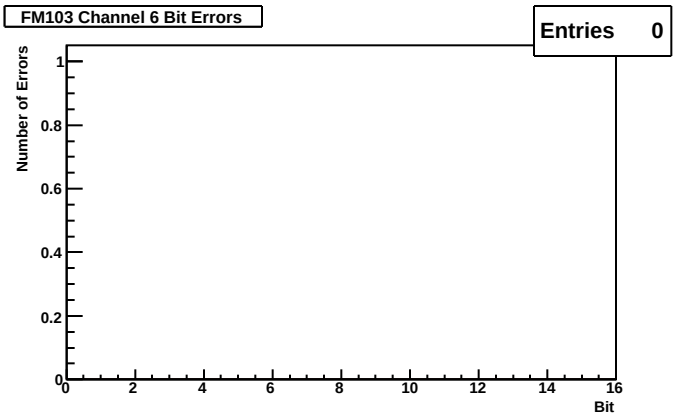

FM103 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

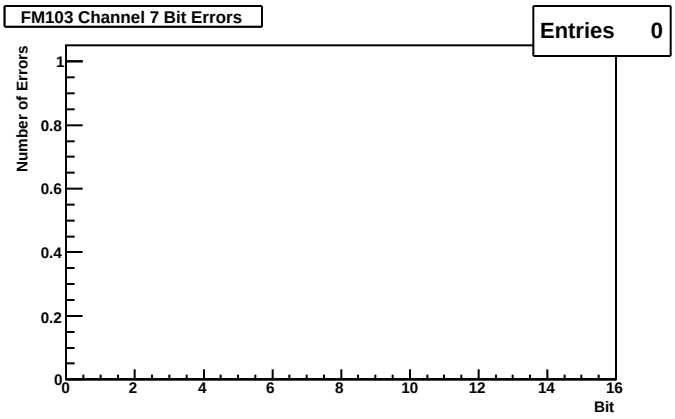

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

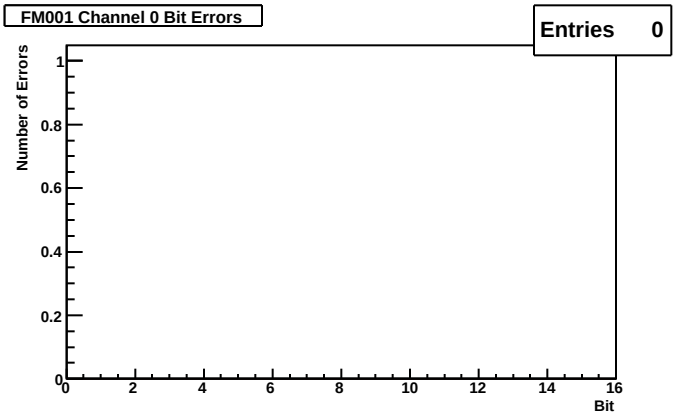

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

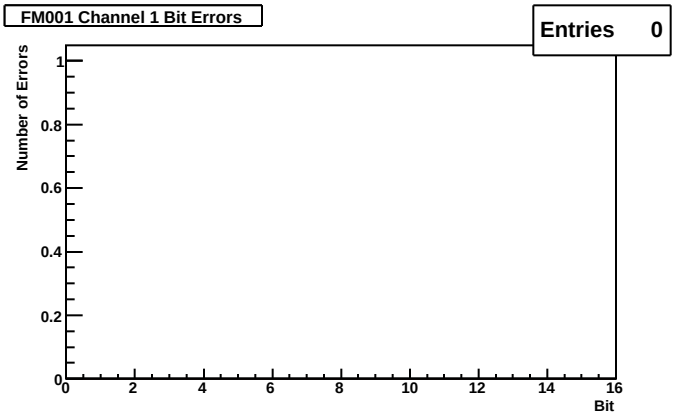

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

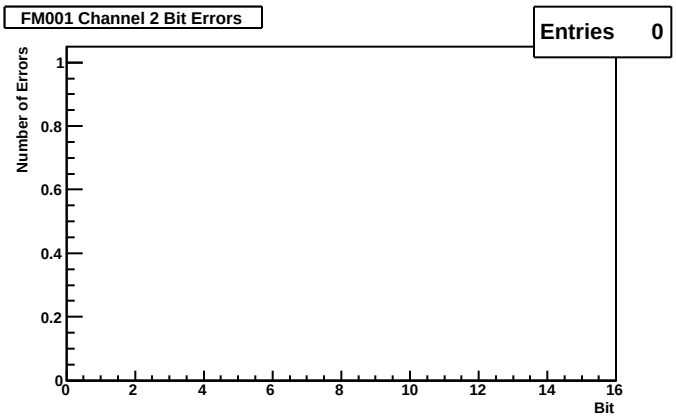

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

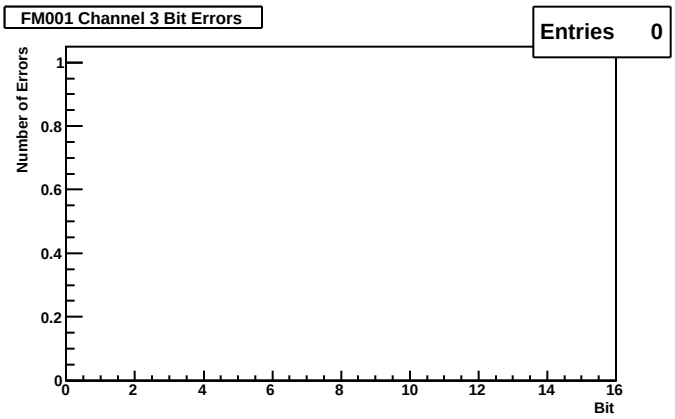

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

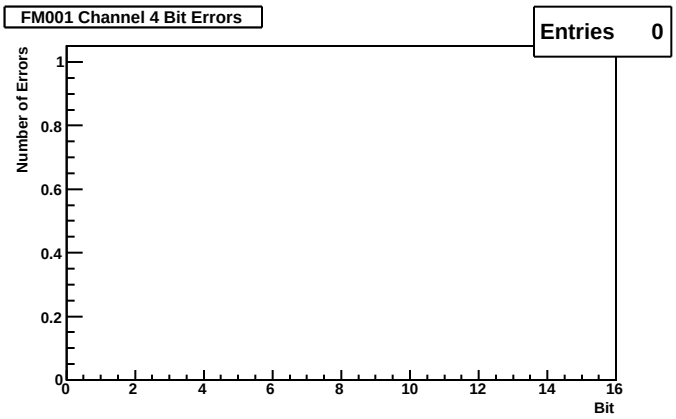

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

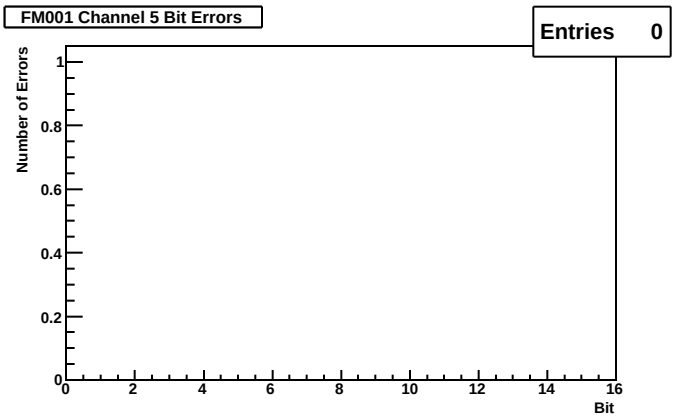

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

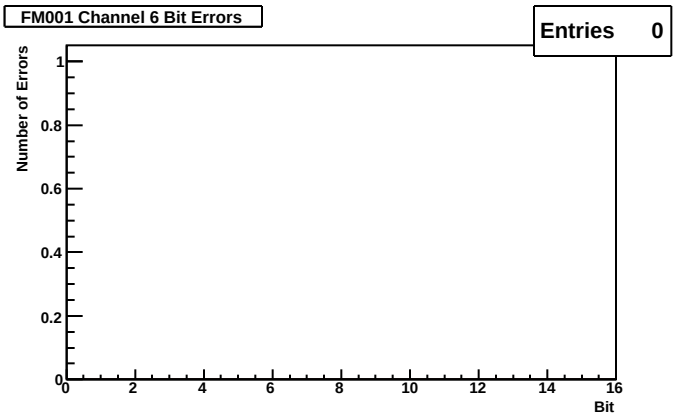

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

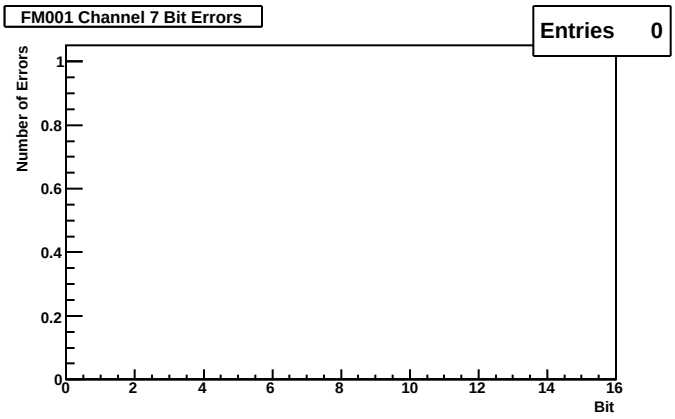

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

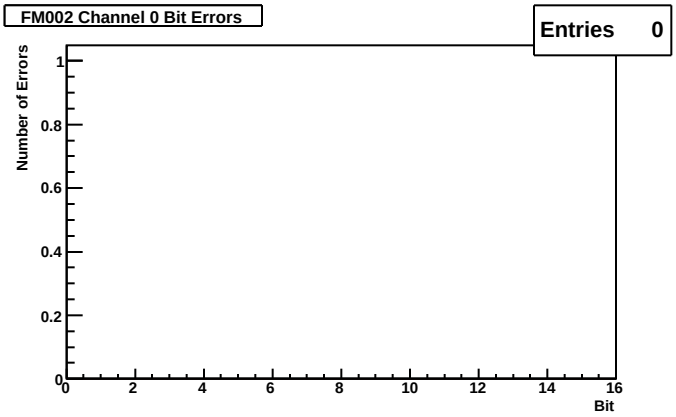

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

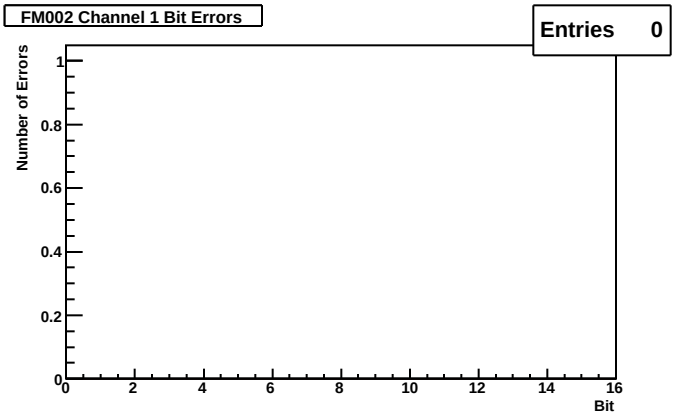

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

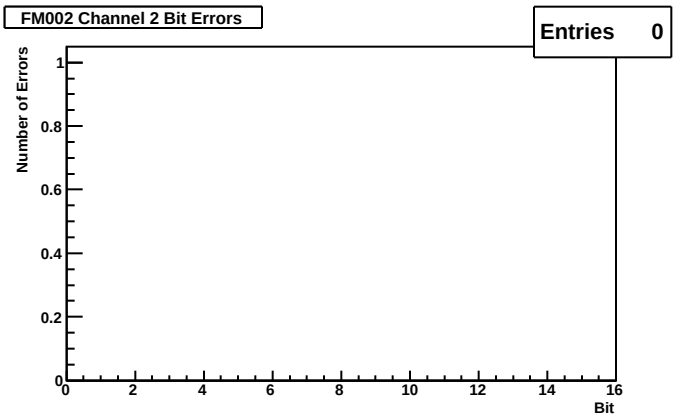

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

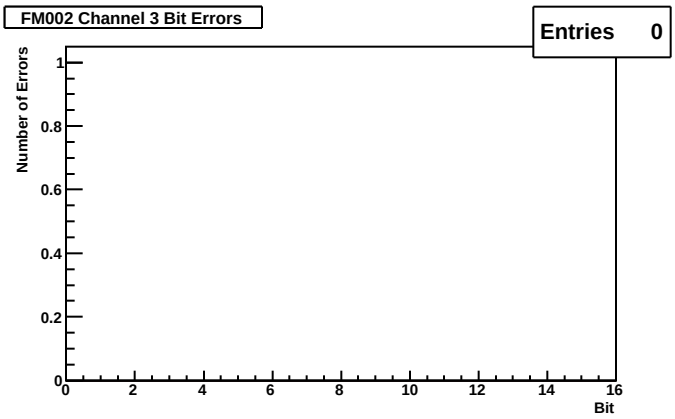

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

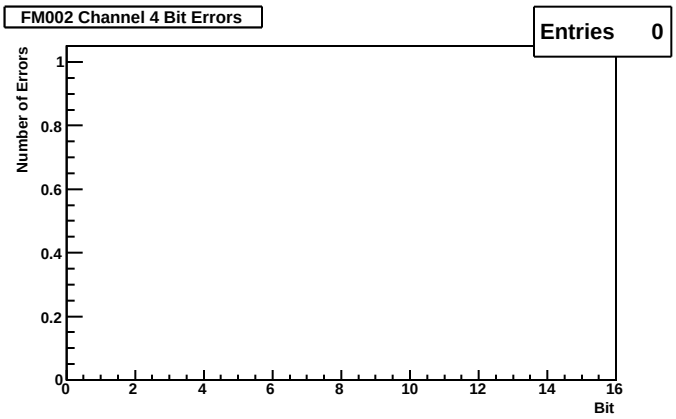

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

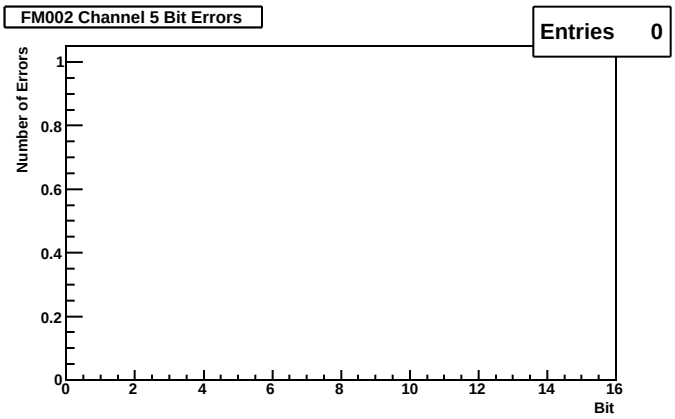

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

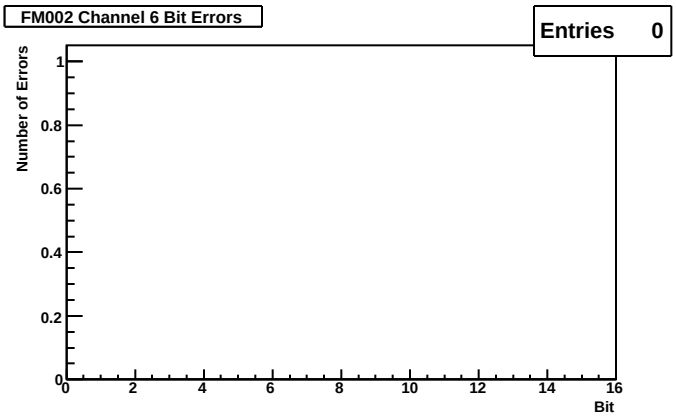

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

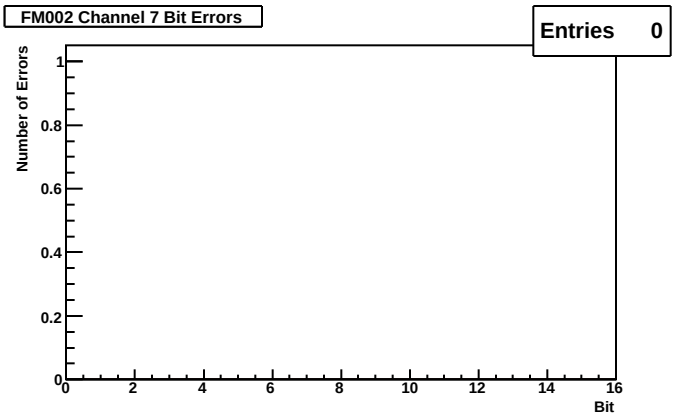

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

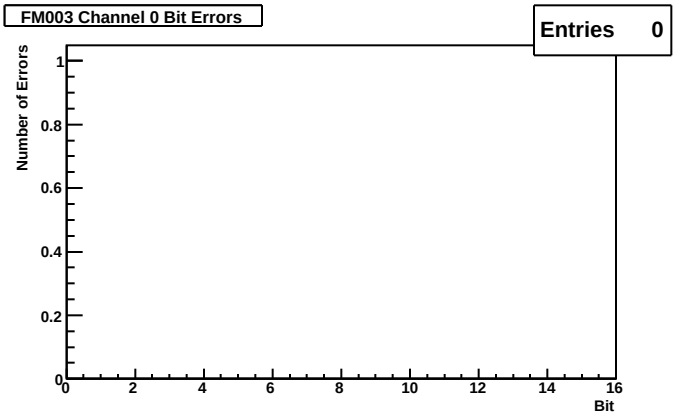

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

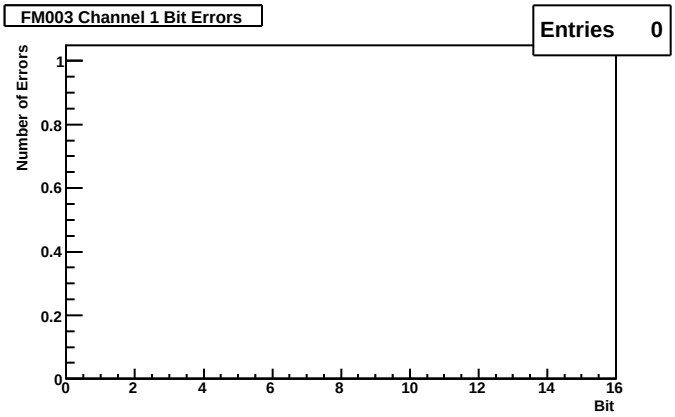

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

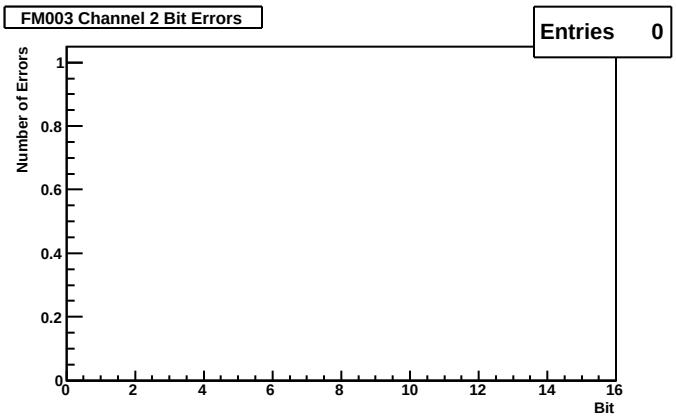

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

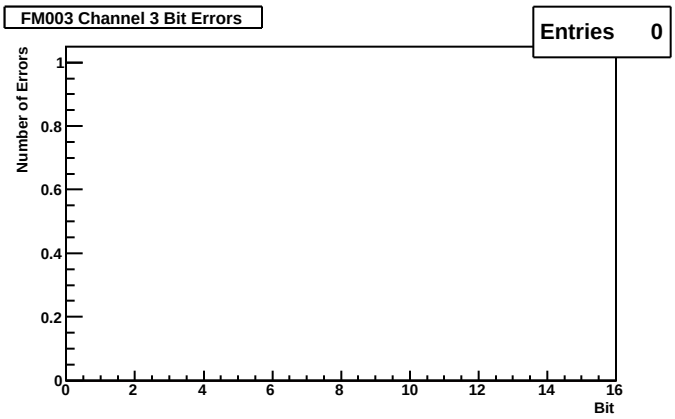

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

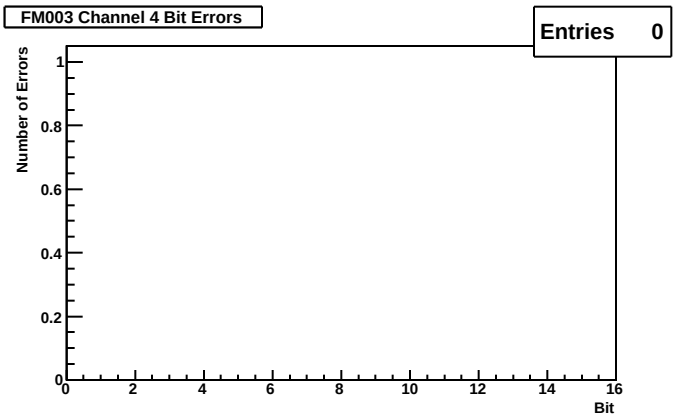

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

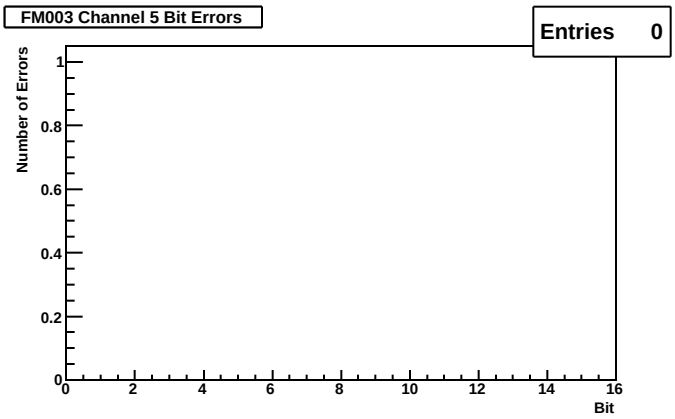

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

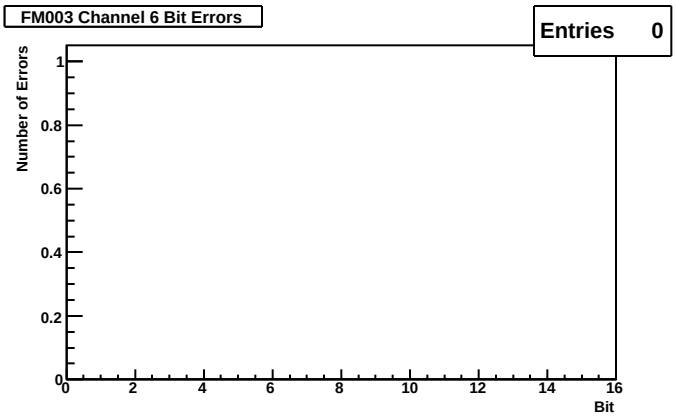

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

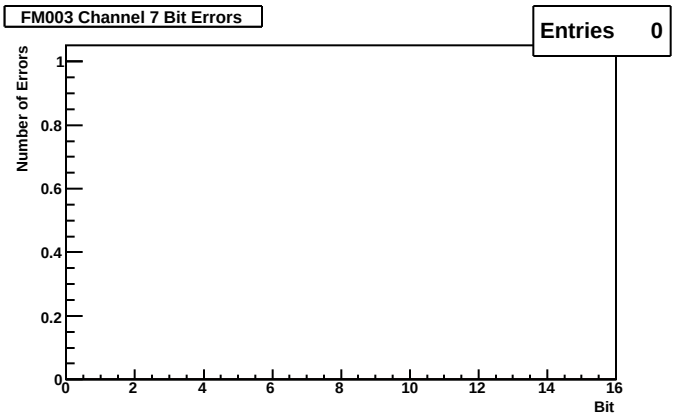

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

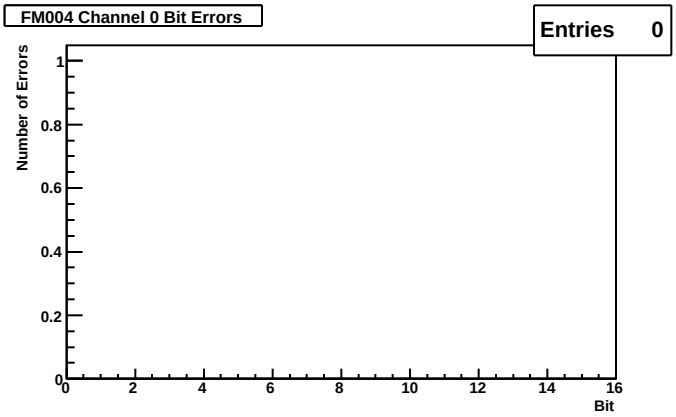

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

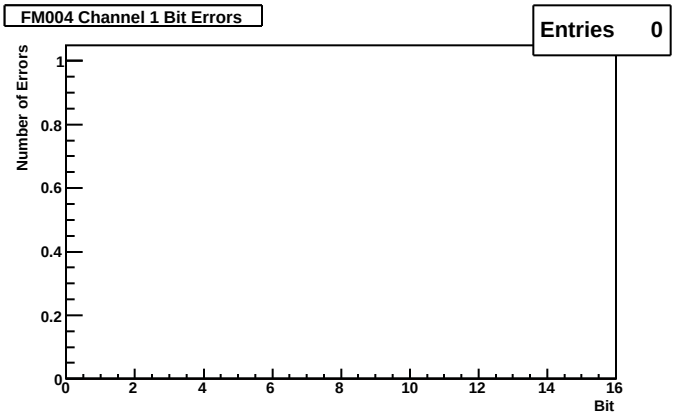

FM003 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

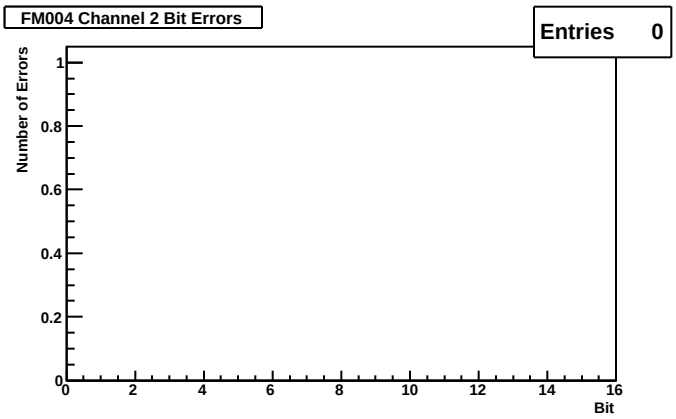

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

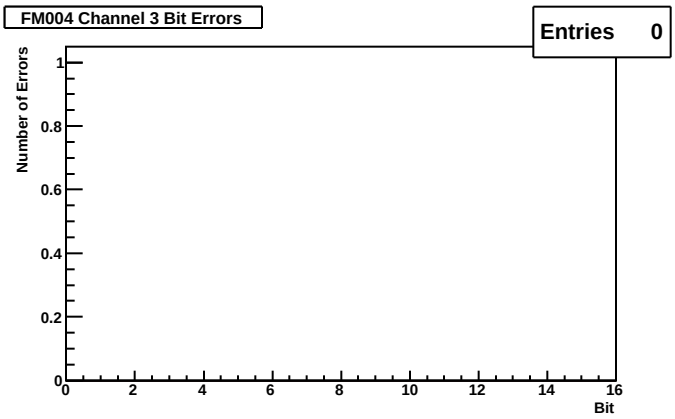

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

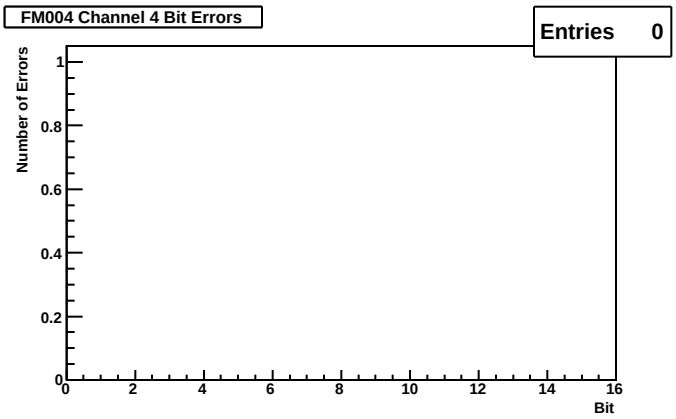

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

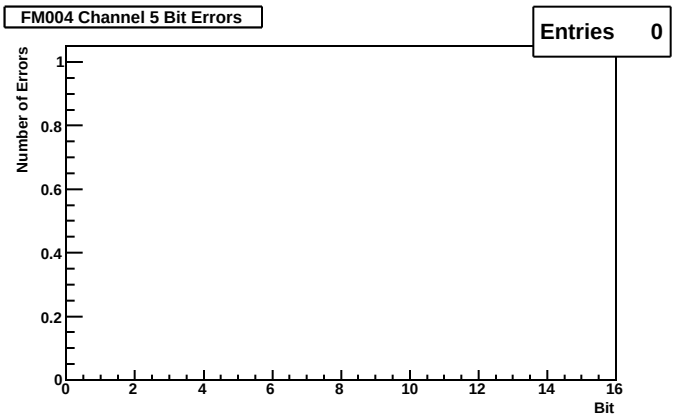

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

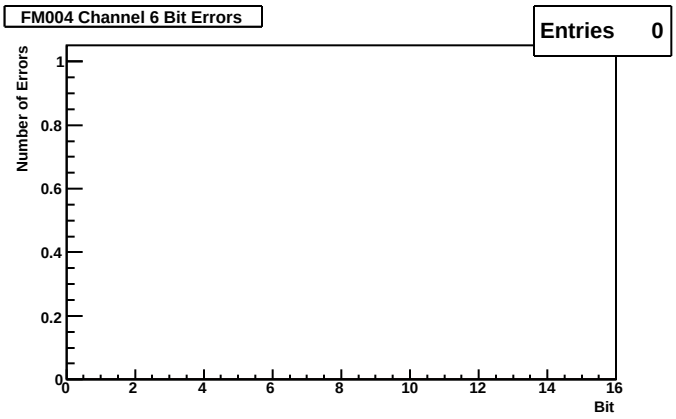

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

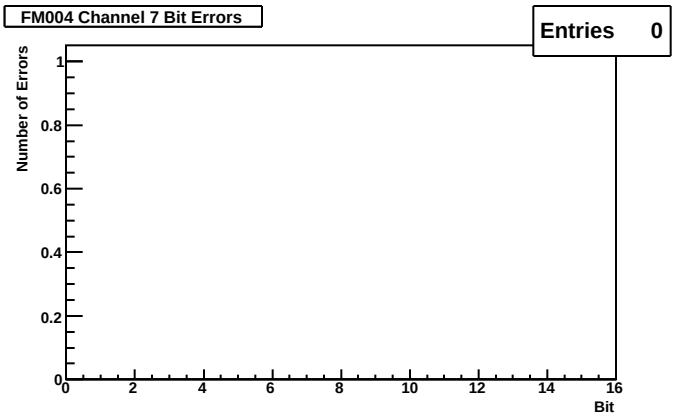

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

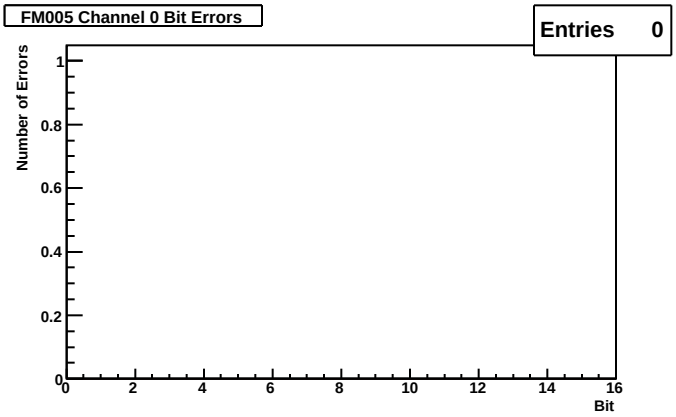

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

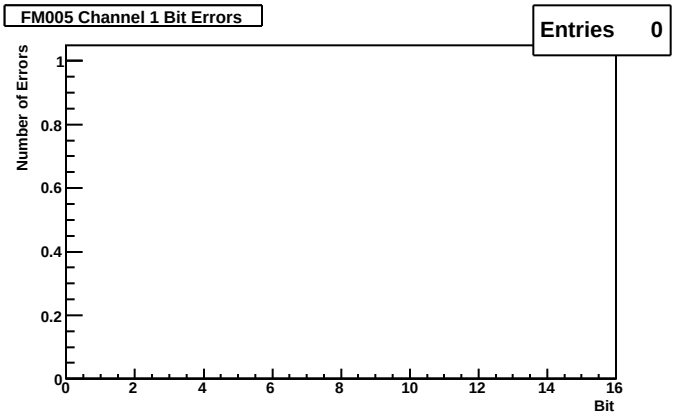

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

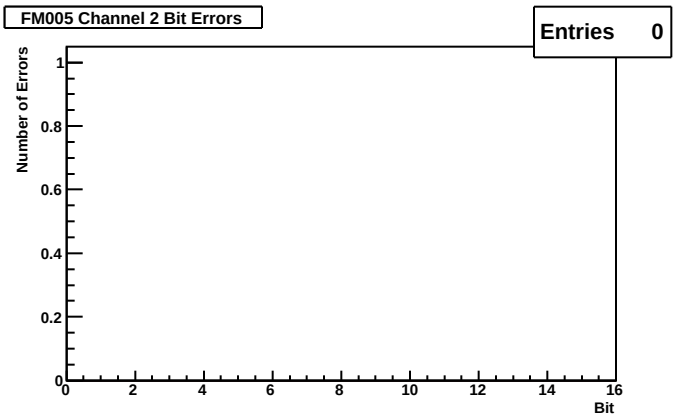

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

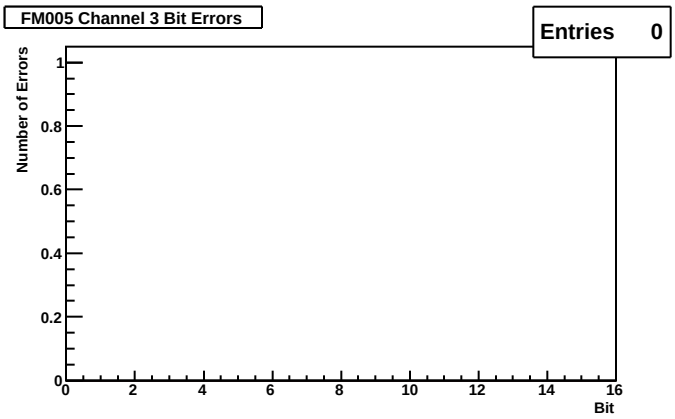

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

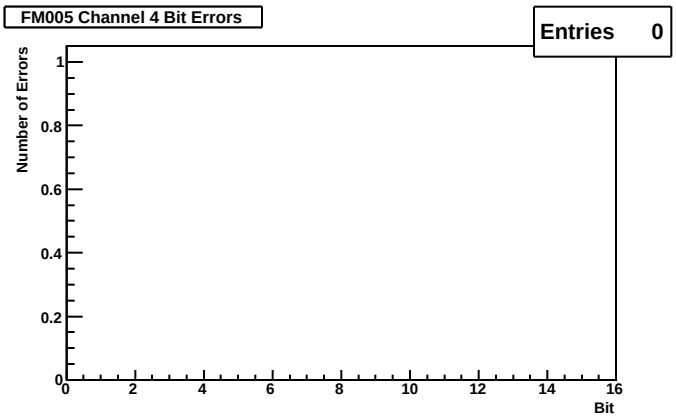

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

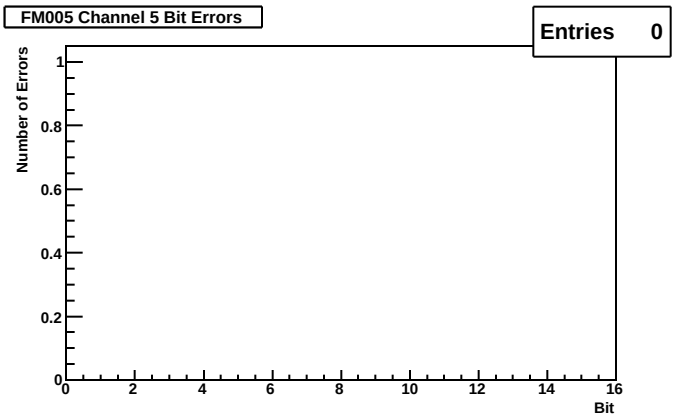

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

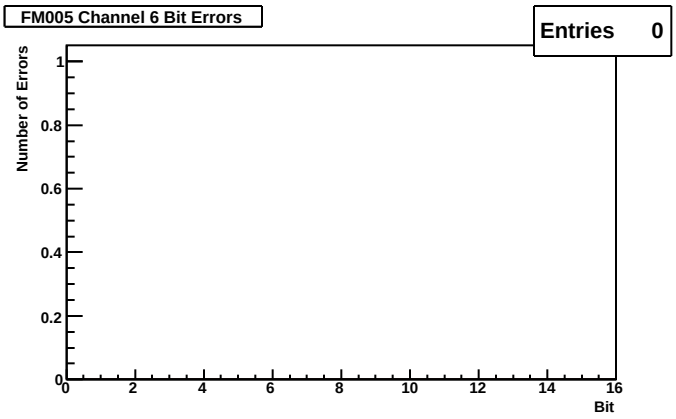

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

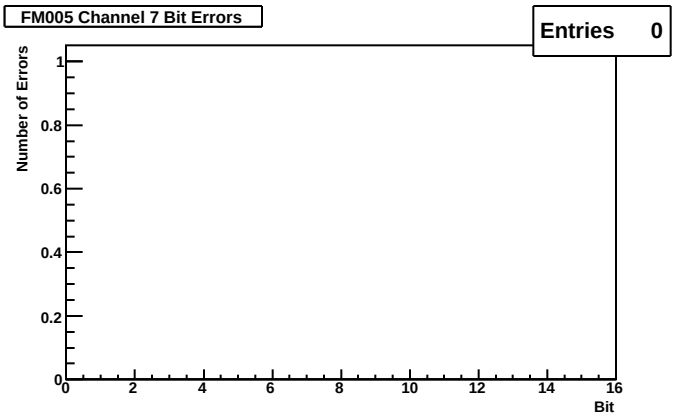

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

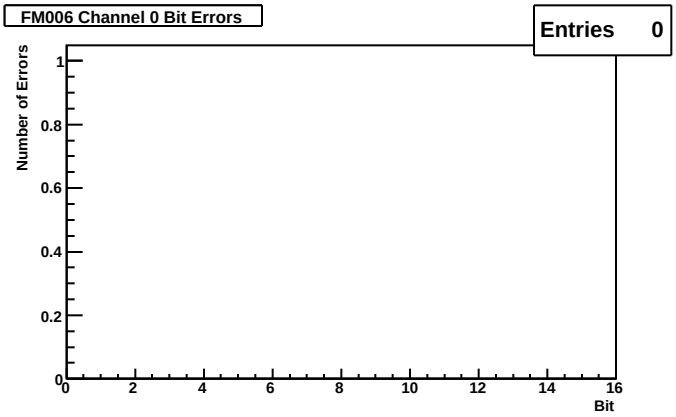

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

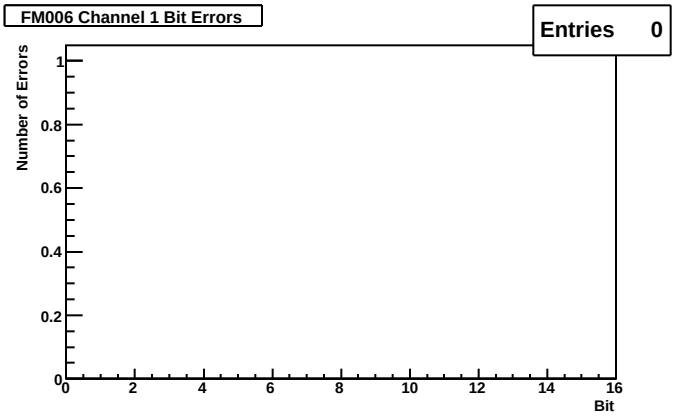

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

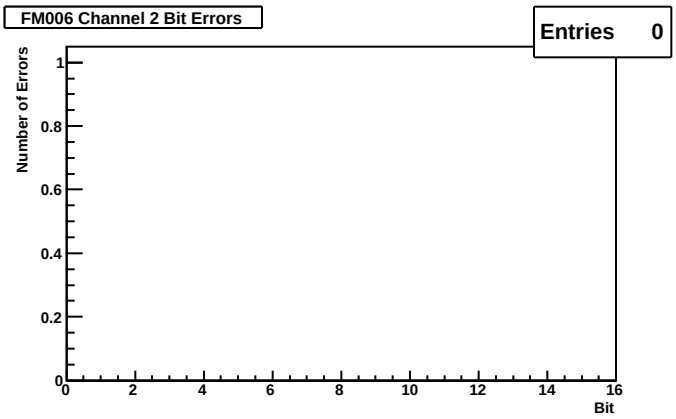

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

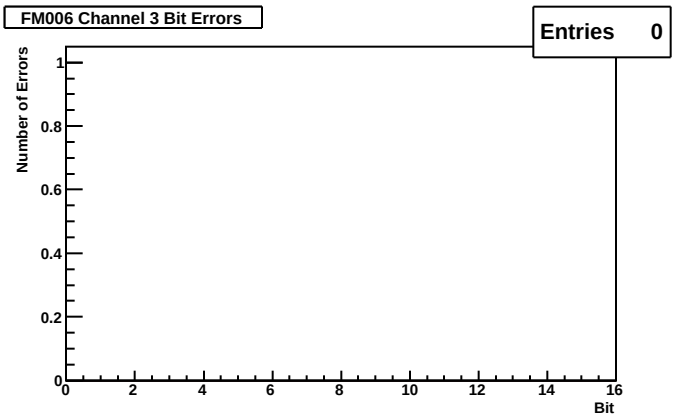

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

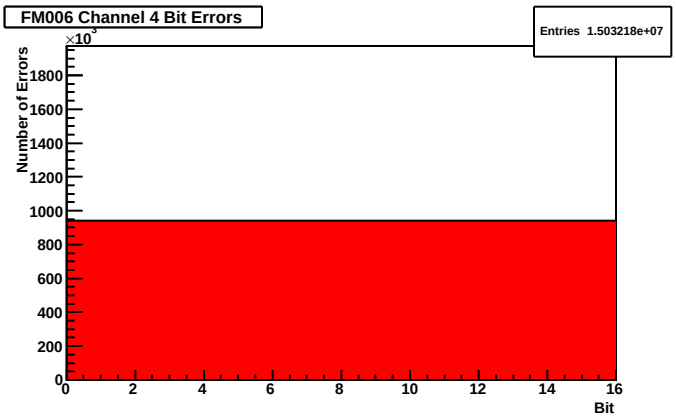

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

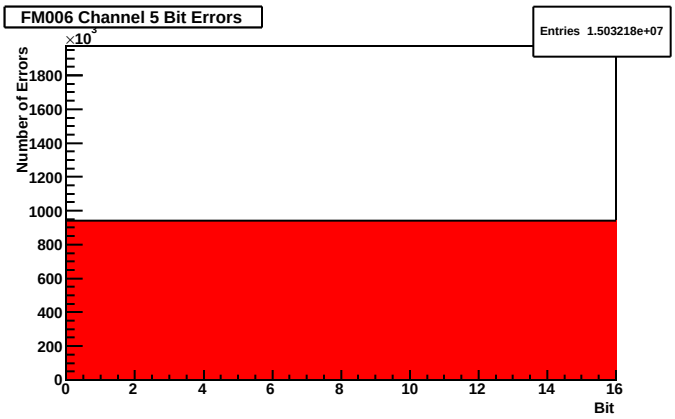

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

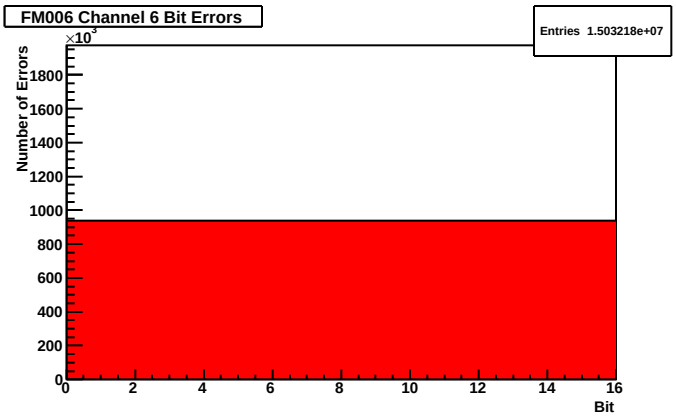

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

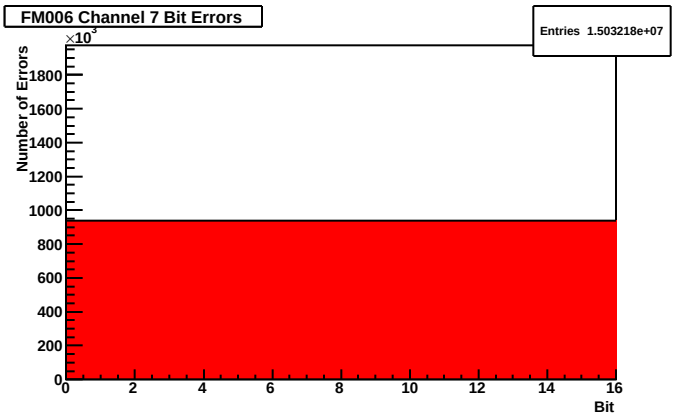

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

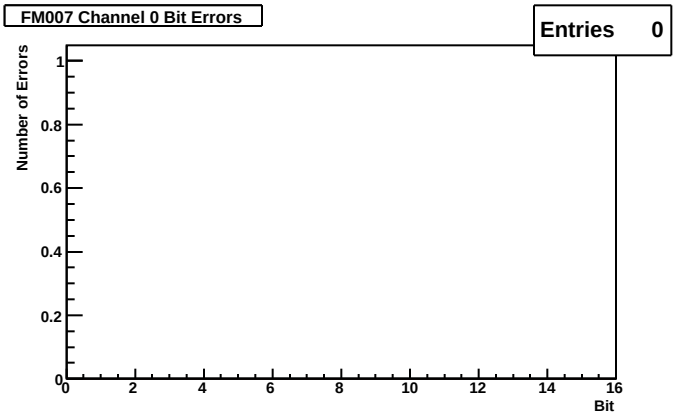

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

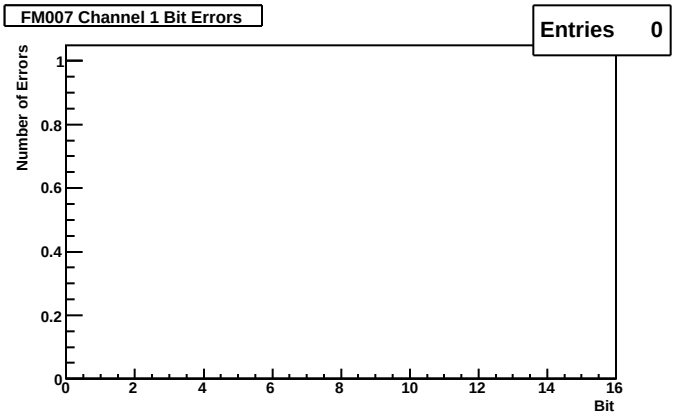

FM006 Channel 4 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

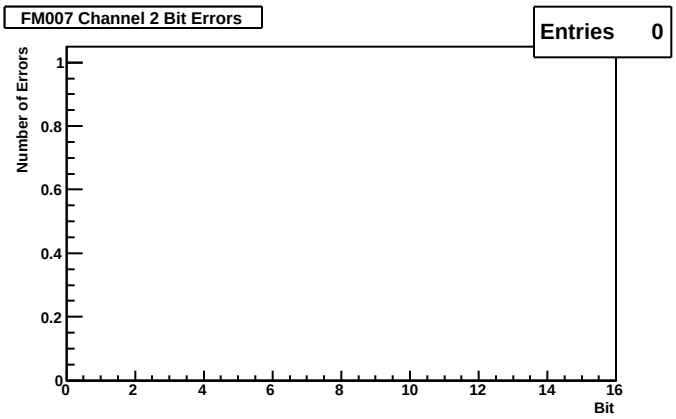

FM006 Channel 5 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

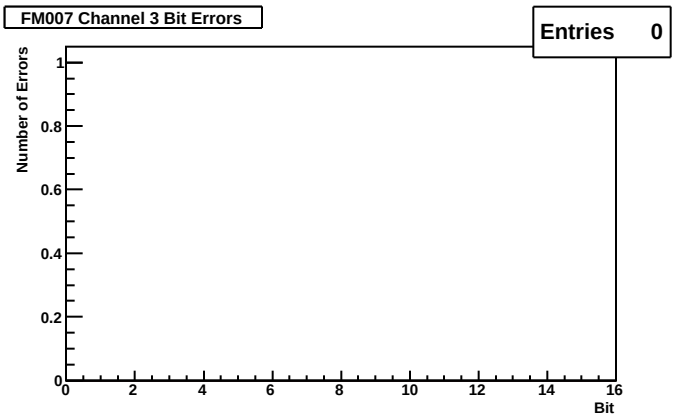

FM006 Channel 6 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

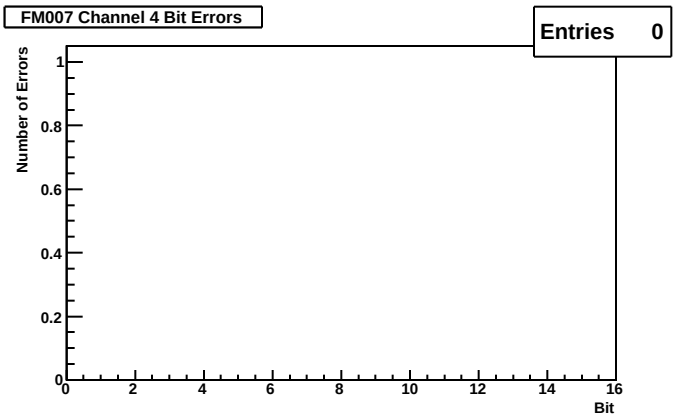

FM006 Channel 7 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

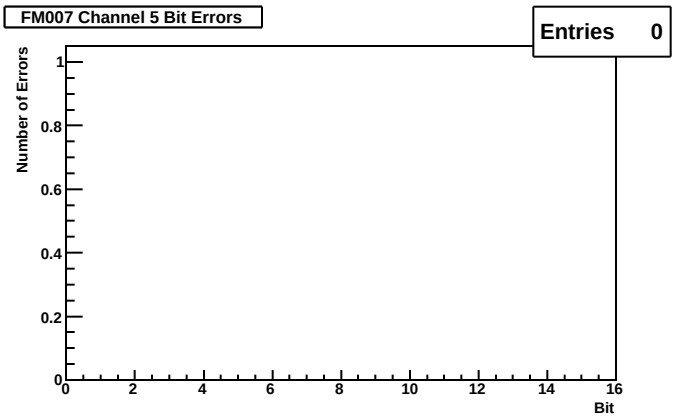

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

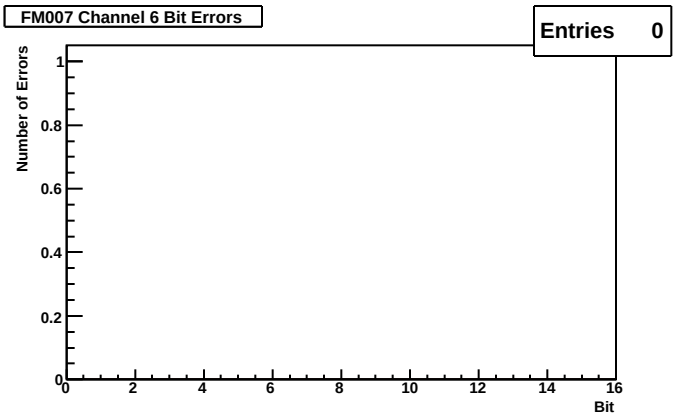

FM007 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

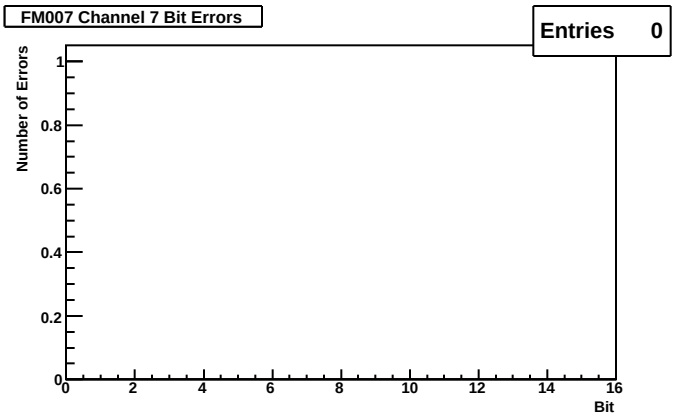

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

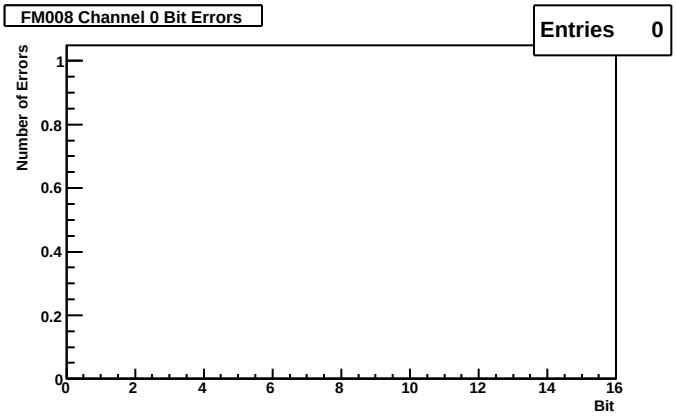

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

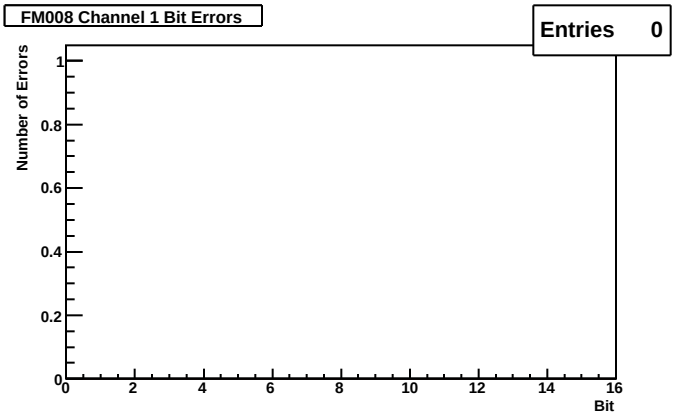

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

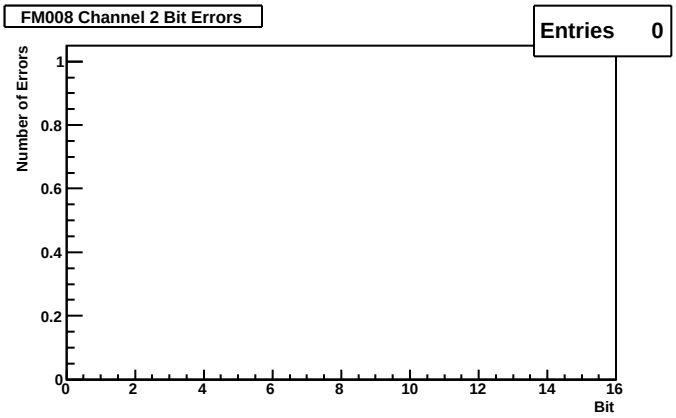

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

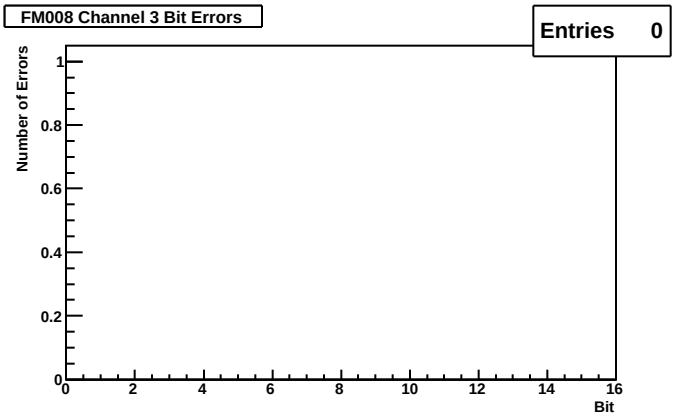

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

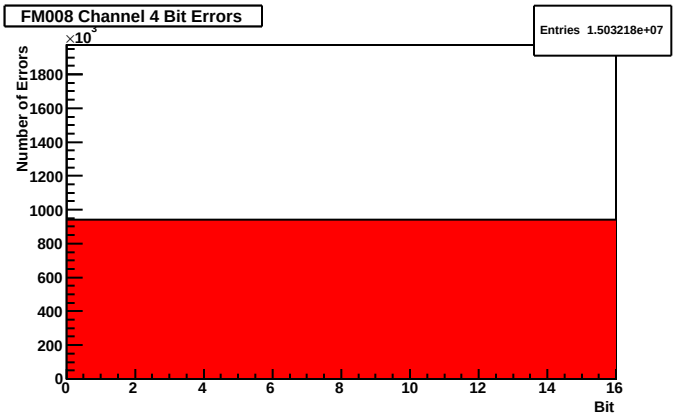

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

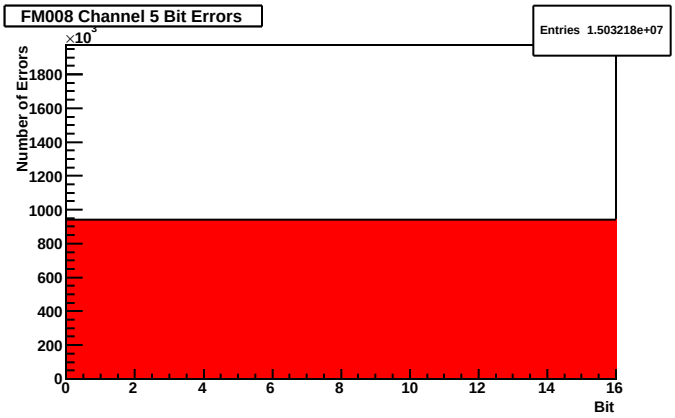

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

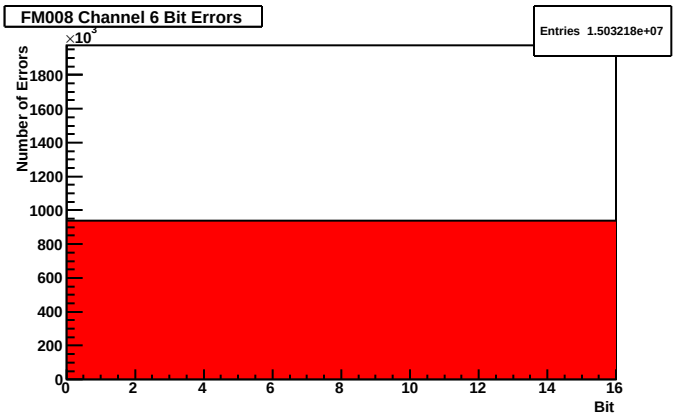

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

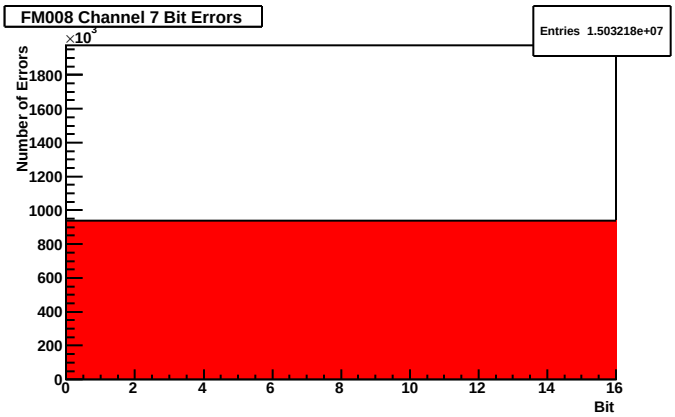

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

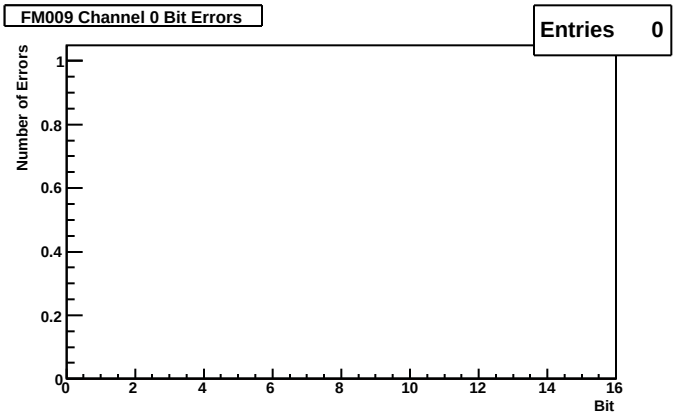

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

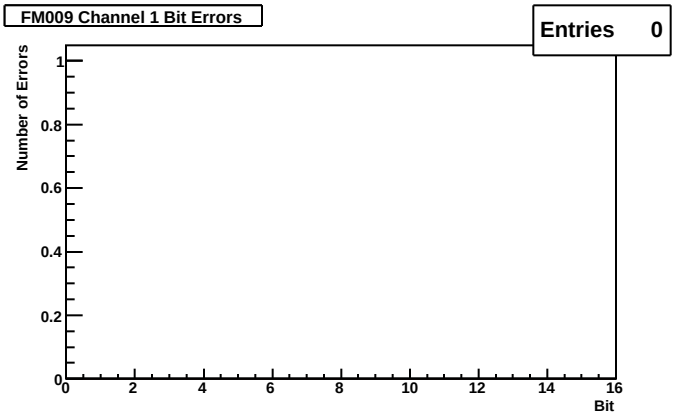

FM008 Channel 4 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

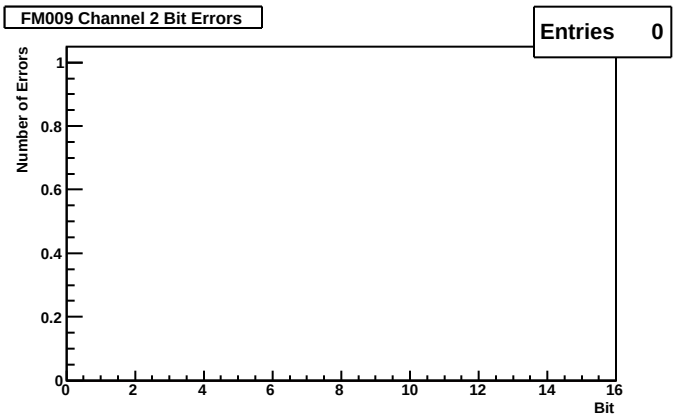

FM008 Channel 5 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

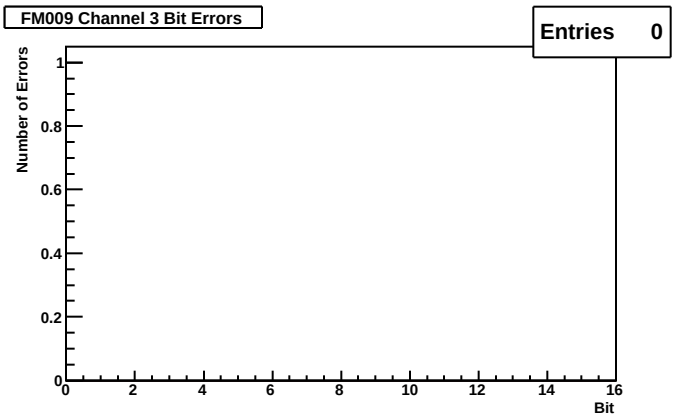

FM008 Channel 6 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

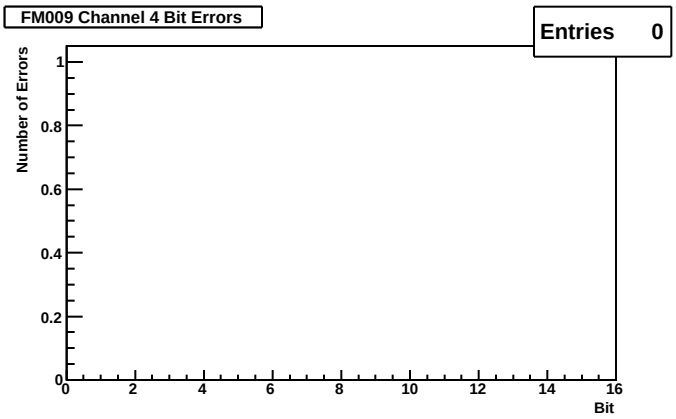

FM008 Channel 7 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

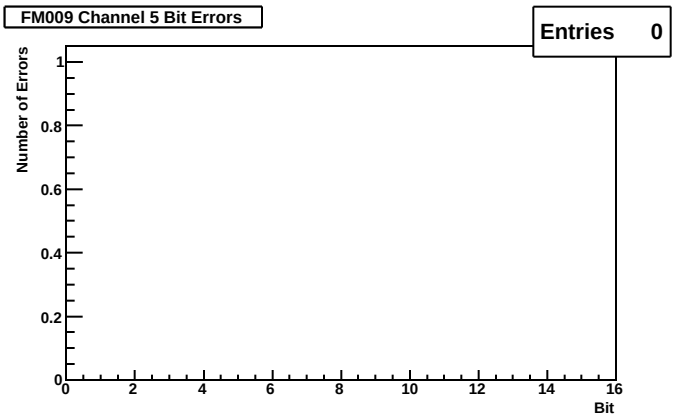

FM009 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

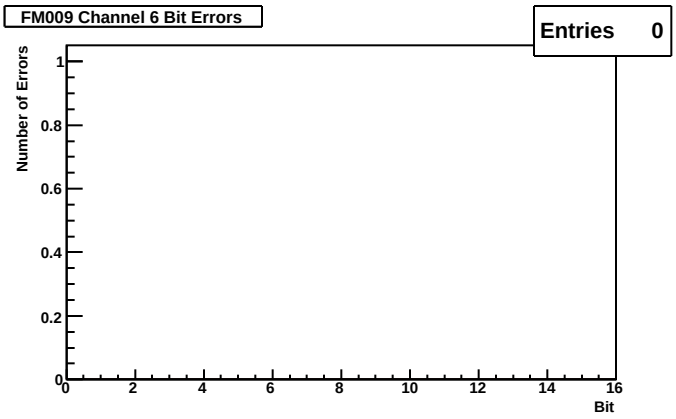

FM009 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

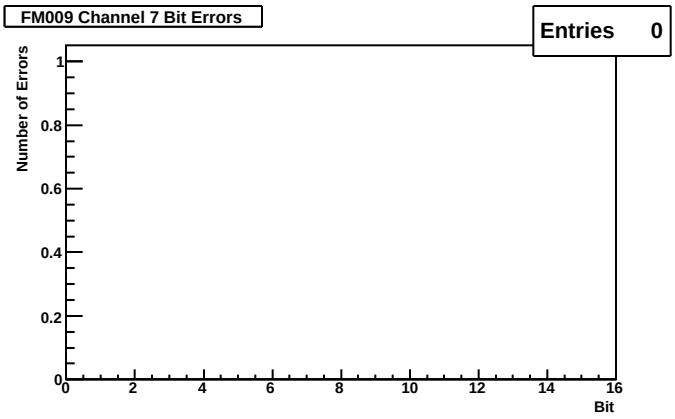

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

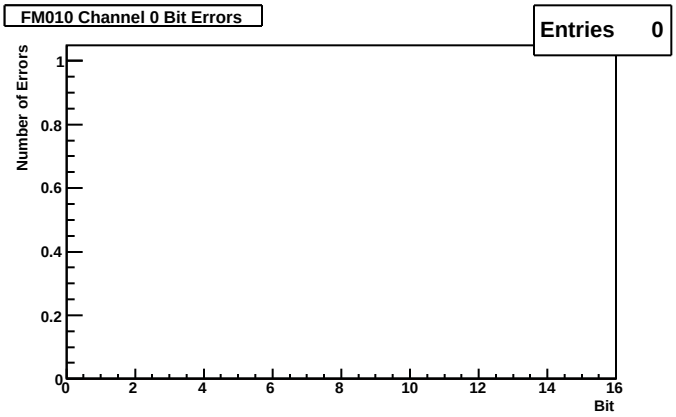

FM009 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

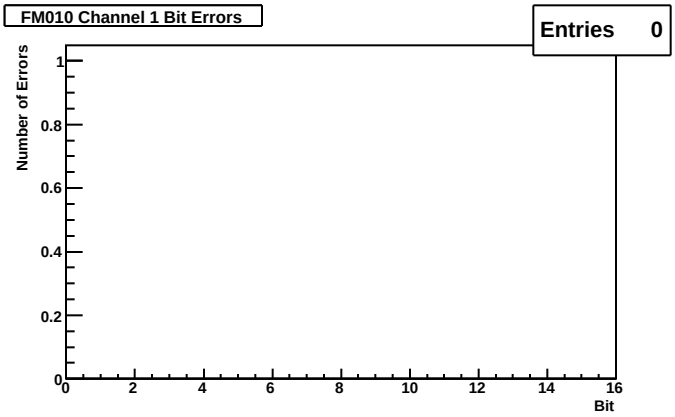

FM009 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

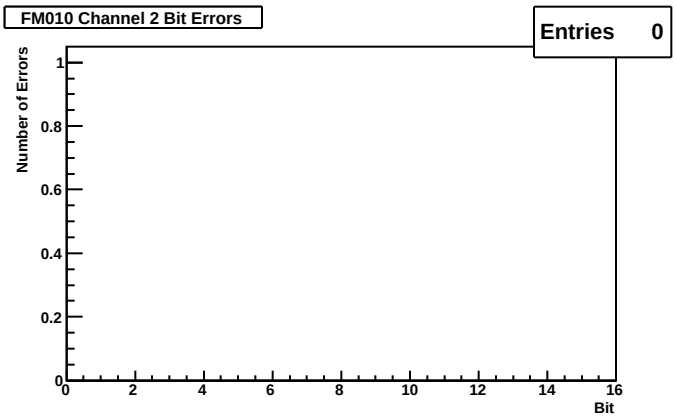

FM009 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

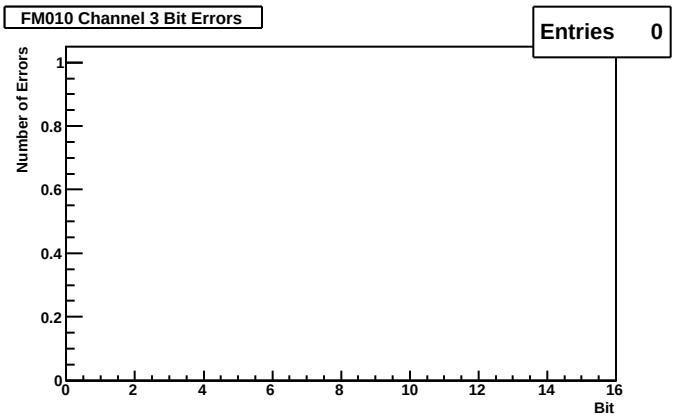

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

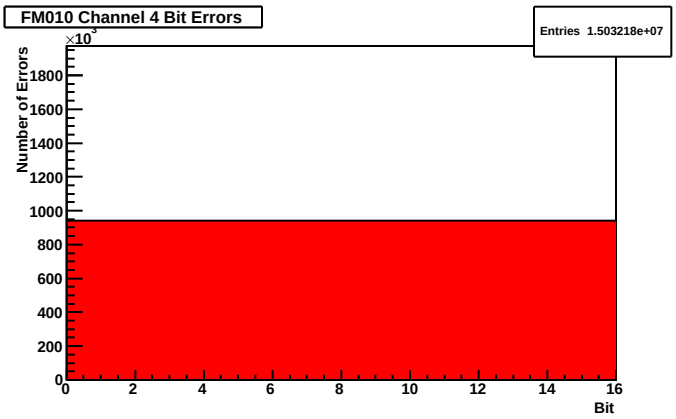

FM009 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

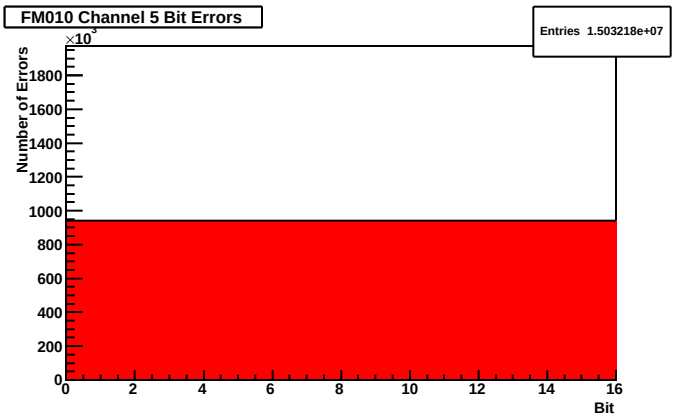

FM010 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

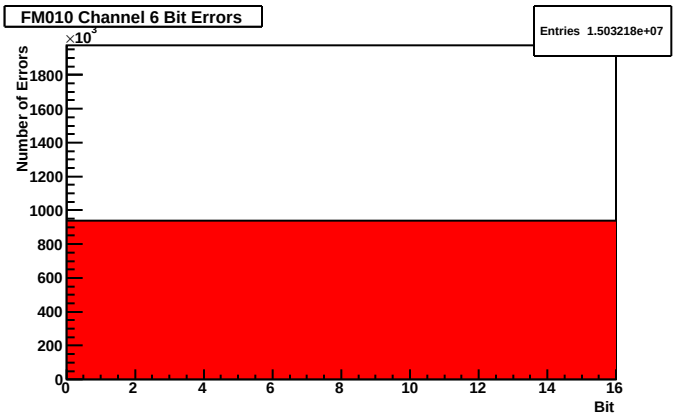

FM010 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

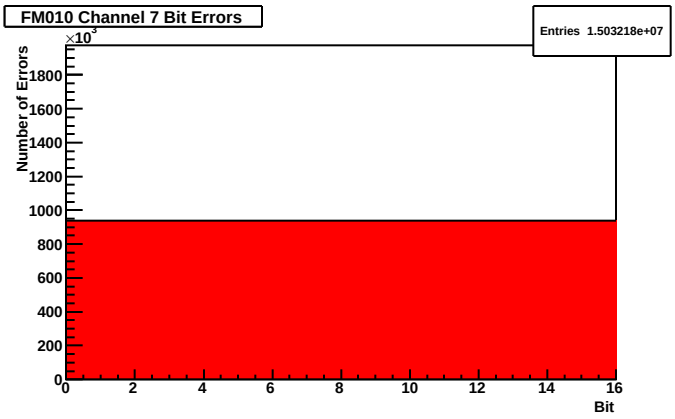

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

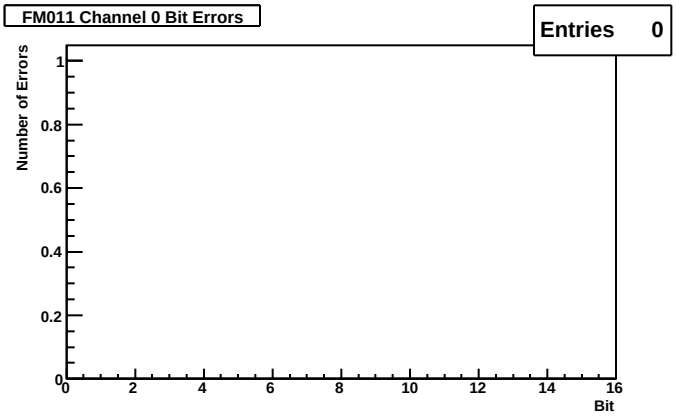

FM010 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

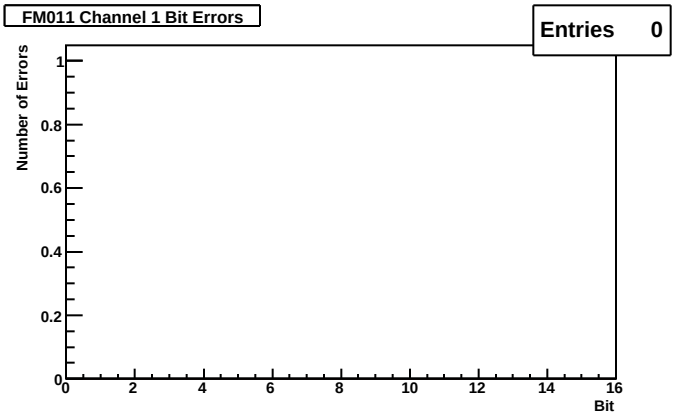

FM010 Channel 4 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit

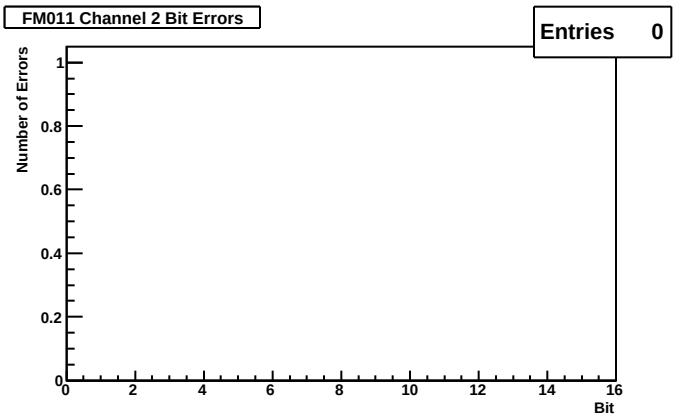

FM010 Channel 5 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit


FM010 Channel 6 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit


FM010 Channel 7 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit


FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit


FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit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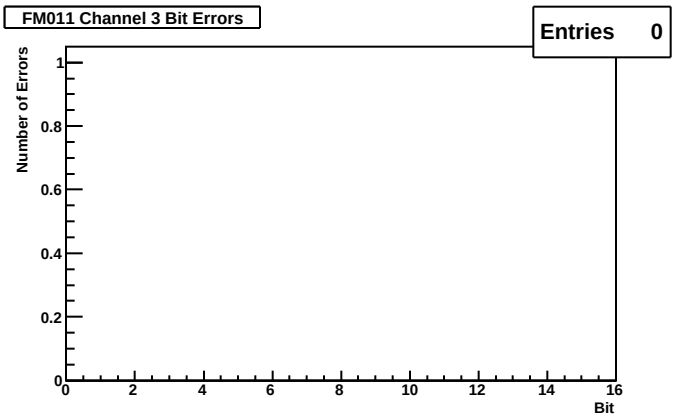
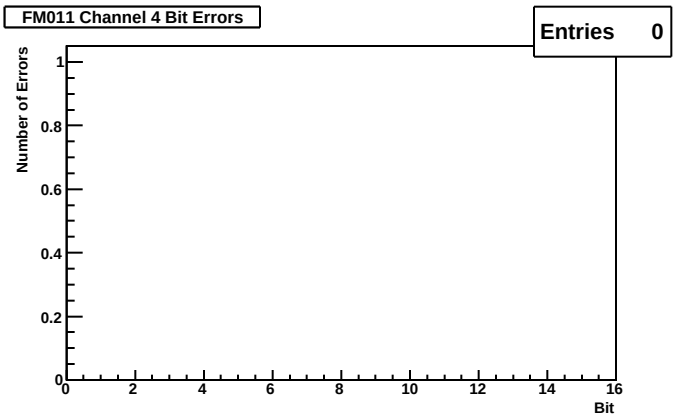
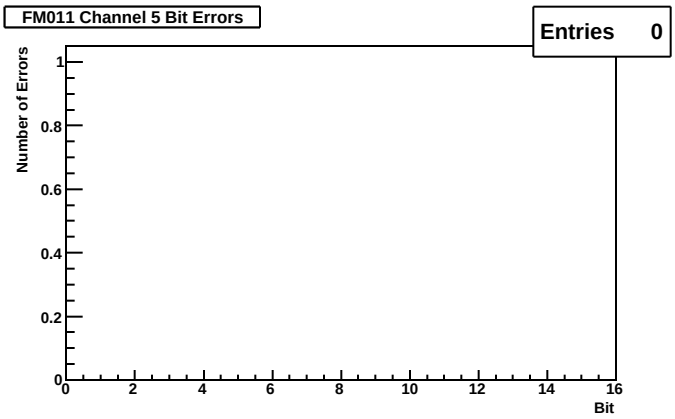
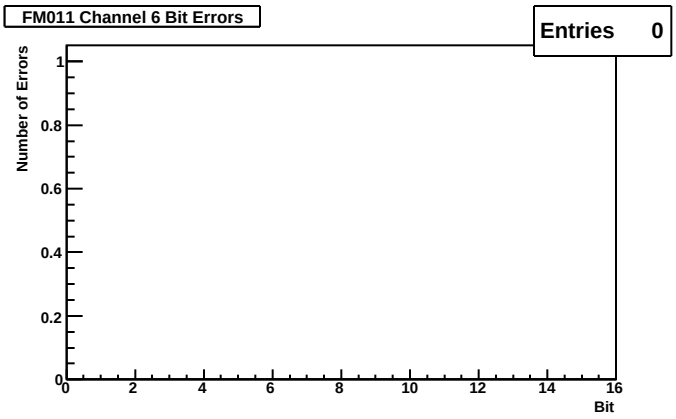
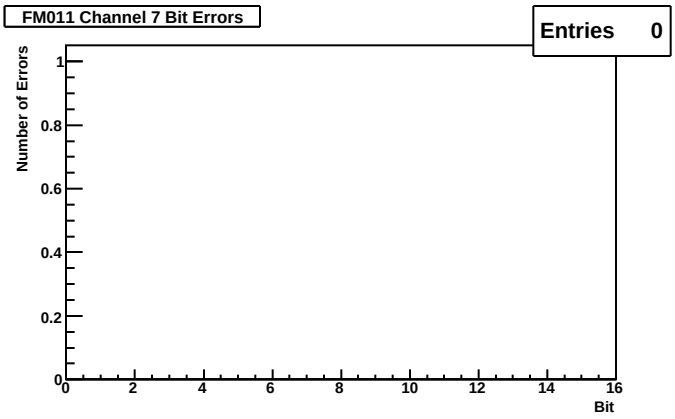
Entries 0
Number of Errors
Bit

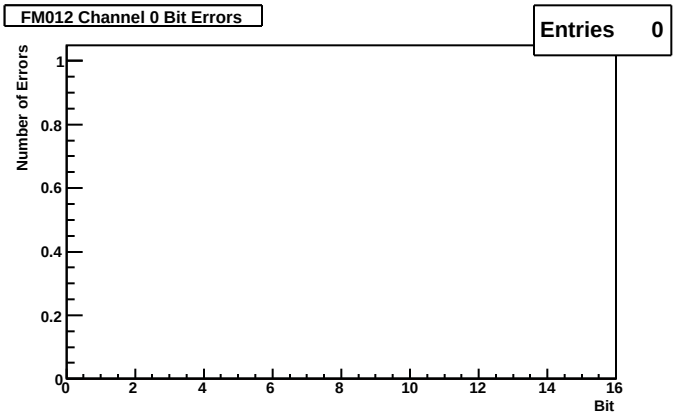

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

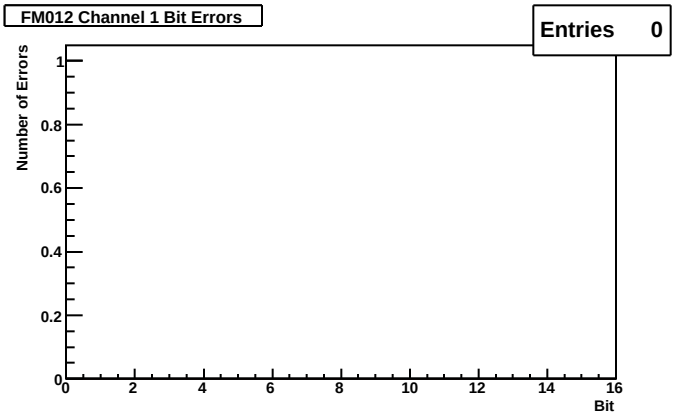

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

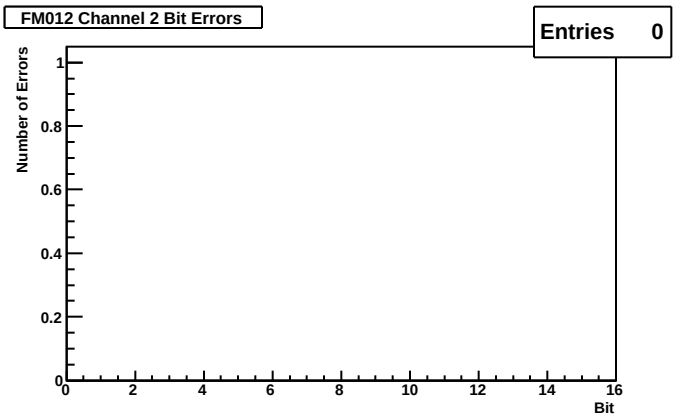

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

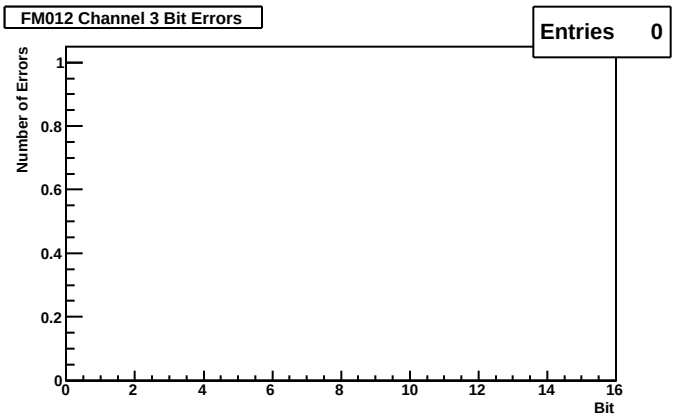

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

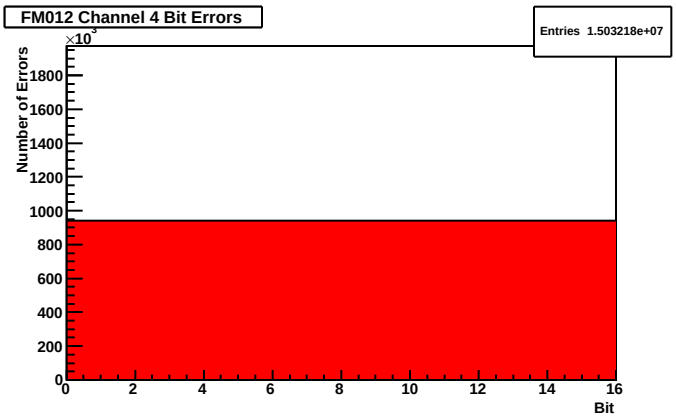

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

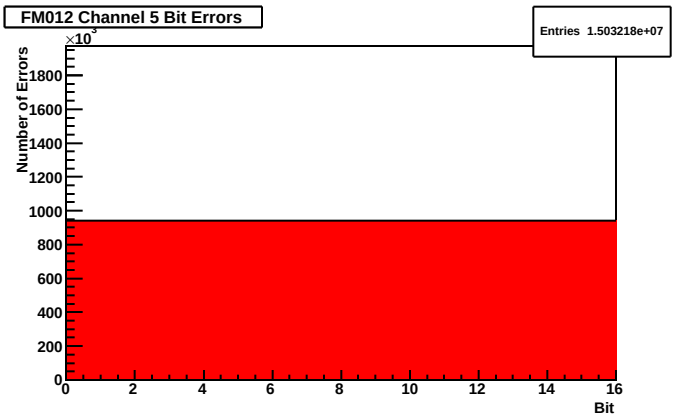

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

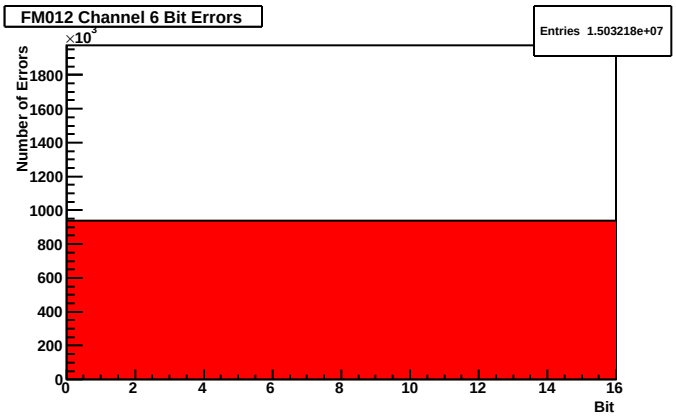

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

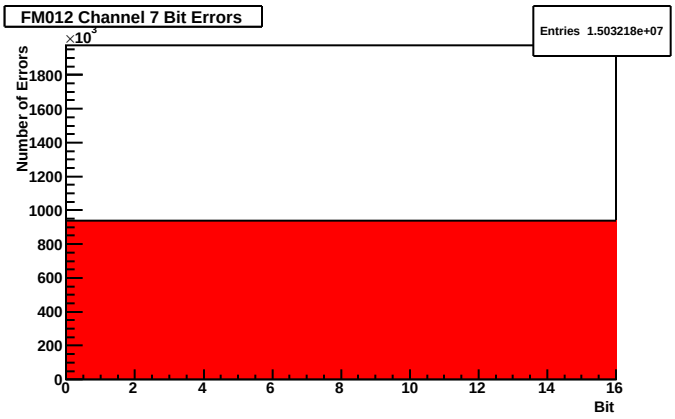

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 1.503218e+07
Number of Errors
Bit