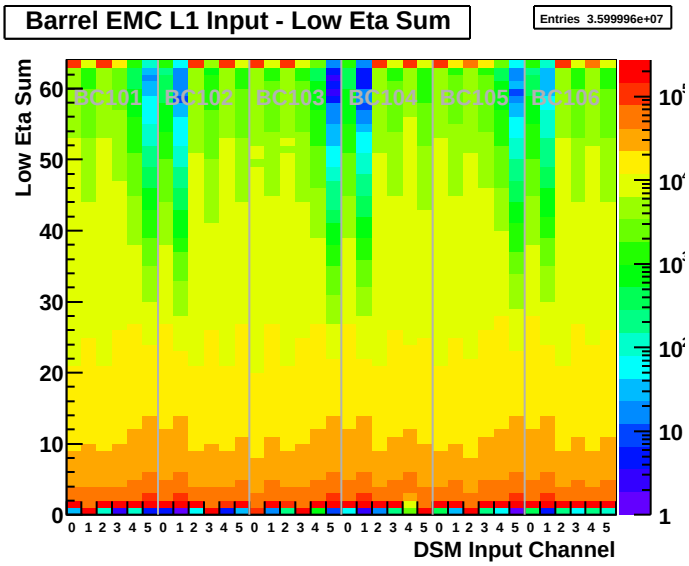
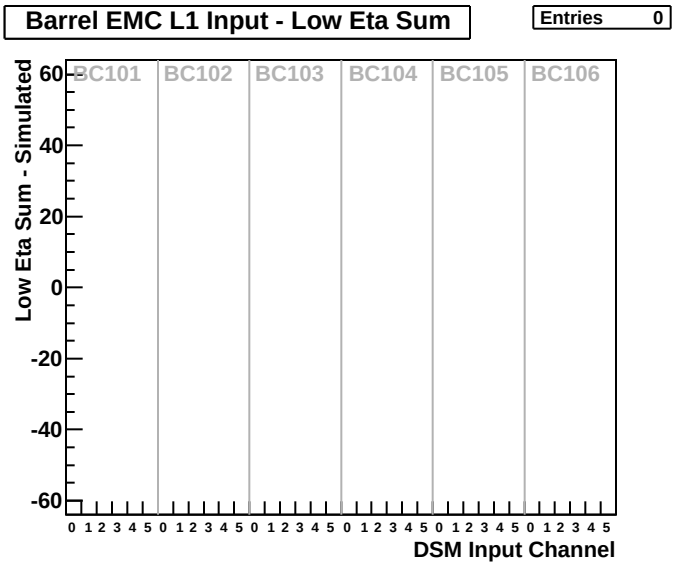
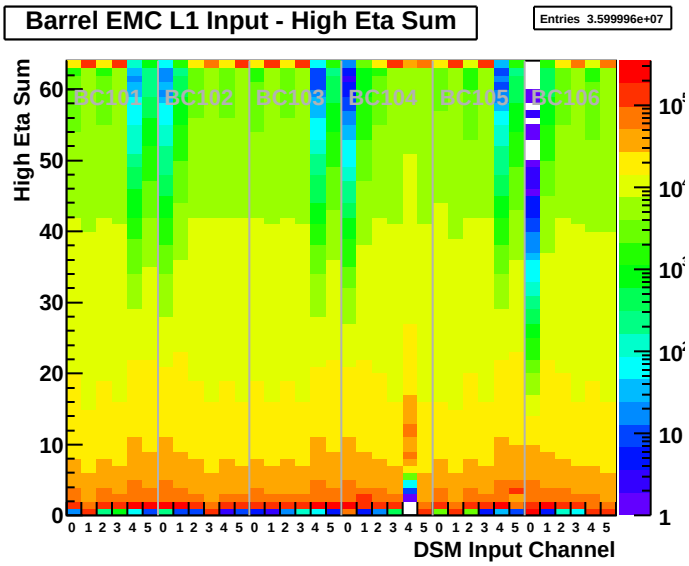
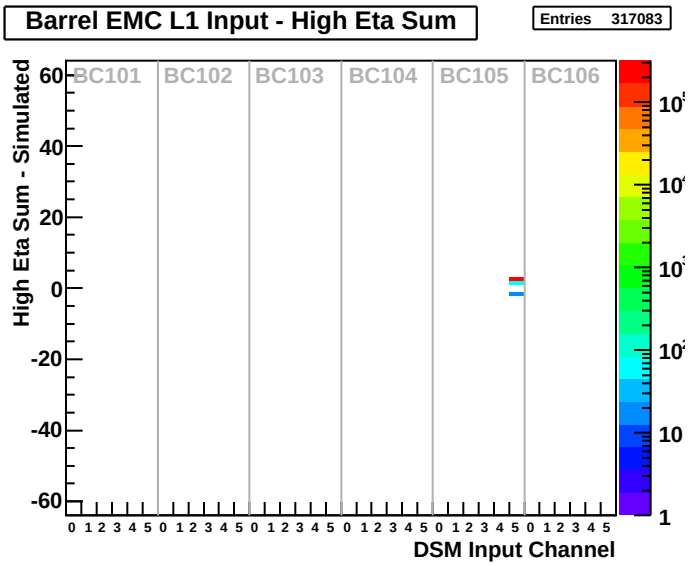
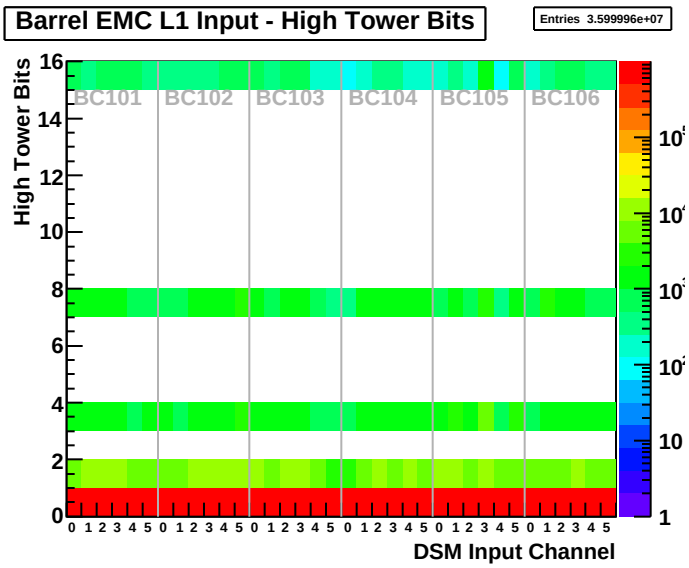
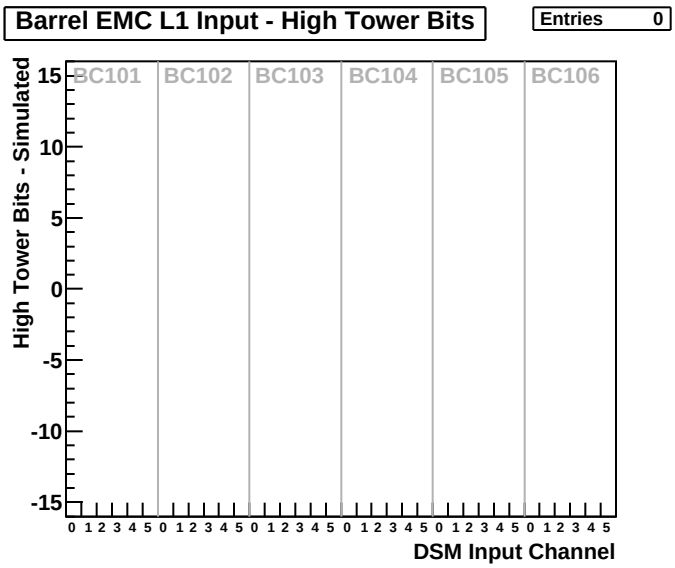

Barrel EMC L1 Input - Low Eta Sum
Entries 3.599996e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

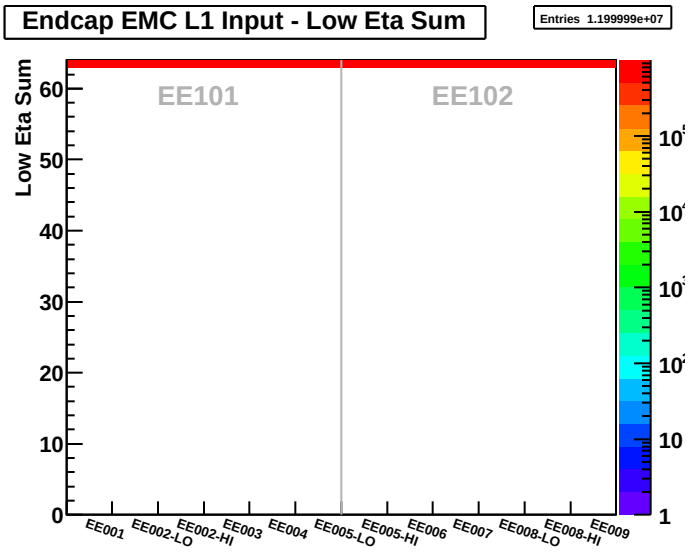

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

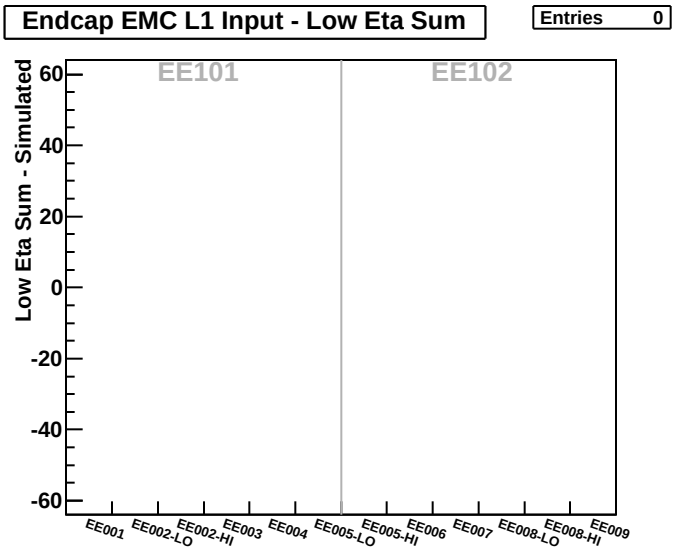

Barrel EMC L1 Input - High Eta Sum
Entries 3.599996e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

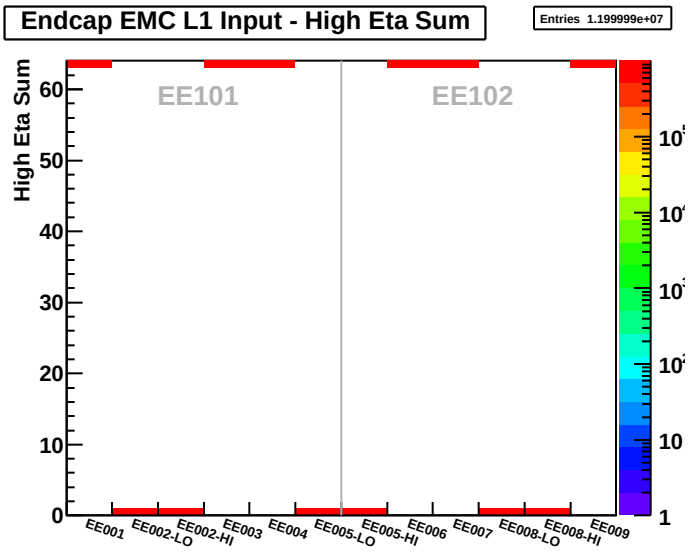

Barrel EMC L1 Input - High Eta Sum
Entries 317083
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

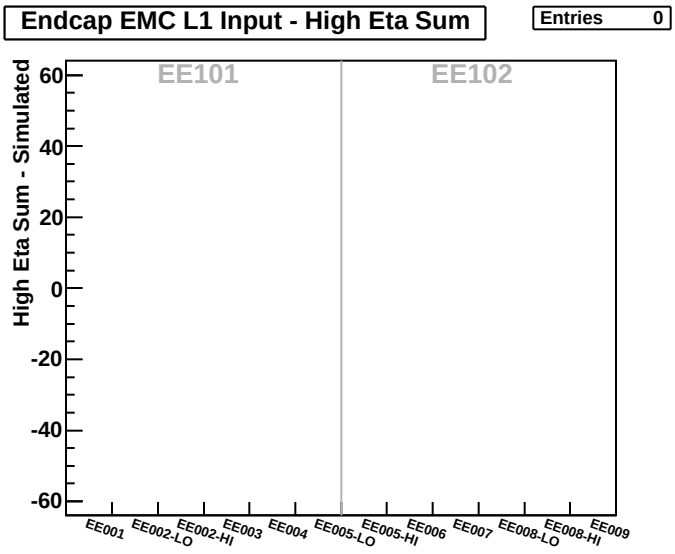

Barrel EMC L1 Input - High Tower Bits
Entries 3.599996e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

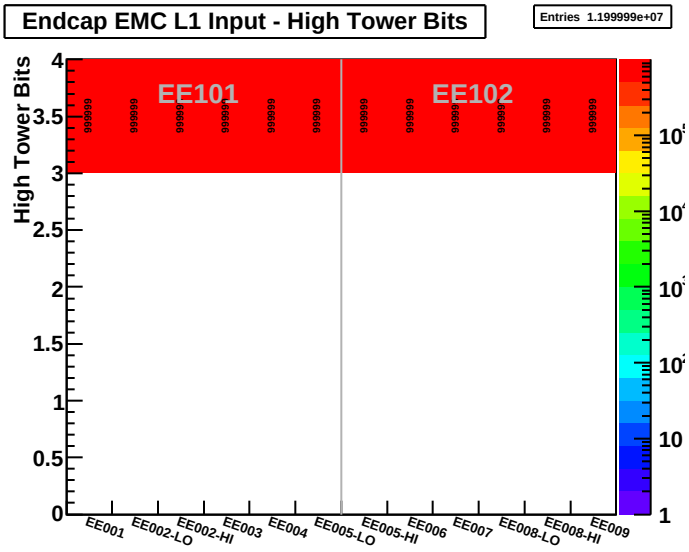

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

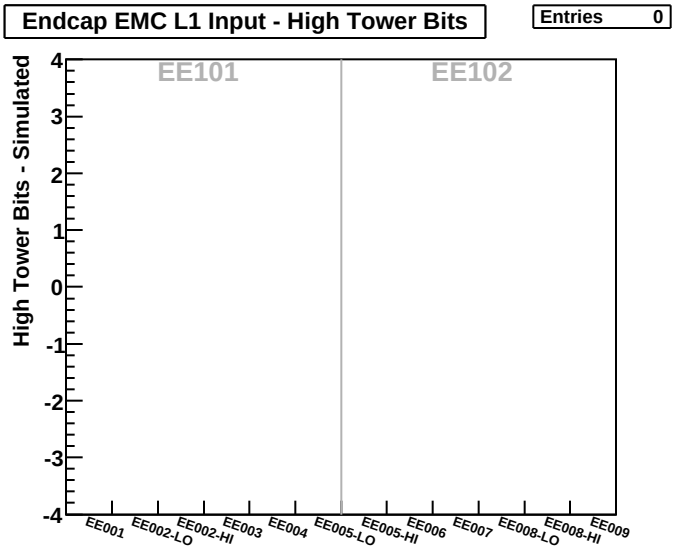

Endcap EMC L1 Input - Low Eta Sum
Entries 1.199999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

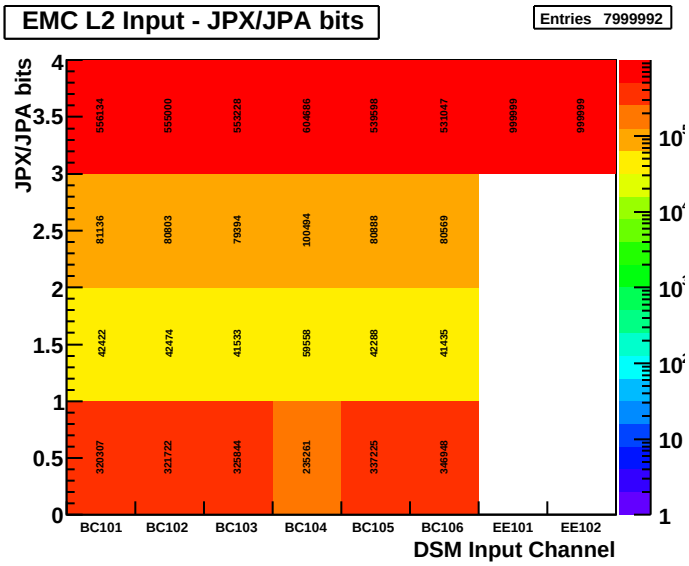

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

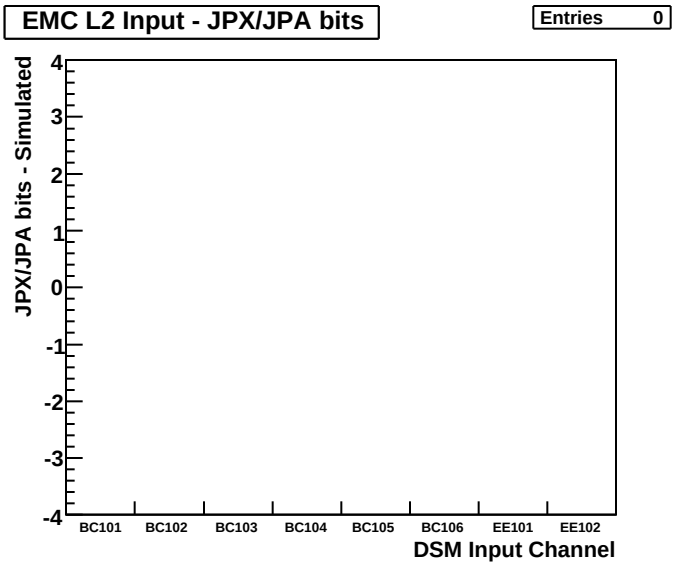

Endcap EMC L1 Input - High Eta Sum
Entries 1.199999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

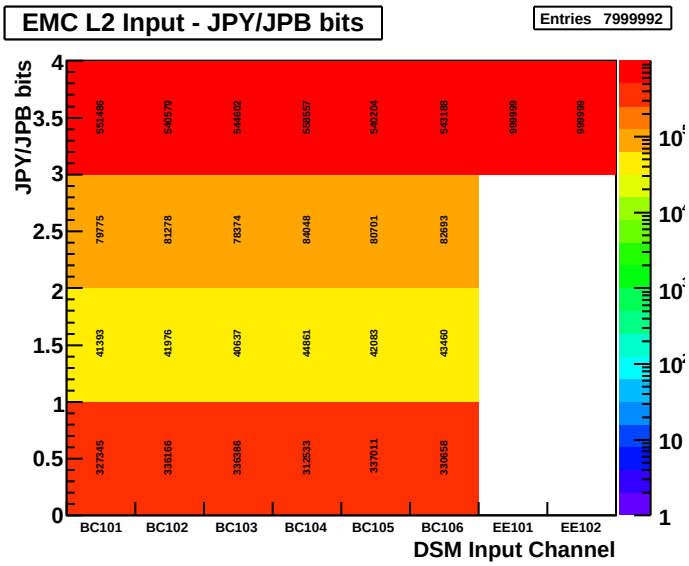

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

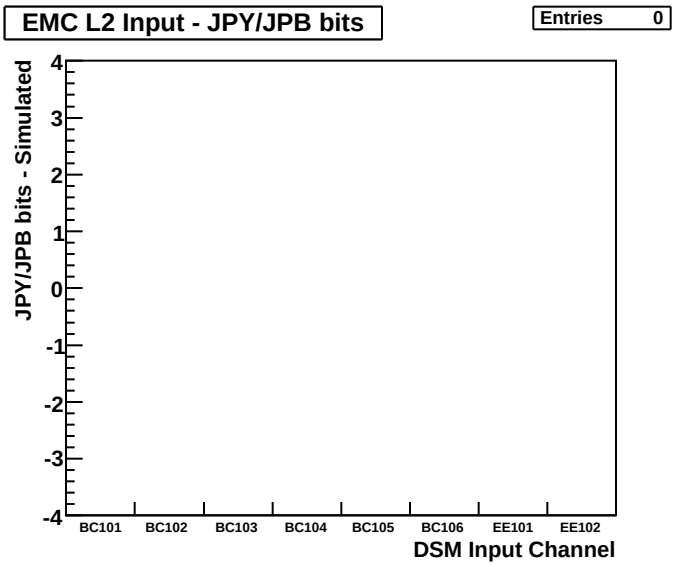

Endcap EMC L1 Input - High Tower Bits
Entries 1.199999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

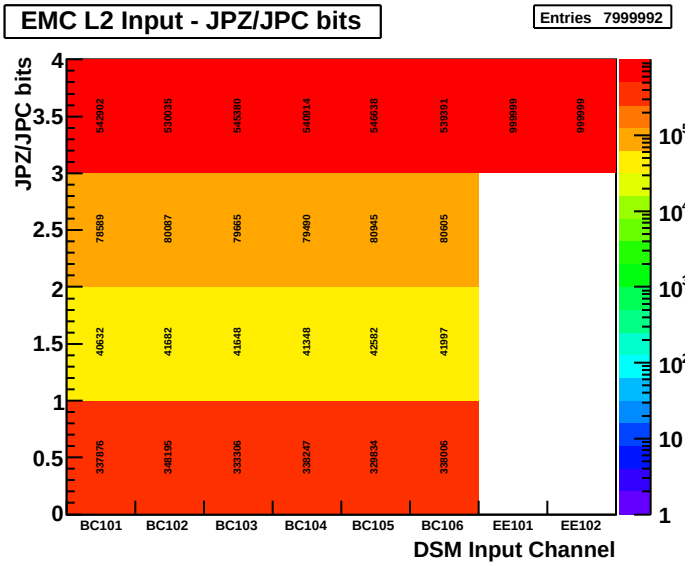

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

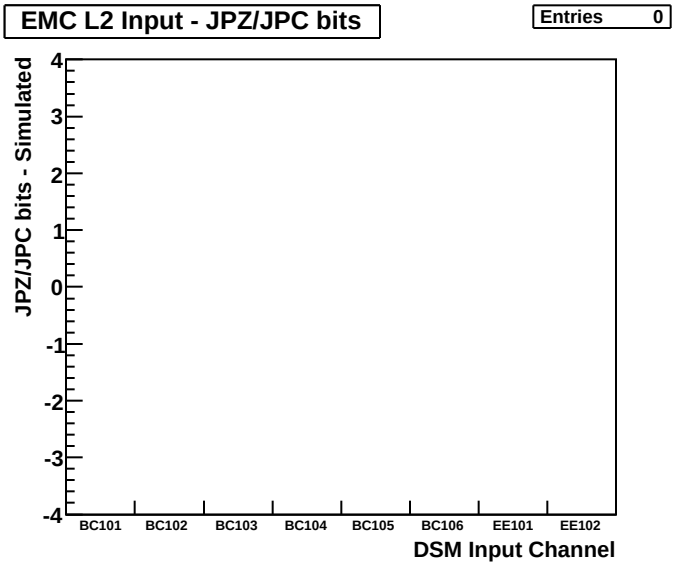

EMC L2 Input - JPX/JPA bits
Entries 7999992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
556134 555000 553228 604686 539598 531047 999999 999999
81136 80803 78394 100494 80888 80569
42422 42474 41533 59558 42288 41435
320307 321722 325844 235261 337225 346948

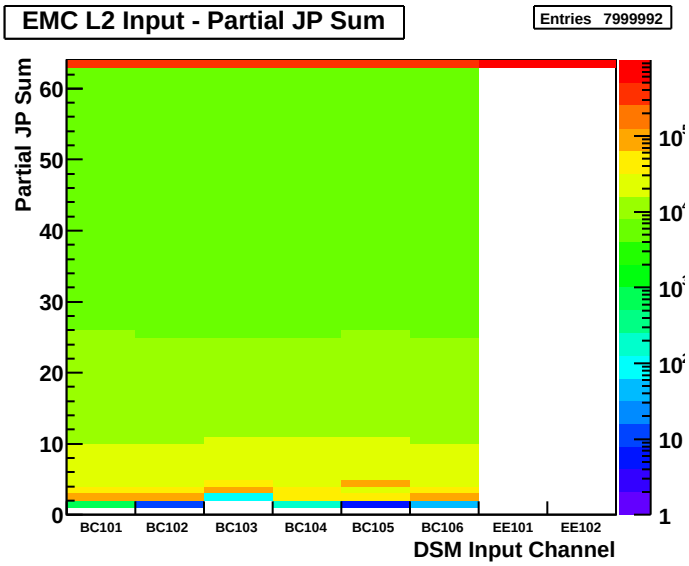

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

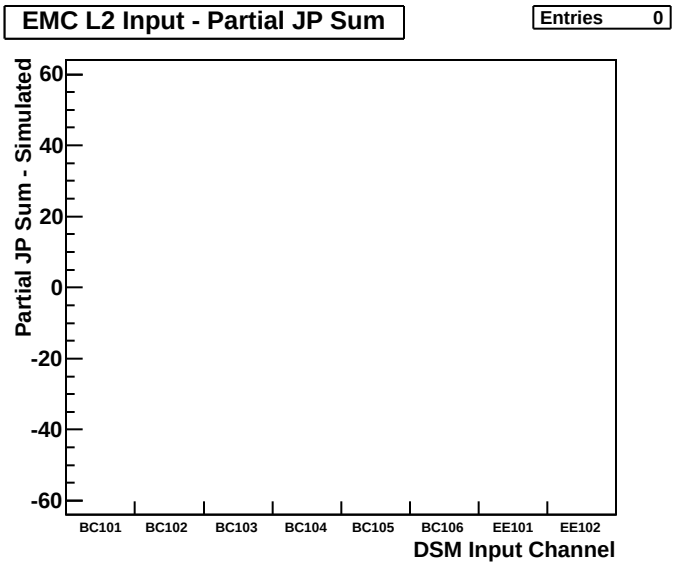

EMC L2 Input - JPY/JPB bits
Entries 7999992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
551486 540579 544602 558557 540204 543188 999999 999999
79775 81278 78374 84048 80701 82693
41393 41976 40637 44861 42083 43460
327345 336166 336386 312533 337011 330658

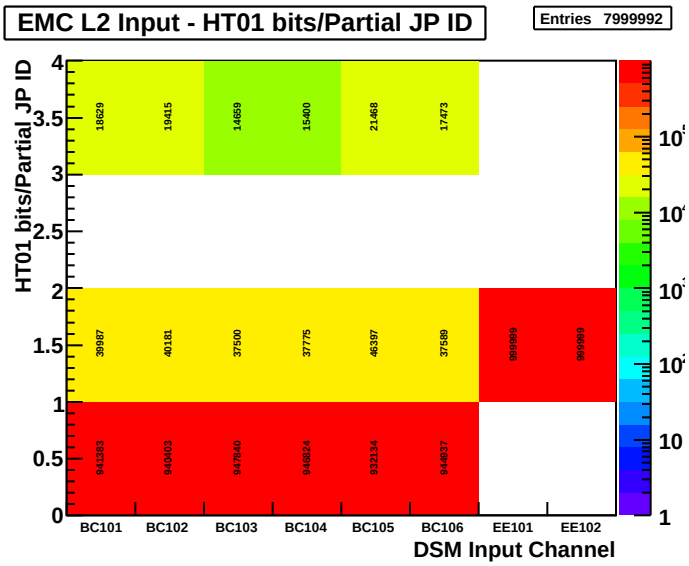

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

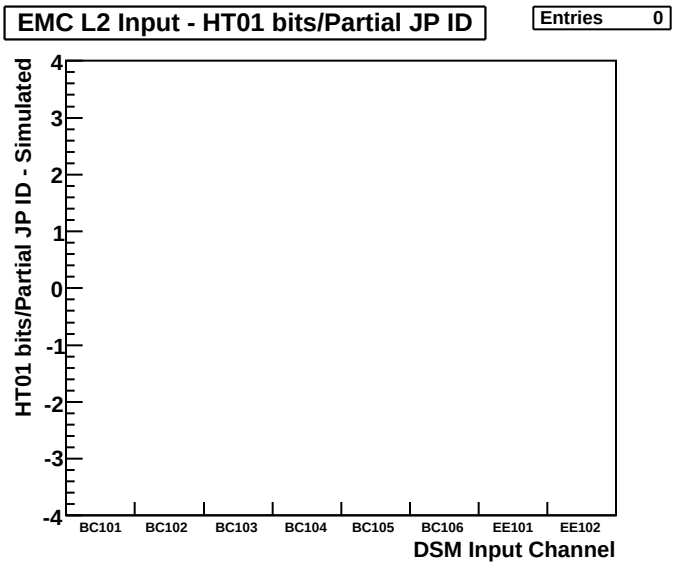

EMC L2 Input - JPZ/JPC bits
Entries 7999992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
542902 530035 545380 540914 546638 539391 999999 999999
78589 80087 79665 79490 80945 80605
40632 41682 41648 41348 42582 41997
337876 348195 333306 338247 329834 338006

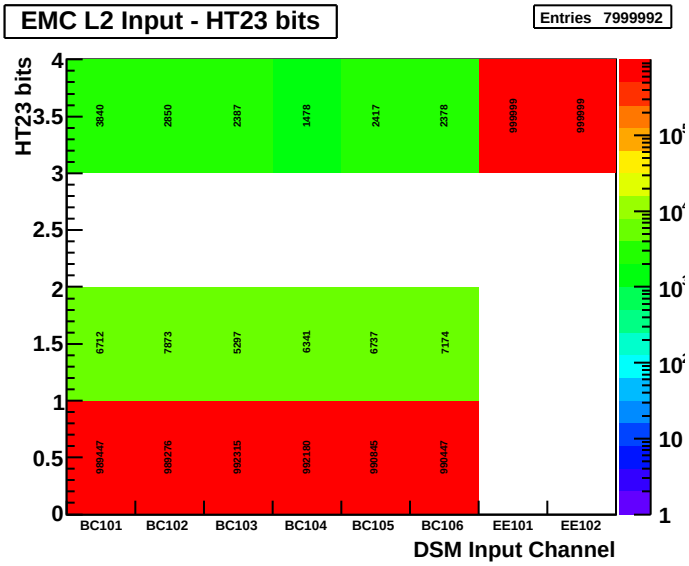

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

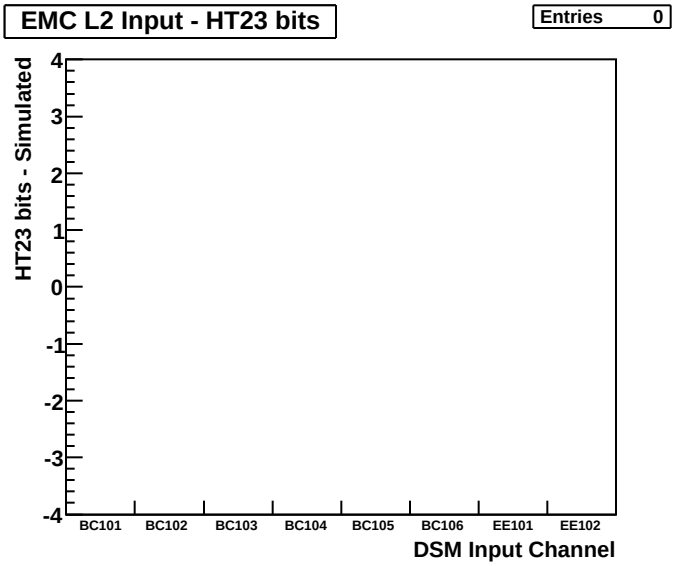

EMC L2 Input - Partial JP Sum
Entries 7999992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

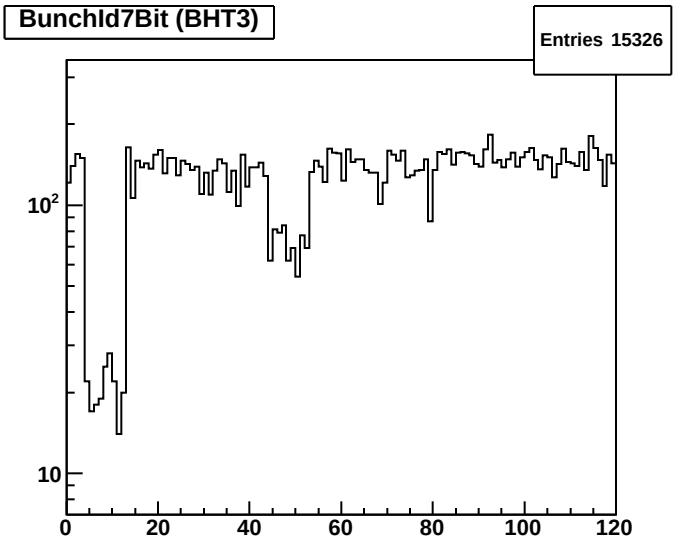

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

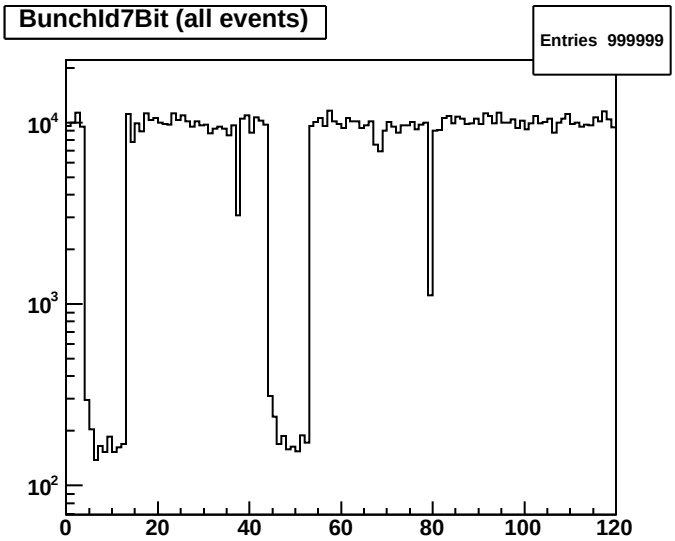

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7999992
HT01 bits/Partial JP ID
18629 19415 14659 15400 21468 17473
39987 40181 37500 37775 46397 37589 999999 999999
941383 940403 947040 946824 932134 944937
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

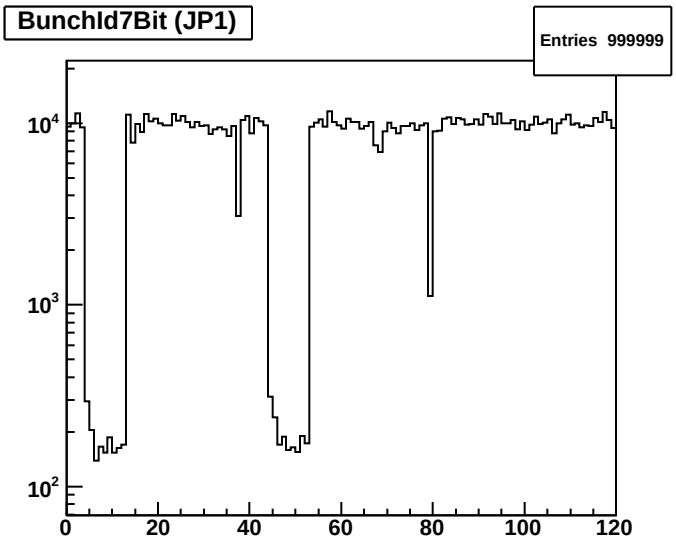

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

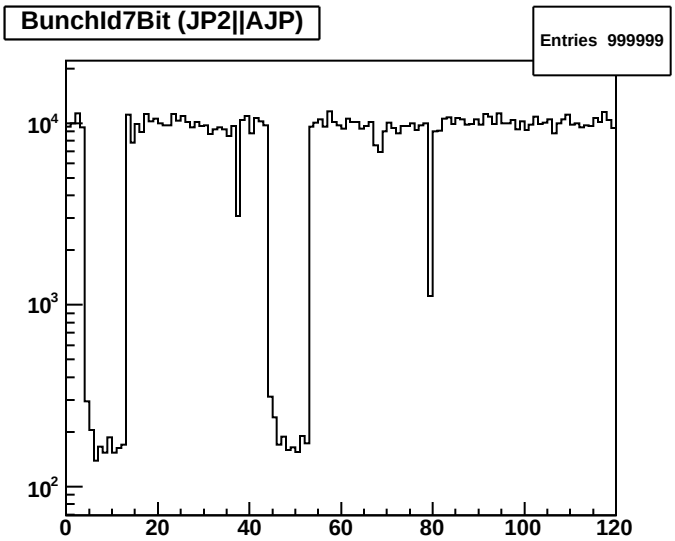

EMC L2 Input - HT23 bits
Entries 7999992
HT23 bits
3840 2950 2387 1478 2417 2378 999999 999999
6712 7673 5297 6341 6737 7174
989447 989276 992315 992180 990945 990447
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

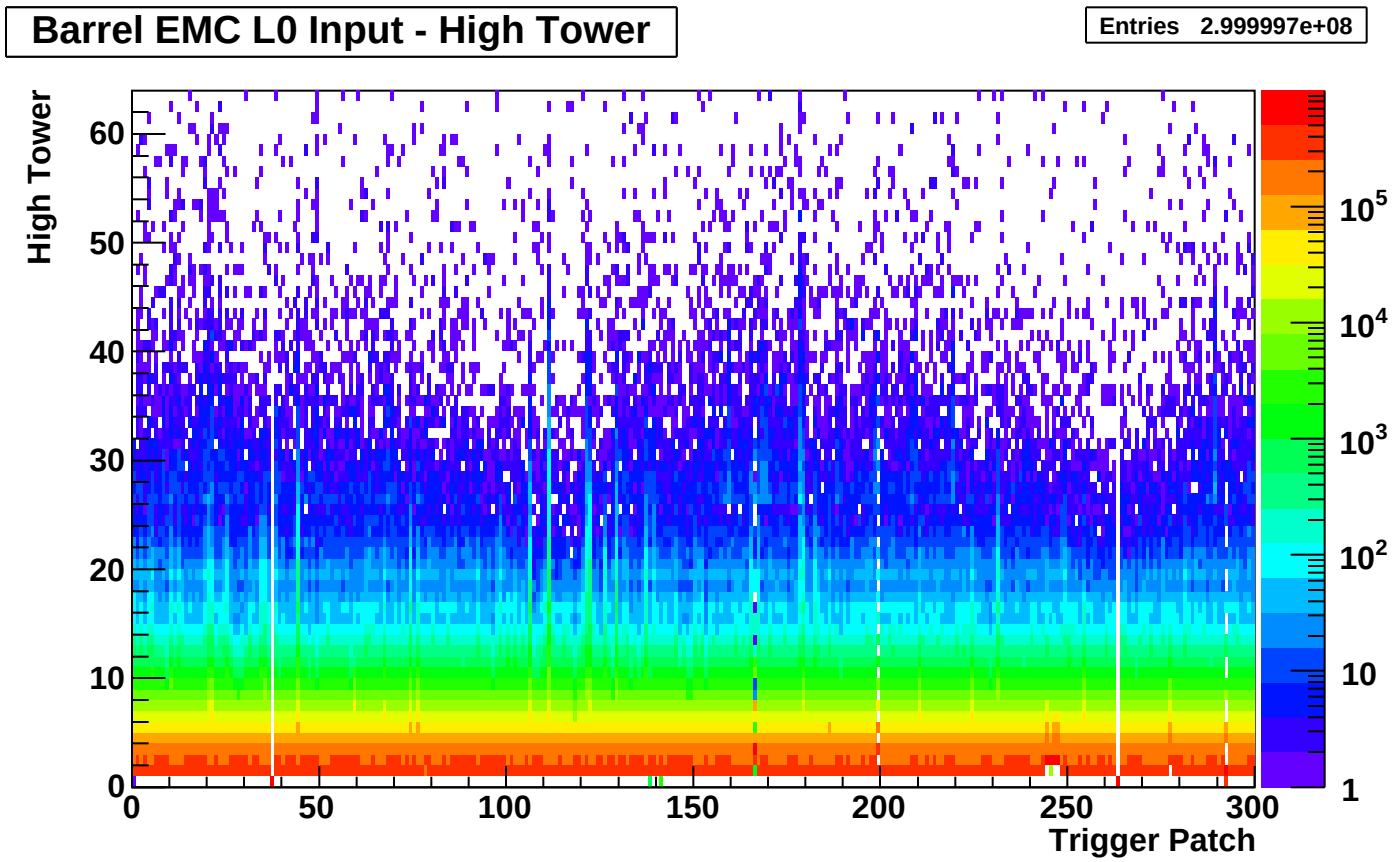

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

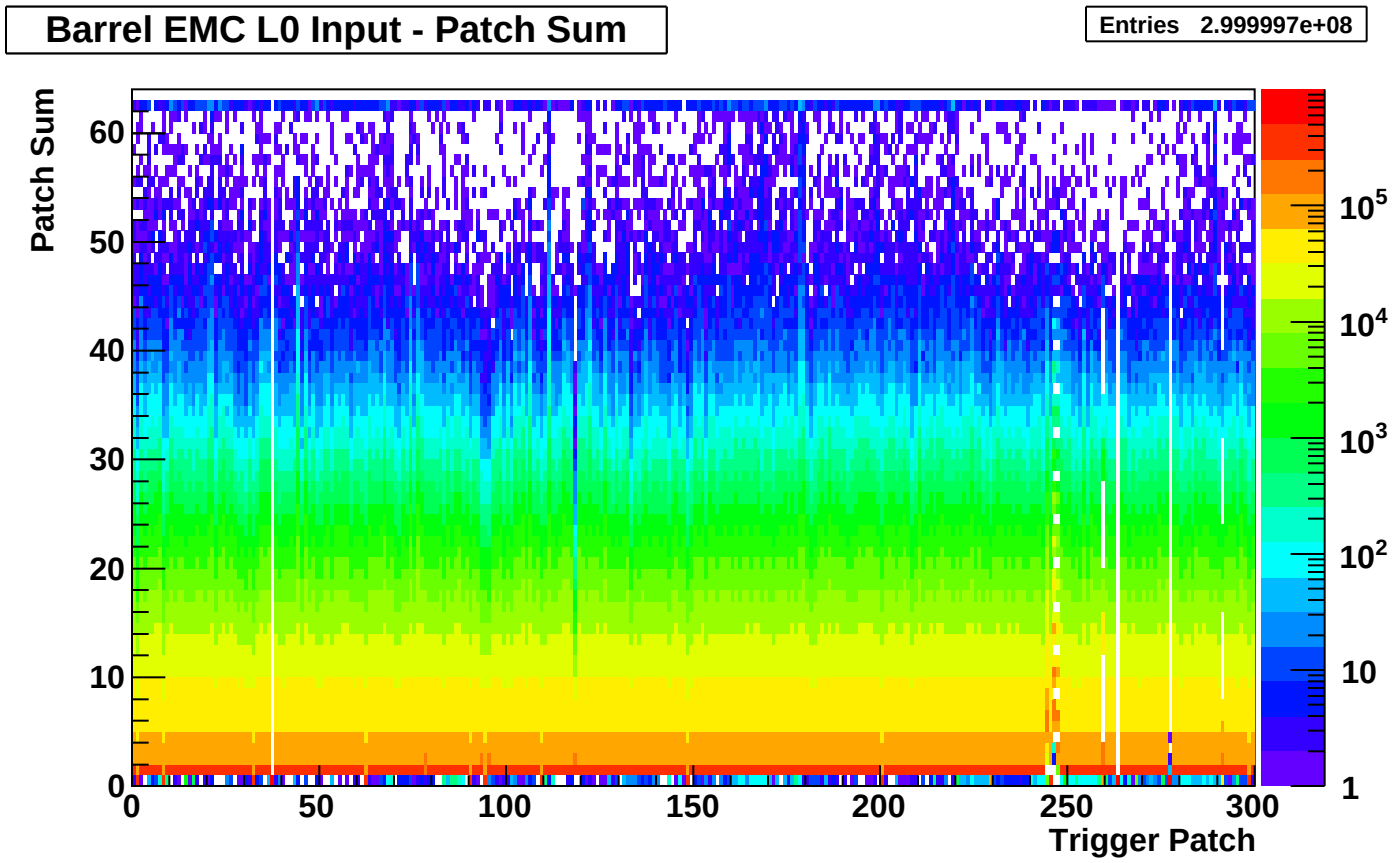

BunchId7Bit (BHT3)
Entries 15326
10^2
10
0
20
40
60
80
100
120

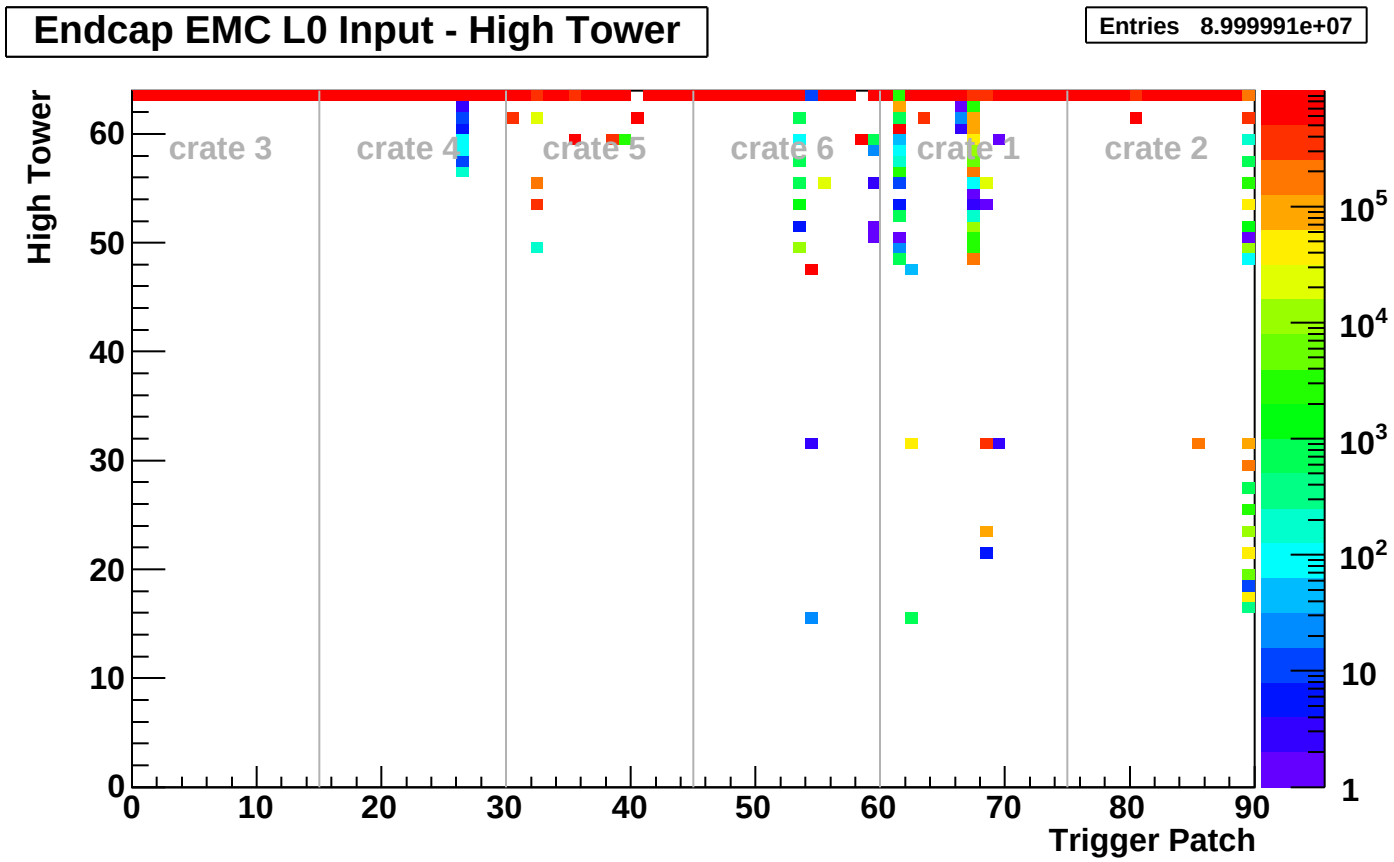

BunchId7Bit (all events)
Entries 999999
10^4
10^3
10^2
0
20
40
60
80
100
120

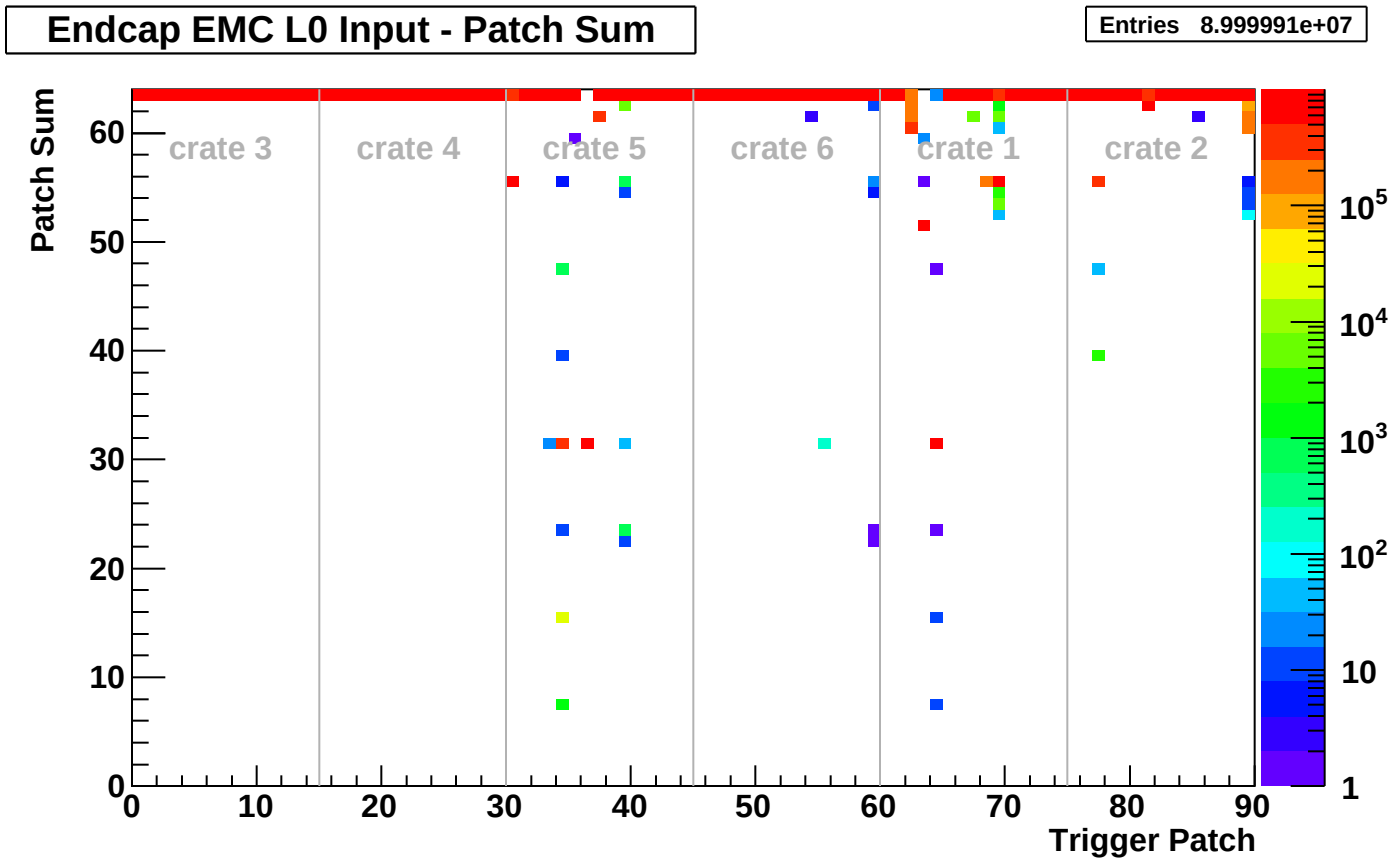

BunchId7Bit (JP1)
Entries 999999
10^4
10^3
10^2
0
20
40
60
80
100
120

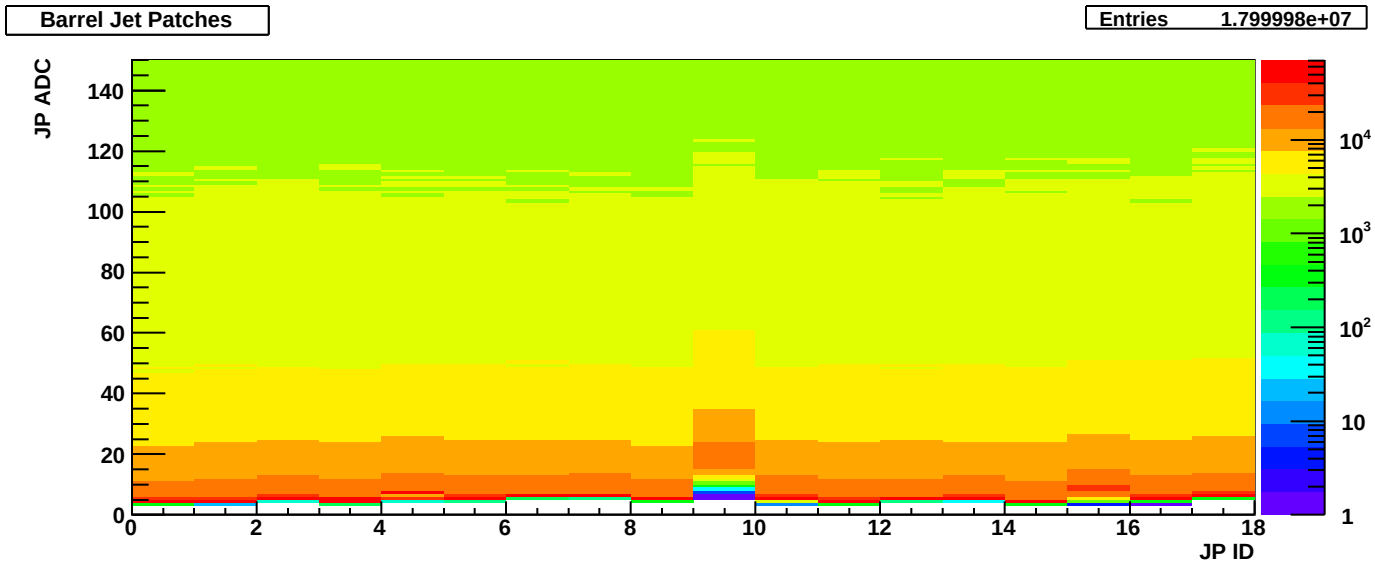

BunchId7Bit (JP2||AJP)
Entries 999999
10^4
10^3
10^2
0
20
40
60
80
100
120

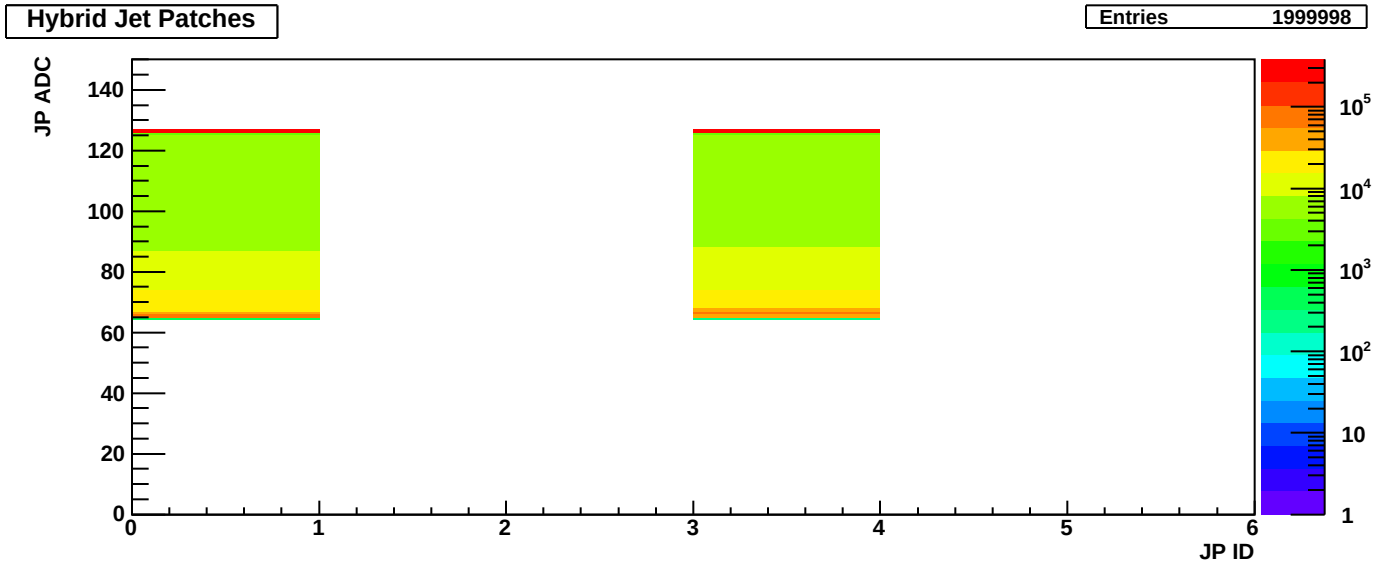

Barrel EMC L0 Input - High Tower
Entries 2.999997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

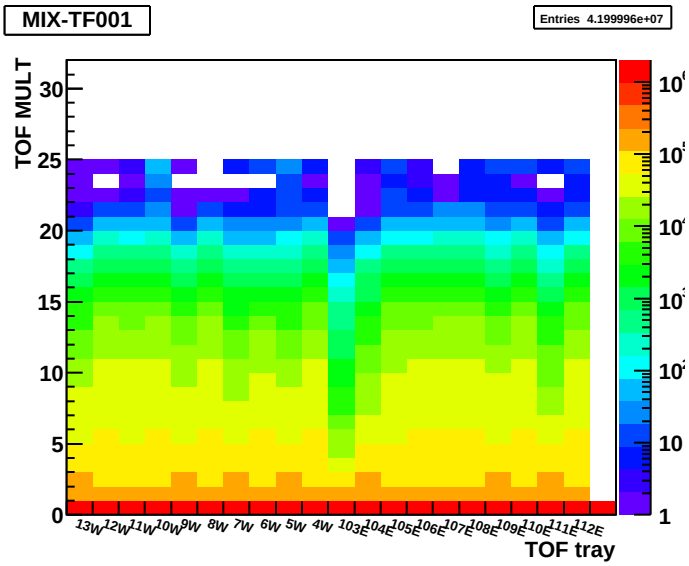

Barrel EMC L0 Input - Patch Sum
Entries 2.999997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

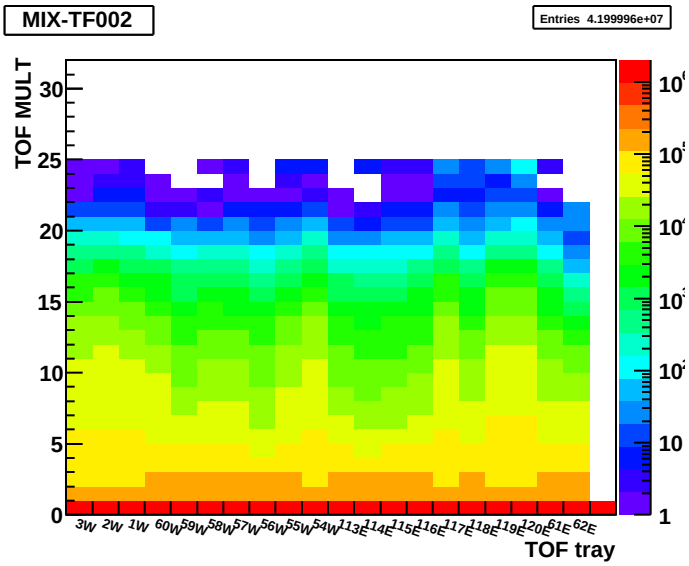

Endcap EMC L0 Input - High Tower
Entries 8.999991e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

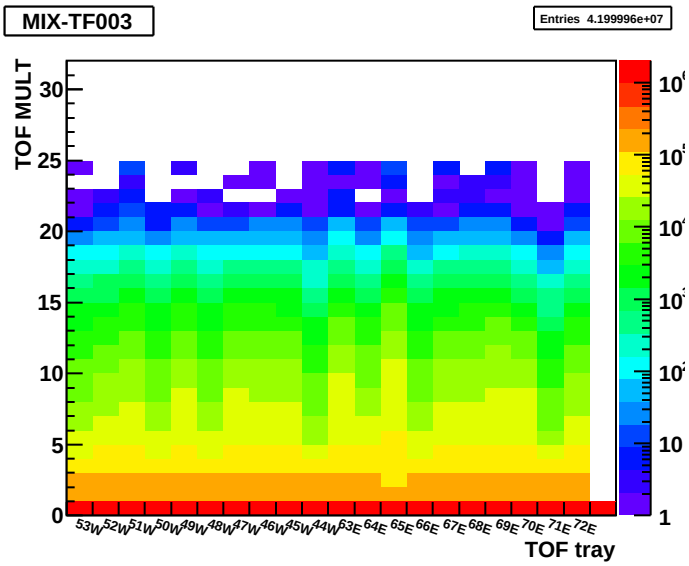

Endcap EMC L0 Input - Patch Sum
Entries 8.999991e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

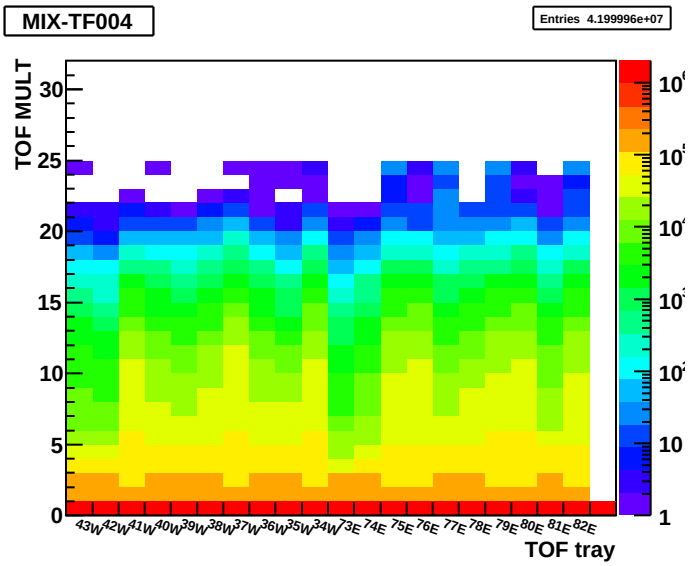

Barrel Jet Patches
Entries 1.799998e+07
JP ADC
JP ID

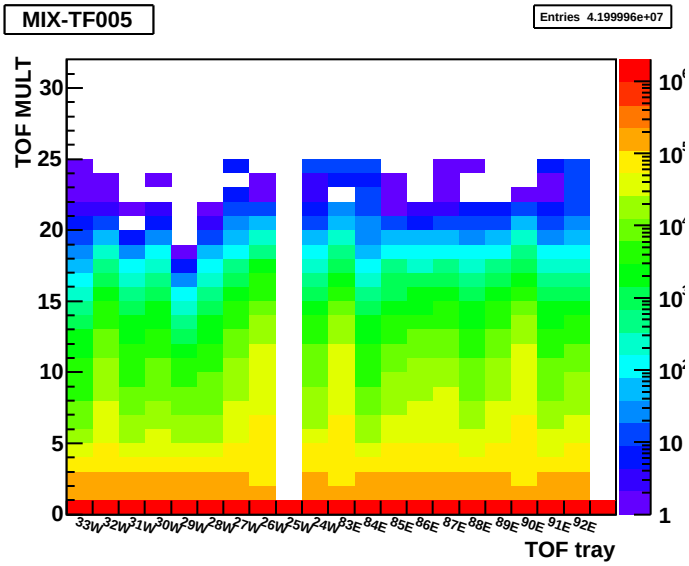

Hybrid Jet Patches
Entries 1999998
JP ADC
JP ID

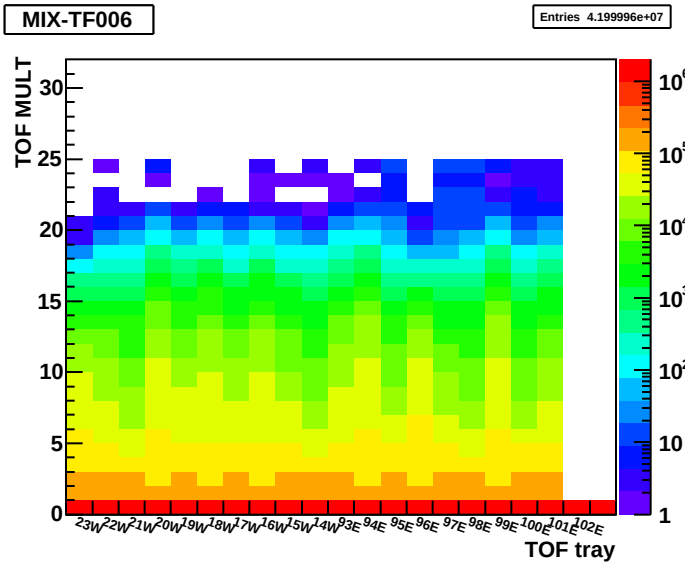

MIX-TF001
Entries 4.199996e+07
TOF MULT
TOF tray

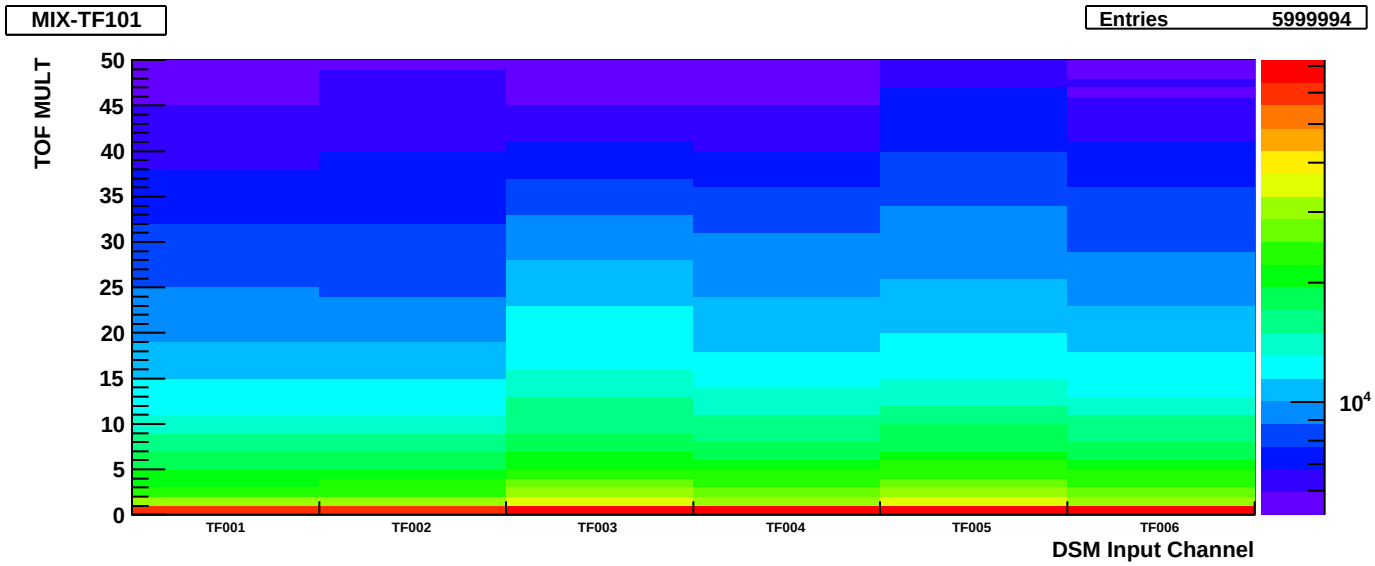

MIX-TF002
Entries 4.199996e+07
TOF MULT
TOF tray

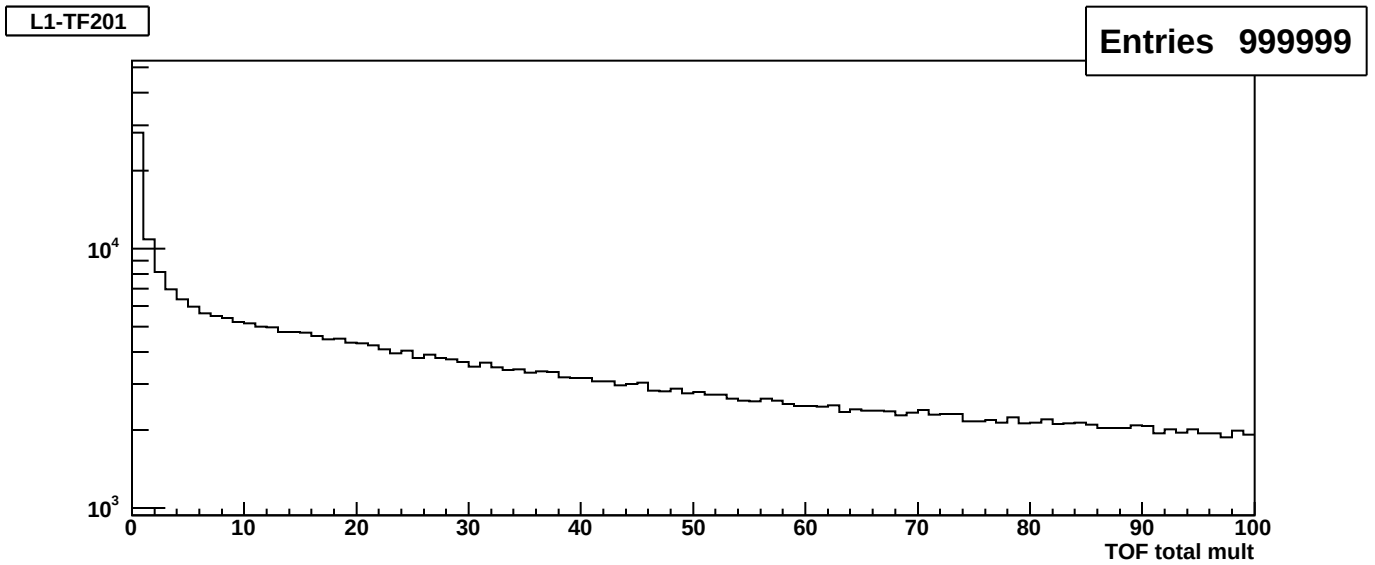

MIX-TF003
Entries 4.199996e+07
TOF MULT
TOF tray

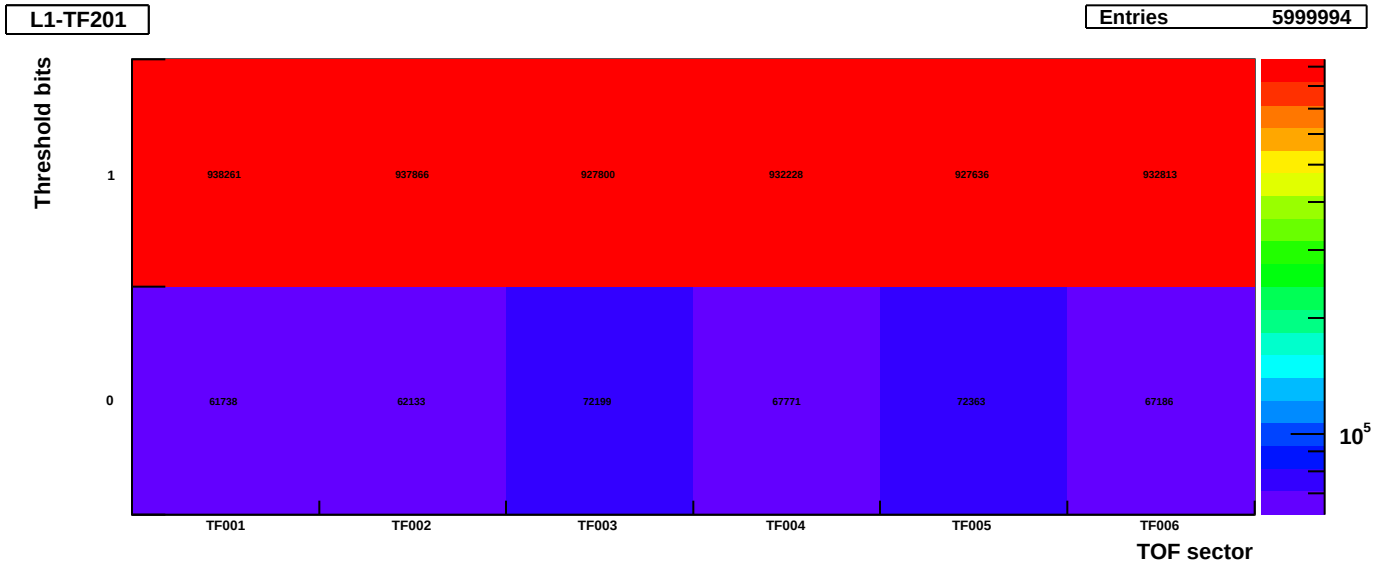

MIX-TF004
Entries 4.199996e+07
TOF MULT
TOF tray

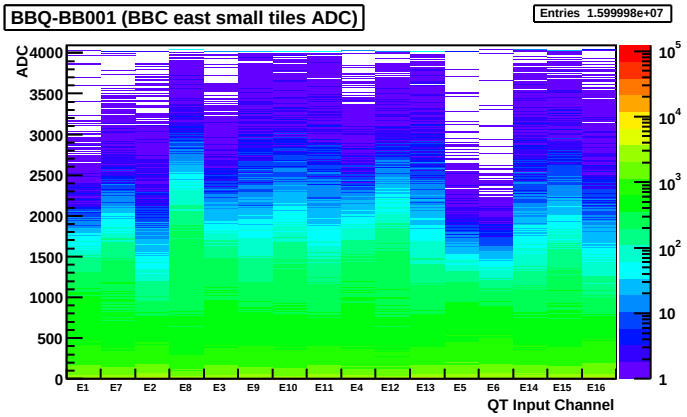

MIX-TF005
Entries 4.199996e+07
TOF MULT
TOF tray

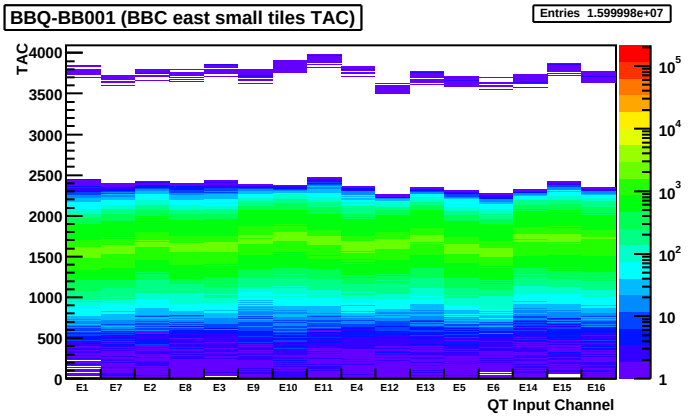

MIX-TF006
Entries 4.199996e+07
TOF MULT
TOF tray

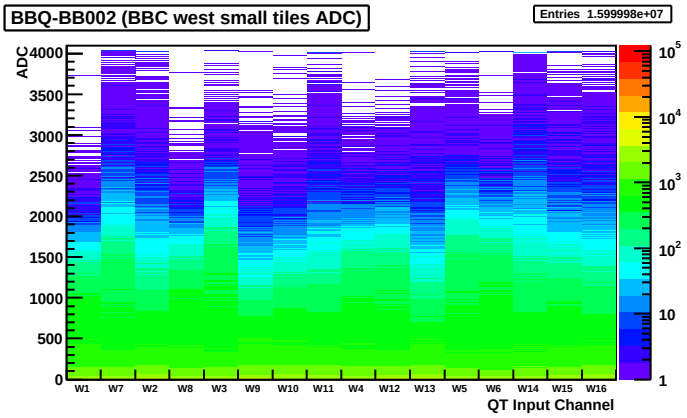

MIX-TF101
Entries 5999994
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

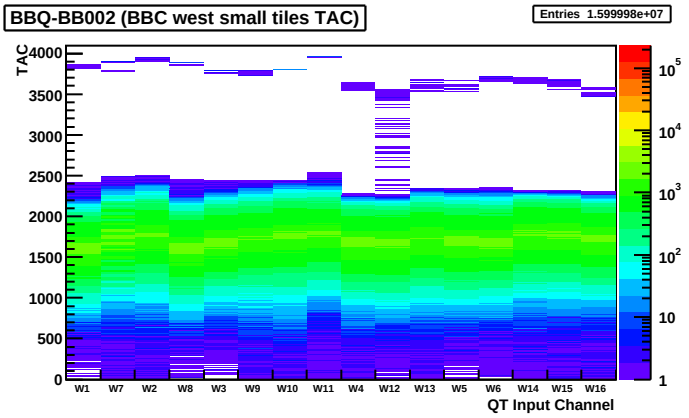

L1-TF201
Entries 999999
10^4
10^3
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

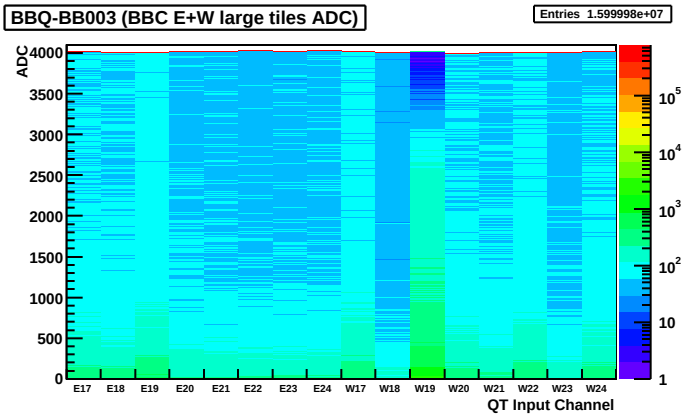

L1-TF201
Entries 5999994
Threshold bits
1
0
938261
937866
927800
932228
927636
932813
61738
62133
72199
67771
72363
67186
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

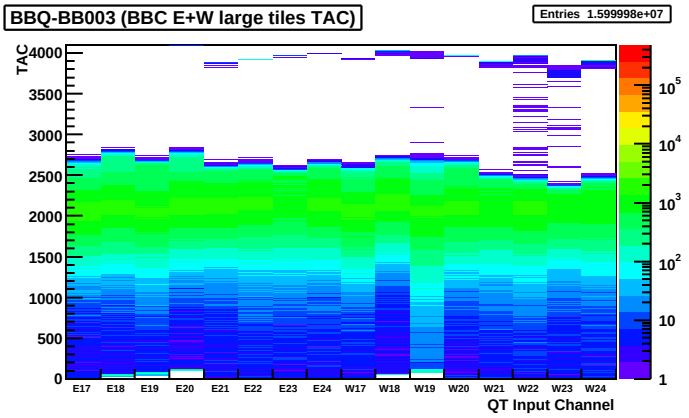

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.599998e+07
ADC
QT Input Channel

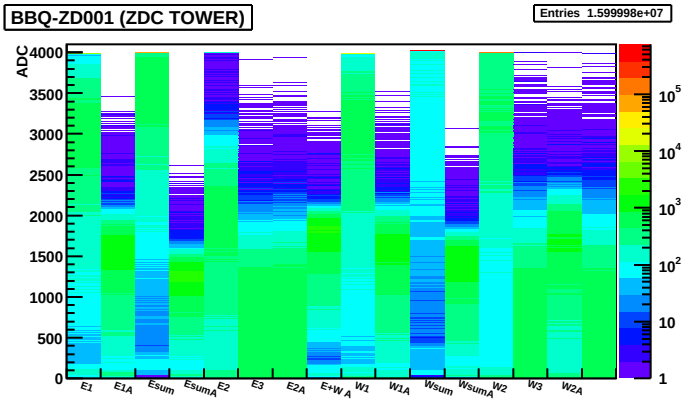

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.599998e+07
TAC
QT Input Channel

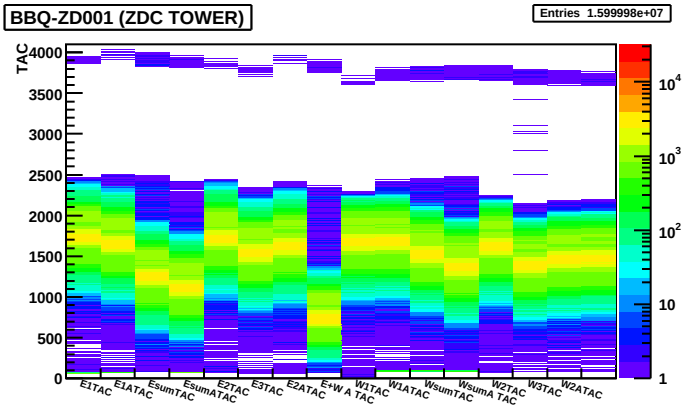

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.599998e+07
ADC
QT Input Channel

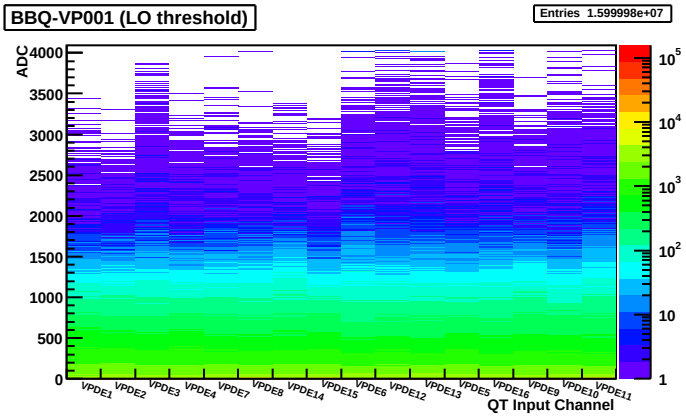

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.599998e+07
TAC
QT Input Channel

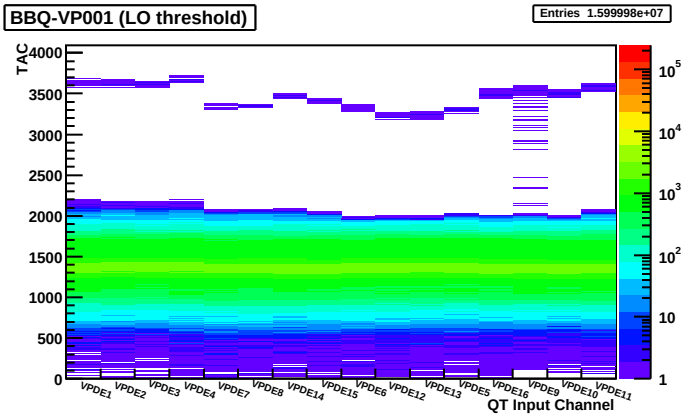

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.599998e+07
ADC
QT Input Channel

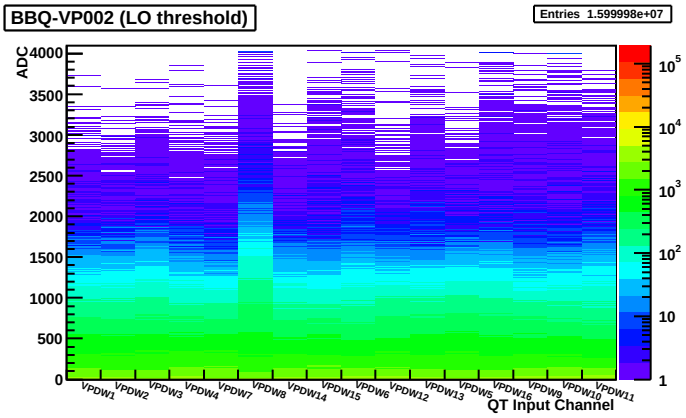

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.599998e+07
TAC
QT Input Channel

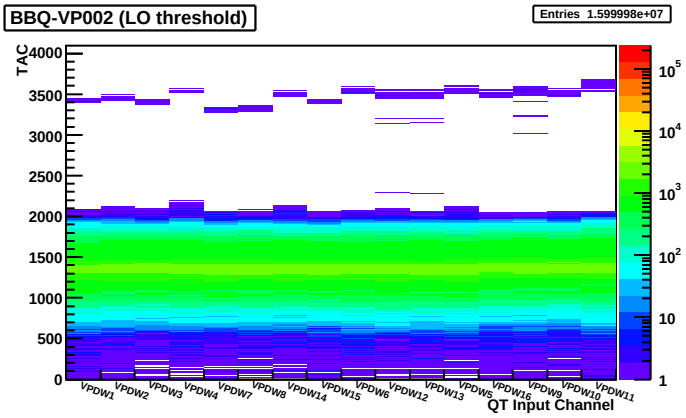

BBQ-ZD001 (ZDC TOWER)
Entries 1.599998e+07
ADC

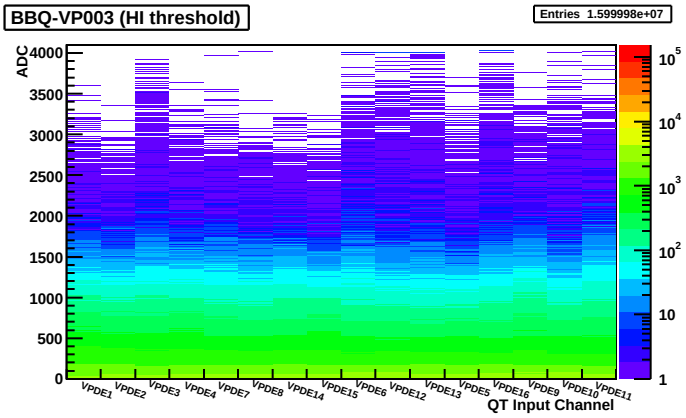

BBQ-ZD001 (ZDC TOWER)
Entries 1.599998e+07
TAC

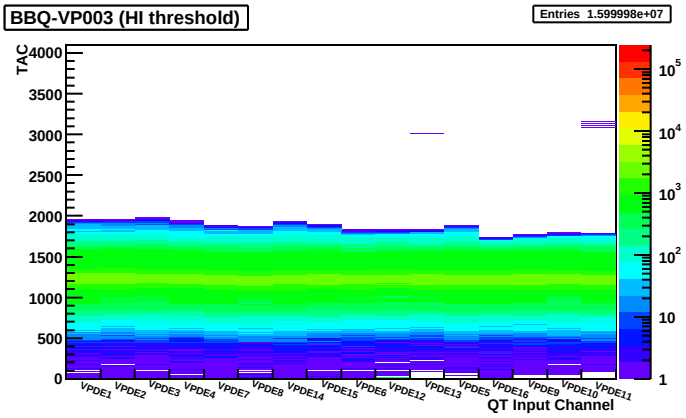

BBQ-VP001 (LO threshold)
Entries 1.599998e+07
ADC
QT Input Channel

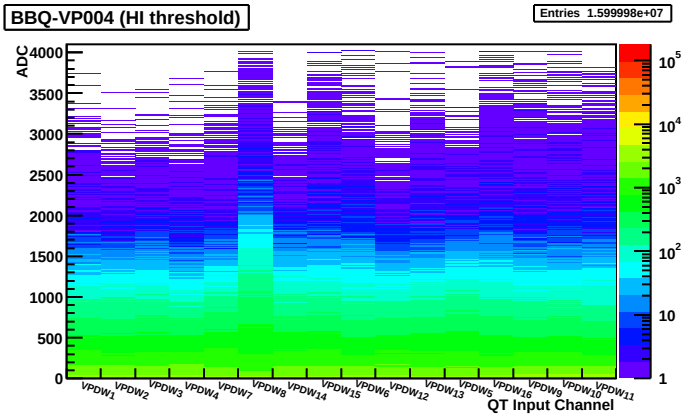

BBQ-VP001 (LO threshold)
Entries 1.599998e+07
TAC
QT Input Channel

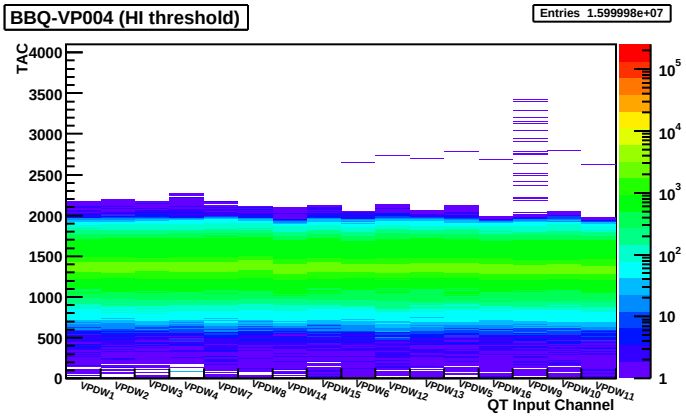

BBQ-VP002 (LO threshold)
Entries 1.599998e+07
ADC
QT Input Channel

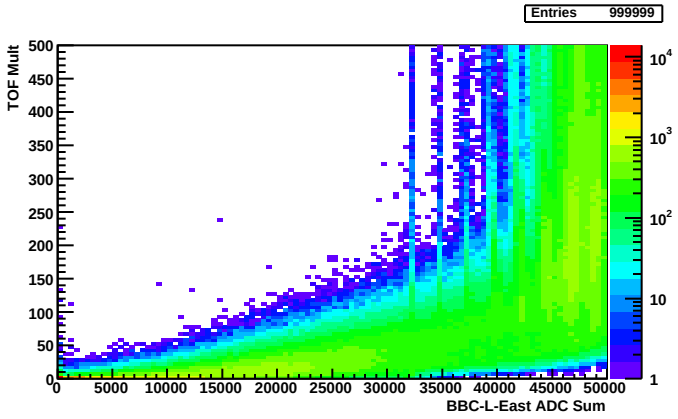

BBQ-VP002 (LO threshold)
Entries 1.599998e+07
TAC
QT Input Channel

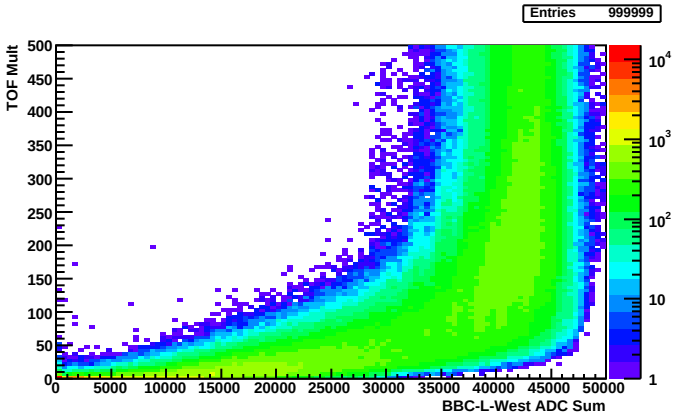

BBQ-VP003 (HI threshold)
Entries 1.599998e+07
ADC
QT Input Channel

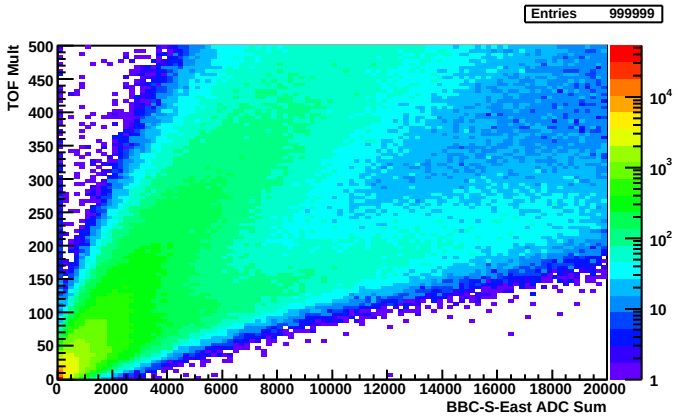

BBQ-VP003 (HI threshold)
Entries 1.599998e+07
TAC
QT Input Channel

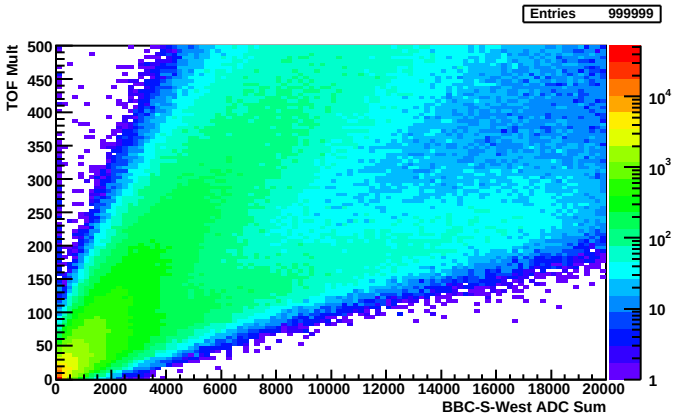

BBQ-VP004 (HI threshold)
Entries 1.599998e+07
ADC
QT Input Channel

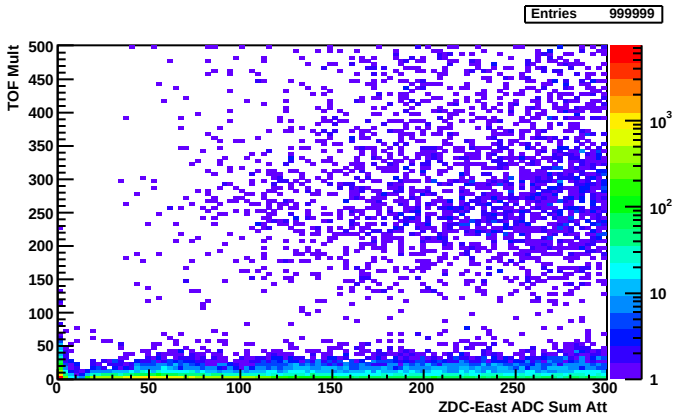

BBQ-VP004 (HI threshold)
Entries 1.599998e+07
TAC
QT Input Channel

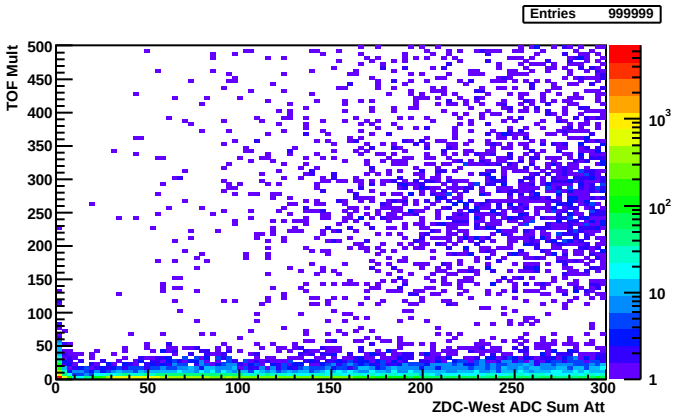

Entries 999999
TOF Mult
BBC-L-East ADC Sum

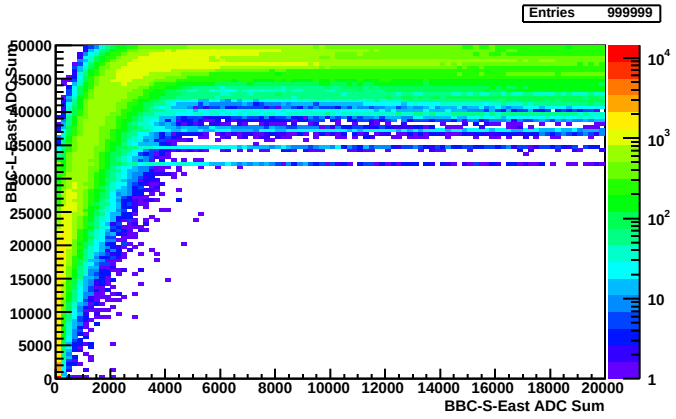

Entries 999999
TOF Mult
BBC-L-West ADC Sum

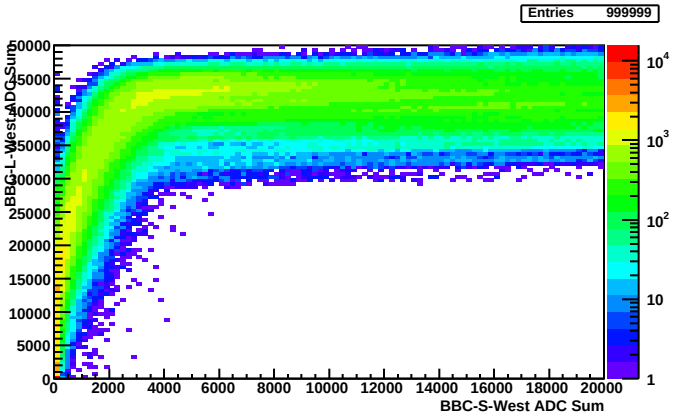

Entries 999999
TOF Mult
BBC-S-East ADC Sum

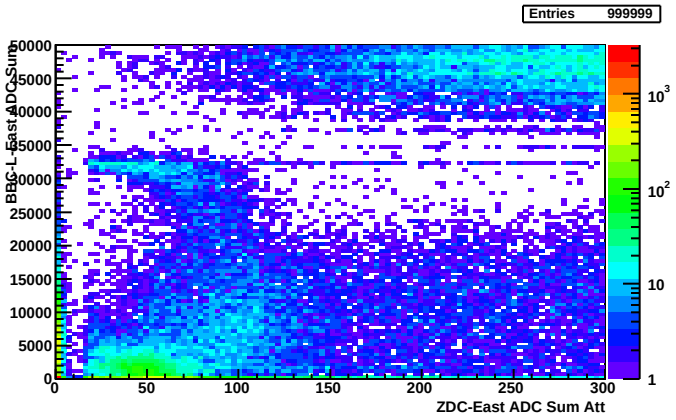

Entries 999999
TOF Mult
BBC-S-West ADC Sum

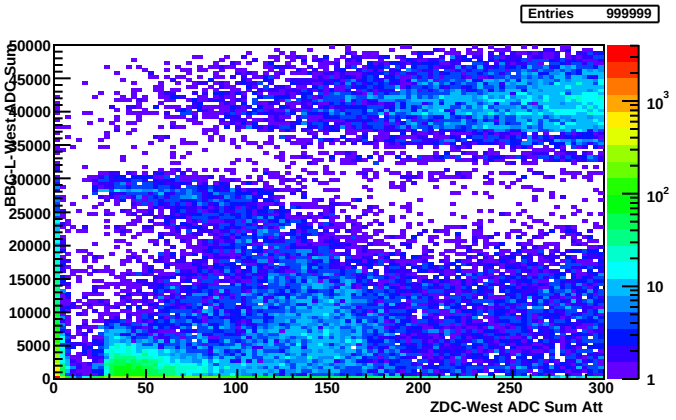

Entries 999999
TOF Mult
ZDC-East ADC Sum Att

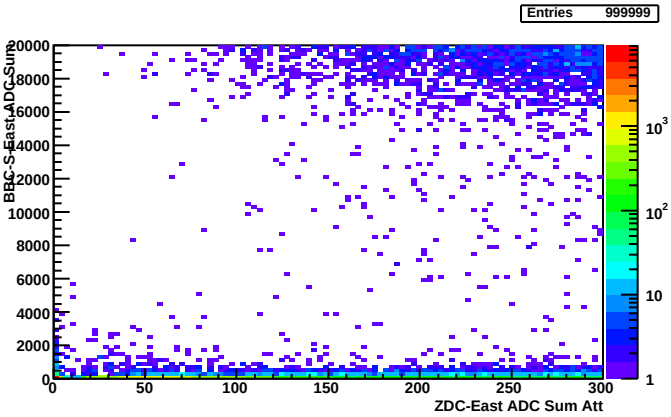

Entries 999999
TOF Mult
ZDC-West ADC Sum Att

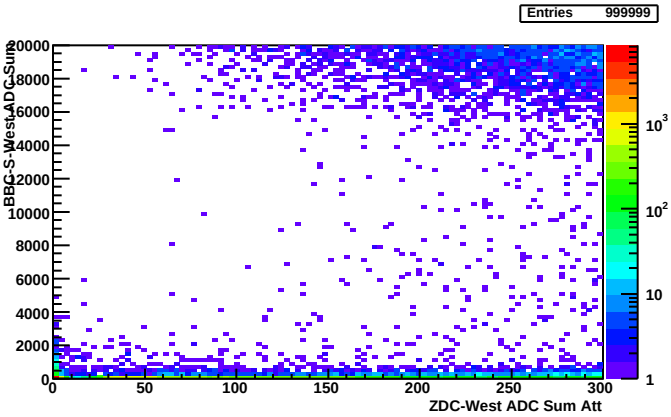

Entries 999999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

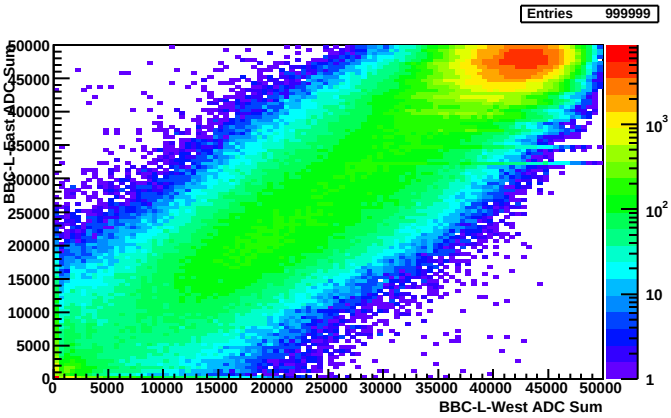

Entries 999999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

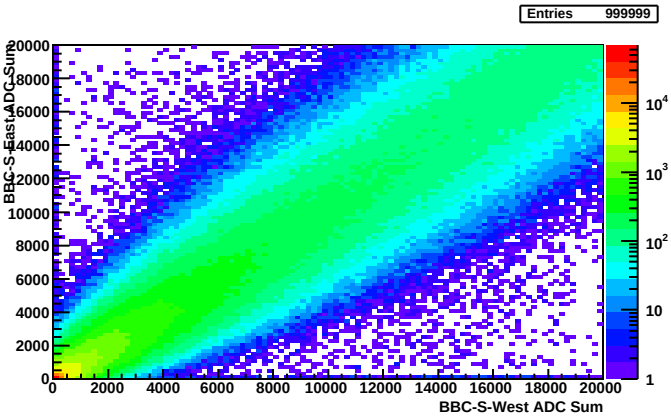

Entries 999999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

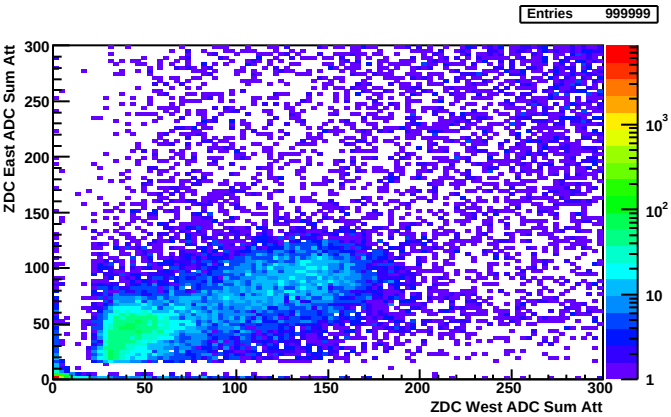

Entries 999999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

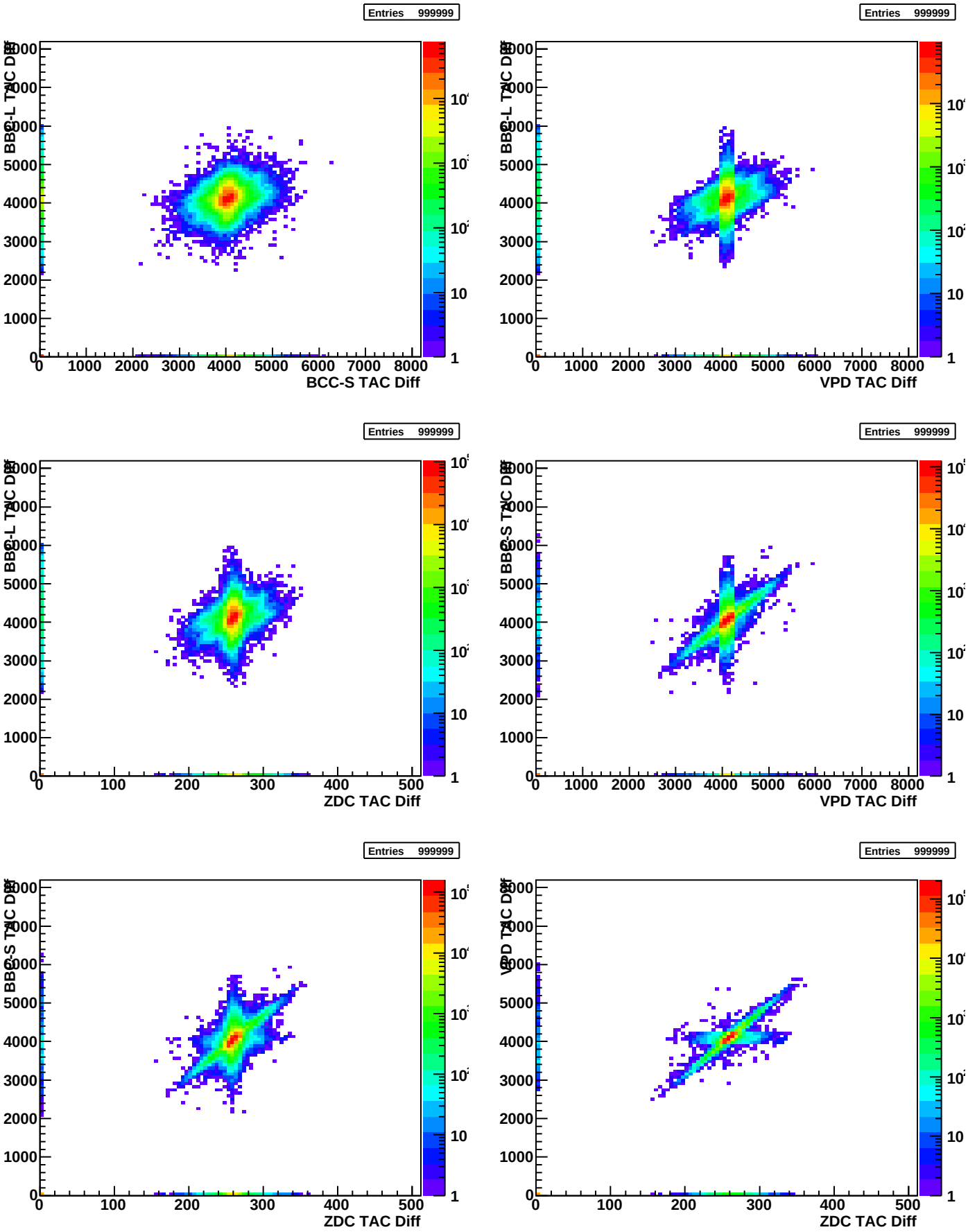

Entries 999999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

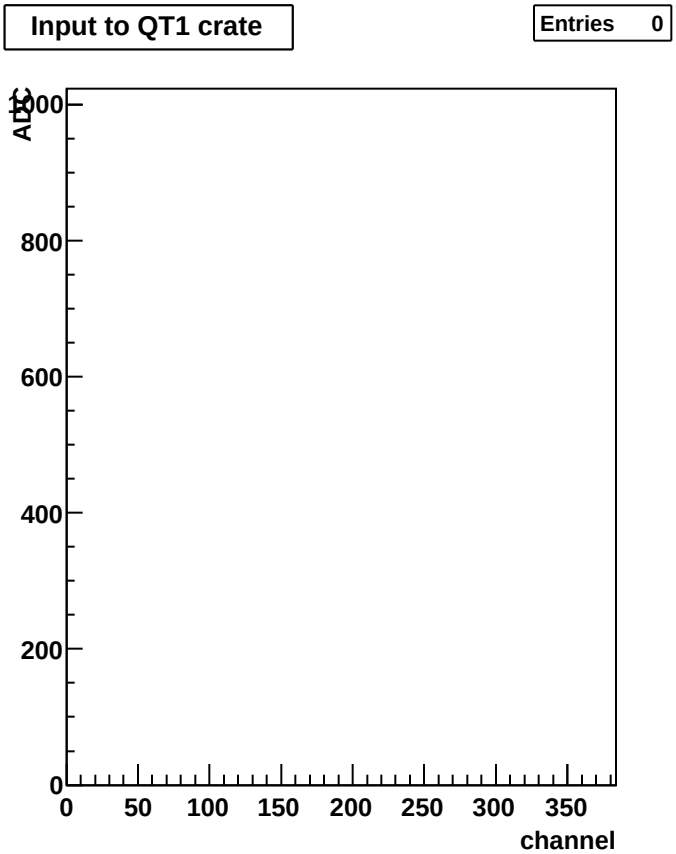

Entries 999999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

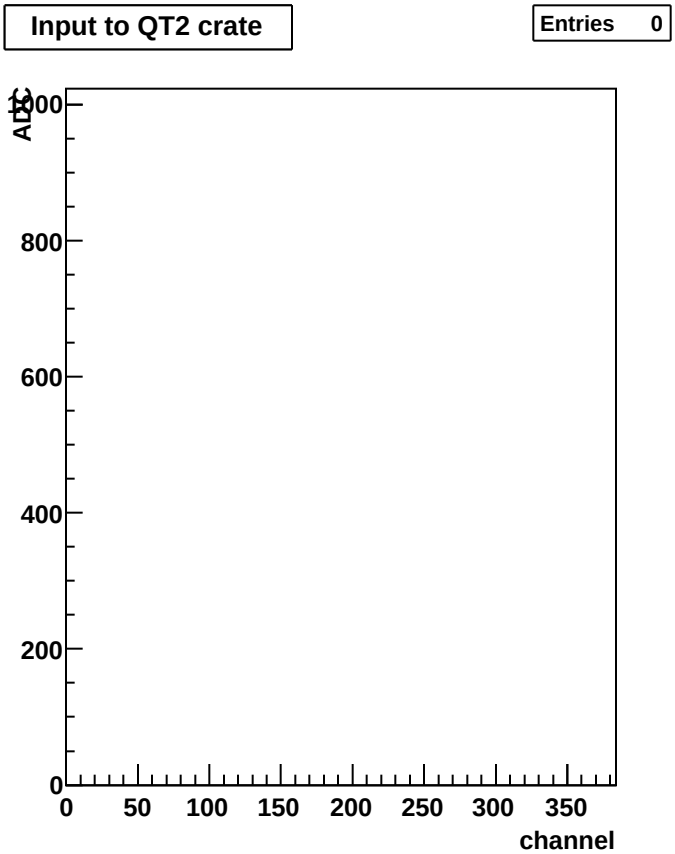

Entries 999999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

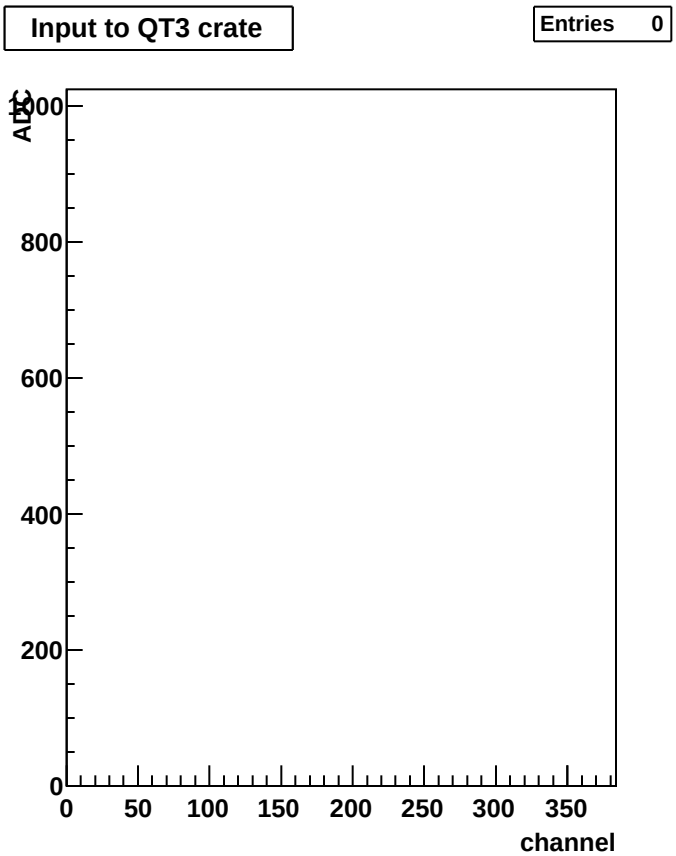

Entries 999999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

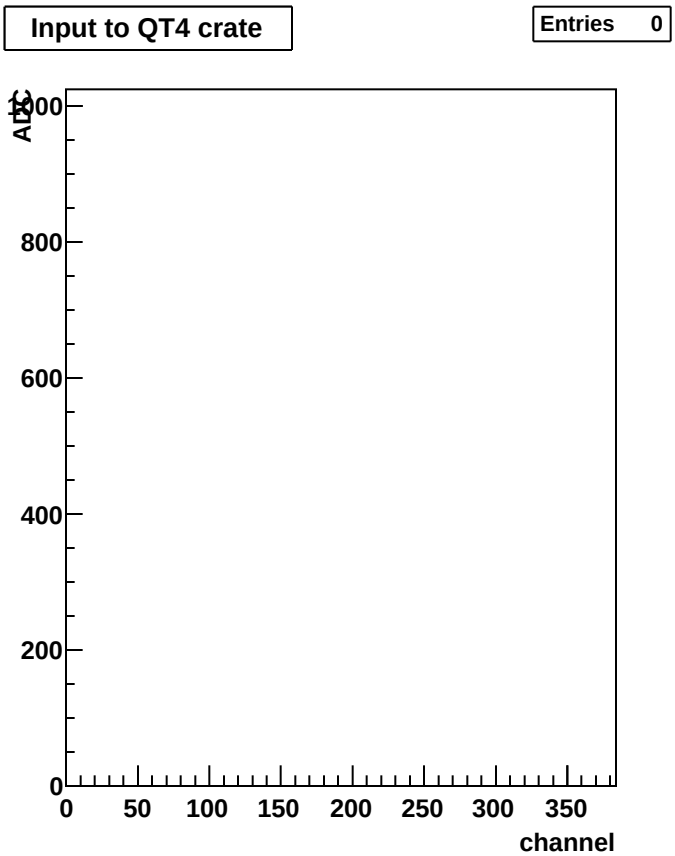

Entries 999999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

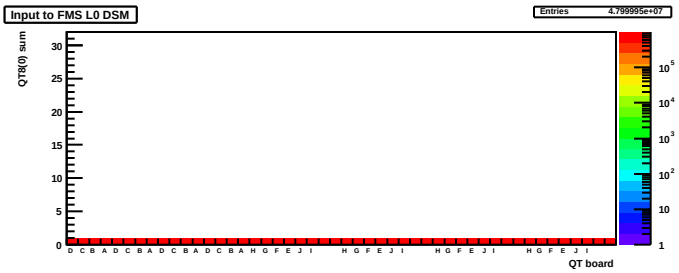

Entries 999999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 999999
BBC-L TAC Diff
VPD TAC Diff
Entries 999999
BBC-L TAC Diff
ZDC TAC Diff
Entries 999999
BBC-S TAC Diff
VPD TAC Diff
Entries 999999
BBC-S TAC Diff
ZDC TAC Diff
Entries 999999
VPD TAC Diff
ZDC TAC Diff

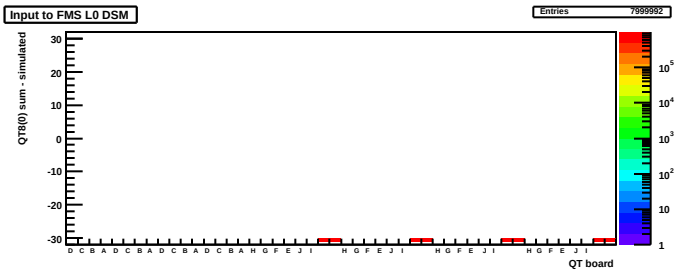

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

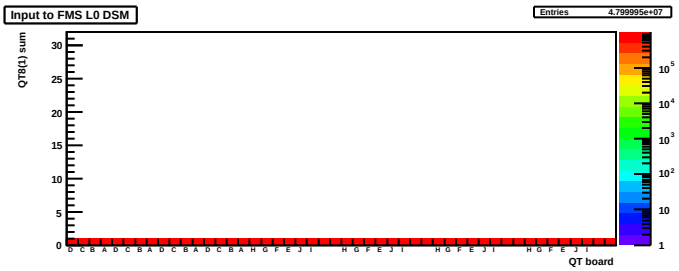

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

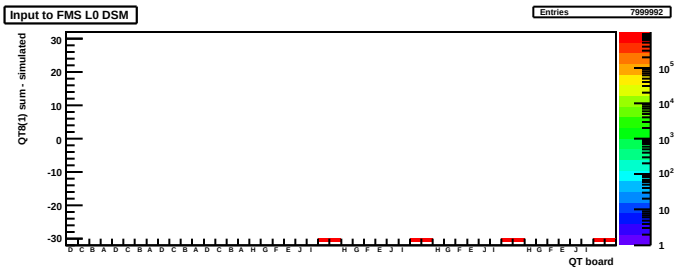

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

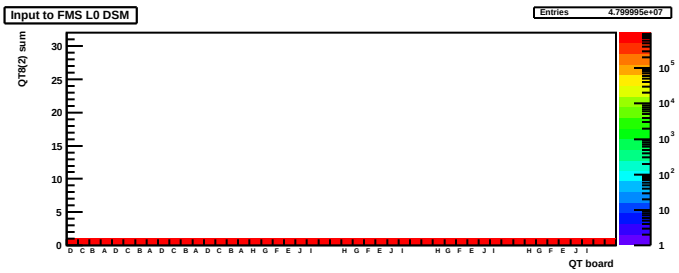

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

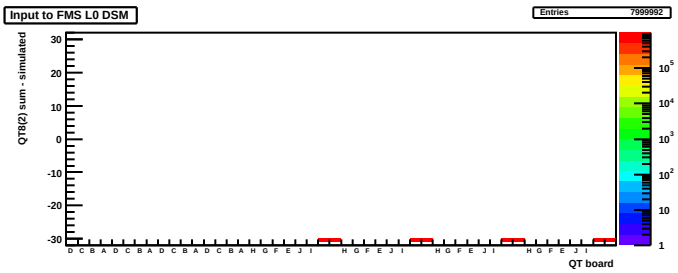

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(0) sum
QT board

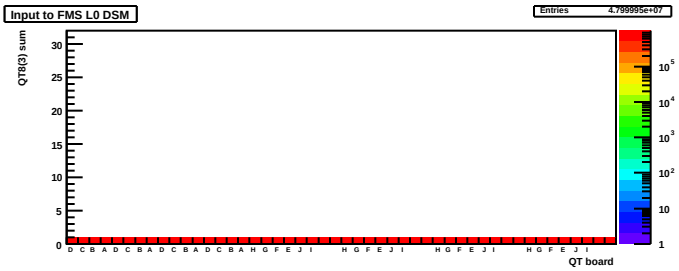

Input to FMS L0 DSM
Entries 7999992
QT8(0) sum - simulated
QT board

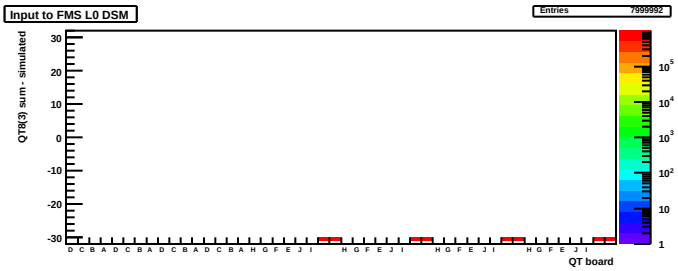

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(1) sum
QT board

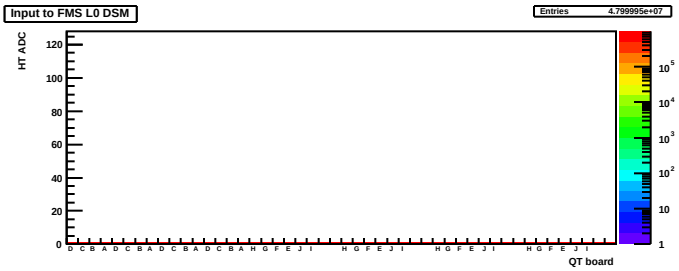

Input to FMS L0 DSM
Entries 7999992
QT8(1) sum - simulated
QT board

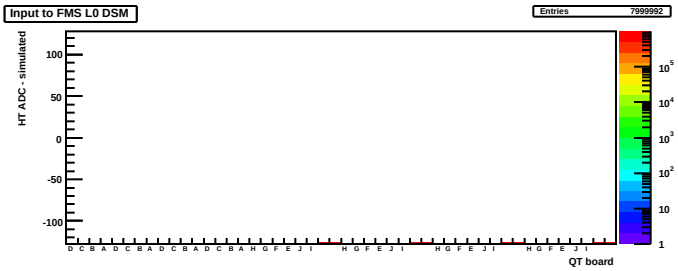

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(2) sum
QT board

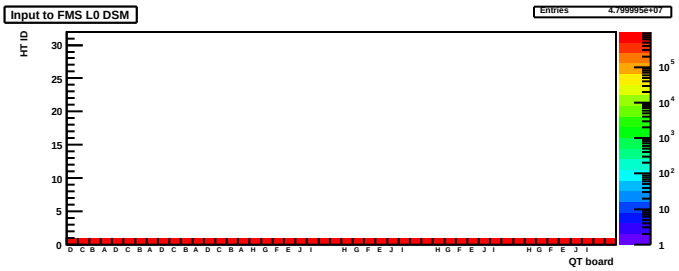

Input to FMS L0 DSM
Entries 7999992
QT8(2) sum - simulated
QT board

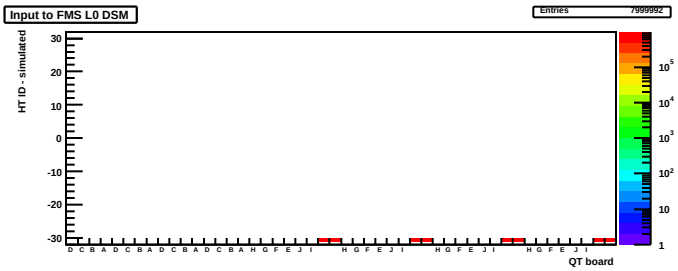

Input to FMS L0 DSM
Entries 4.799995e+07
QT8(3) sum
QT board

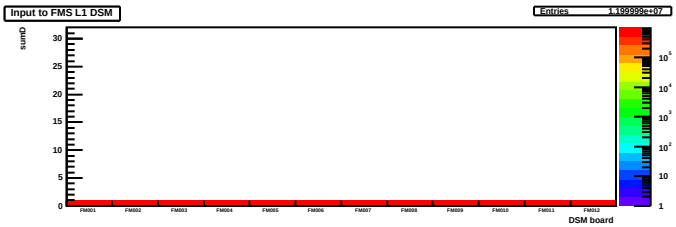

Input to FMS L0 DSM
Entries 7999992
QT8(3) sum - simulated
QT board

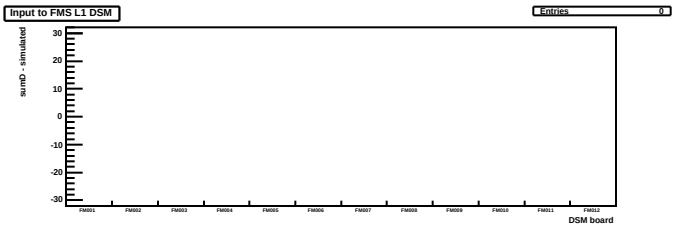

Input to FMS L0 DSM
Entries 4.799995e+07
HT ADC
QT board

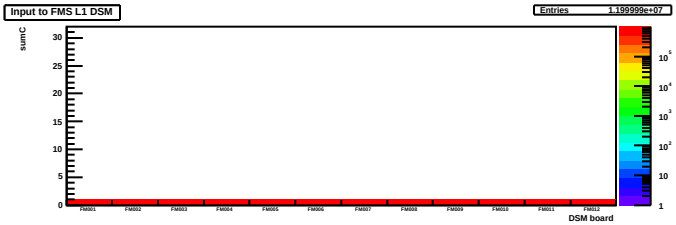

Input to FMS L0 DSM
Entries 7999992
HT ADC - simulated
QT board

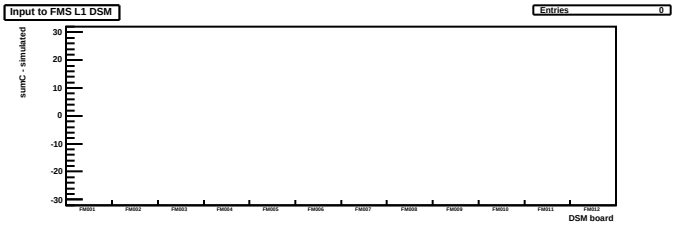

Input to FMS L0 DSM
Entries 4.799995e+07
HT ID
QT board

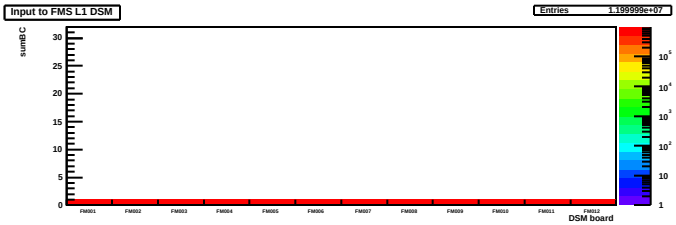

Input to FMS L0 DSM
Entries 7999992
HT ID - simulated
QT board

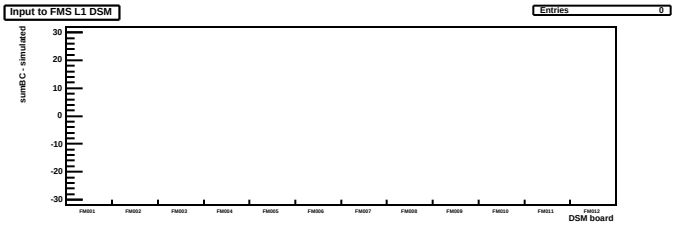

Input to FMS L1 DSM
Entries 1.199999e+07
sumD
DSM board

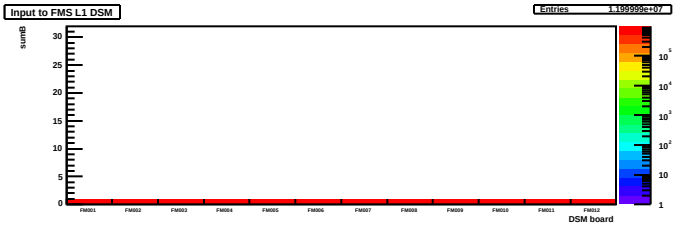

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

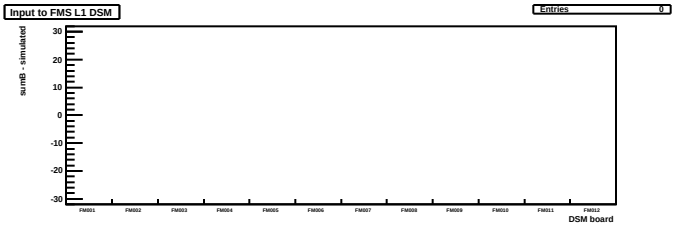

Input to FMS L1 DSM
Entries 1.199999e+07
sumC
DSM board

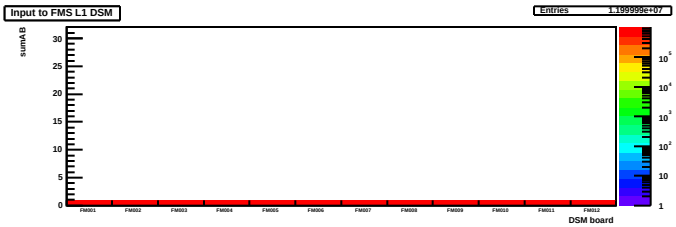

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

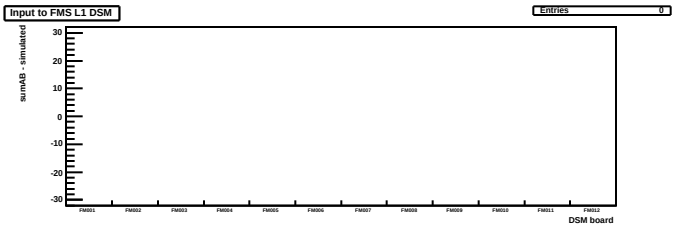

Input to FMS L1 DSM
Entries 1.199999e+07
sumBC
DSM board

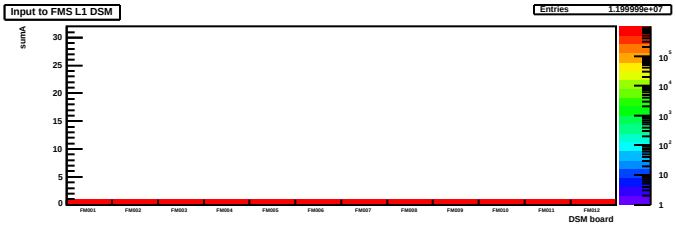

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

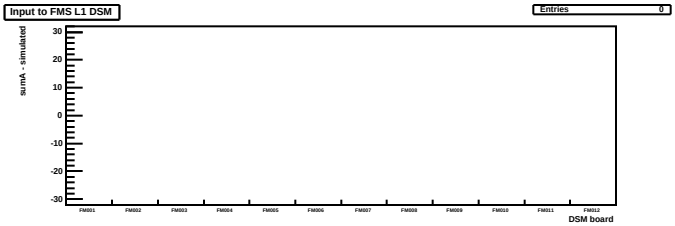

Input to FMS L1 DSM
Entries 1.199999e+07
sumB
DSM board

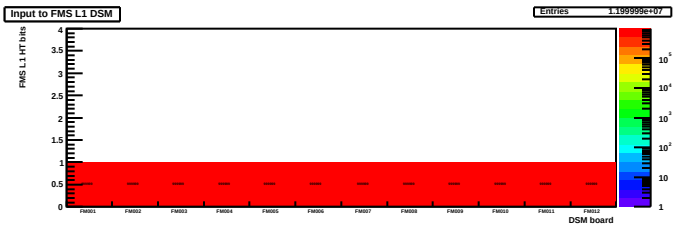

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

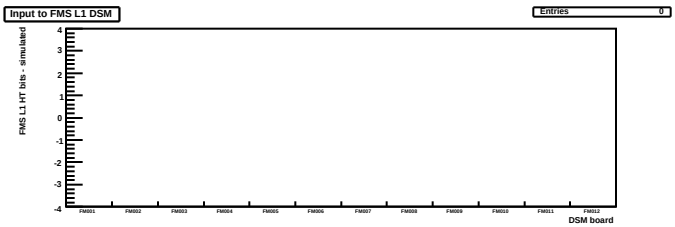

Input to FMS L1 DSM
Entries 1.199999e+07
sumAB
DSM board

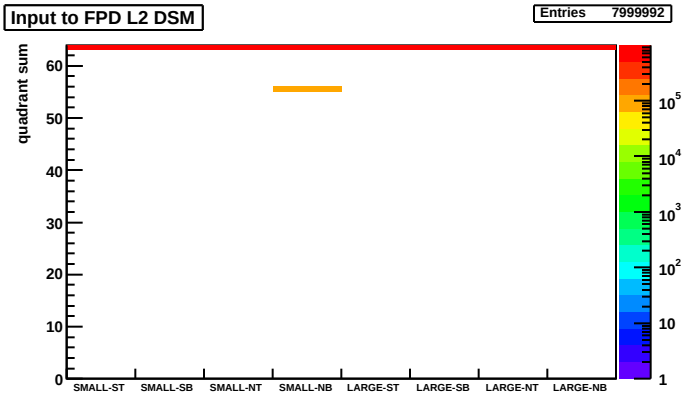

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

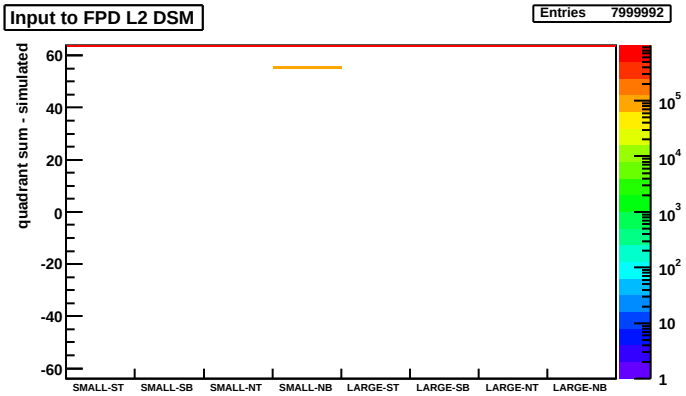

Input to FMS L1 DSM
Entries 1.199999e+07
sumA
DSM board

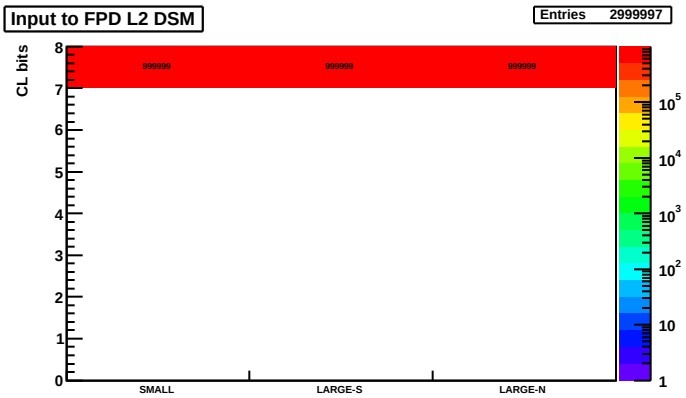

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

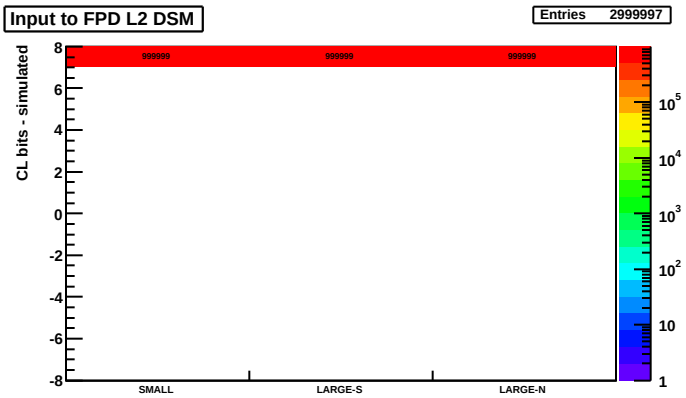

Input to FMS L1 DSM
Entries 1.199999e+07
FMS L1 HT bits
DSM board

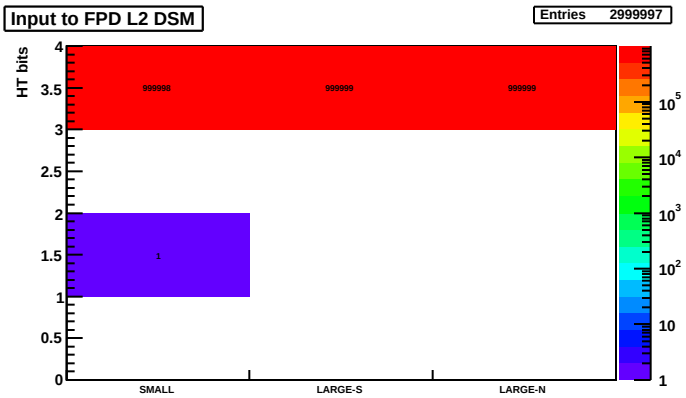

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

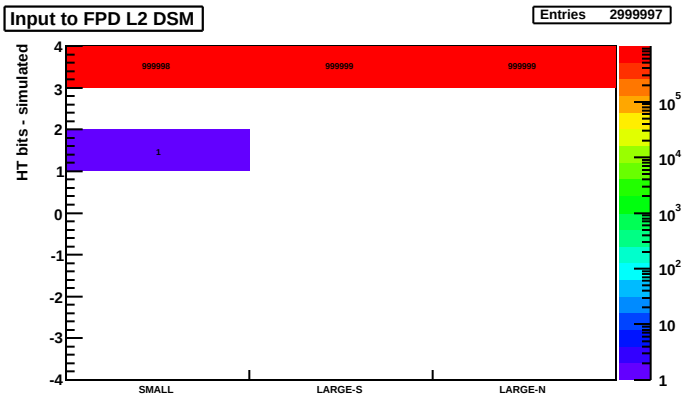

Input to FPD L2 DSM
Entries 7999992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

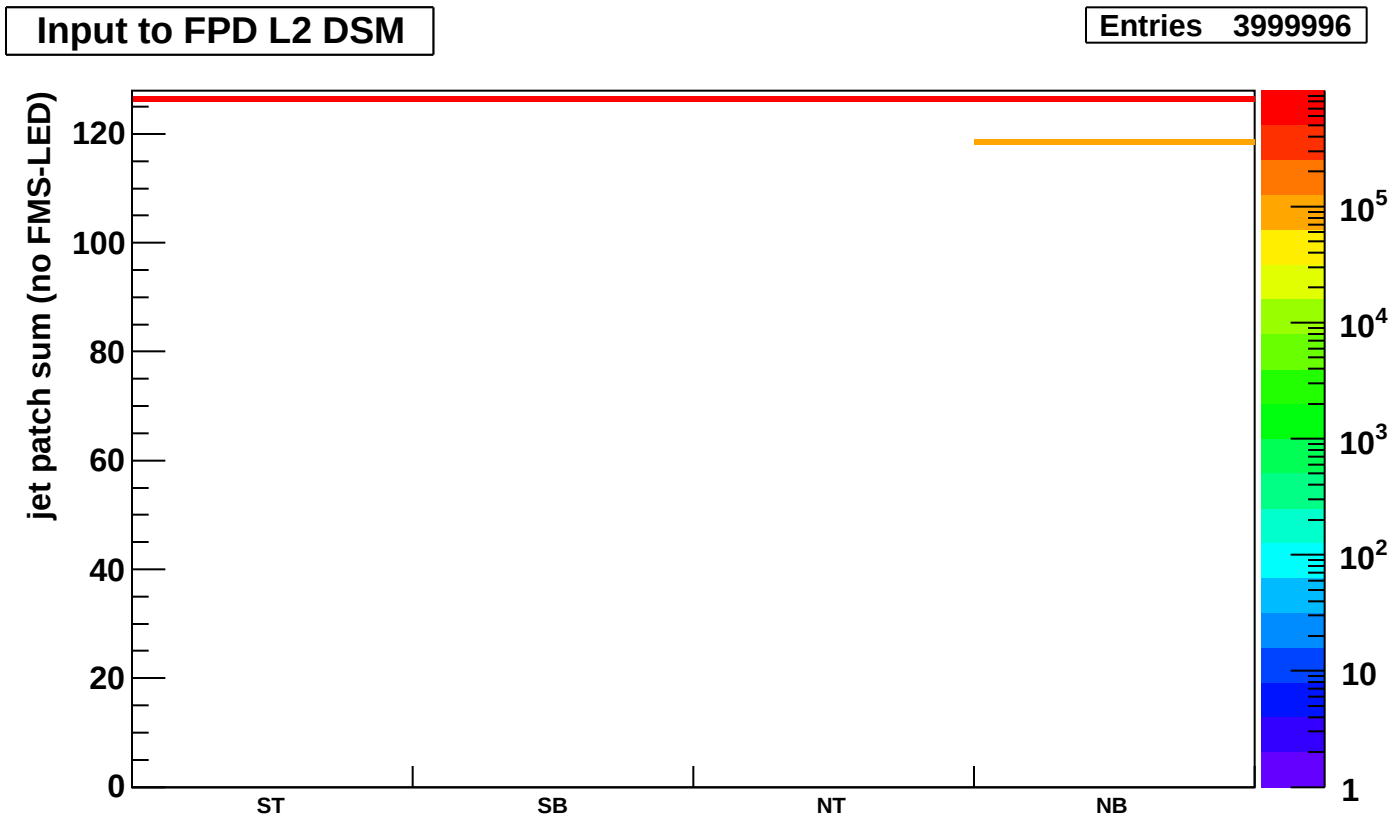

Input to FPD L2 DSM
Entries 7999992
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

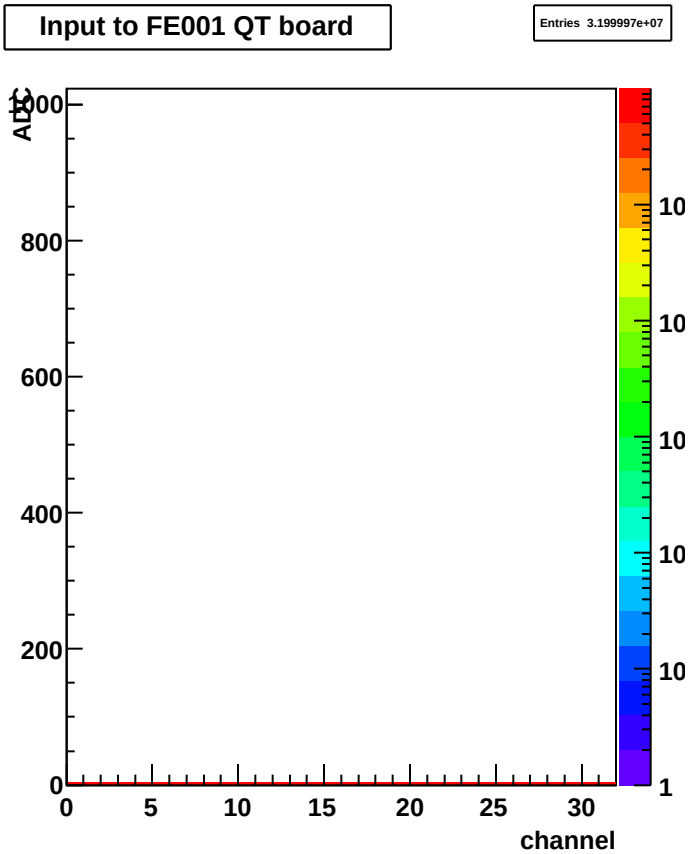

Input to FPD L2 DSM
Entries 2999997
CL bits
999999
999999
999999
SMALL
LARGE-S
LARGE-N

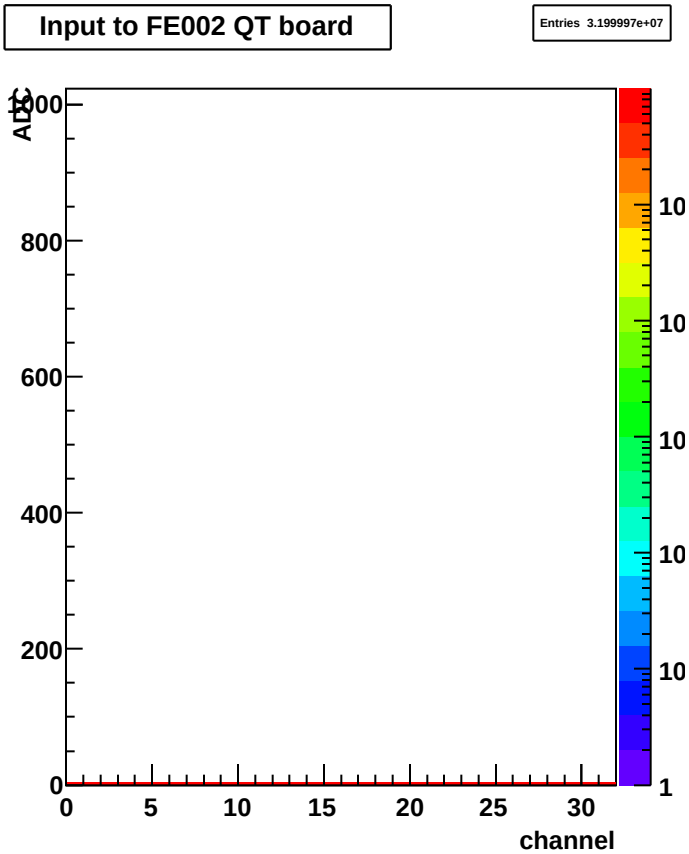

Input to FPD L2 DSM
Entries 2999997
CL bits - simulated
999999
999999
999999
SMALL
LARGE-S
LARGE-N

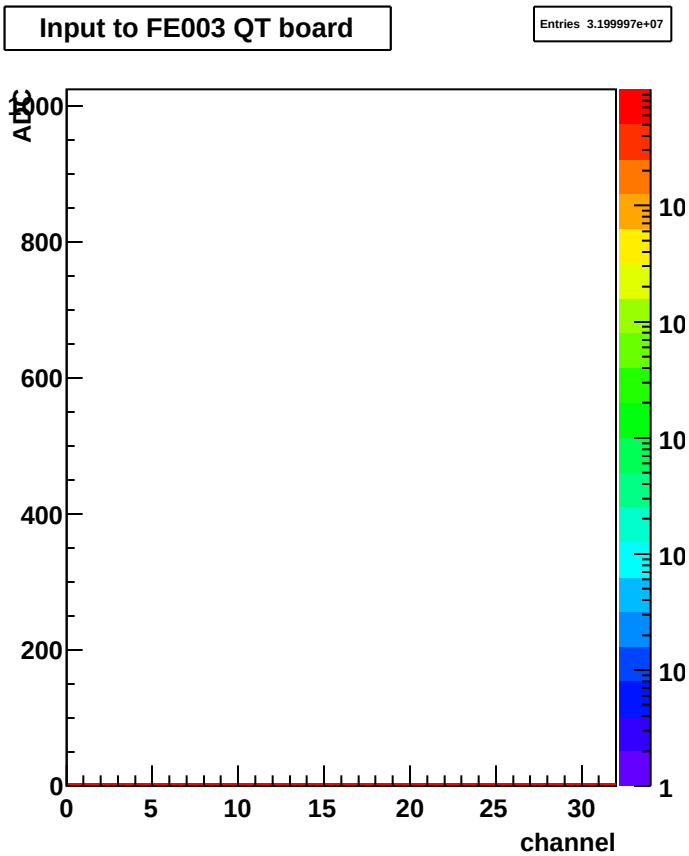

Input to FPD L2 DSM
Entries 2999997
HT bits
999998
999999
999999
1
SMALL
LARGE-S
LARGE-N

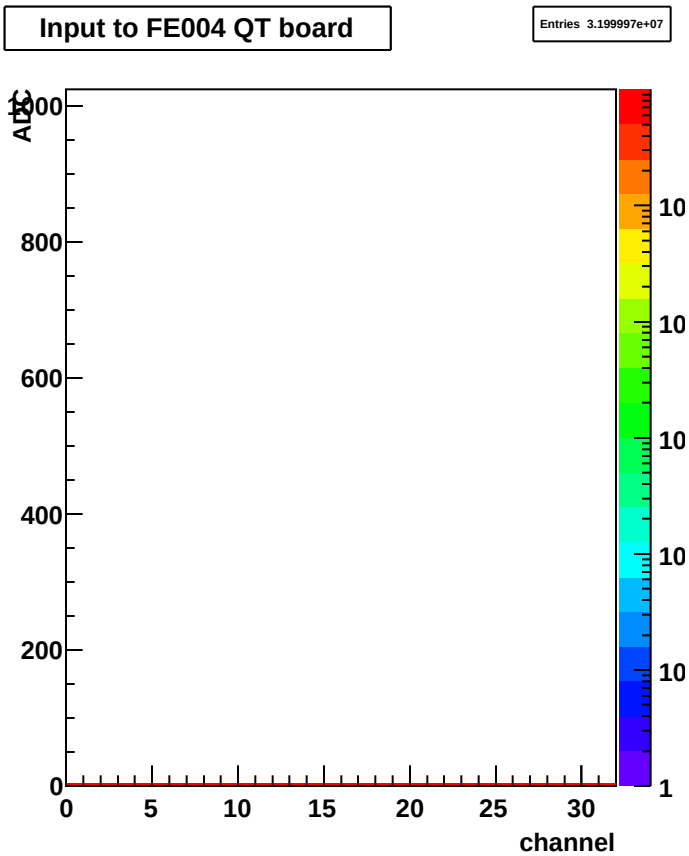

Input to FPD L2 DSM
Entries 2999997
HT bits - simulated
999998
999999
999999
1
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3999996
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

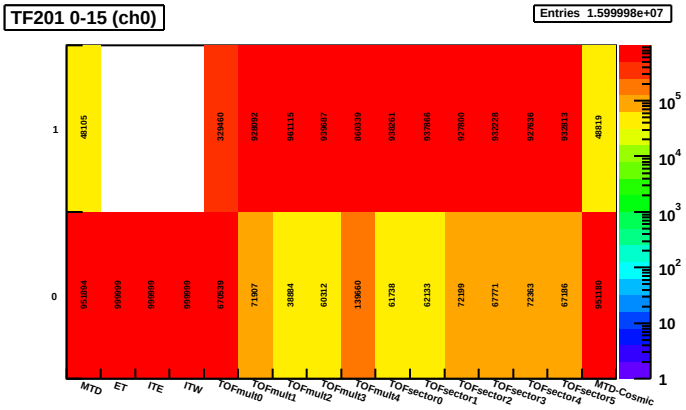

Input to FE001 QT board
Entries 3.199997e+07
ADC
channel


Input to FE002 QT board
Entries 3.199997e+07
ADC
channel

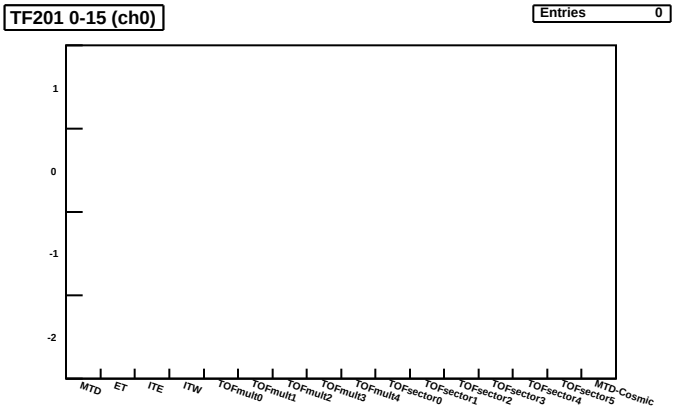

Input to FE003 QT board
Entries 3.199997e+07
ADC
channel


Input to FE004 QT board
Entries 3.199997e+07
ADC
channel

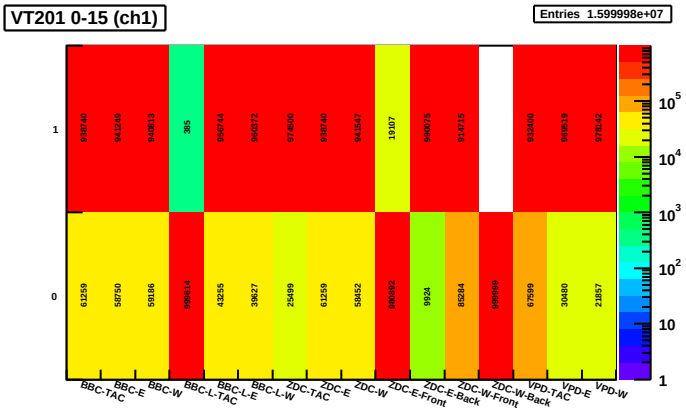
TF201 0-15 (ch0)



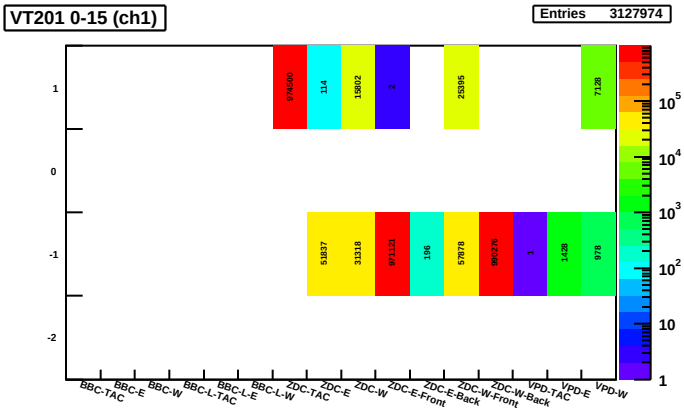
TF201 0-15 (ch0)



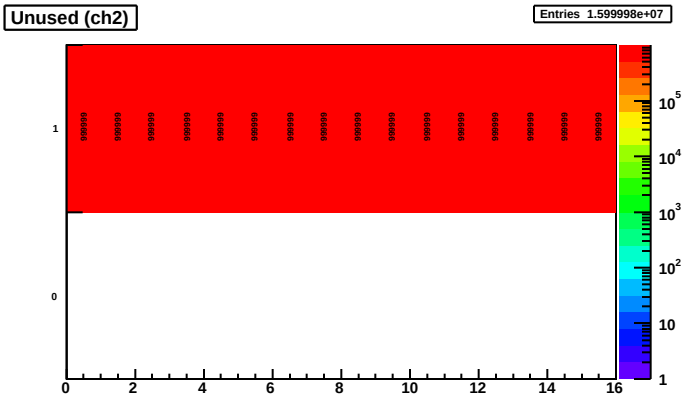
VT201 0-15 (ch1)

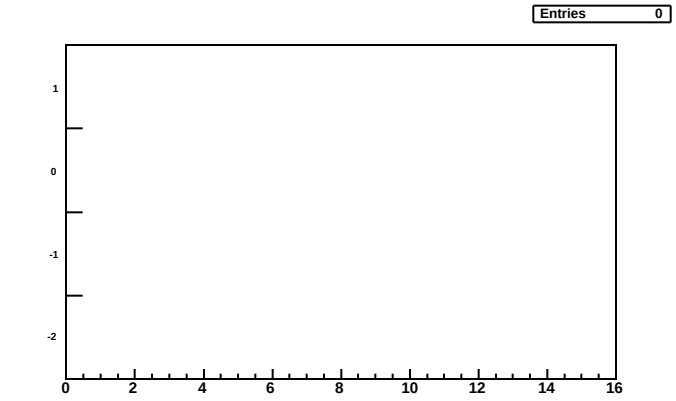


VT201 0-15 (ch1)

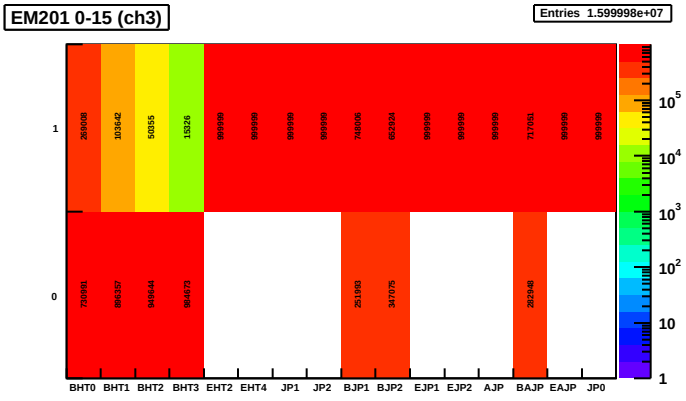


Unused (ch2)

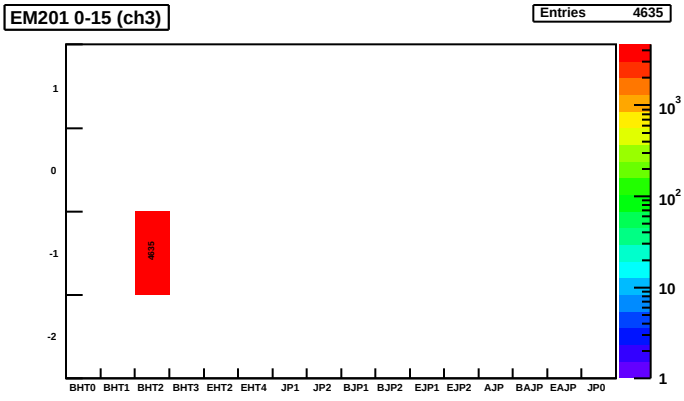




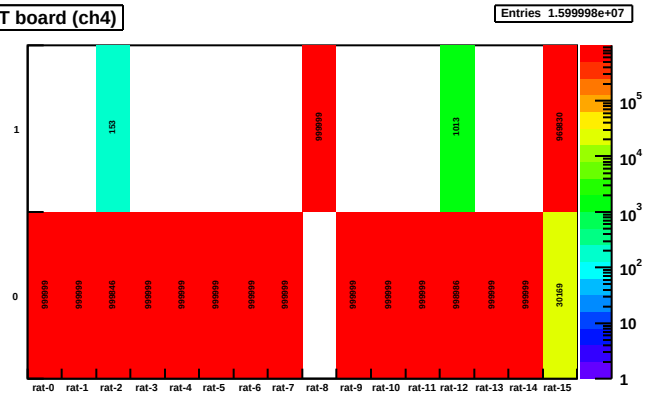
EM201 0-15 (ch3)

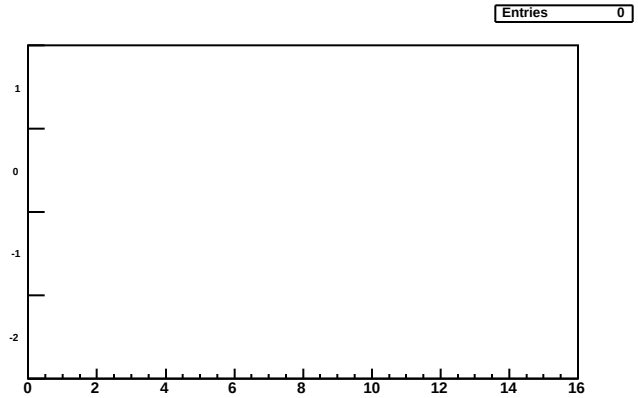


EM201 0-15 (ch3)

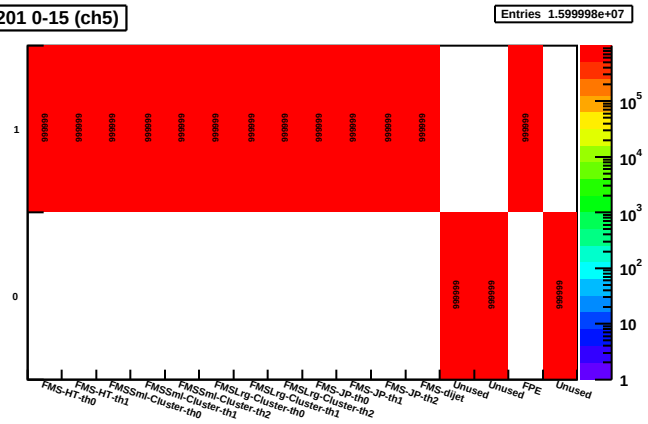


RAT board (ch4)

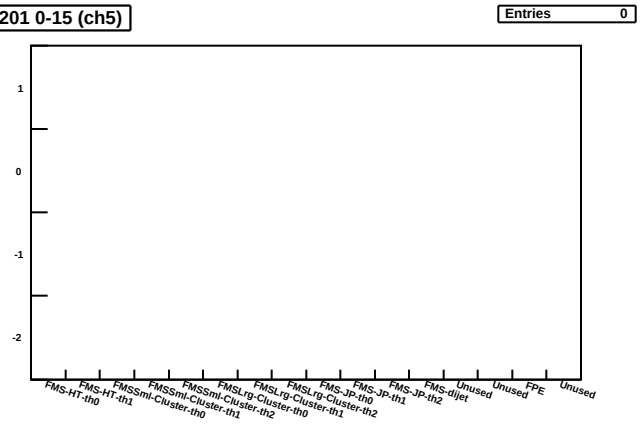




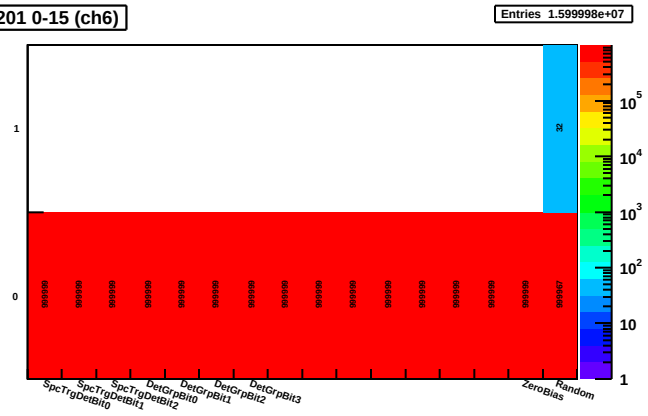
FP201 0-15 (ch5)

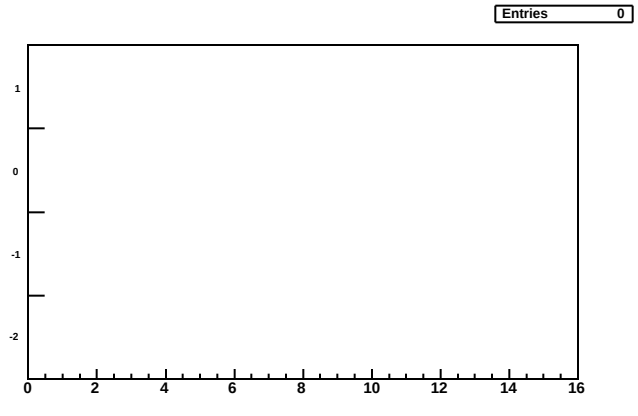


FP201 0-15 (ch5)

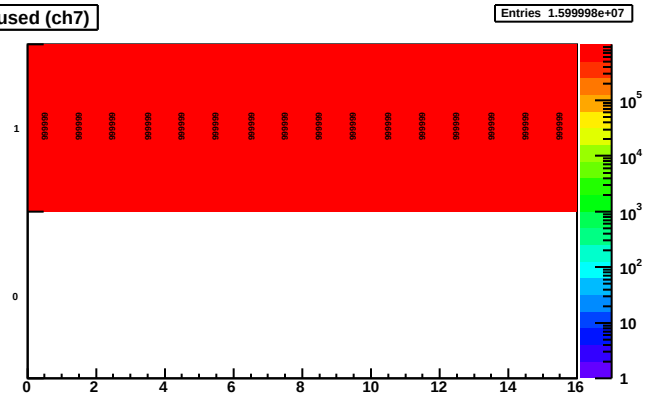


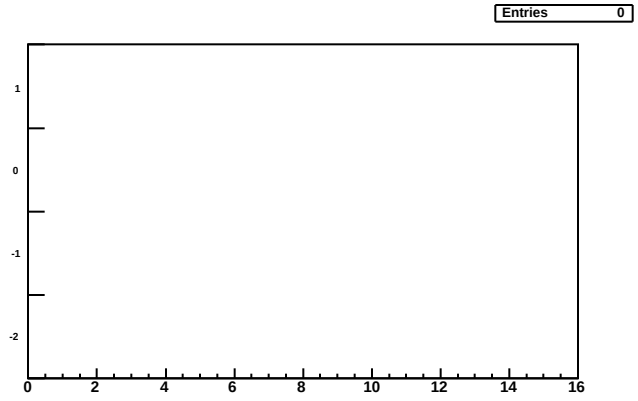
ST201 0-15 (ch6)

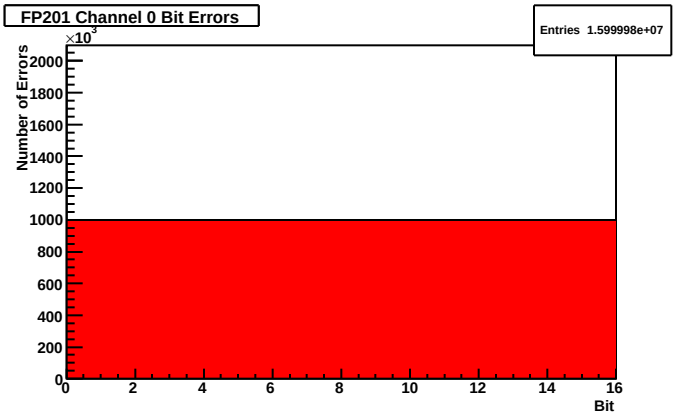


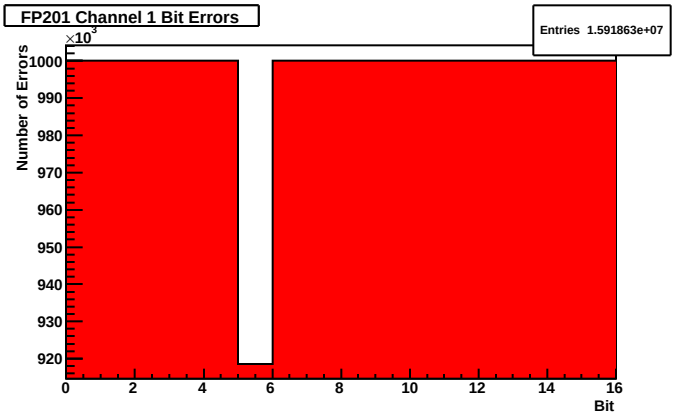


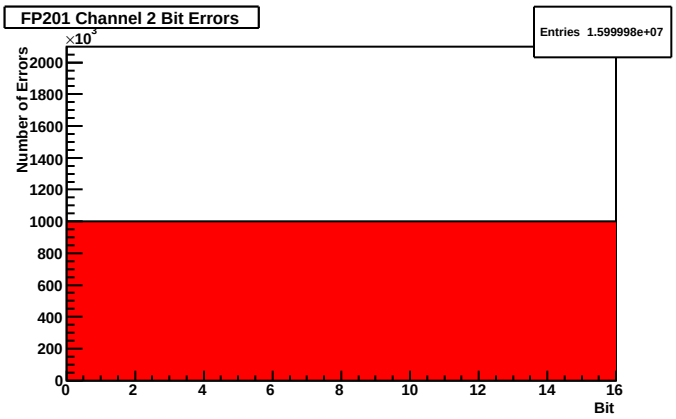
Unused (ch7)

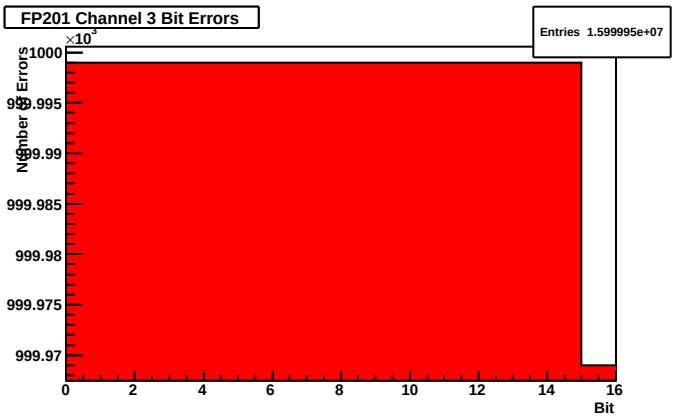


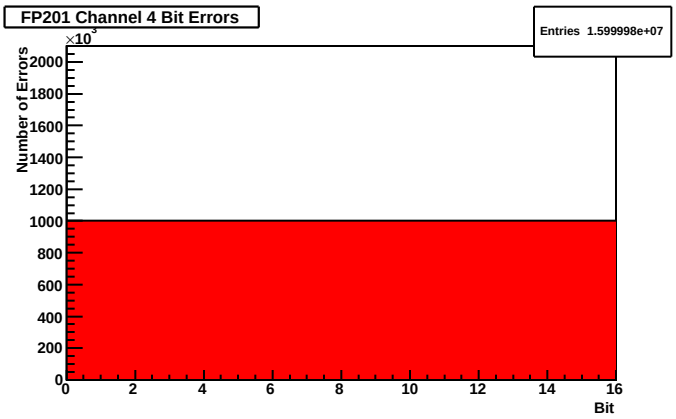


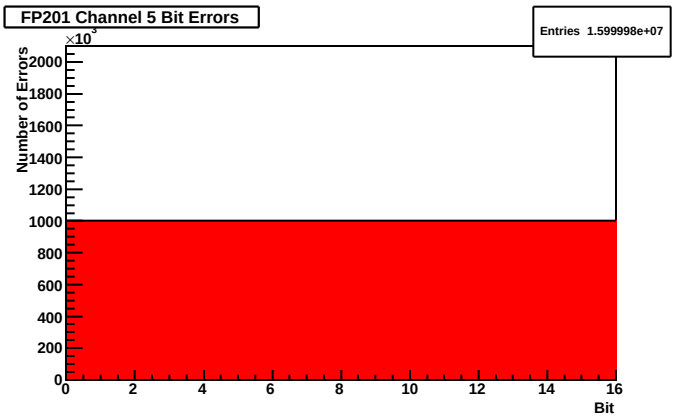

FP201 Channel 0 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

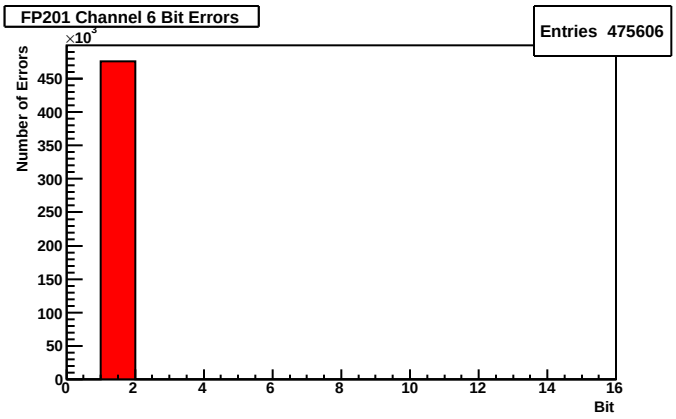

FP201 Channel 1 Bit Errors
Entries 1.591863e+07
Number of Errors
Bit

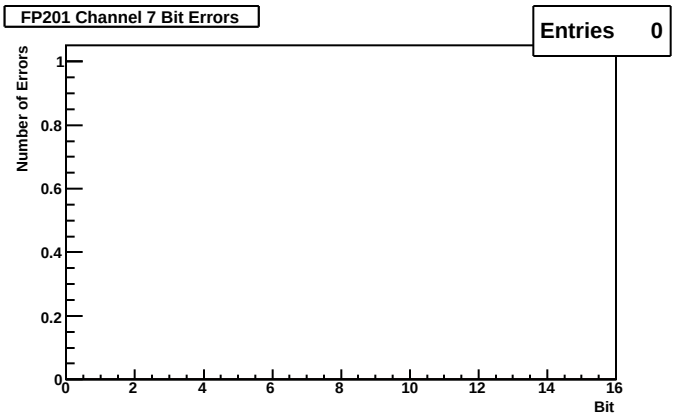

FP201 Channel 2 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

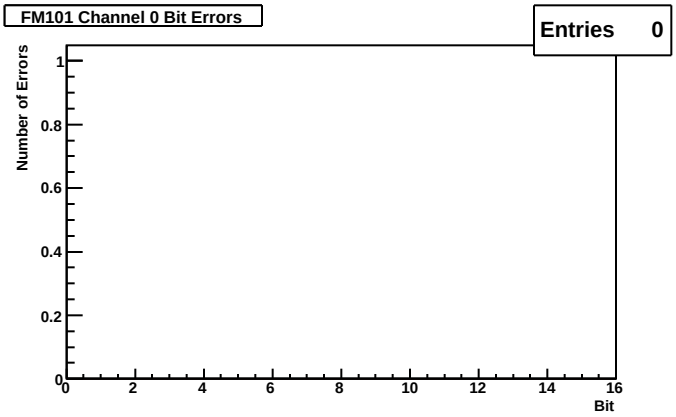

FP201 Channel 3 Bit Errors
Entries 1.599995e+07
Number of Errors
Bit

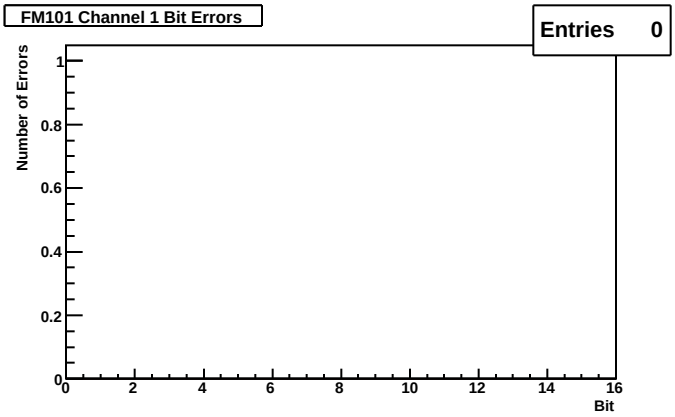

FP201 Channel 4 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

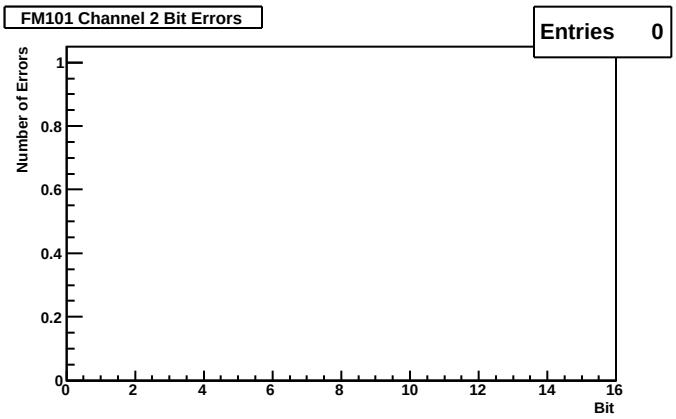

FP201 Channel 5 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

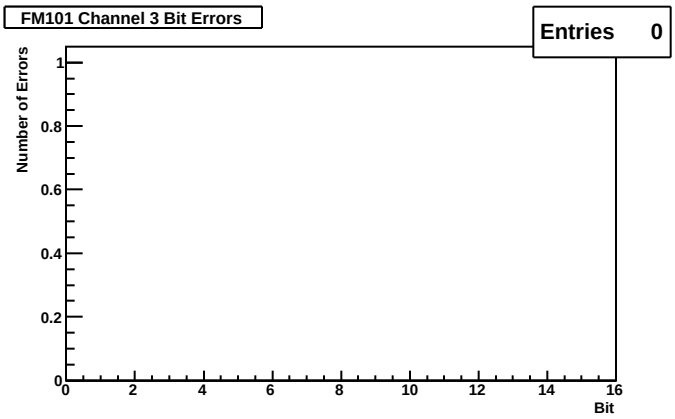

FP201 Channel 6 Bit Errors
Entries 475606
Number of Errors
Bit

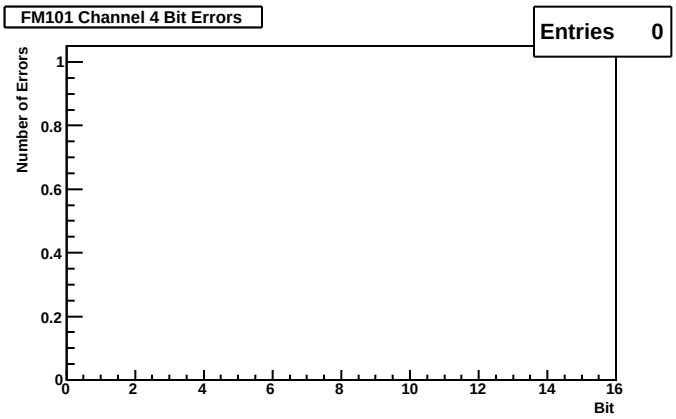

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

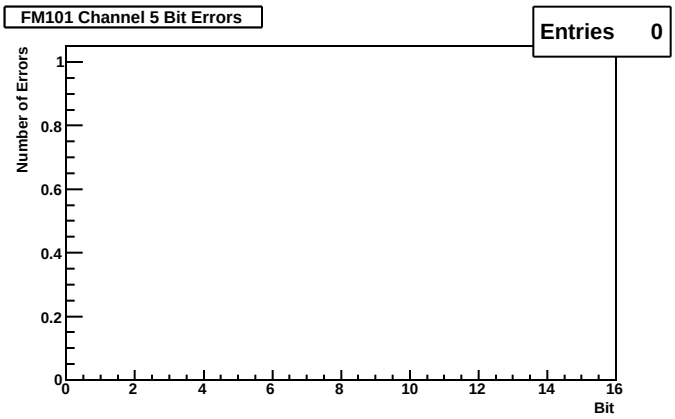

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

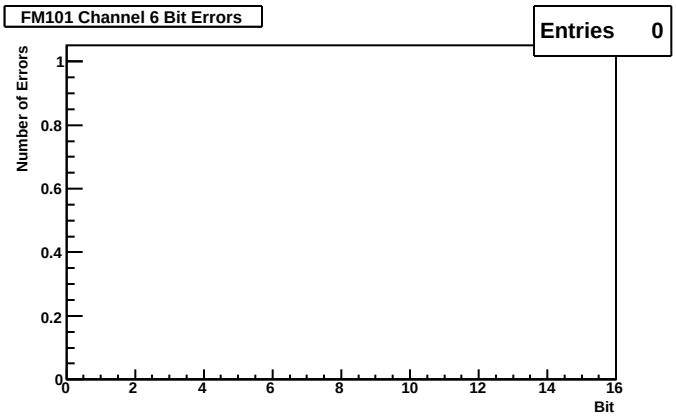

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

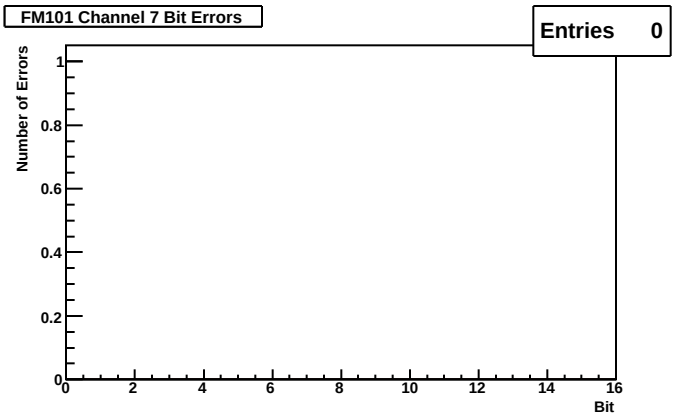

FM101 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

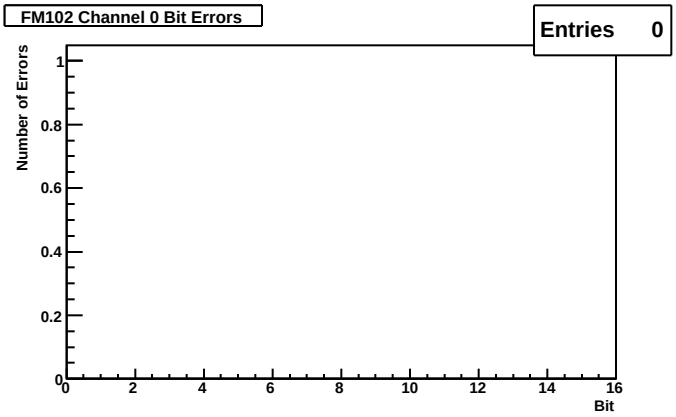

FM101 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

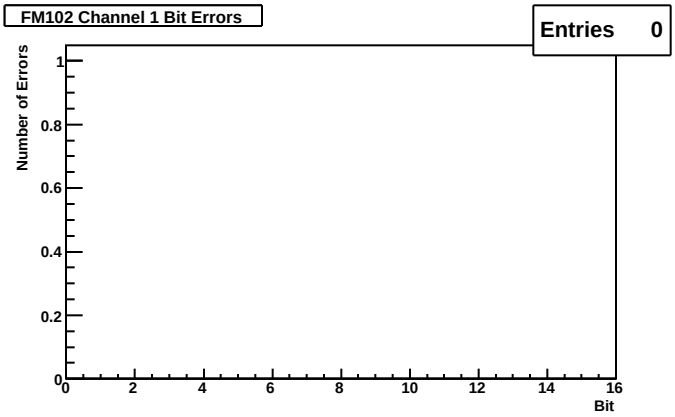

FM101 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

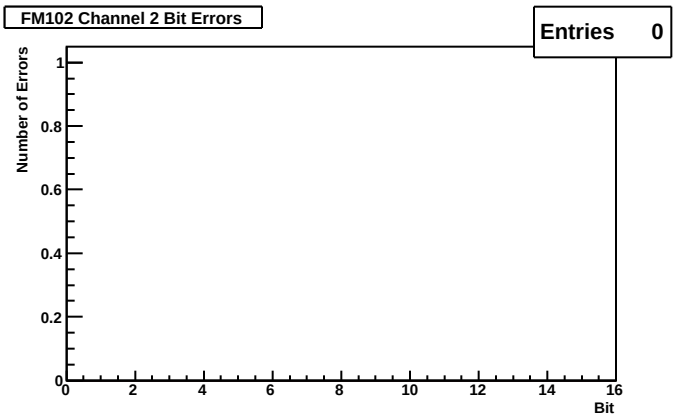

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

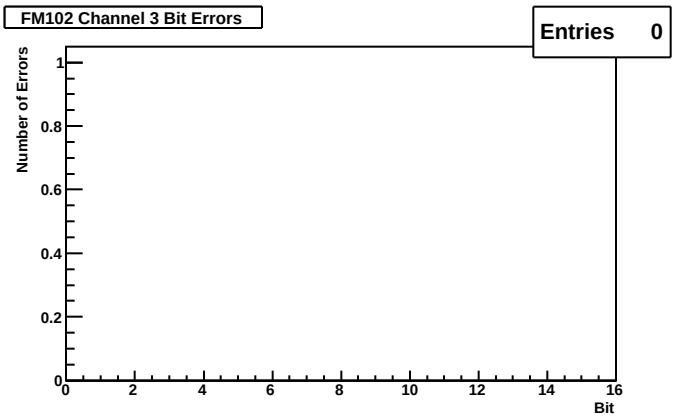

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

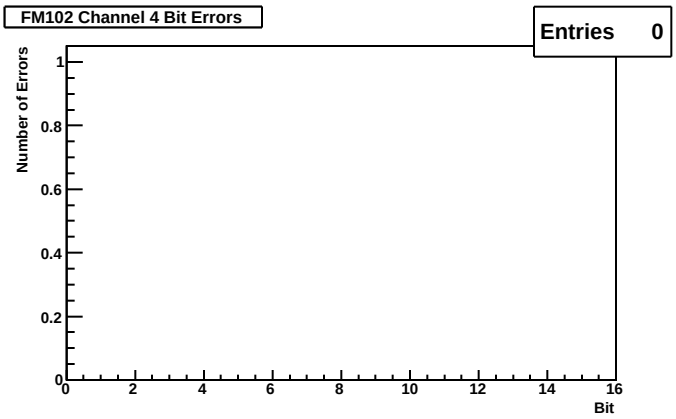

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

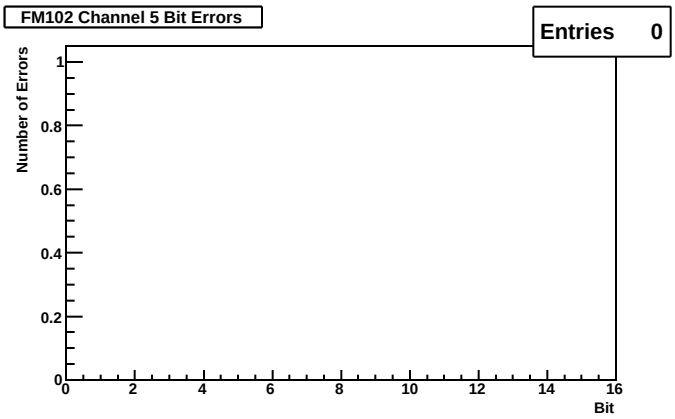

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

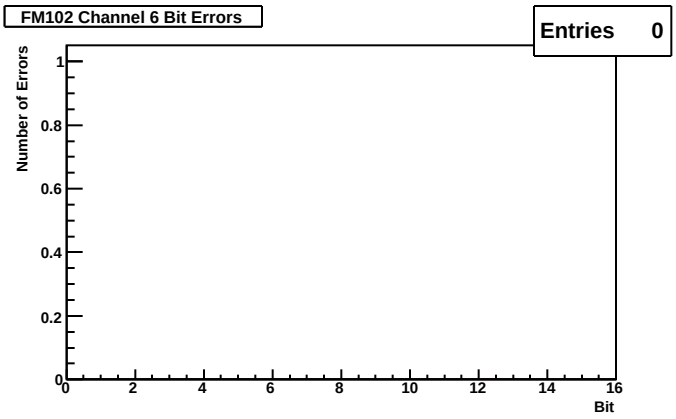

FM102 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

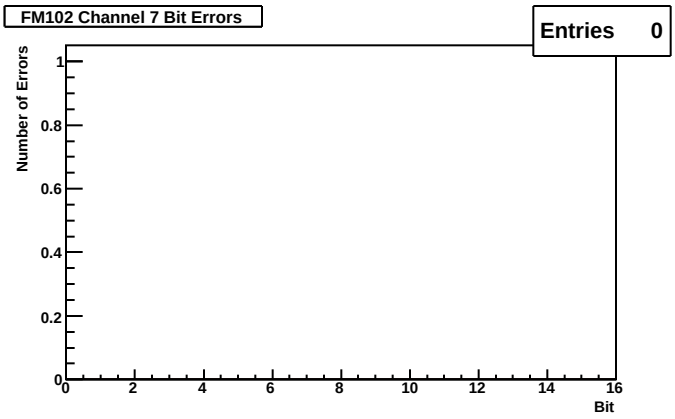

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

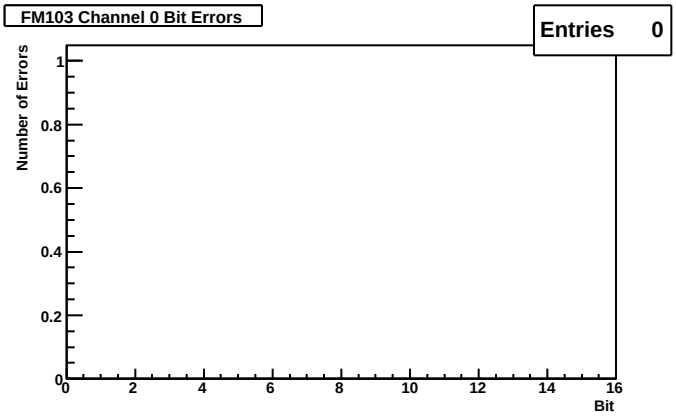

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

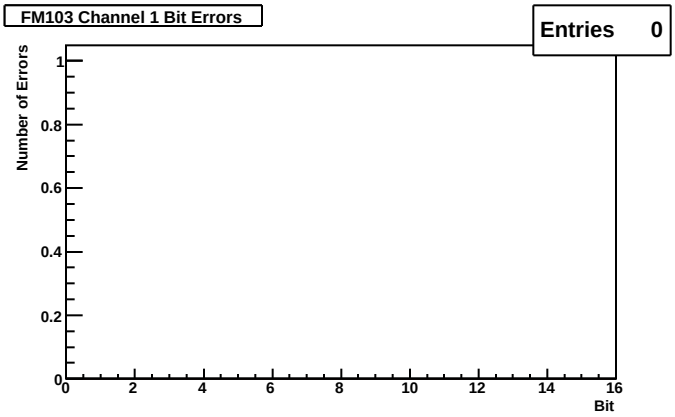

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

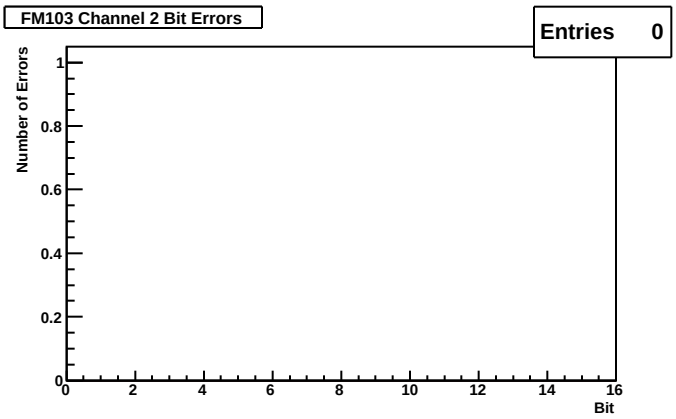

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

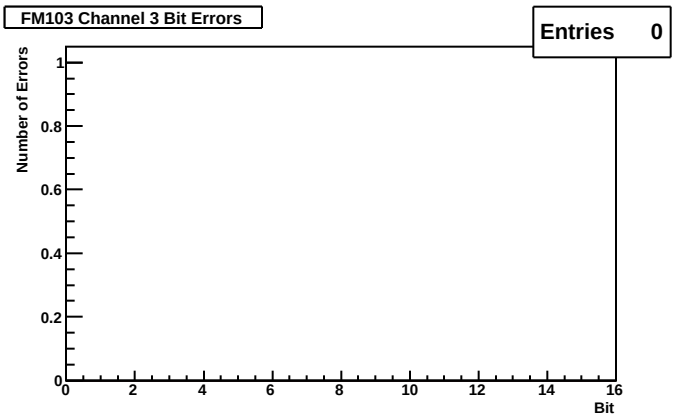

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

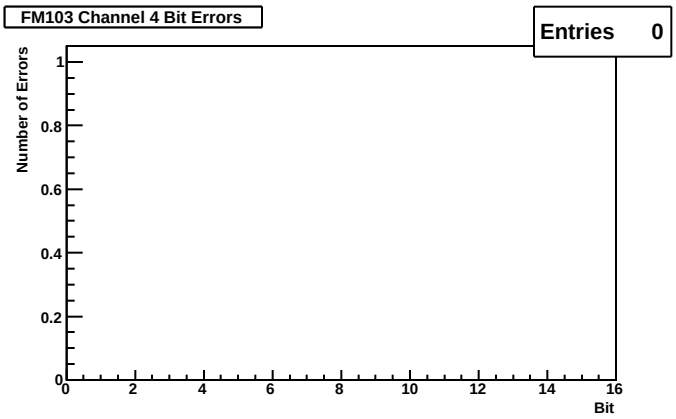

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

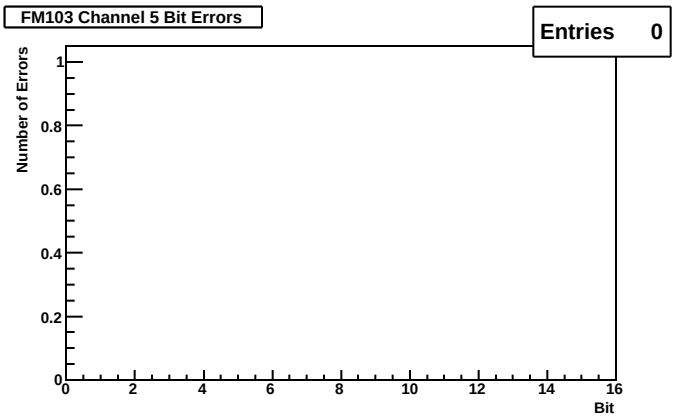

FM103 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

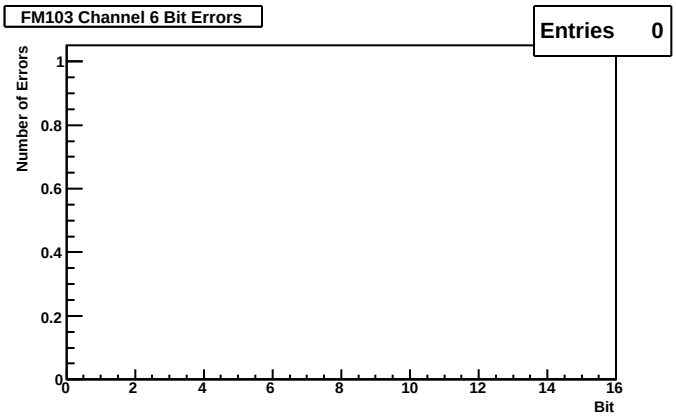

FM103 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

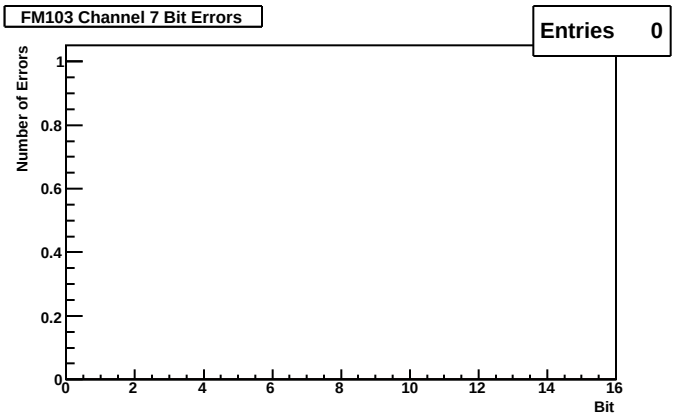

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

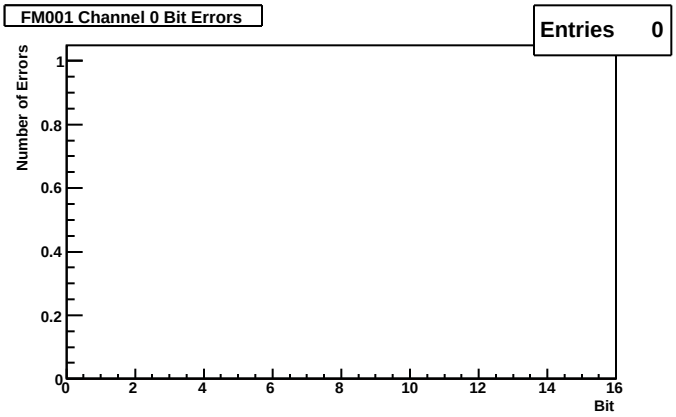

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

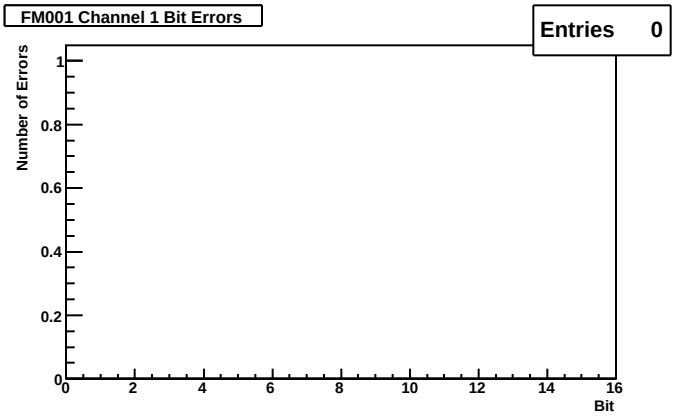

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

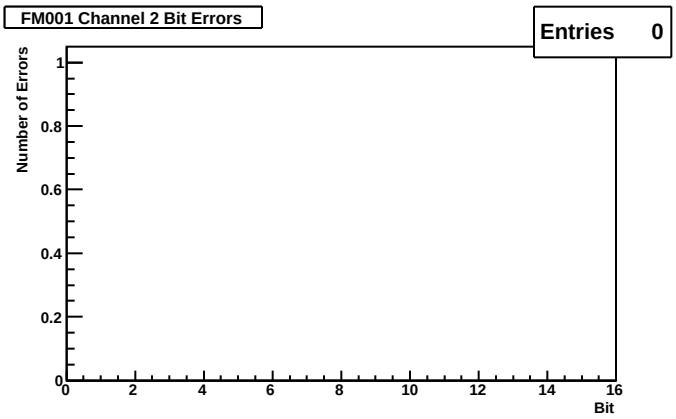

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

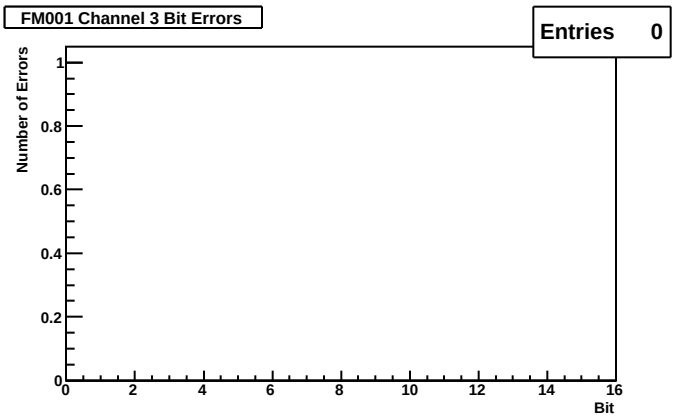

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

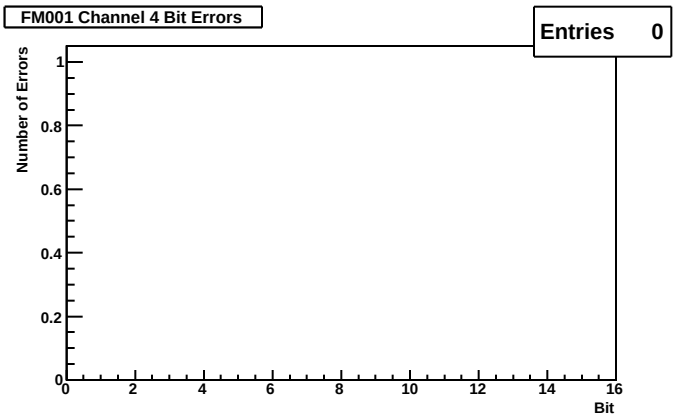

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

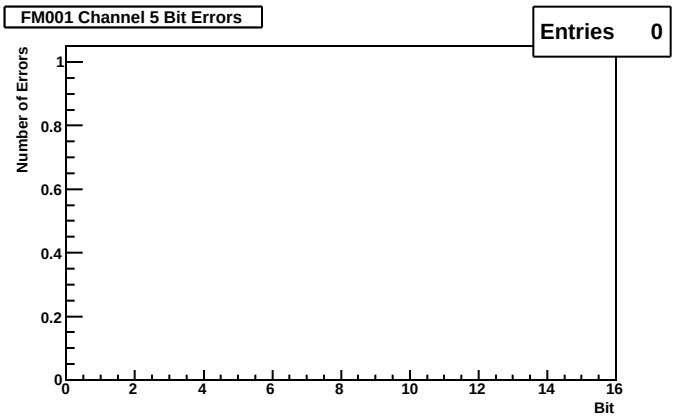

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

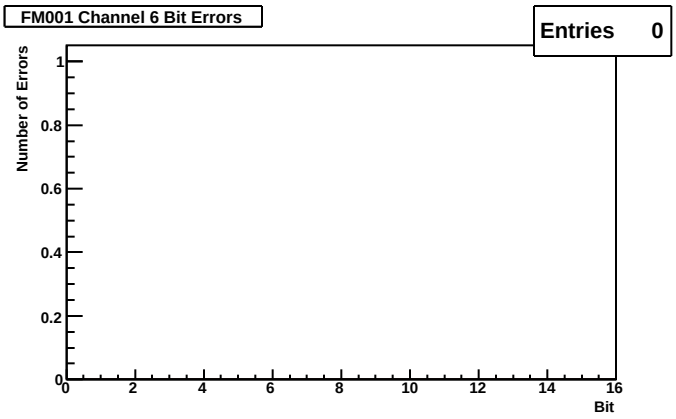

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

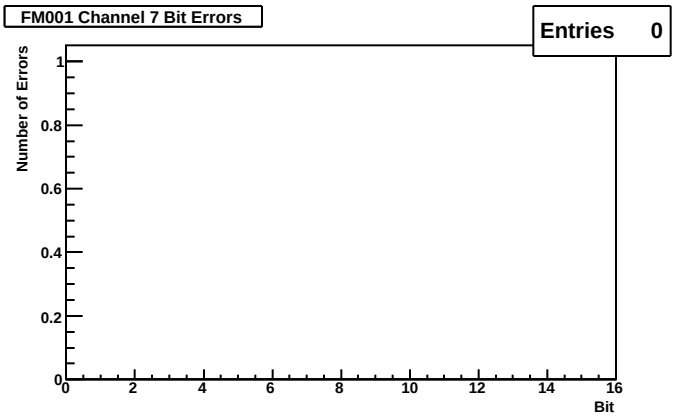

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

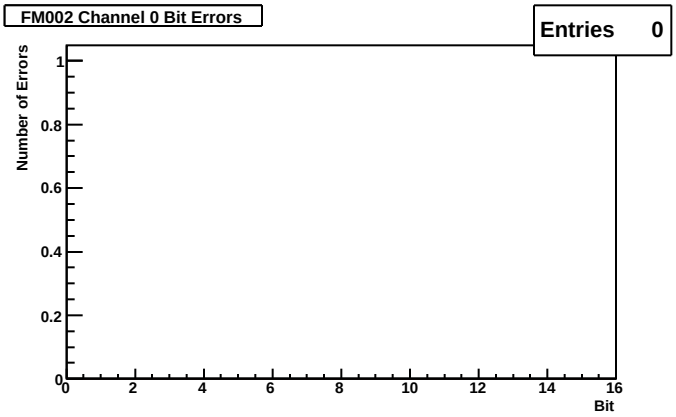

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

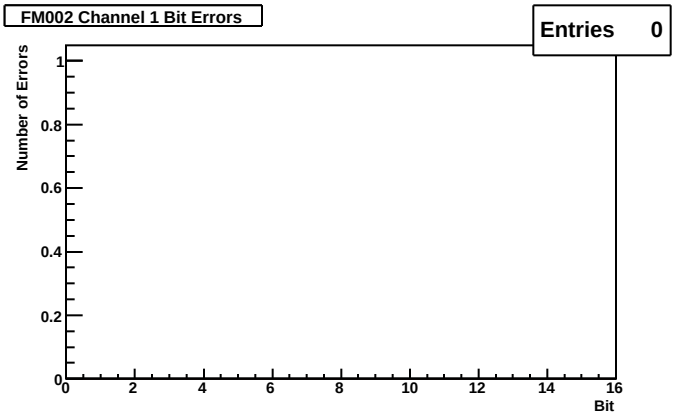

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

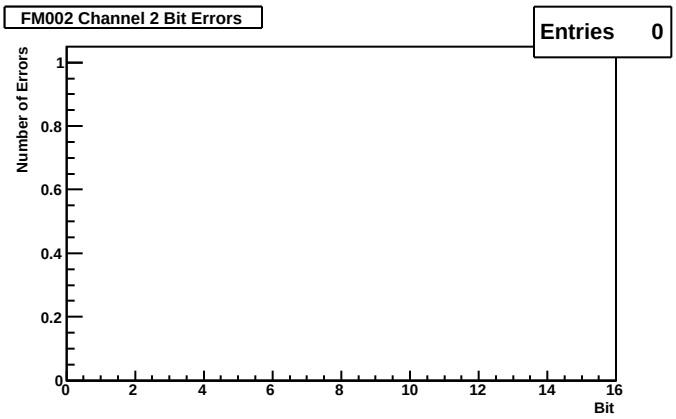

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

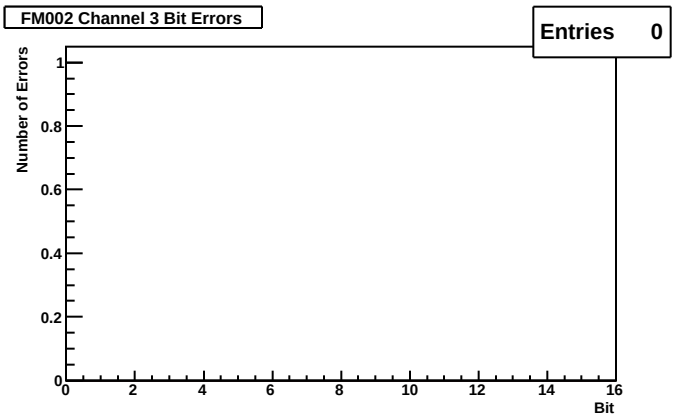

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

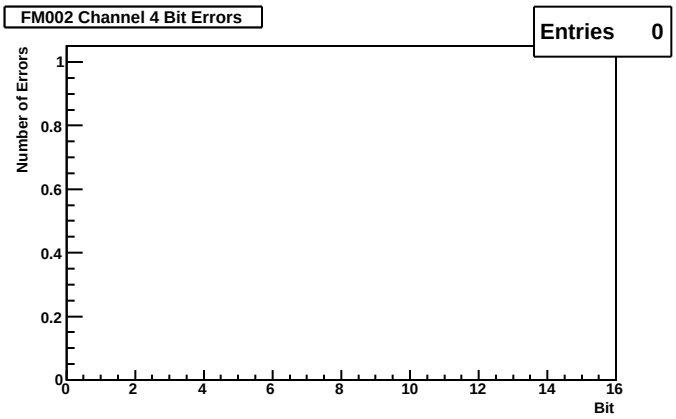

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

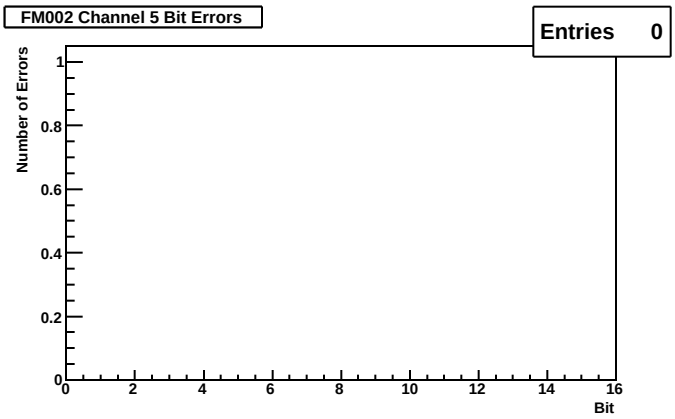

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

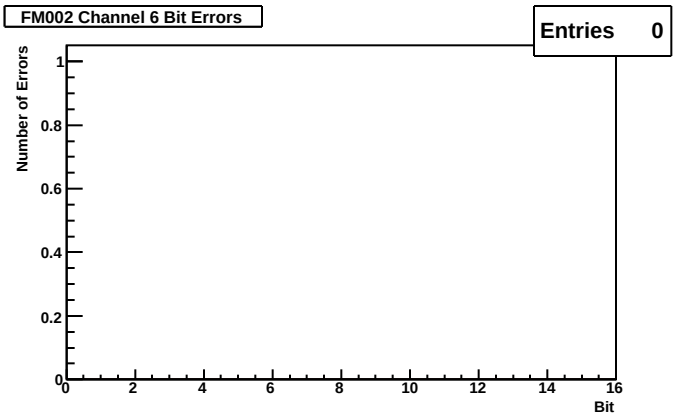

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

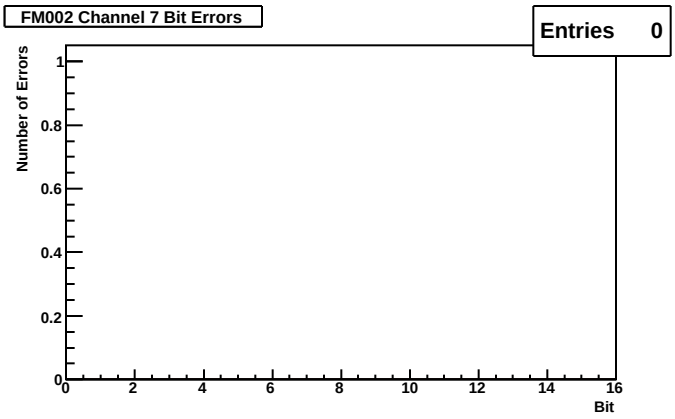

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

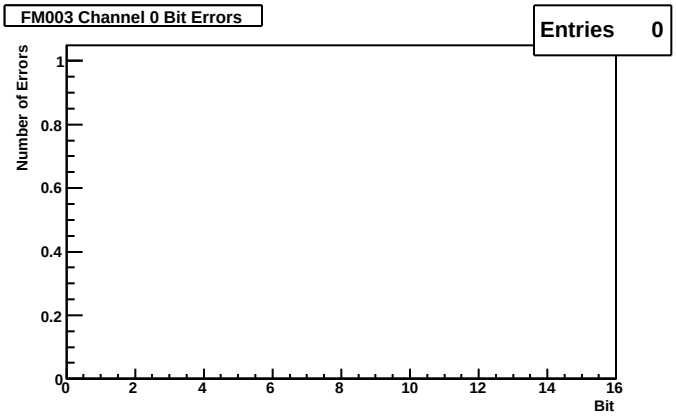

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

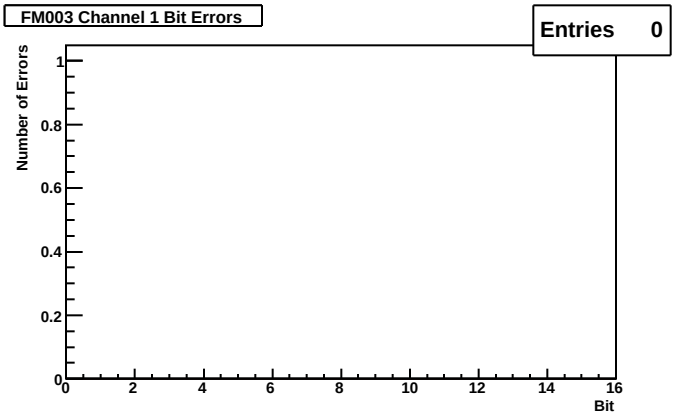

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

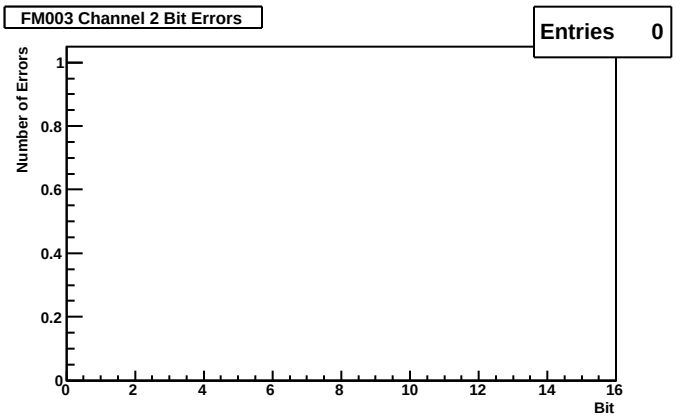

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

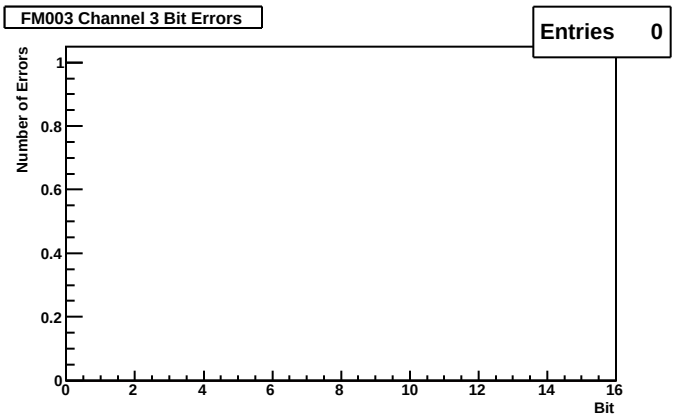

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

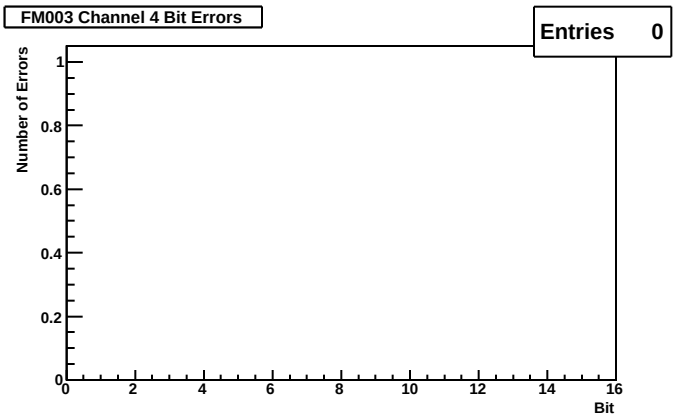

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

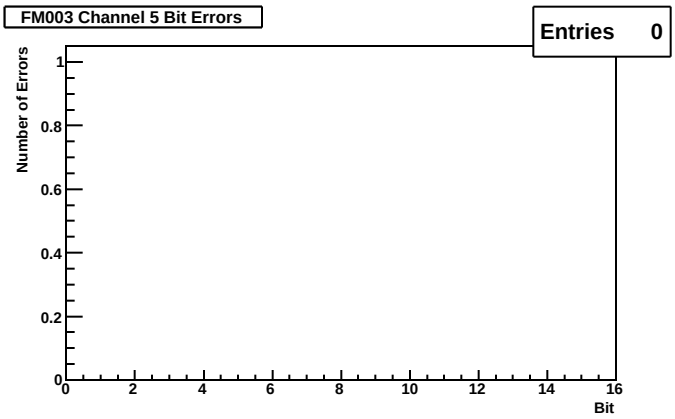

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

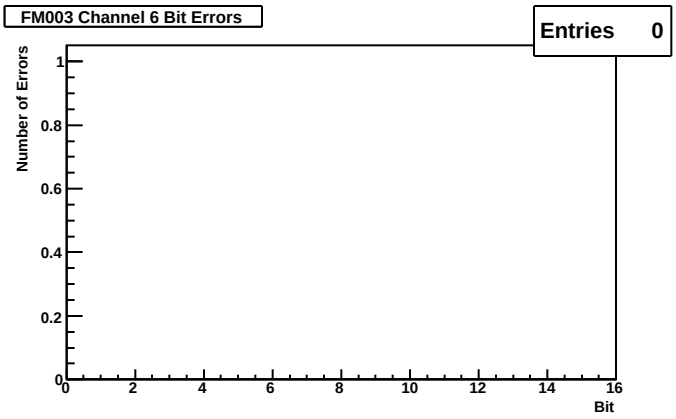

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

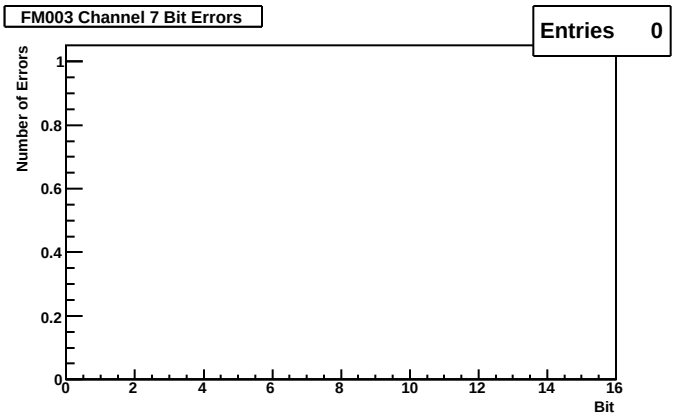

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

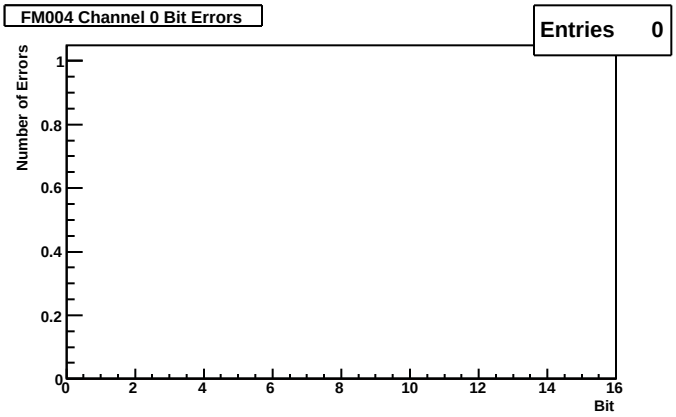

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

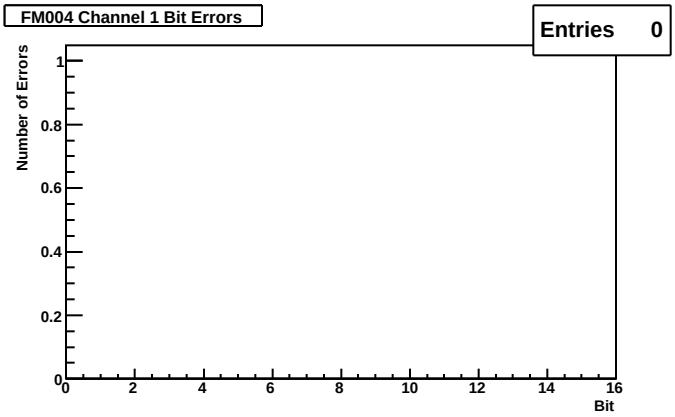

FM003 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

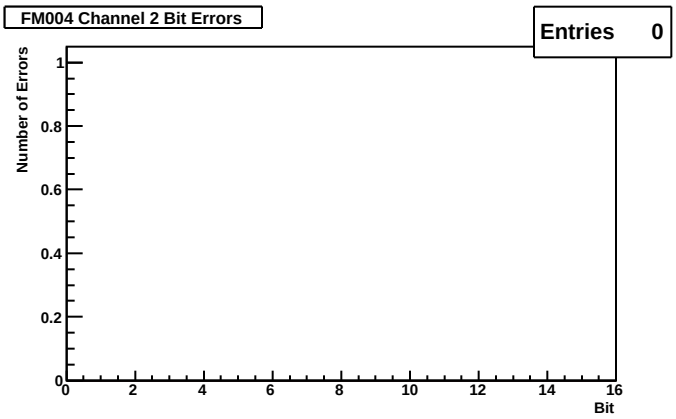

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

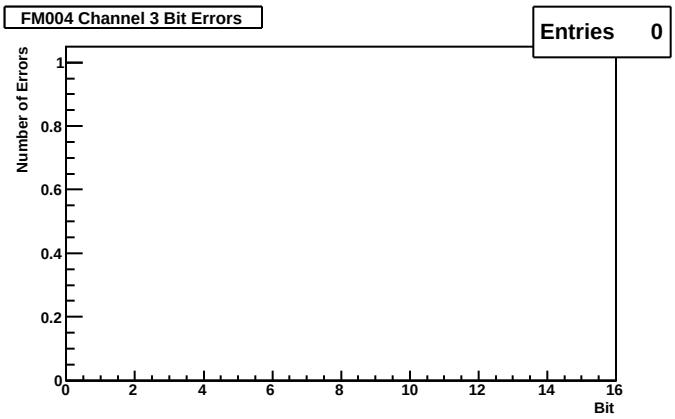

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

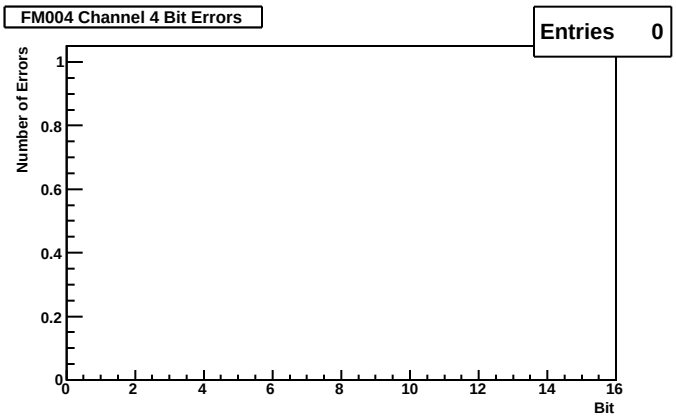

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

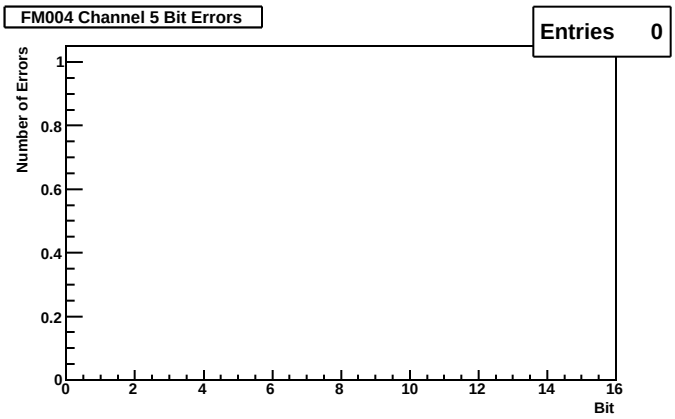

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

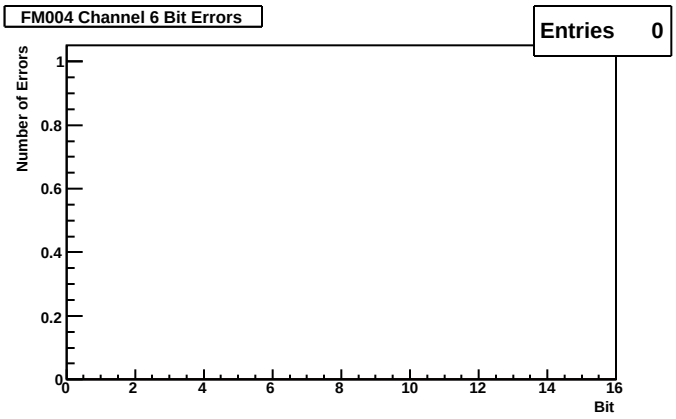

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

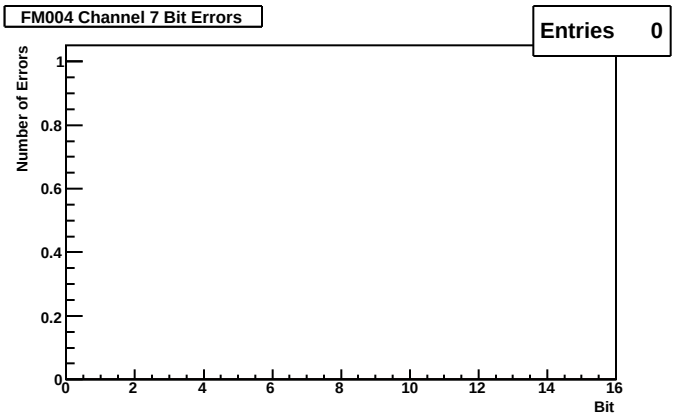

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

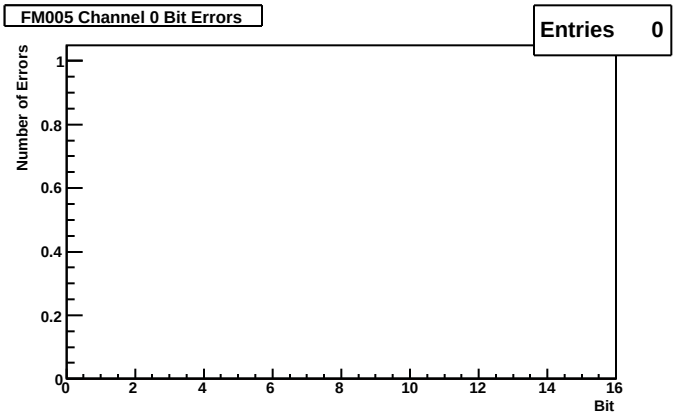

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

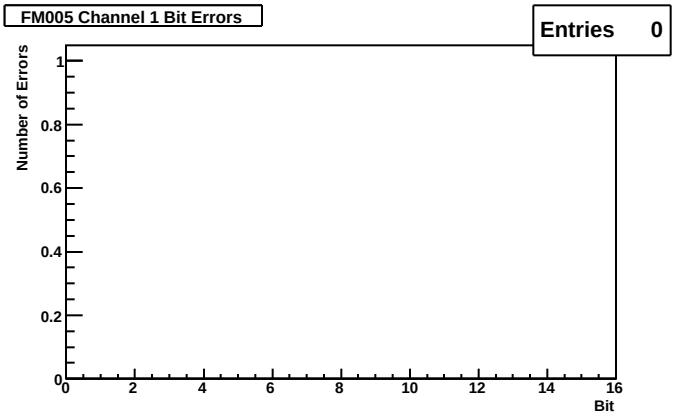

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

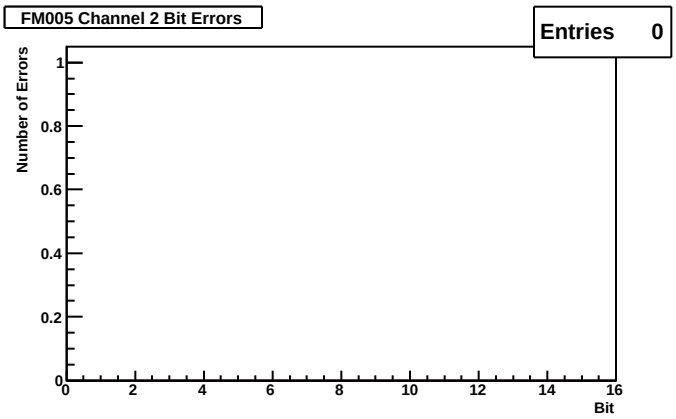

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

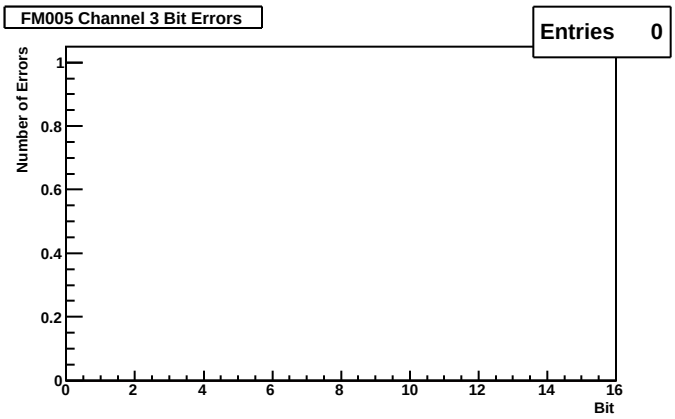

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

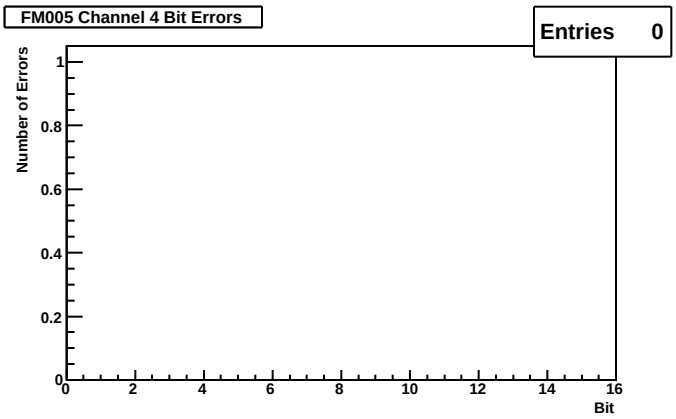

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

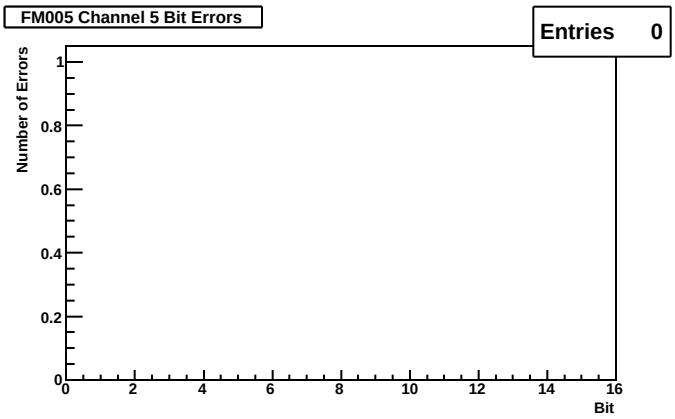

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

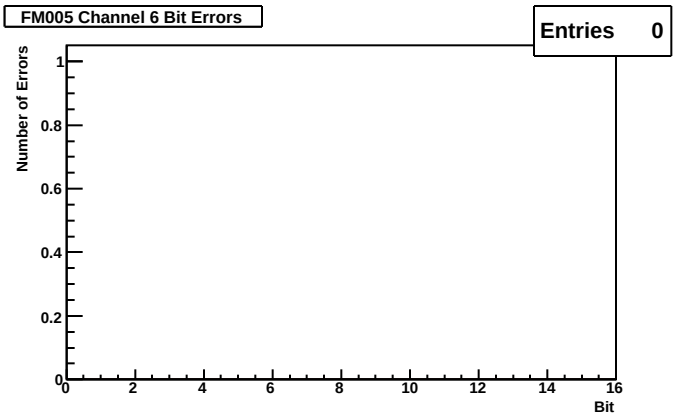

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

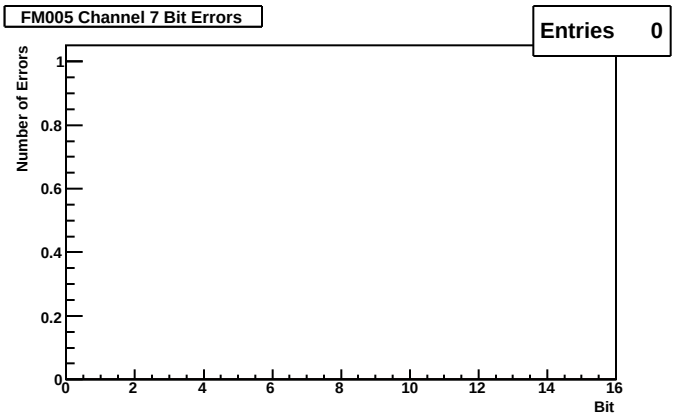

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

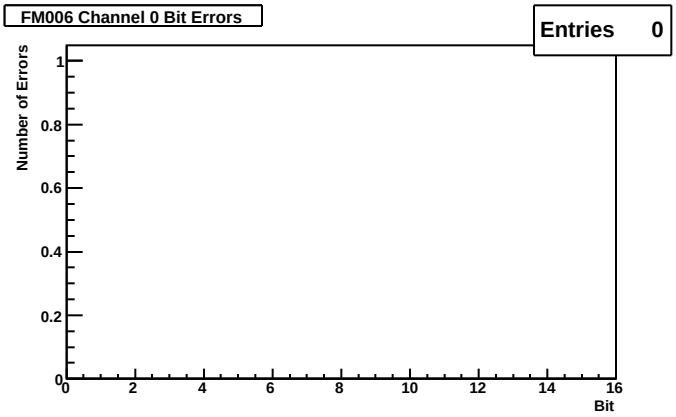

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

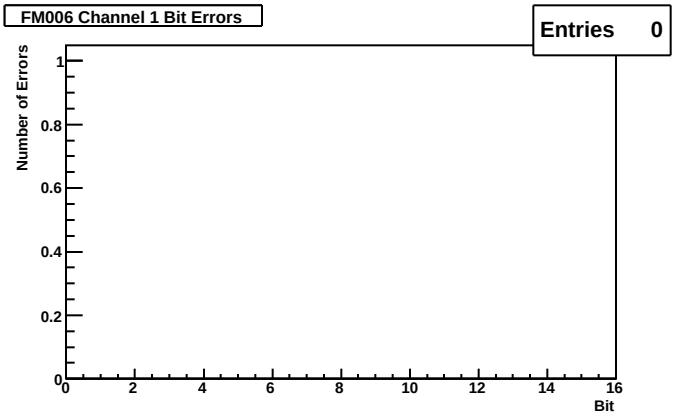

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

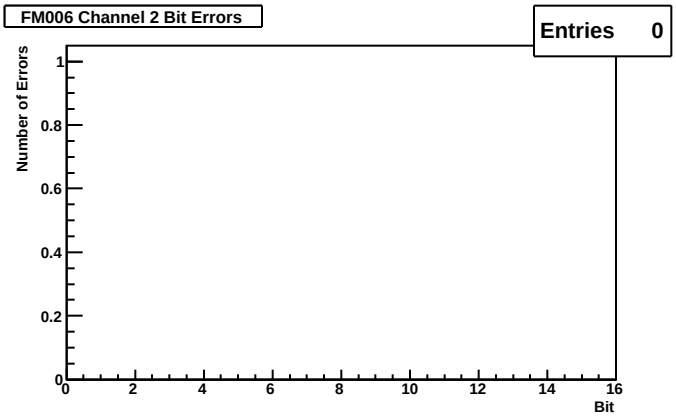

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

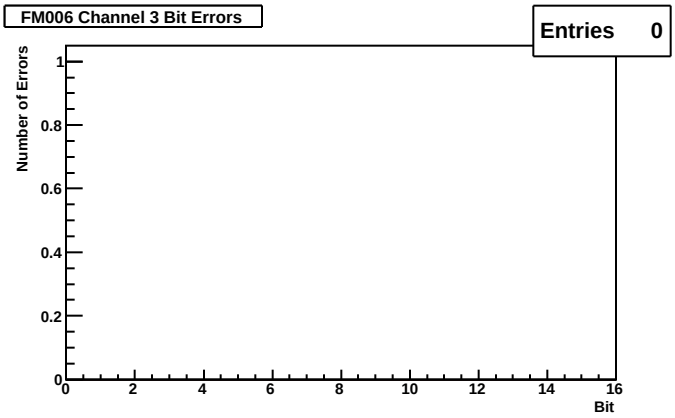

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

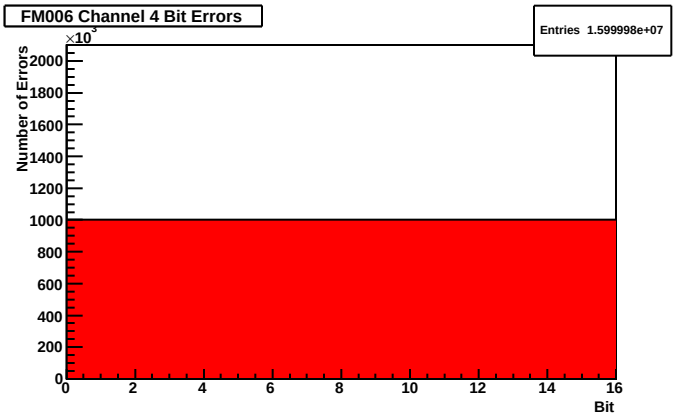

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

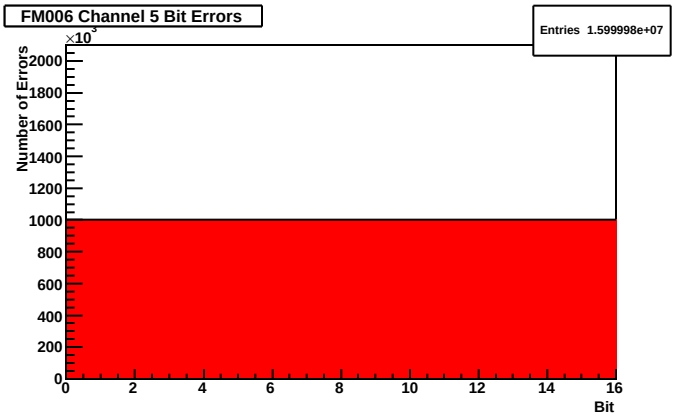

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

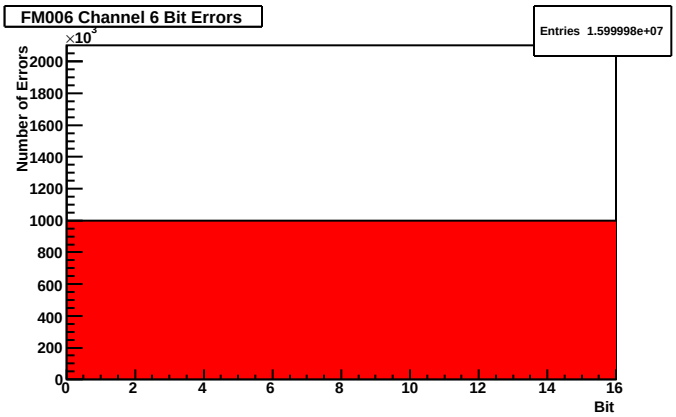

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

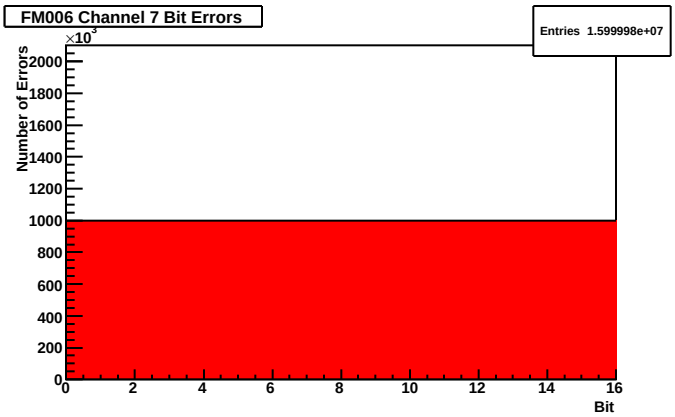

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

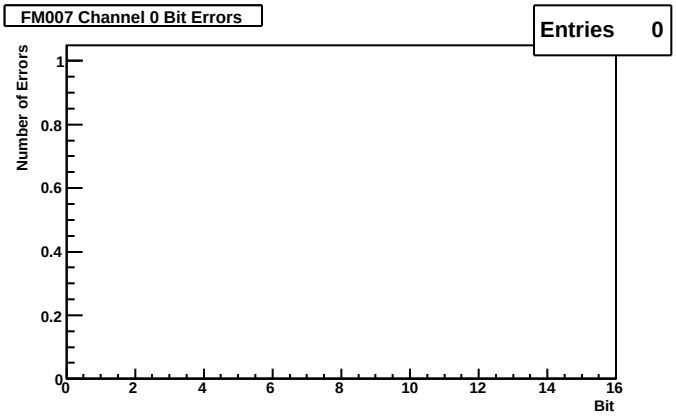

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

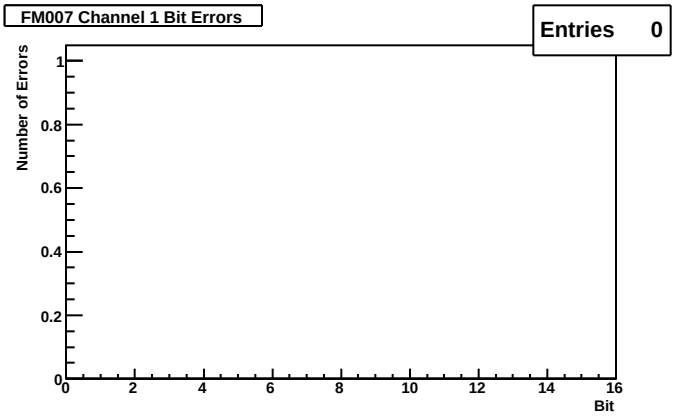

FM006 Channel 4 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

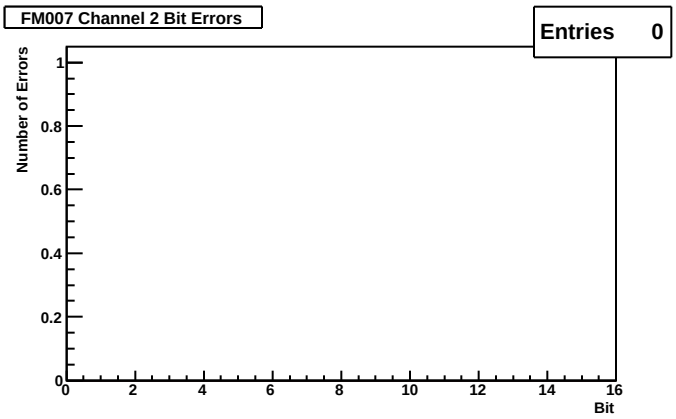

FM006 Channel 5 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

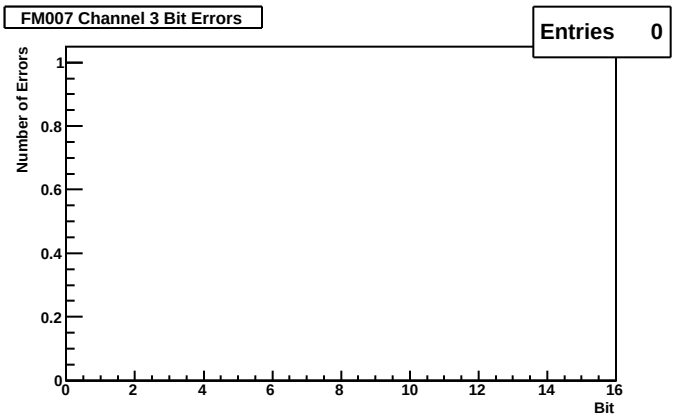

FM006 Channel 6 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

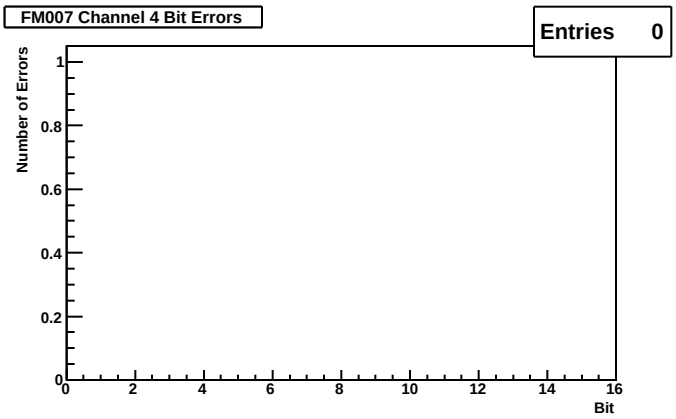

FM006 Channel 7 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

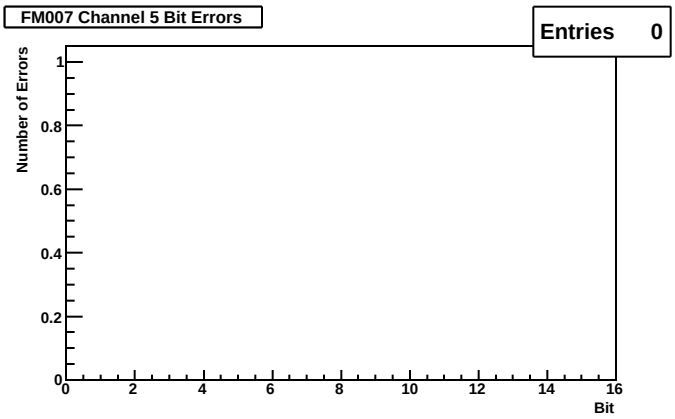

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

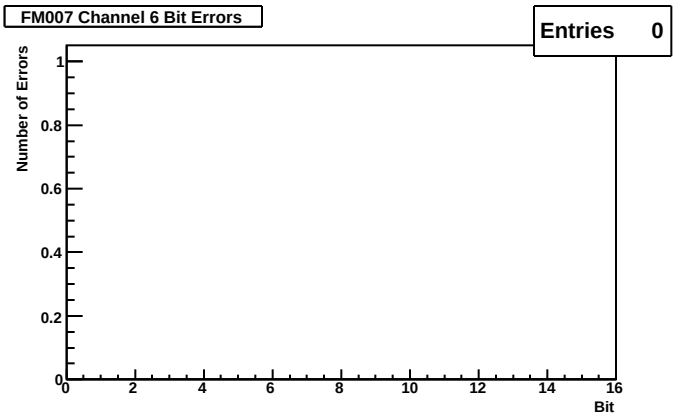

FM007 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

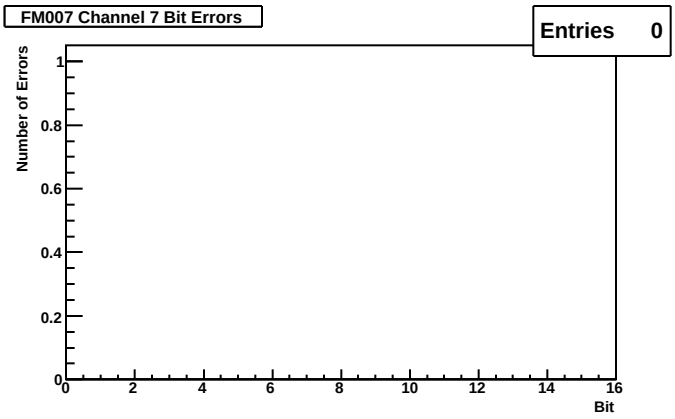

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

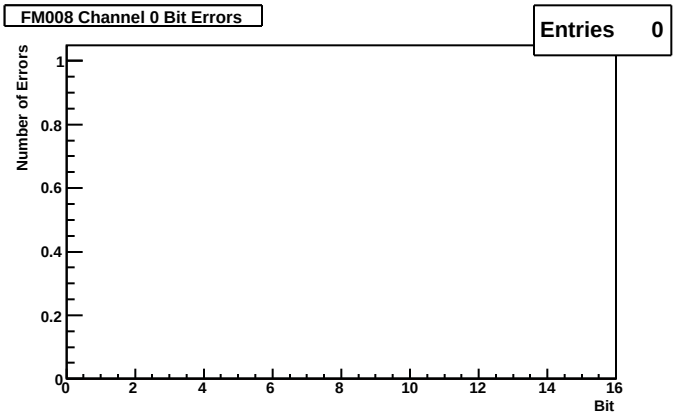

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

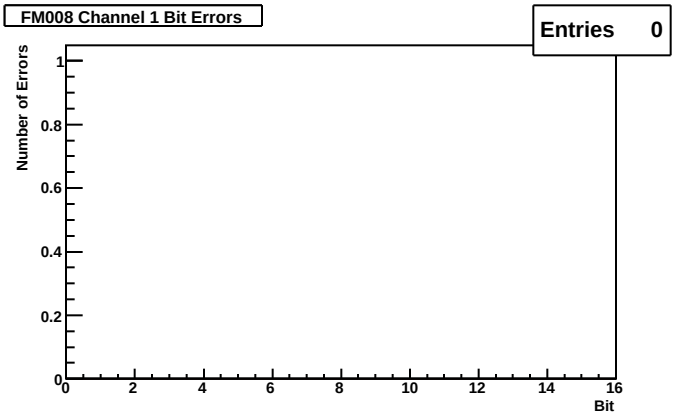

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

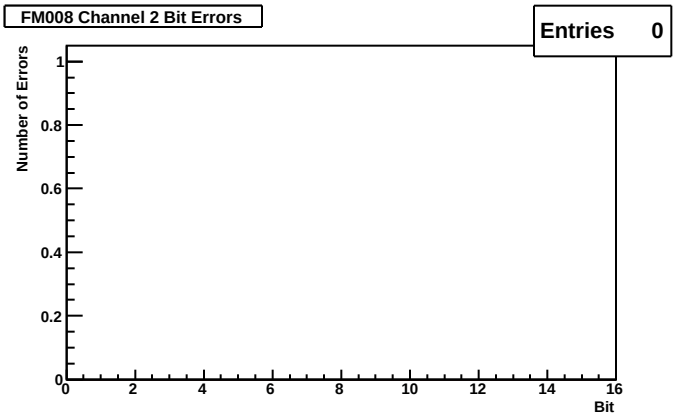

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

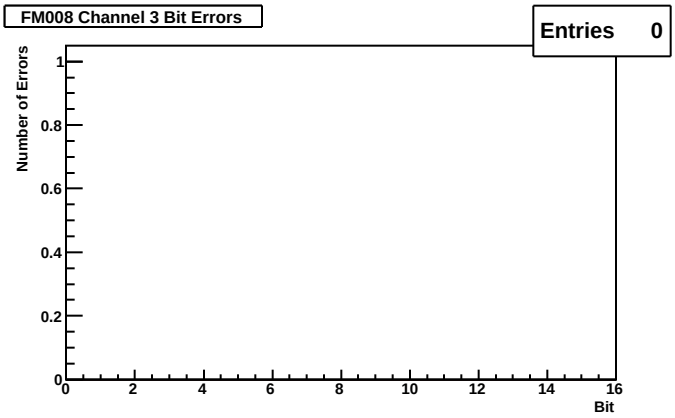

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

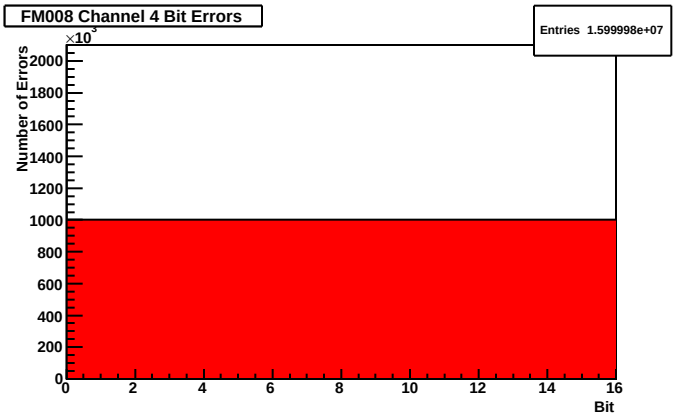

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

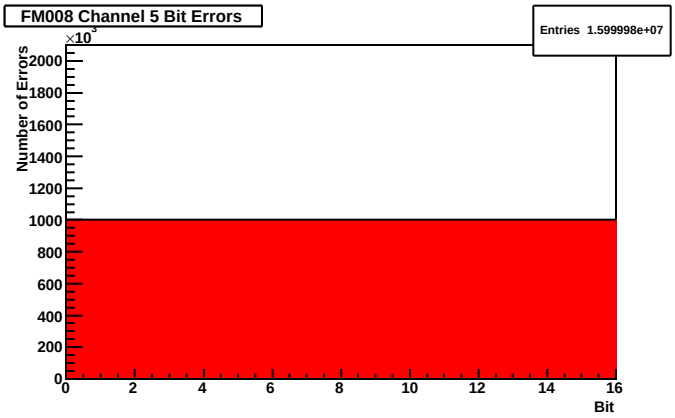

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

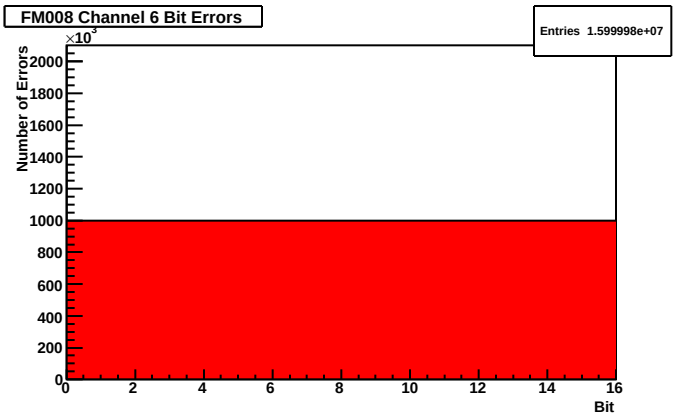

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

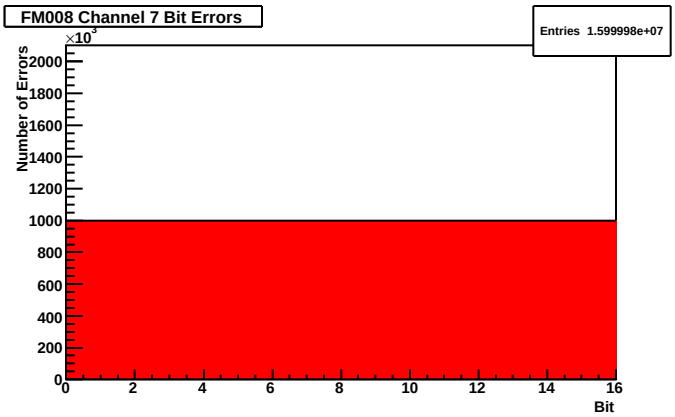

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

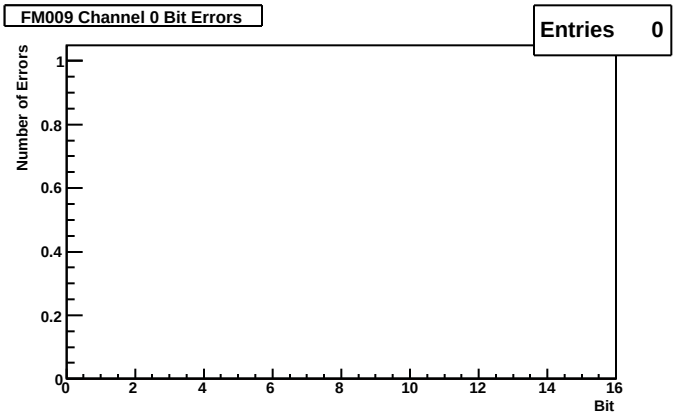

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

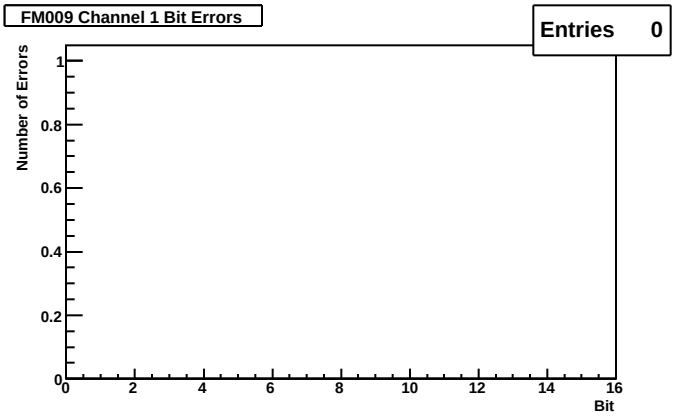

FM008 Channel 4 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

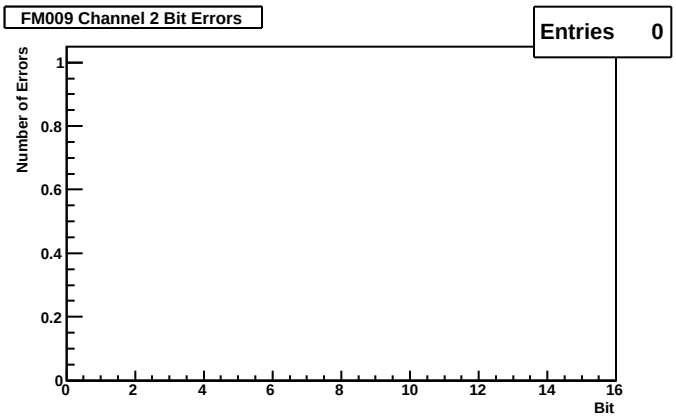

FM008 Channel 5 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

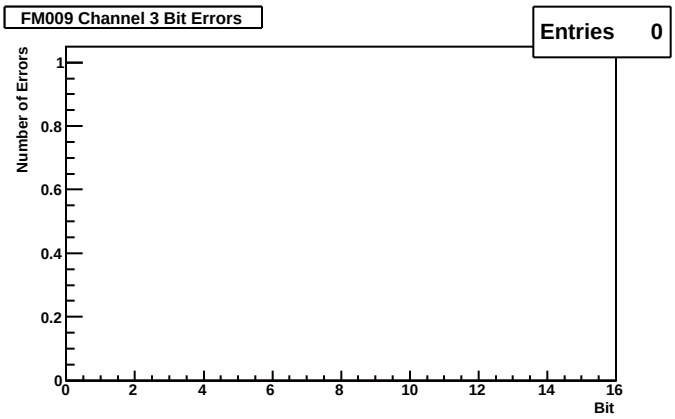

FM008 Channel 6 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

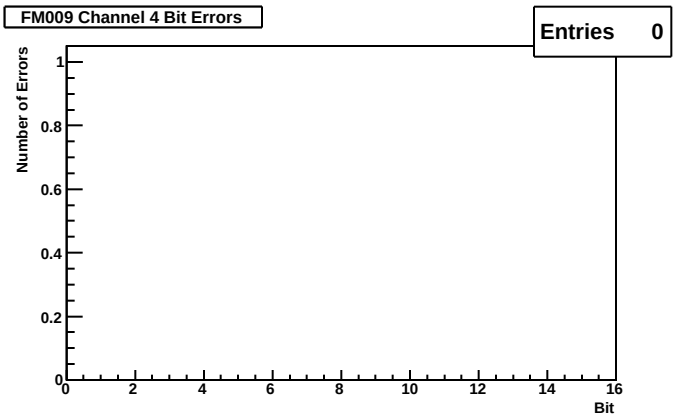

FM008 Channel 7 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

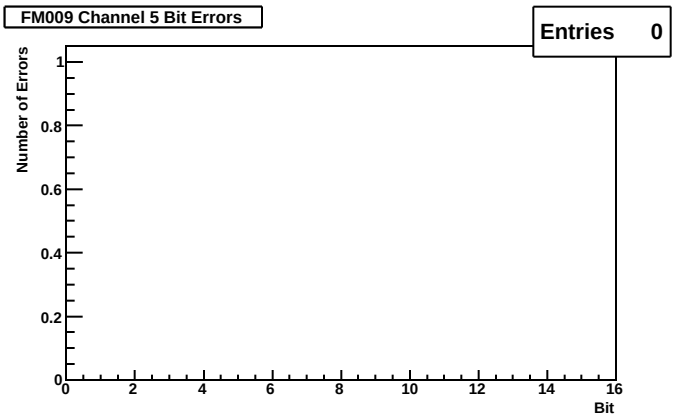

FM009 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

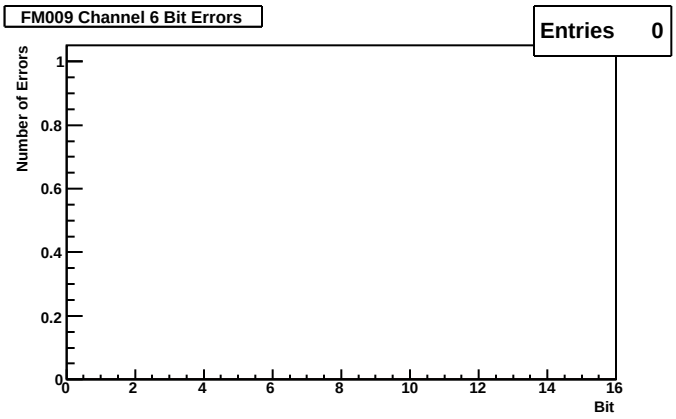

FM009 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

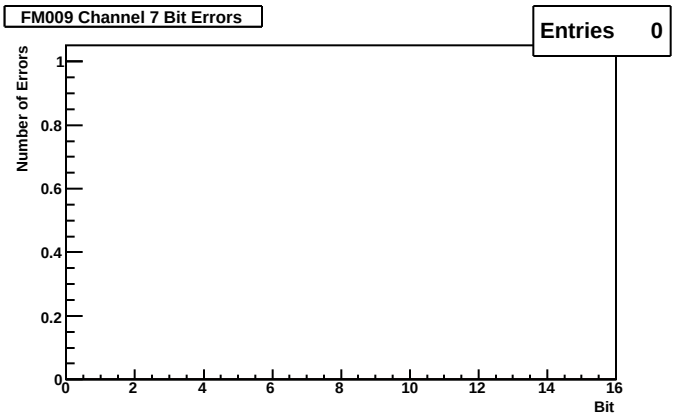

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

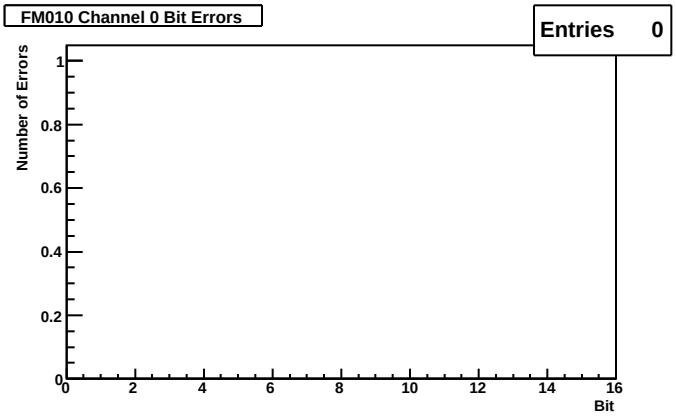

FM009 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

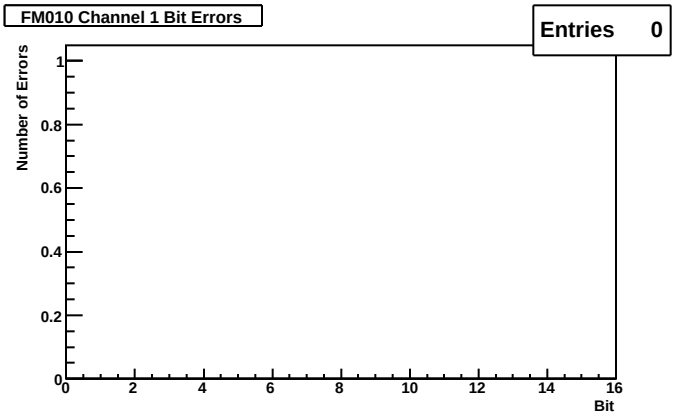

FM009 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

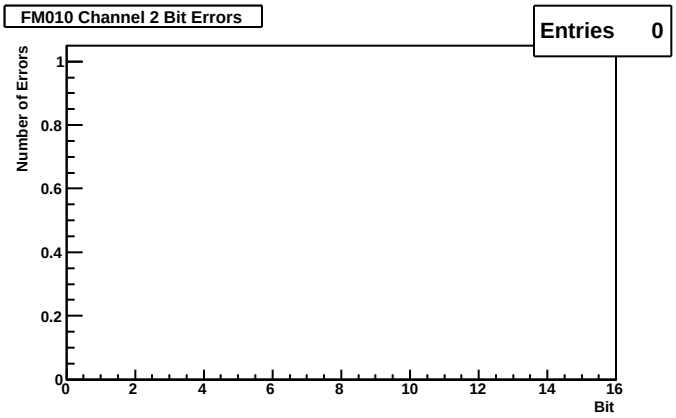

FM009 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

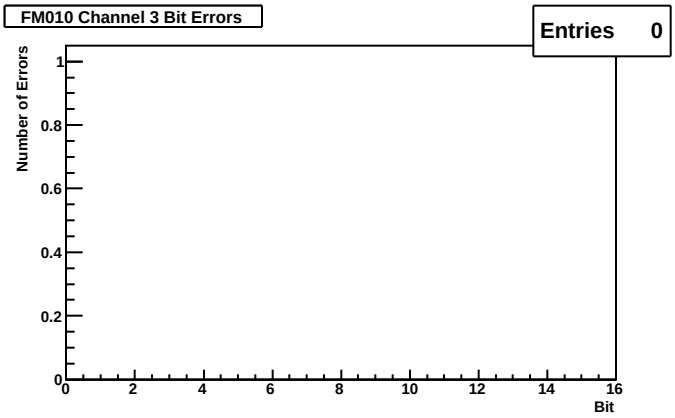

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

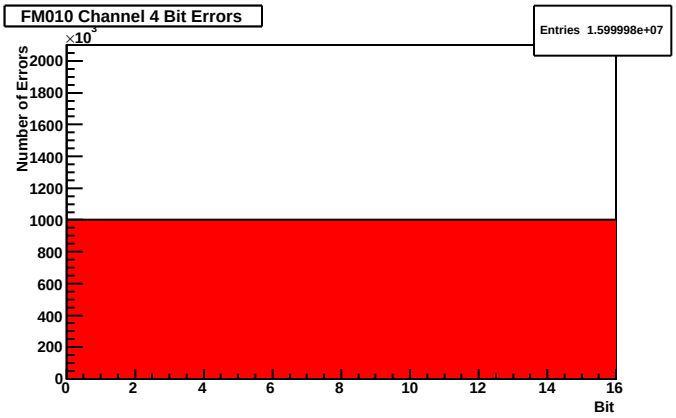

FM009 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

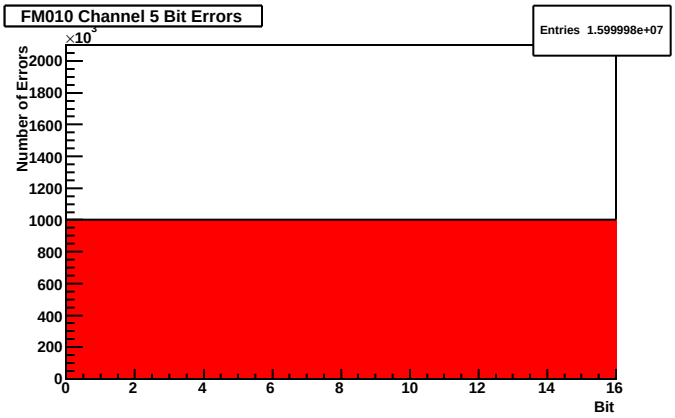

FM010 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

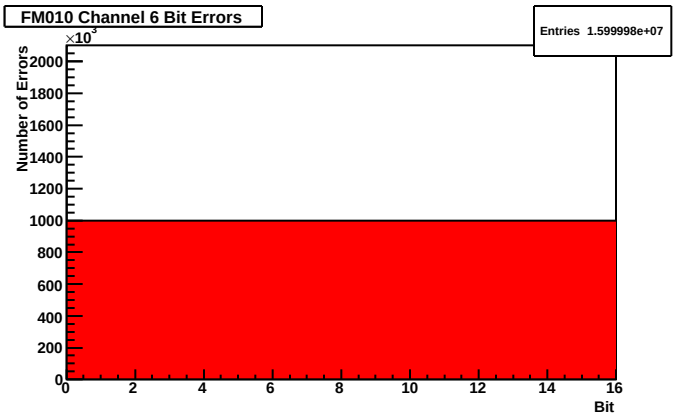

FM010 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

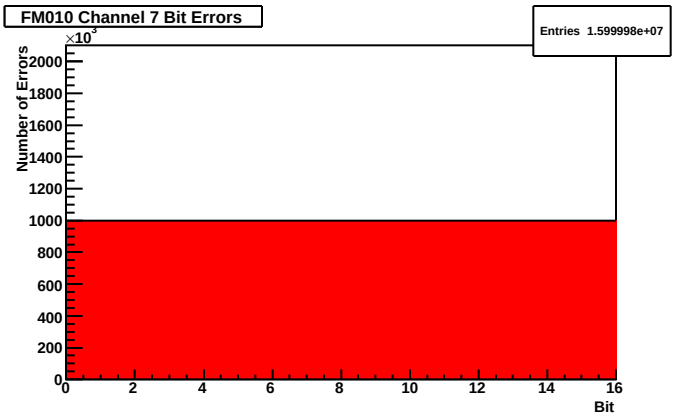

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

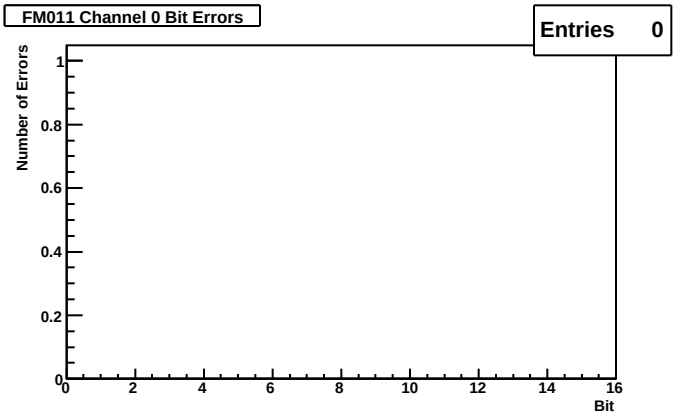

FM010 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

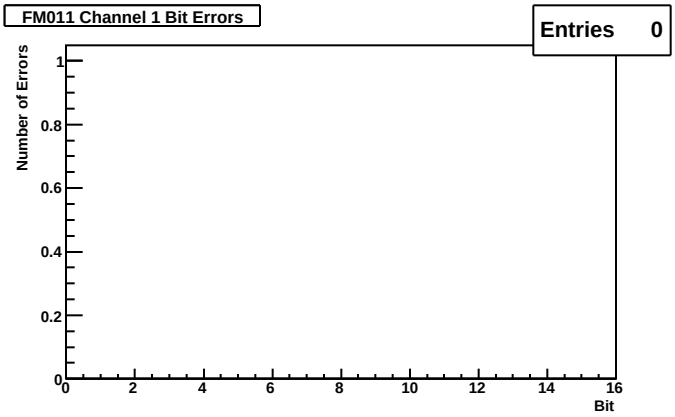

FM010 Channel 4 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

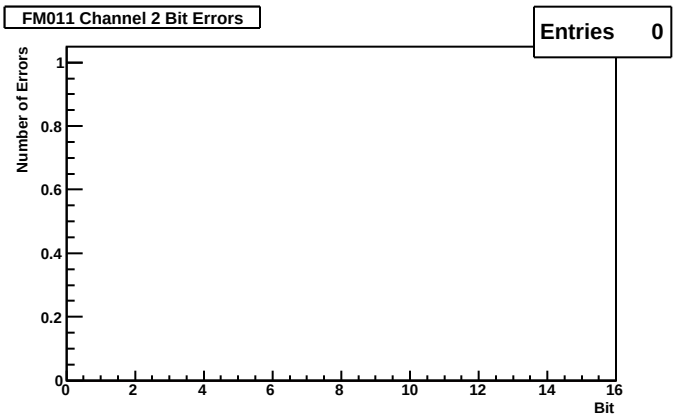

FM010 Channel 5 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

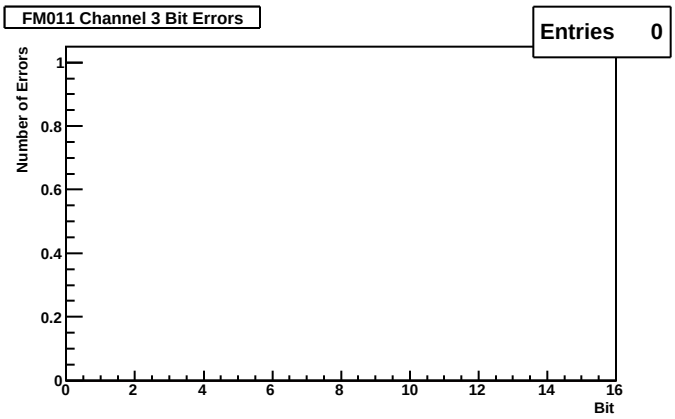

FM010 Channel 6 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

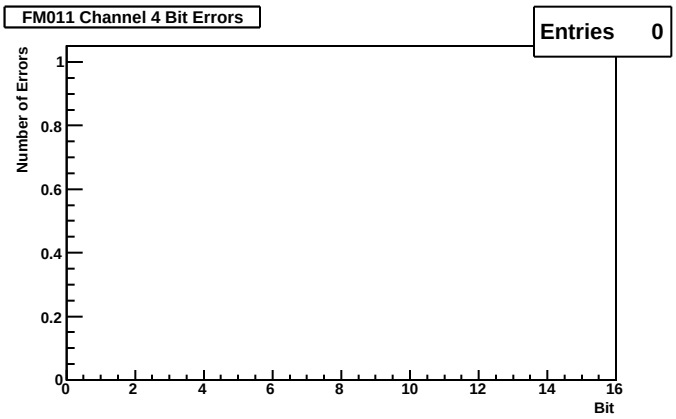

FM010 Channel 7 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit

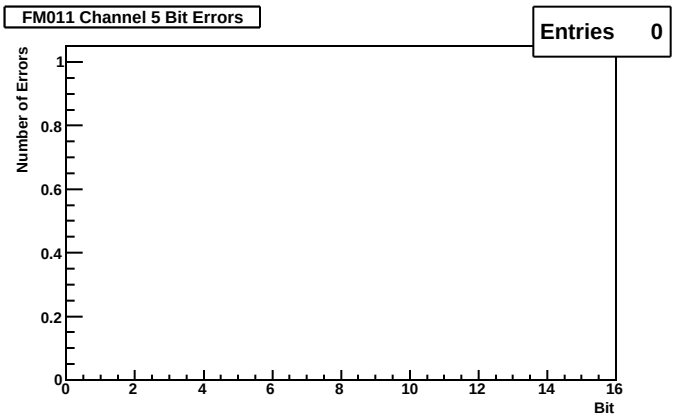

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

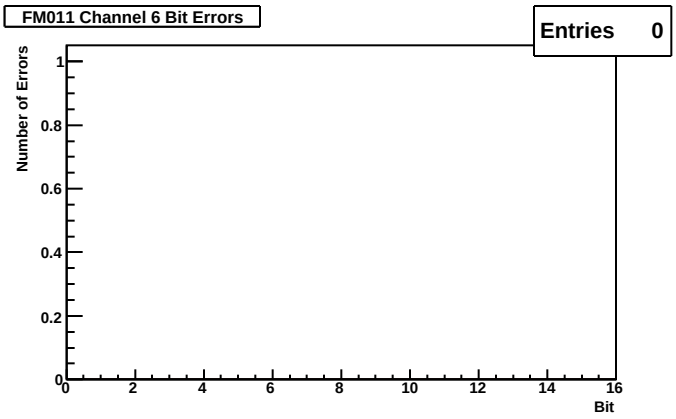

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

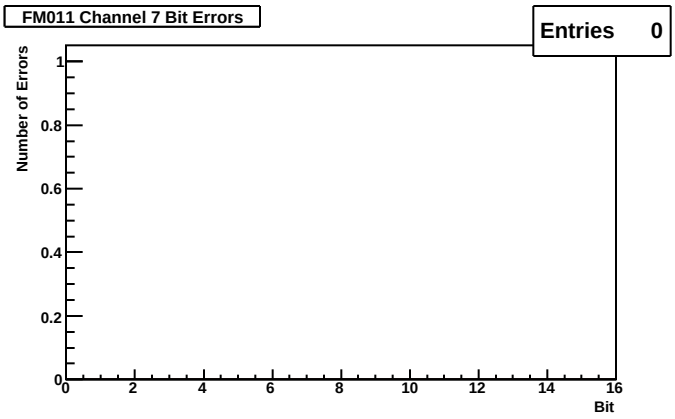

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

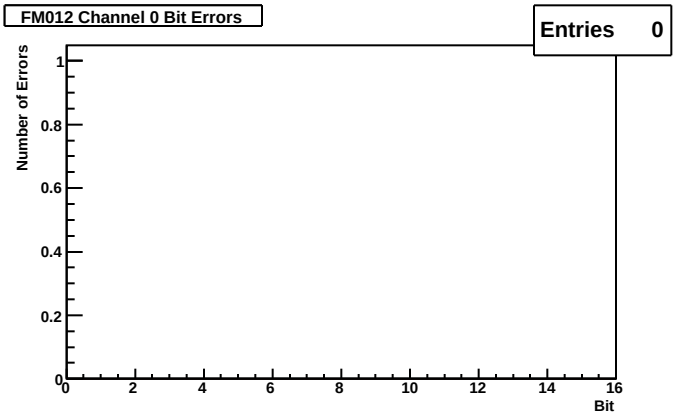

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

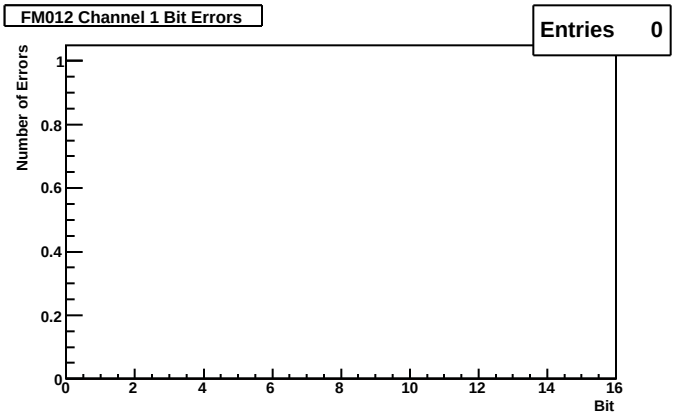

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

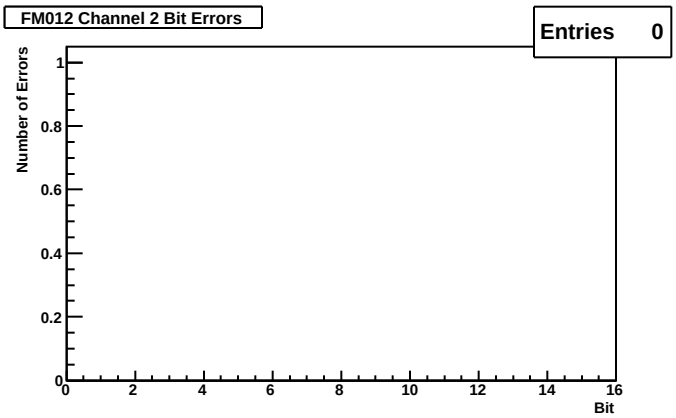

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

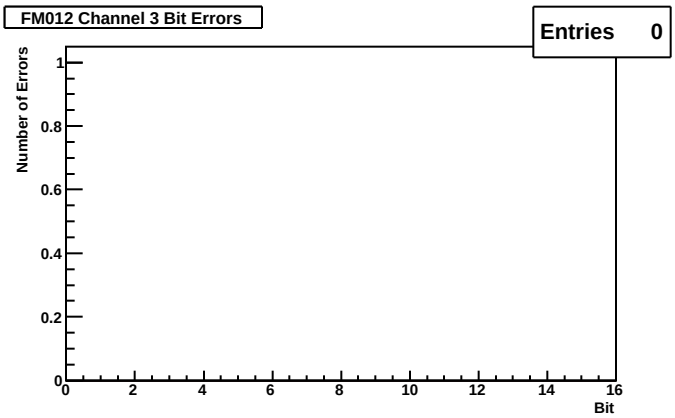

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

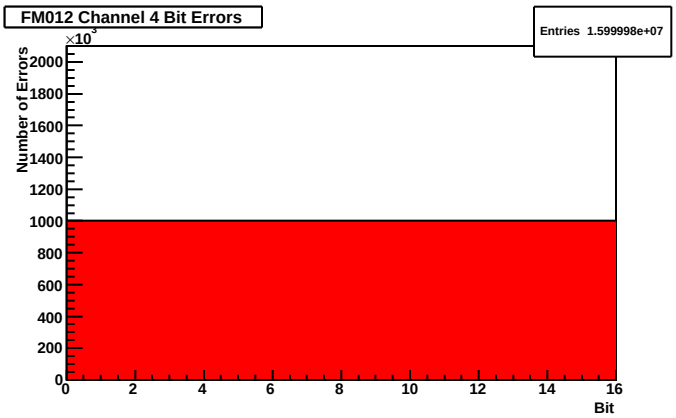

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

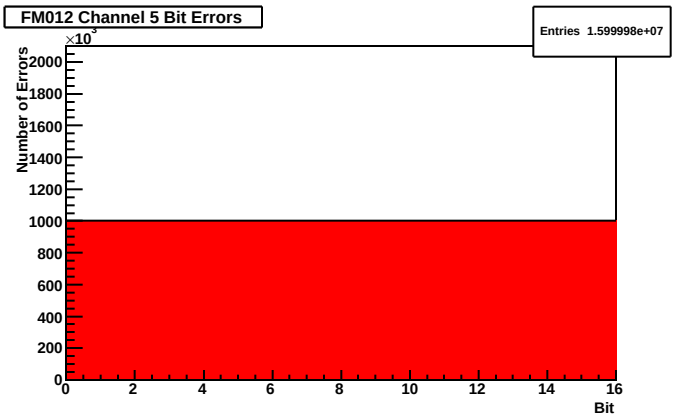

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

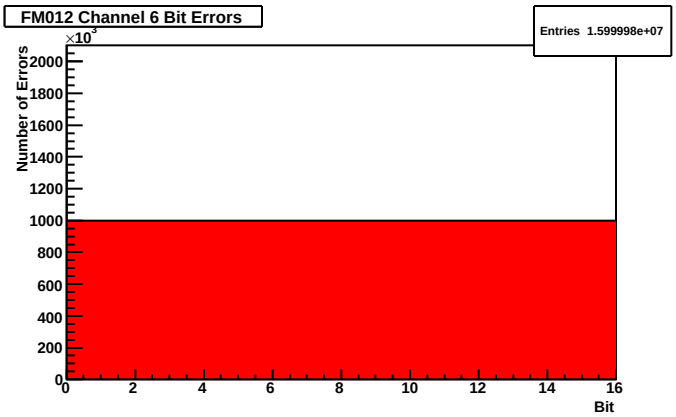

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

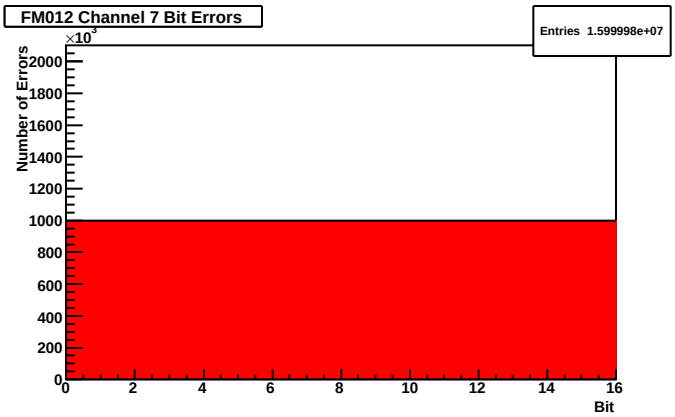

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 1.599998e+07
Number of Errors
Bit