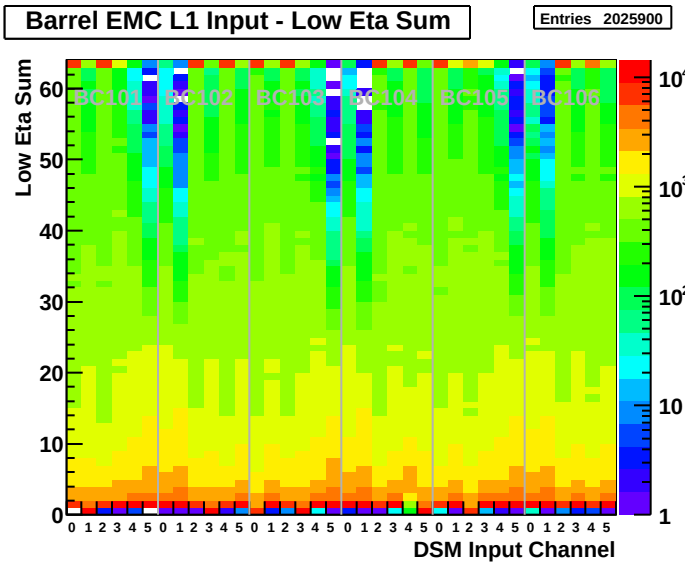
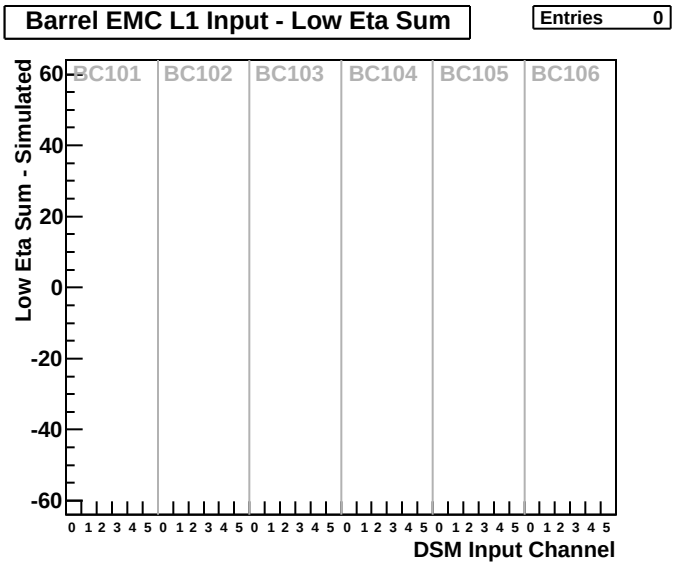
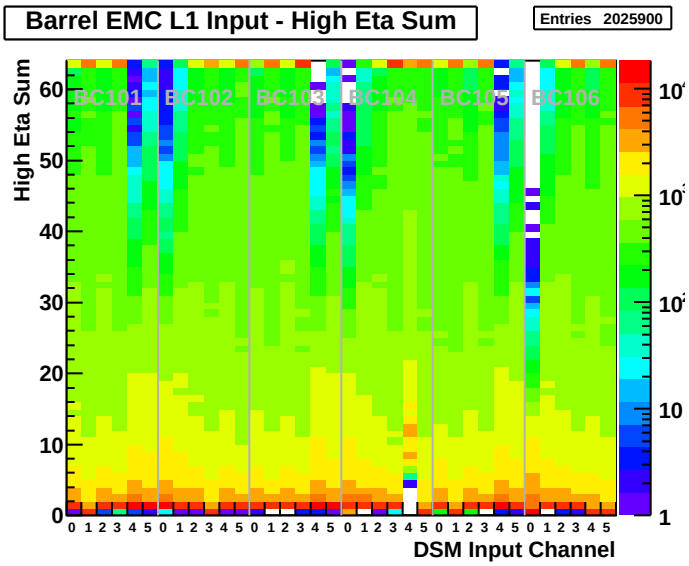
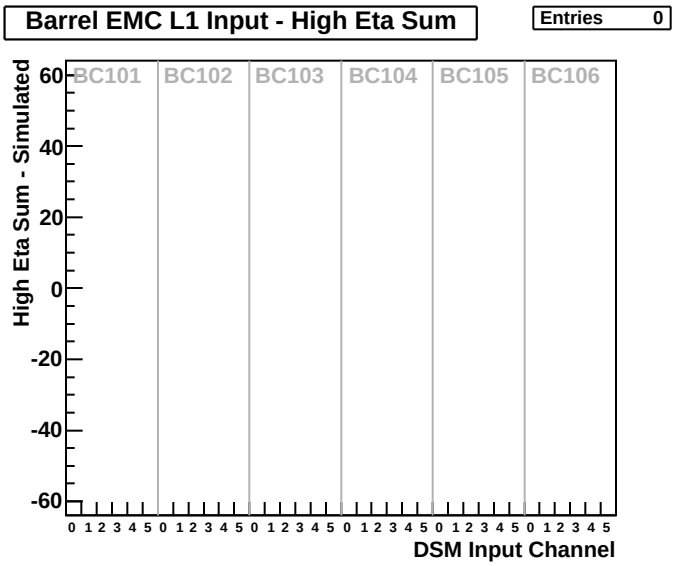
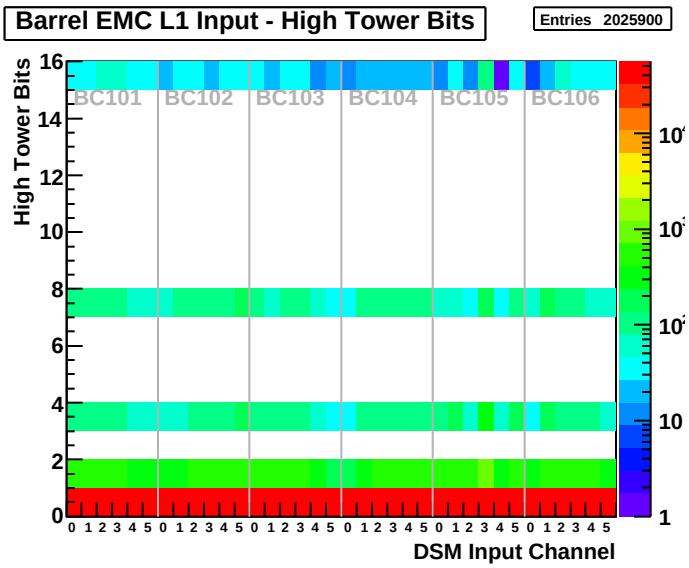
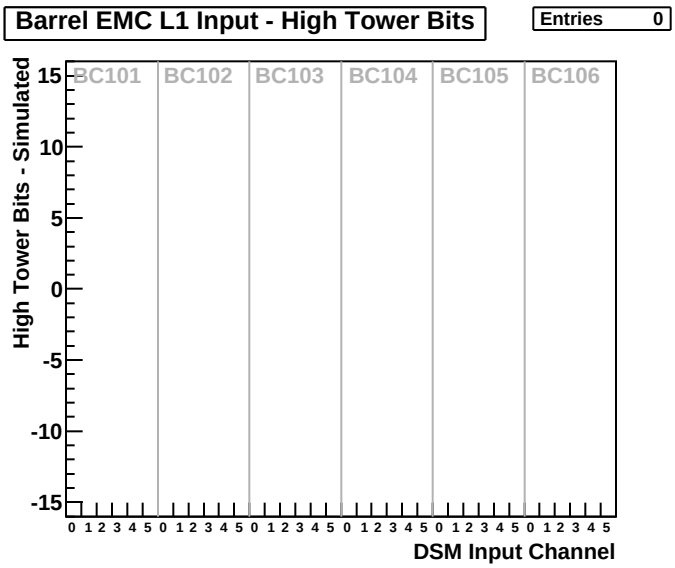

Barrel EMC L1 Input - Low Eta Sum
Entries 2025900
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

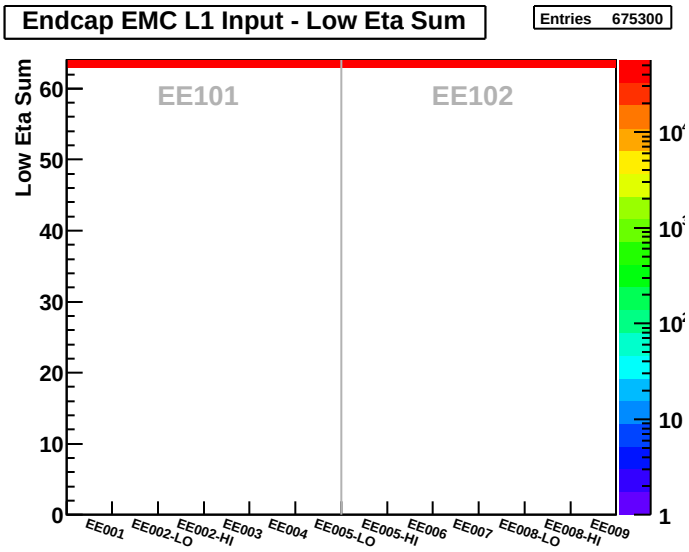

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

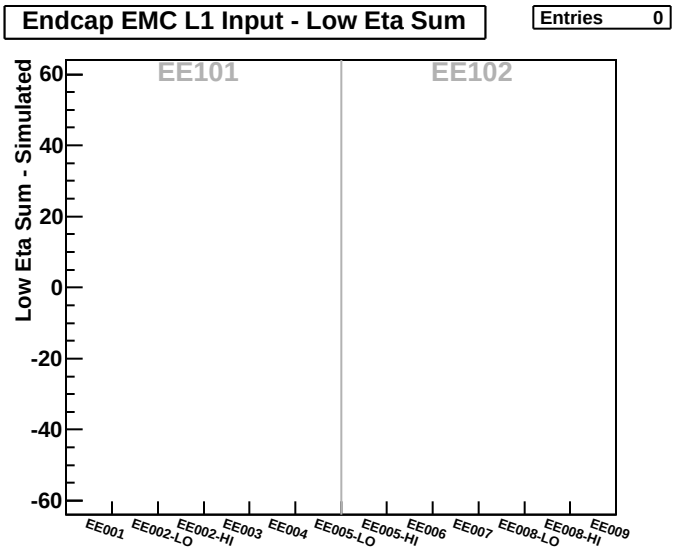

Barrel EMC L1 Input - High Eta Sum
Entries 2025900
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

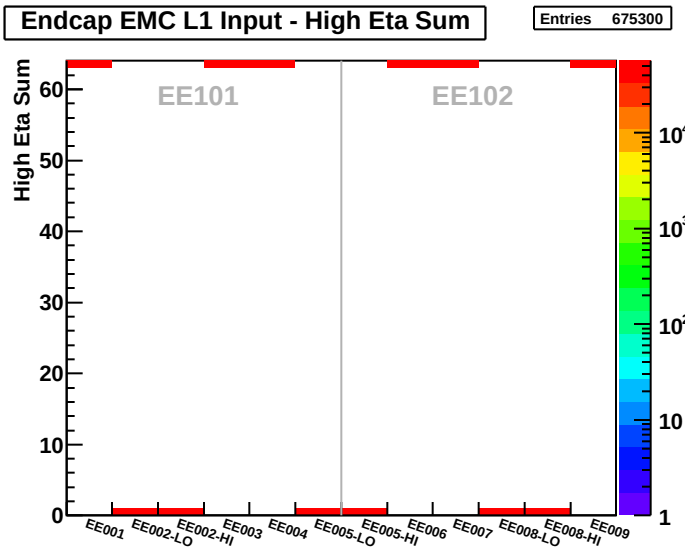

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

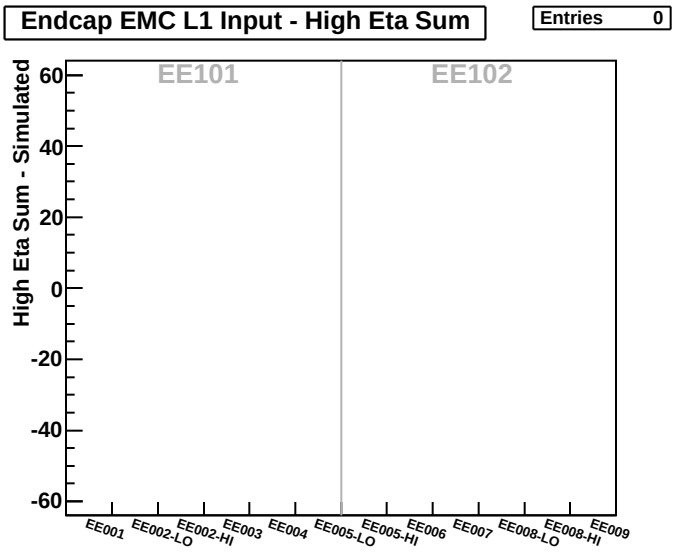

Barrel EMC L1 Input - High Tower Bits
Entries 2025900
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

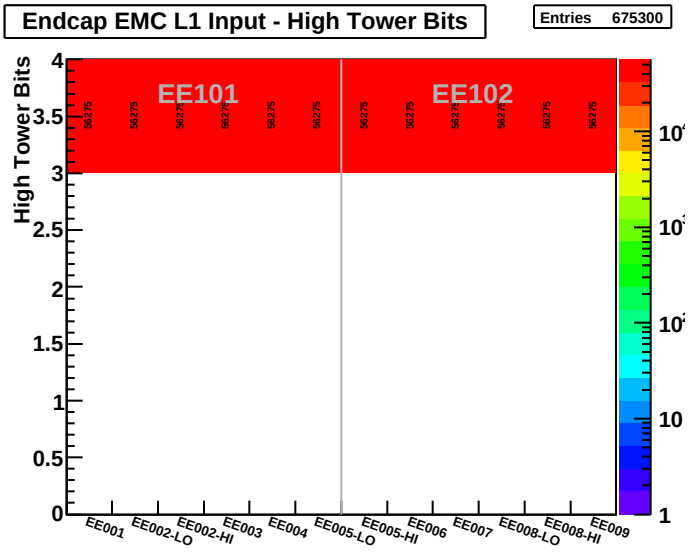

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

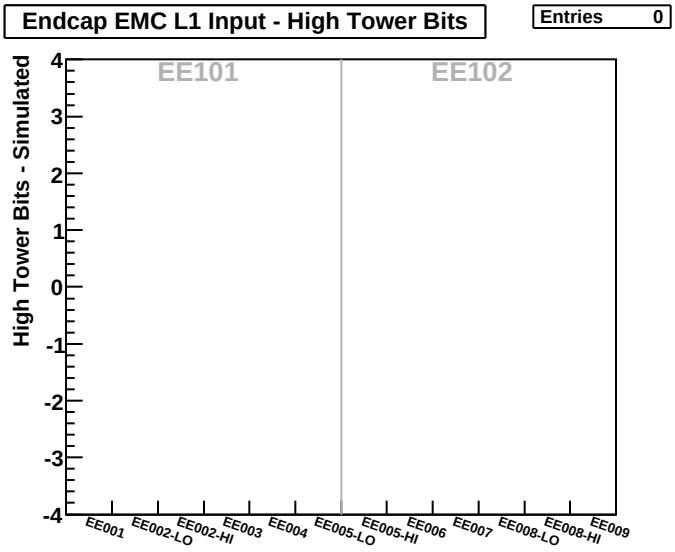

Endcap EMC L1 Input - Low Eta Sum
Entries 675300
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

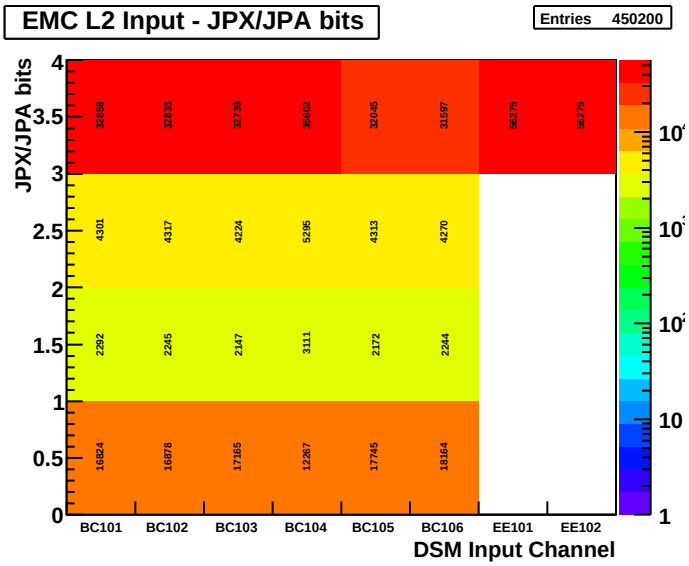

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

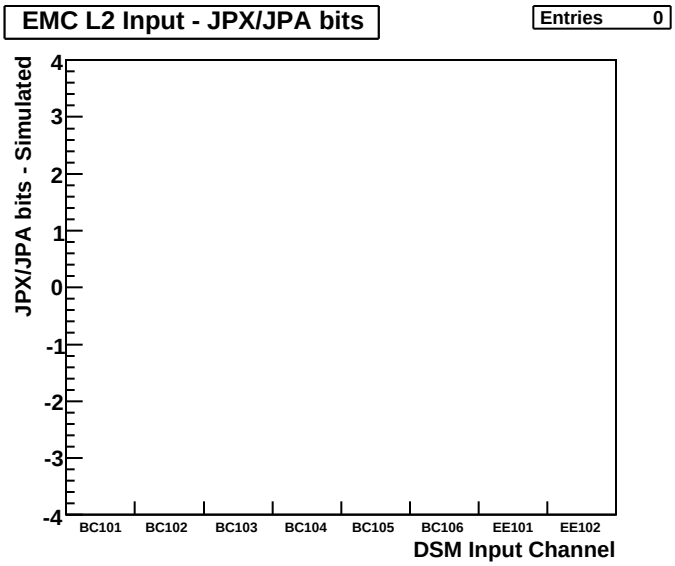

Endcap EMC L1 Input - High Eta Sum
Entries 675300
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

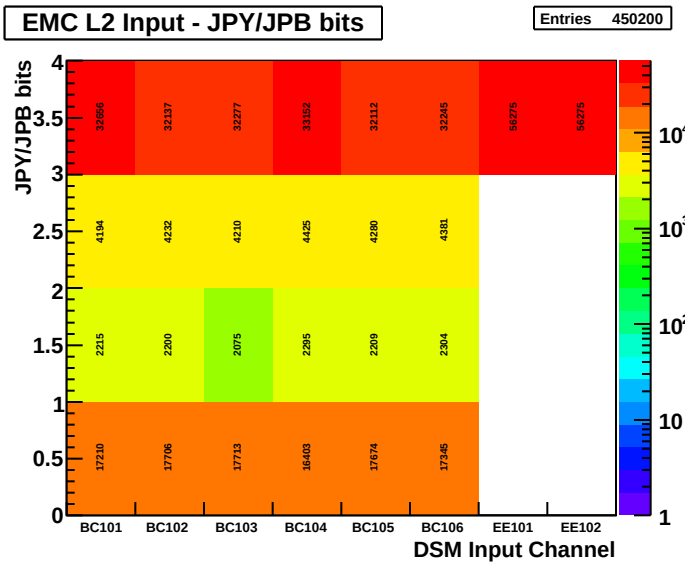

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

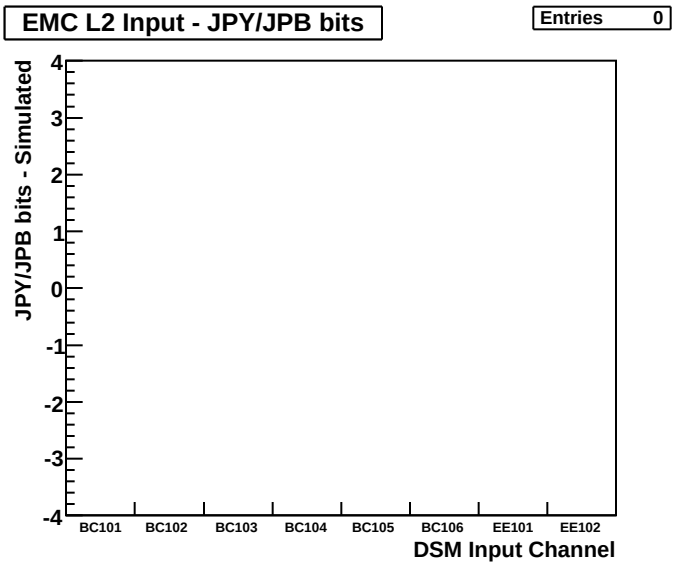

Endcap EMC L1 Input - High Tower Bits
Entries 675300
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

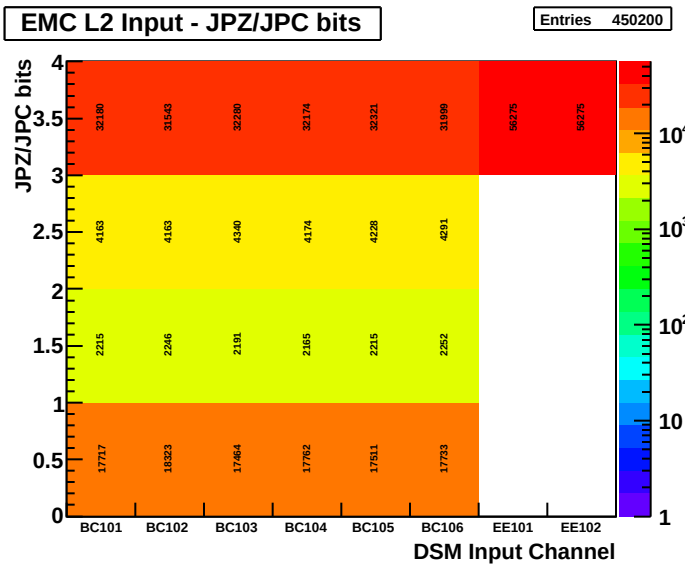

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

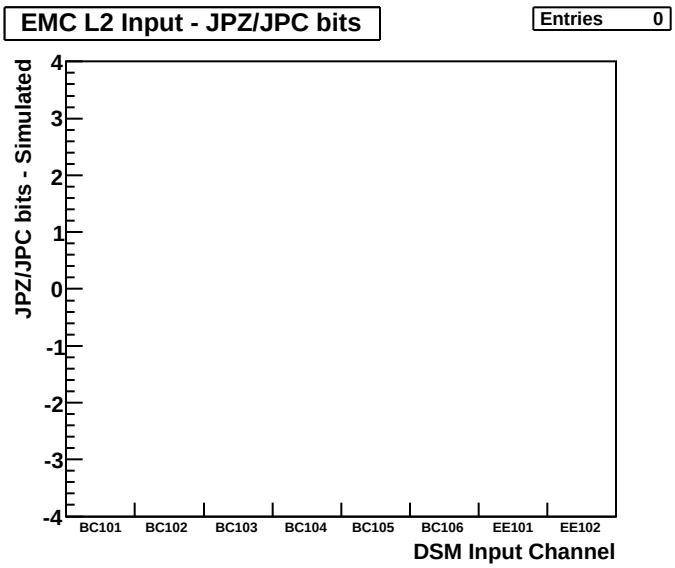

EMC L2 Input - JPX/JPA bits
Entries 450200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
32858 32835 32739 35602 32045 31597 56275 56275
4301 4317 4224 5295 4313 4270
2292 2245 2147 3111 2172 2244
16824 16878 17165 12267 17745 18164

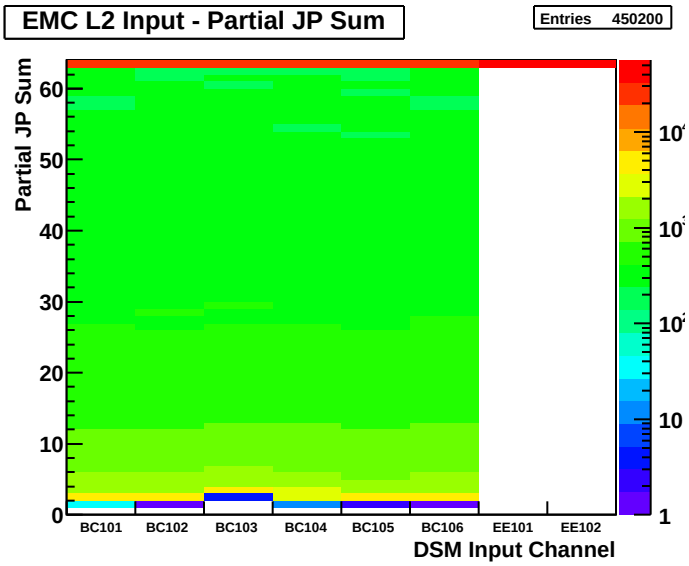

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

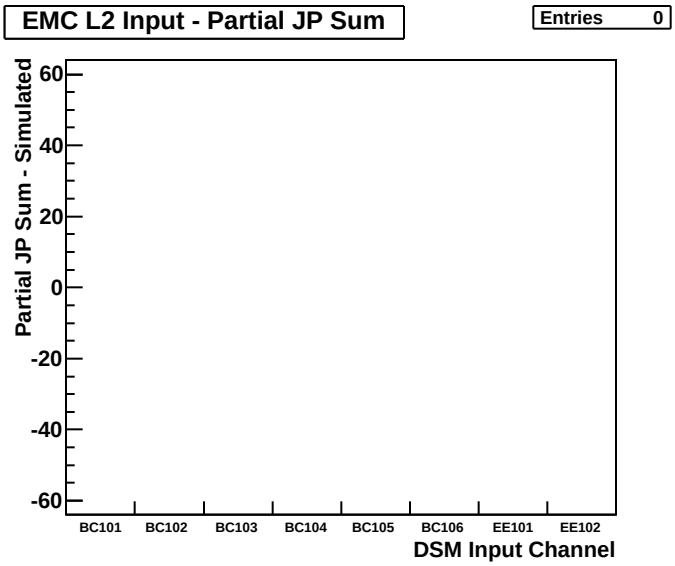

EMC L2 Input - JPY/JPB bits
Entries 450200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
32656 32137 32277 33152 32112 32245 56275 56275
4194 4232 4210 4425 4280 4381
2215 2200 2075 2295 2209 2304
17210 17706 17713 16403 17674 17345

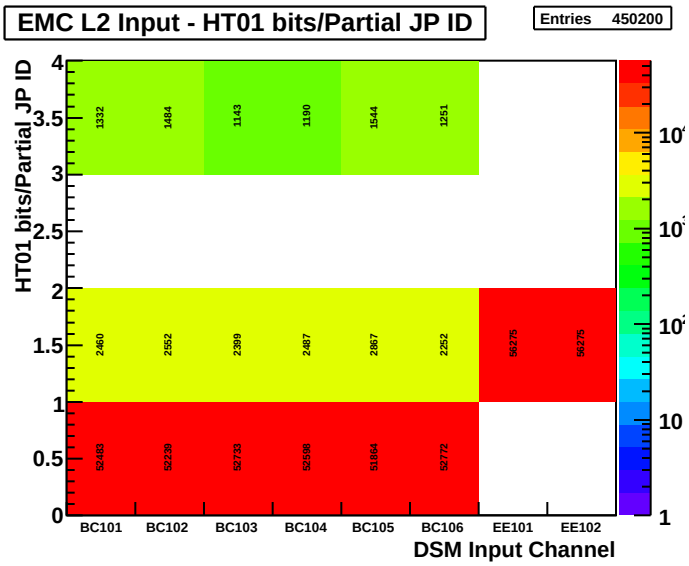

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

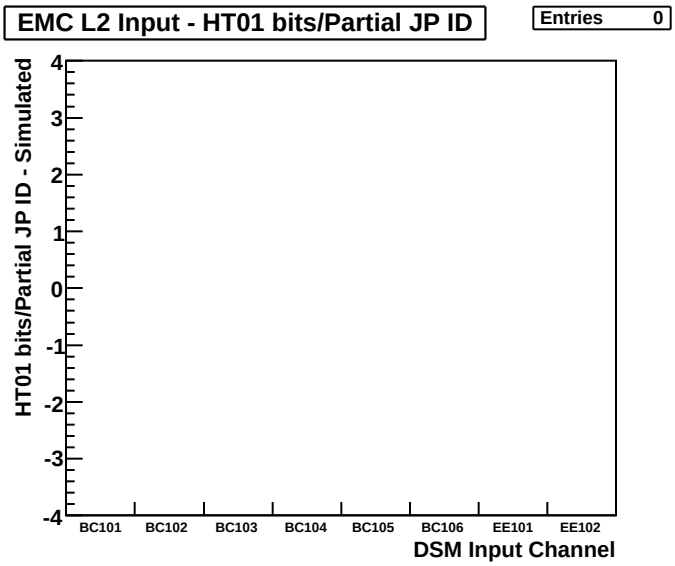

EMC L2 Input - JPZ/JPC bits
Entries 450200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
32180 31543 32280 32174 32321 31999 56275 56275
4163 4163 4340 4174 4228 4291
2215 2246 2191 2165 2215 2252
17717 18323 17464 17762 17511 17733

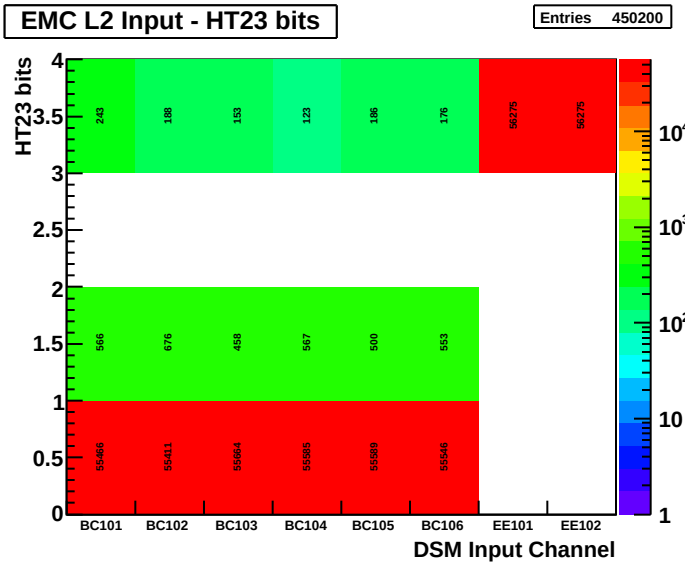

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

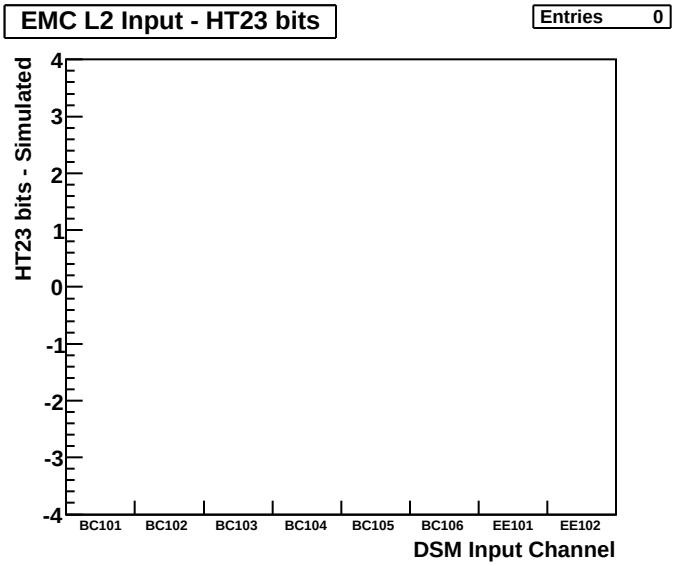

EMC L2 Input - Partial JP Sum
Entries 450200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

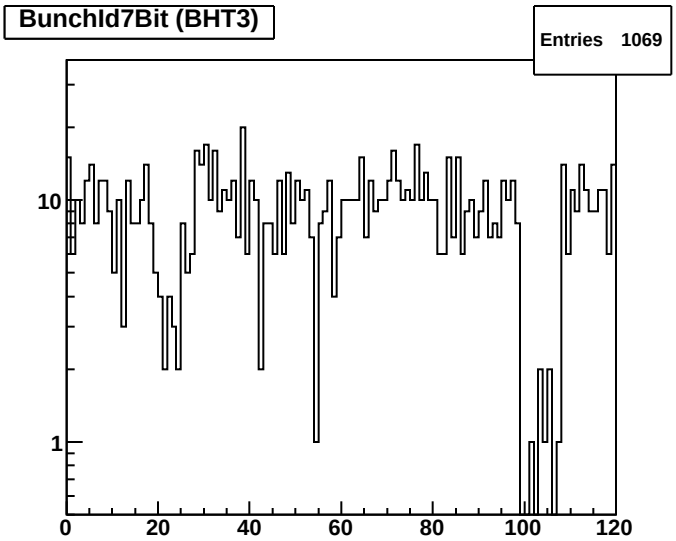

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

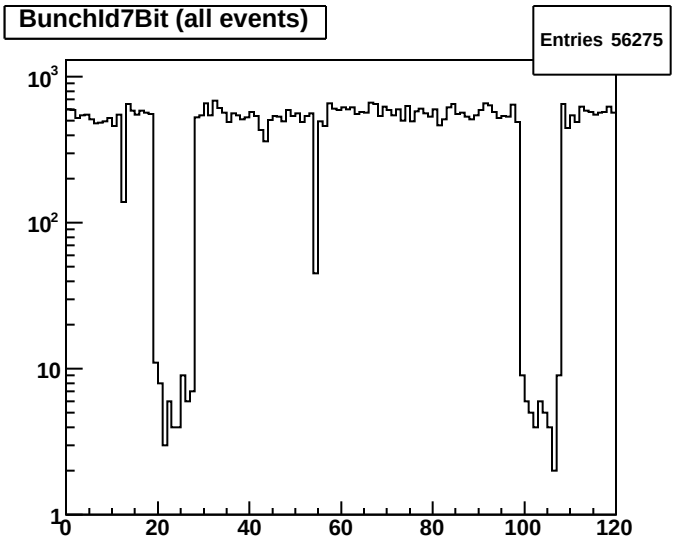

EMC L2 Input - HT01 bits/Partial JP ID
Entries 450200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

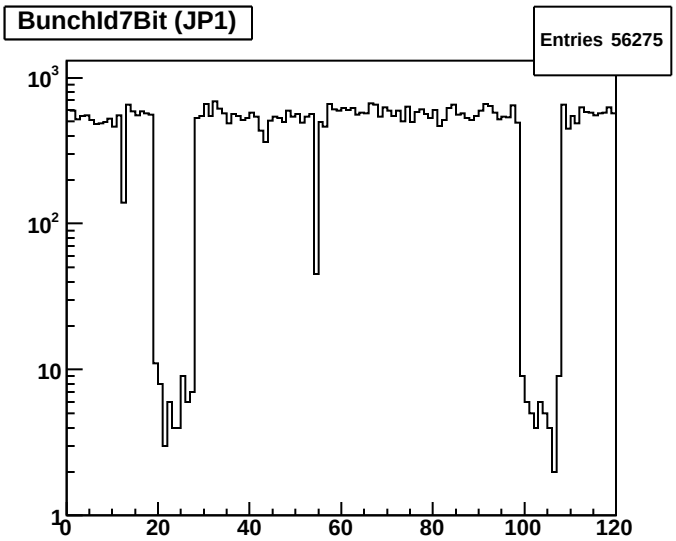

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

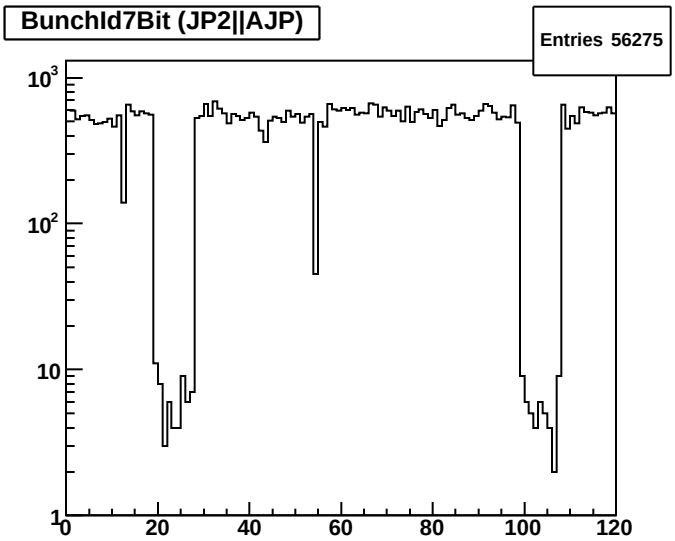

EMC L2 Input - HT23 bits
Entries 450200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

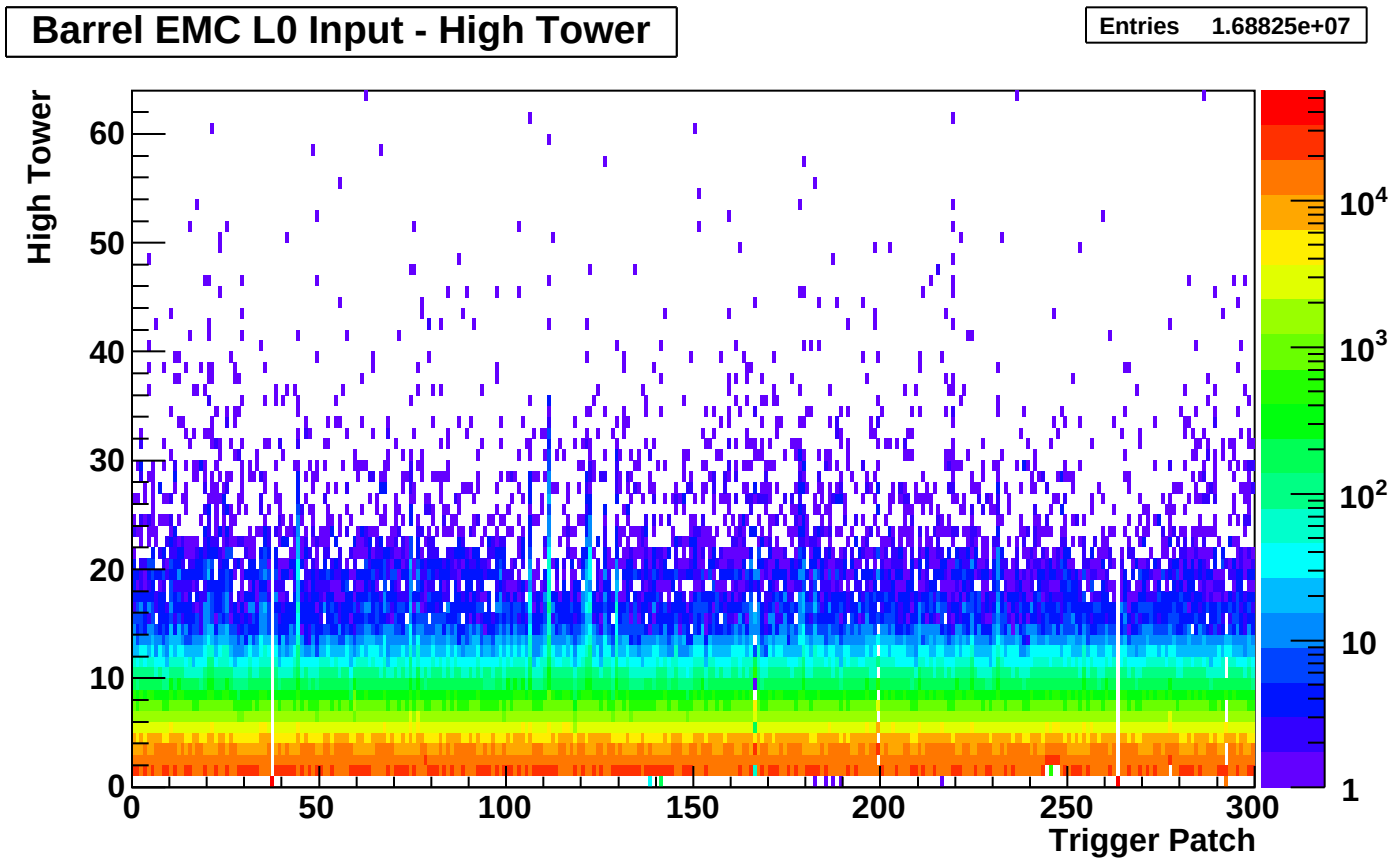

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

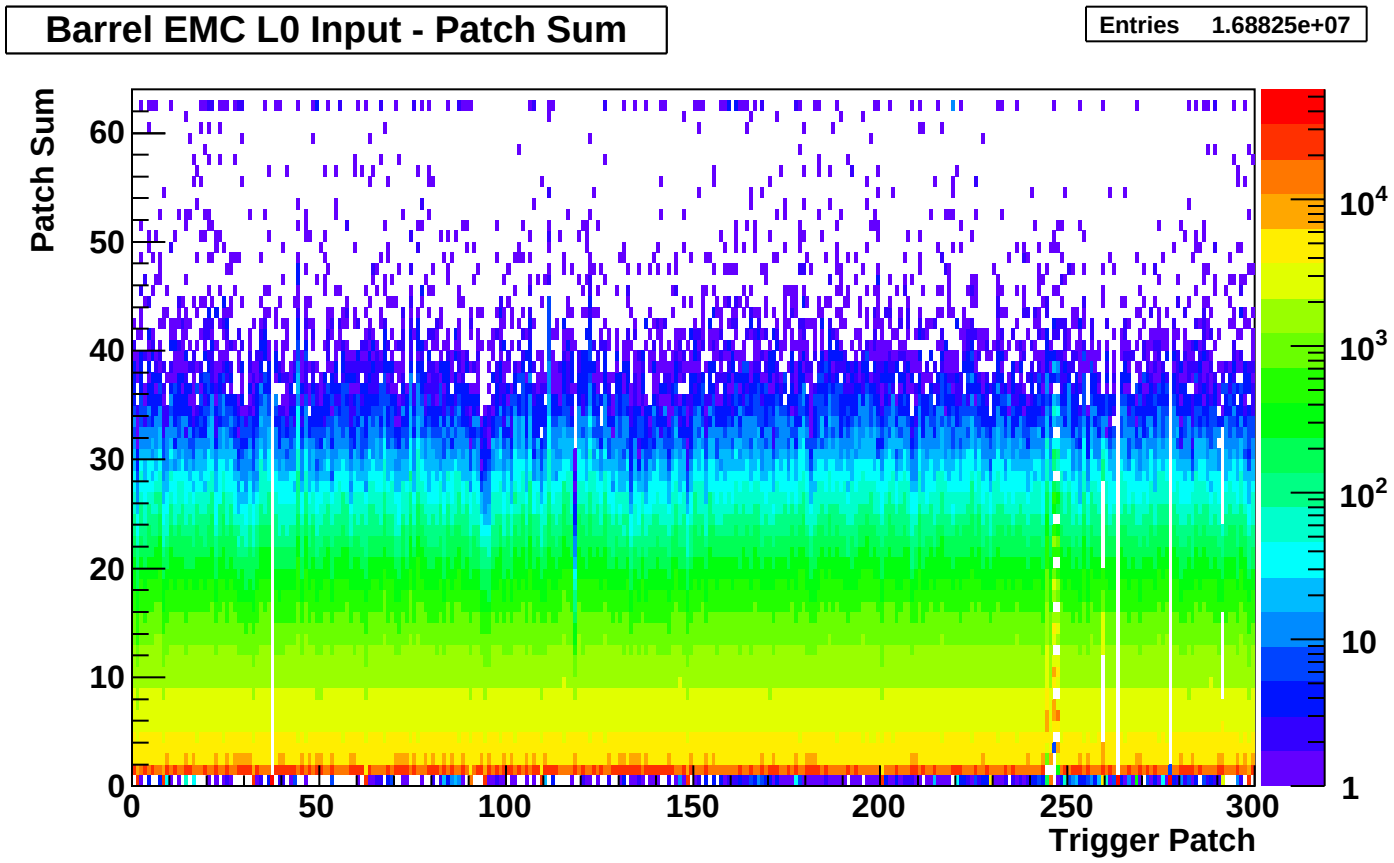

BunchId7Bit (BHT3)
Entries 1069
10
1
0
20
40
60
80
100
120

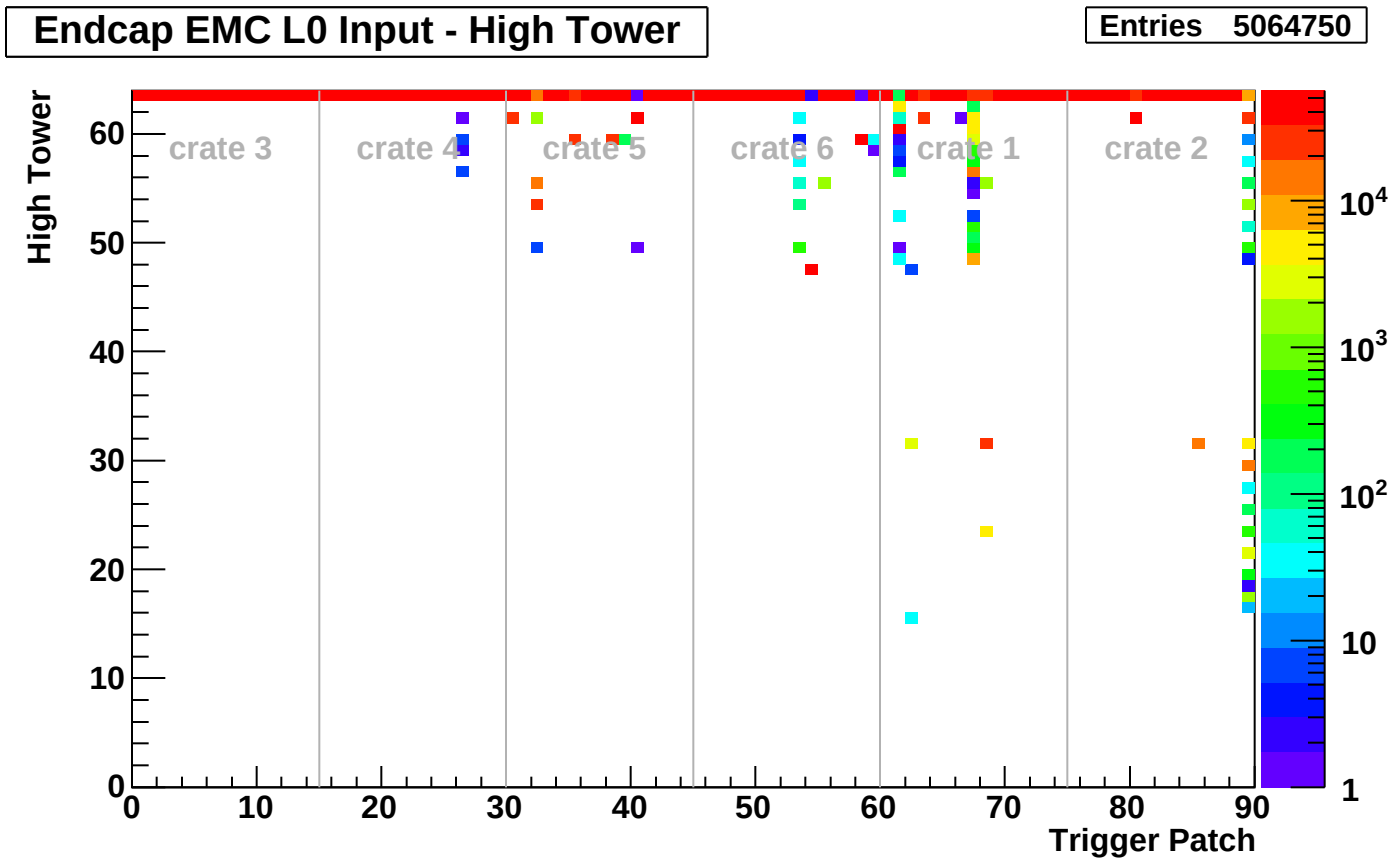

BunchId7Bit (all events)
Entries 56275
10^3
10^2
10
1
0
20
40
60
80
100
120

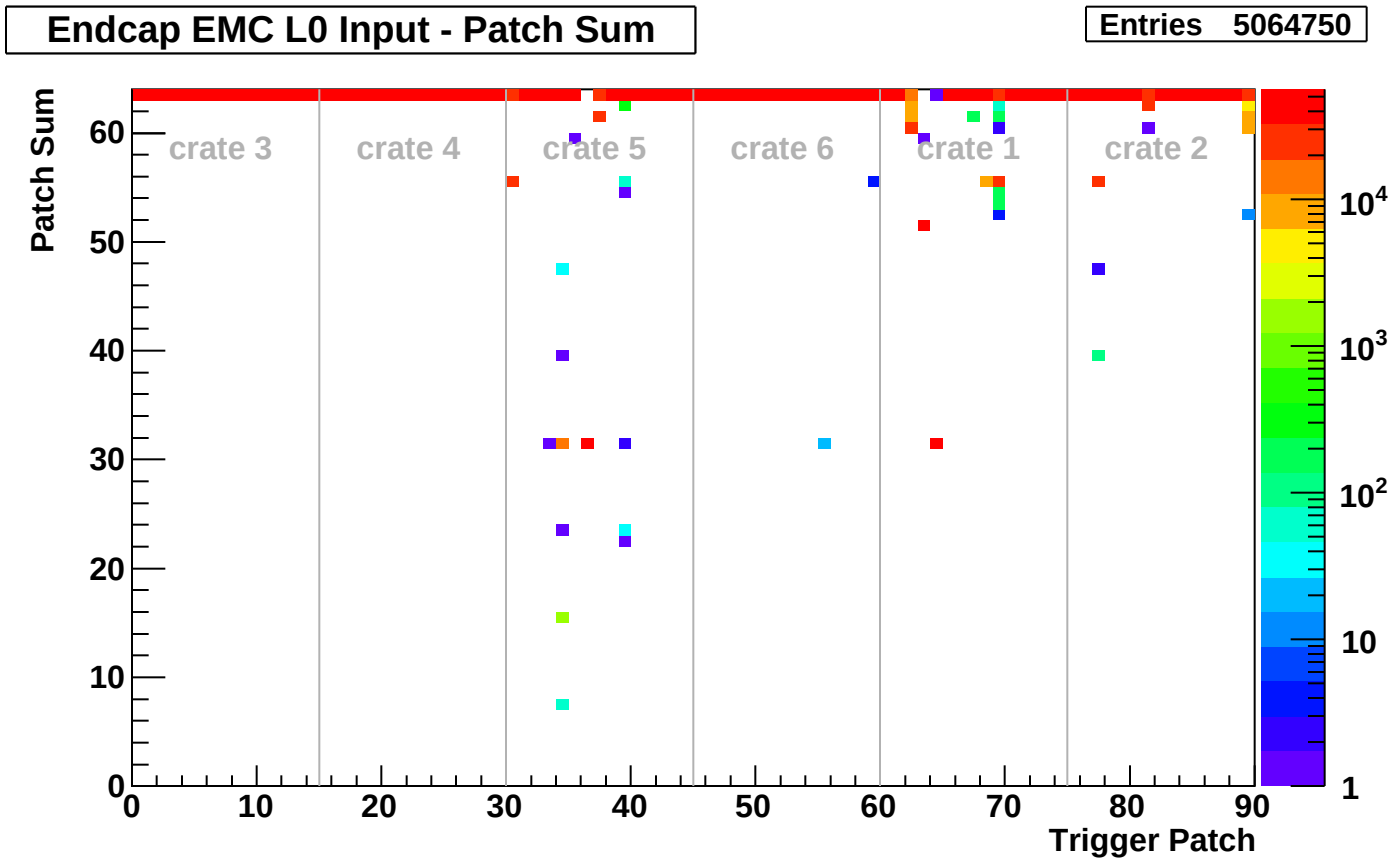

BunchId7Bit (JP1)
Entries 56275
10^3
10^2
10
1
0
20
40
60
80
100
120

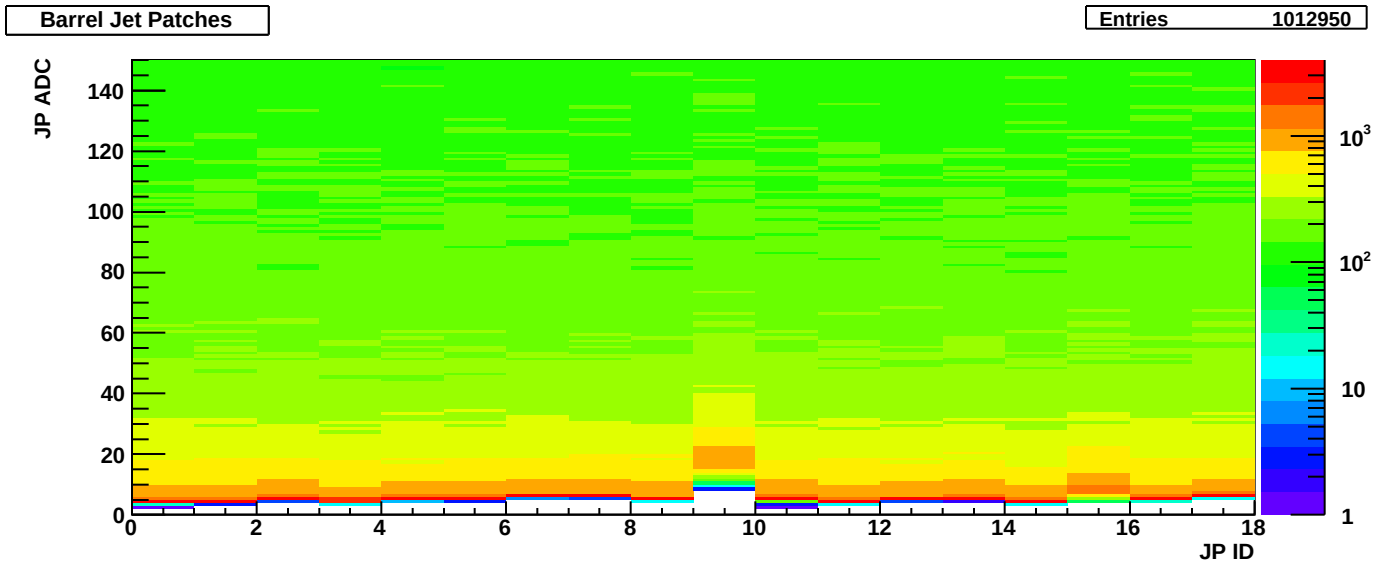

BunchId7Bit (JP2||AJP)
Entries 56275
10^3
10^2
10
1
0
20
40
60
80
100
120

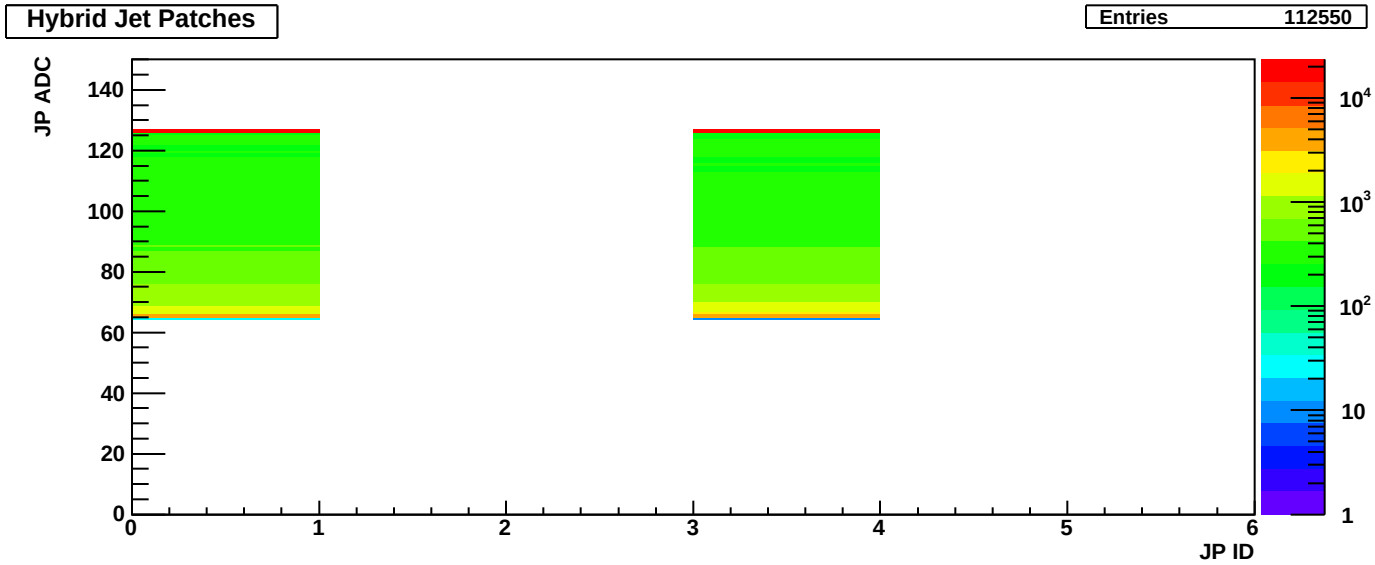

Barrel EMC L0 Input - High Tower
Entries 1.68825e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

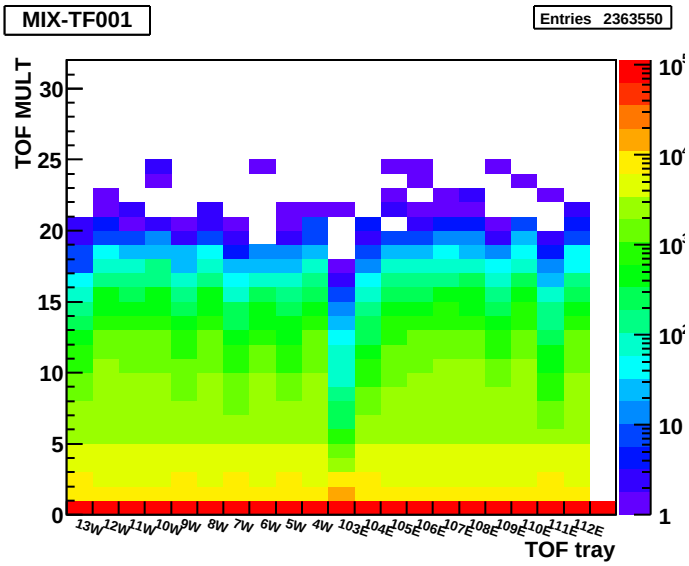

Barrel EMC L0 Input - Patch Sum
Entries 1.68825e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

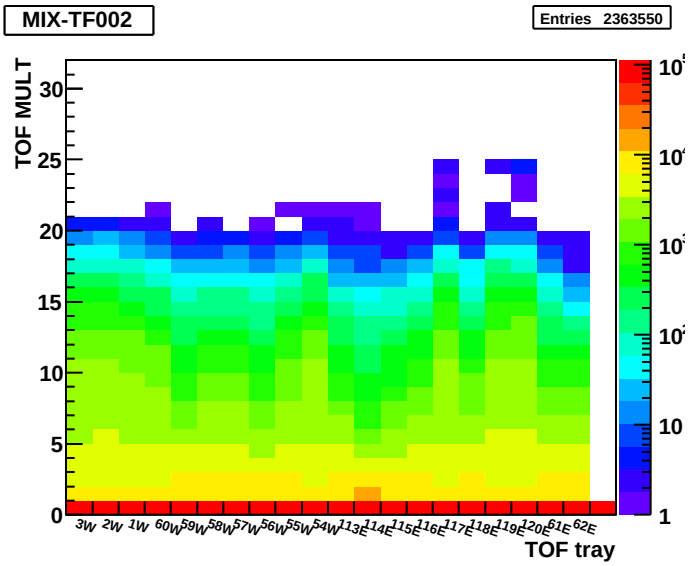

Endcap EMC L0 Input - High Tower
Entries 5064750
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

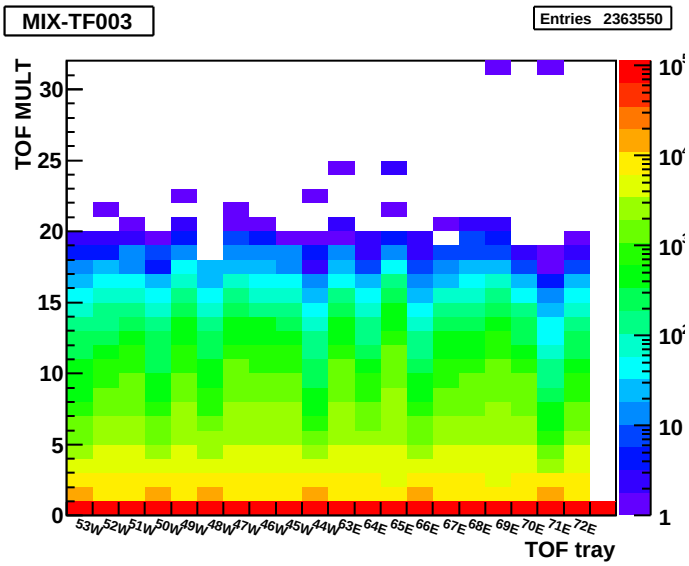

Endcap EMC L0 Input - Patch Sum
Entries 5064750
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

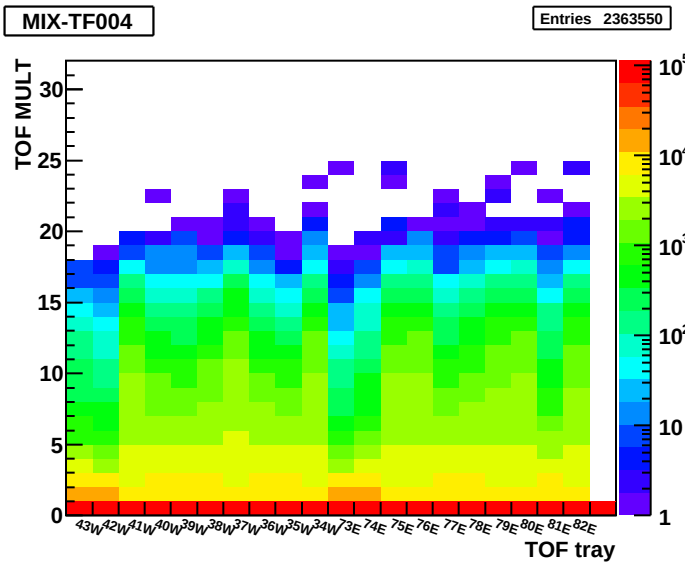

Barrel Jet Patches
Entries 1012950
JP ADC
JP ID

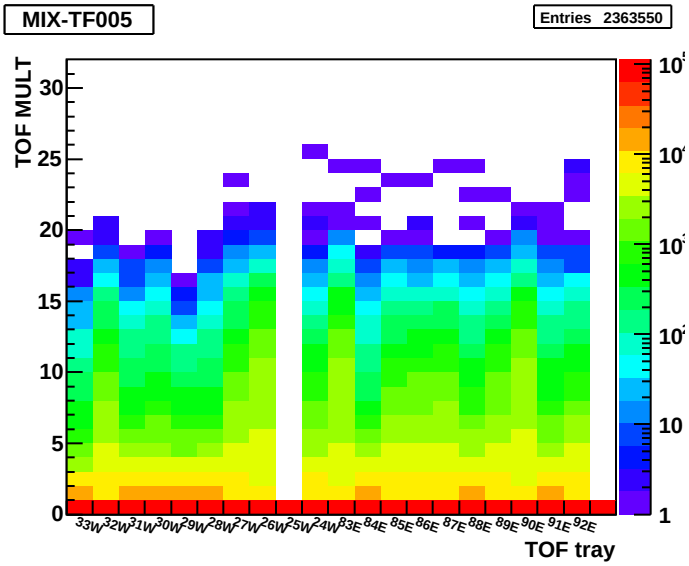

Hybrid Jet Patches
Entries 112550
JP ADC
JP ID

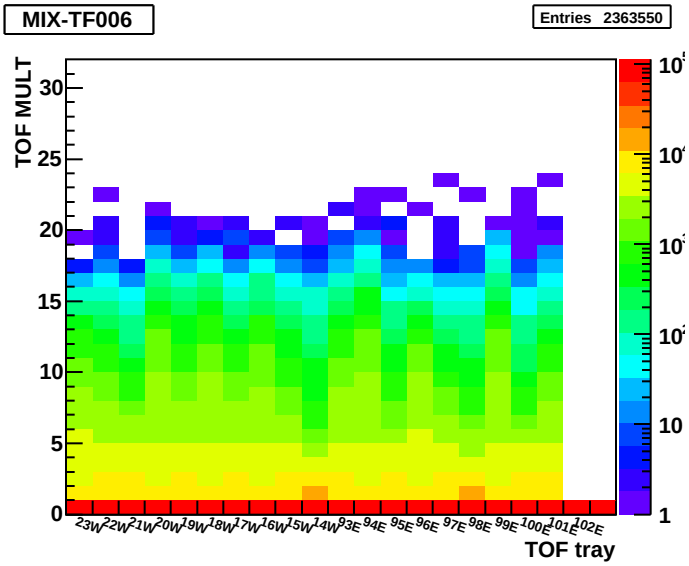

MIX-TF001
Entries 2363550
TOF MULT
TOF tray

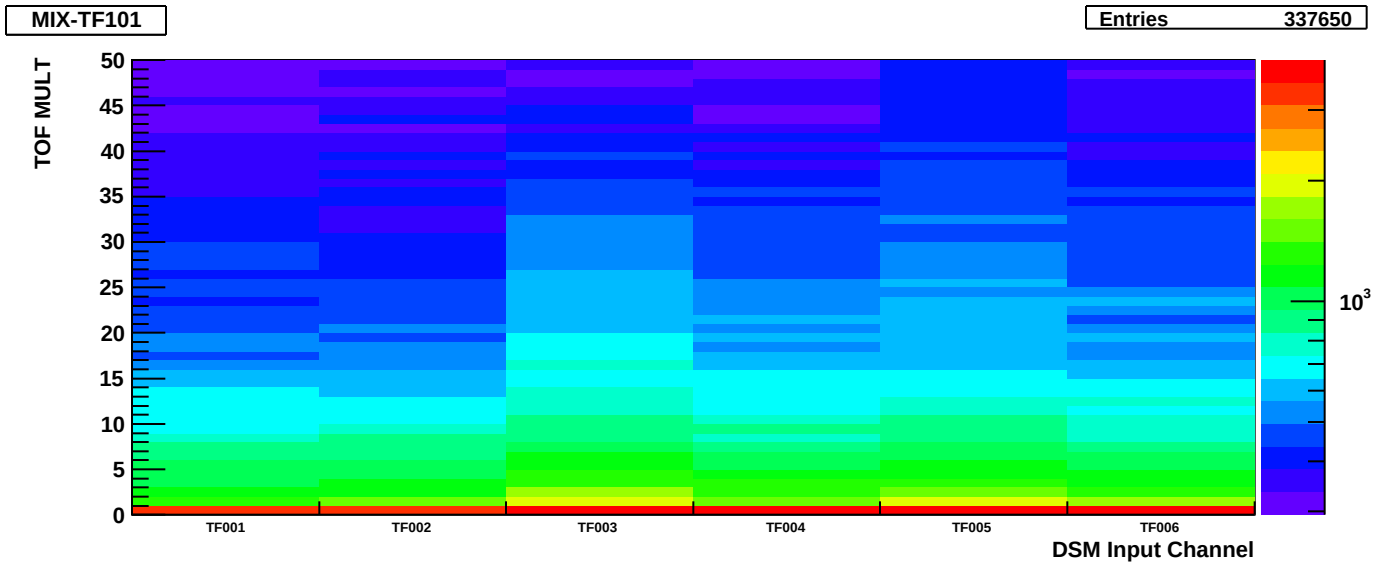

MIX-TF002
Entries 2363550
TOF MULT
TOF tray

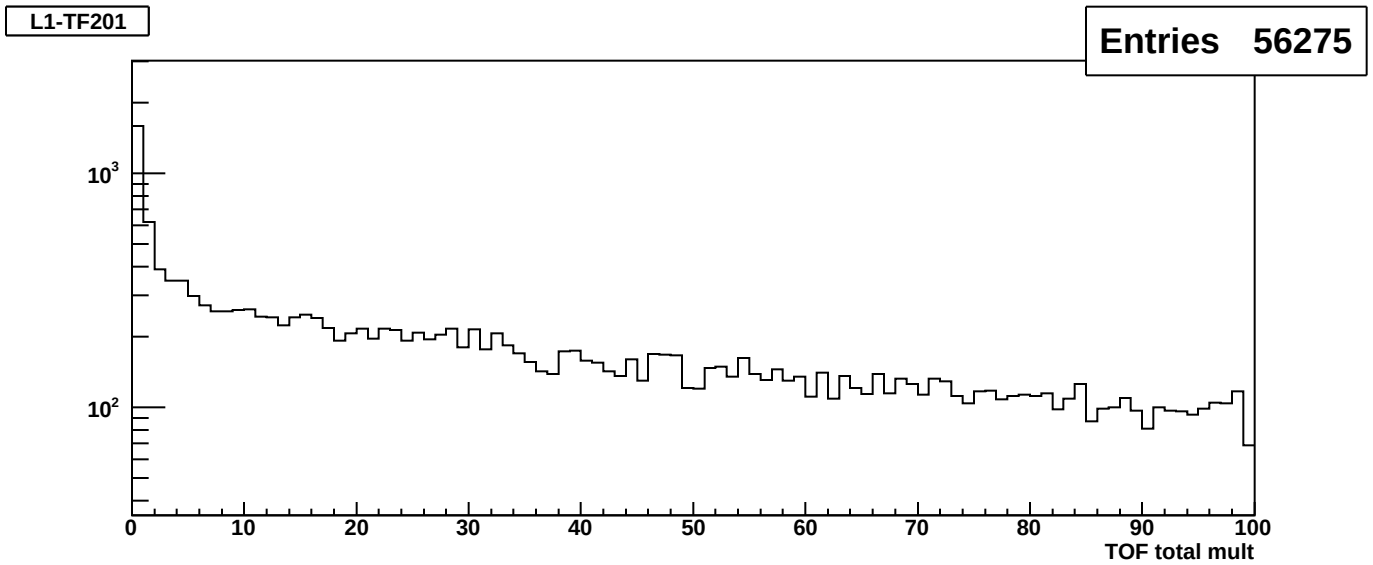

MIX-TF003
Entries 2363550
TOF MULT
TOF tray

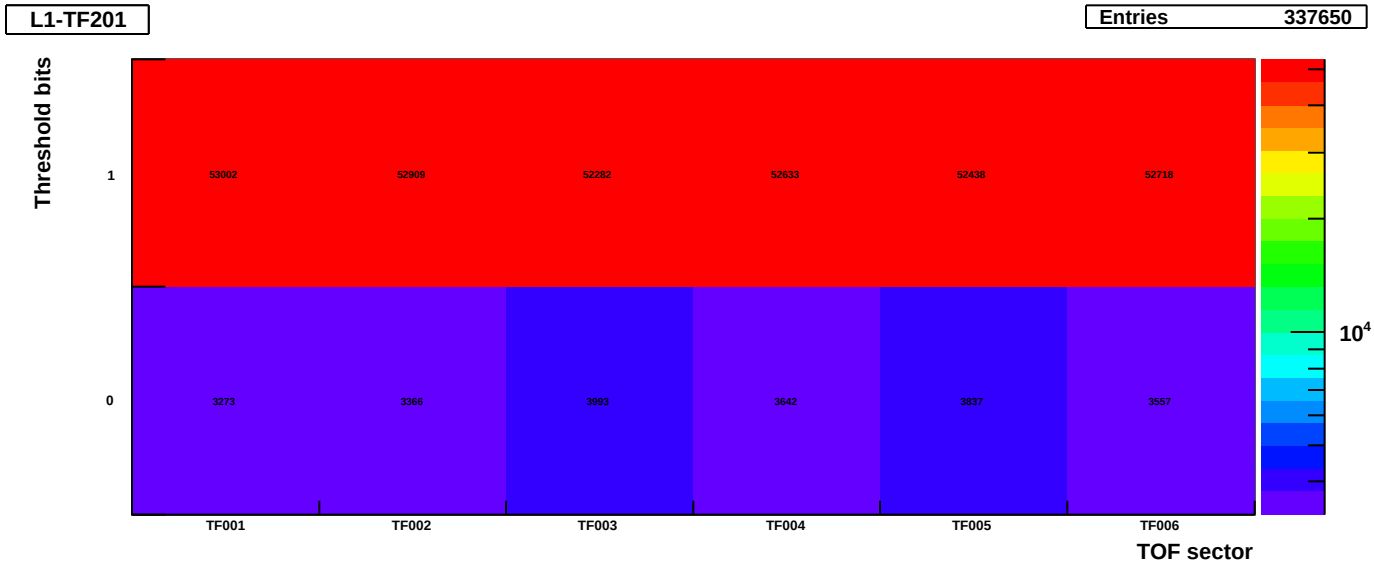

MIX-TF004
Entries 2363550
TOF MULT
TOF tray

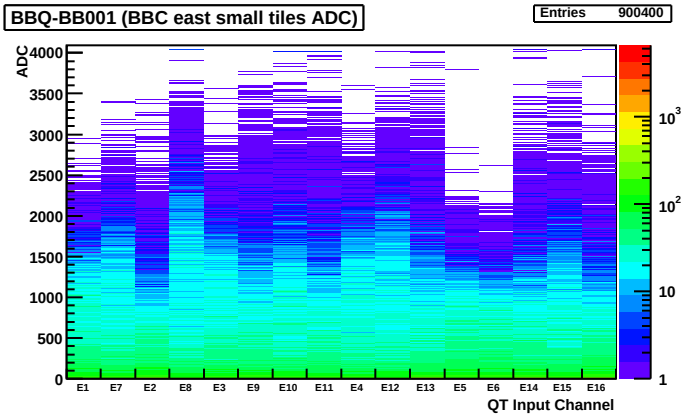

MIX-TF005
Entries 2363550
TOF MULT
TOF tray

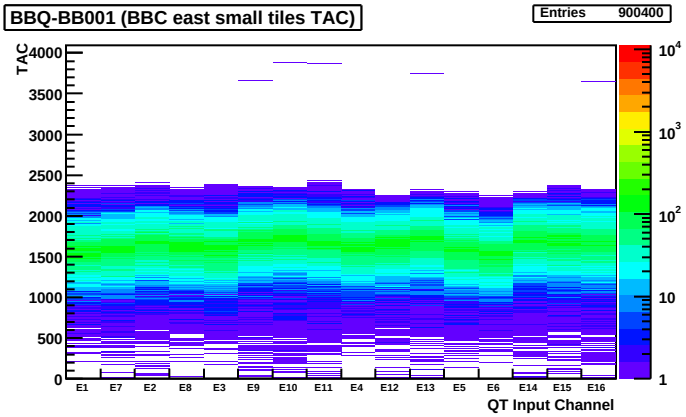

MIX-TF006
Entries 2363550
TOF MULT
TOF tray

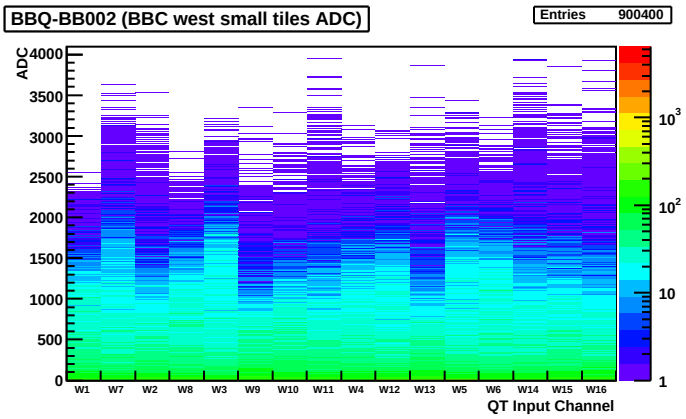

MIX-TF101
Entries 337650
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3

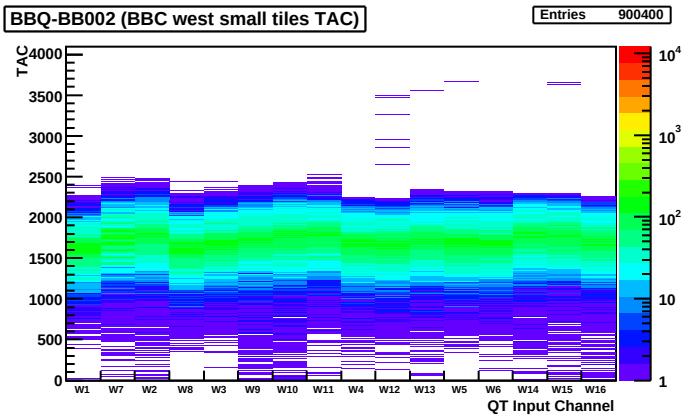

L1-TF201
Entries 56275
10^3
10^2
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

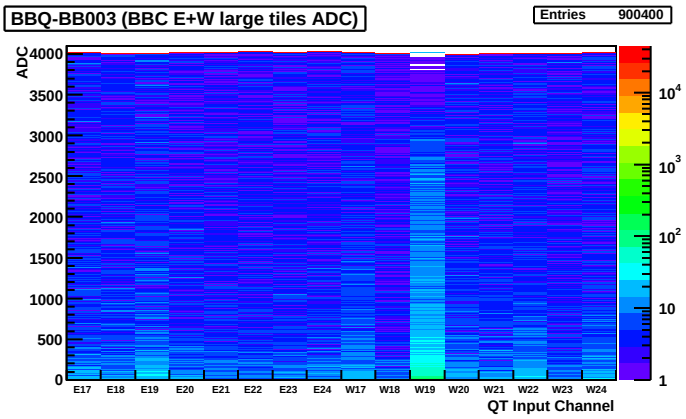

L1-TF201
Entries 337650
Threshold bits
1
0
53002
52909
52282
52633
52438
52718
3273
3366
3993
3642
3837
3557
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

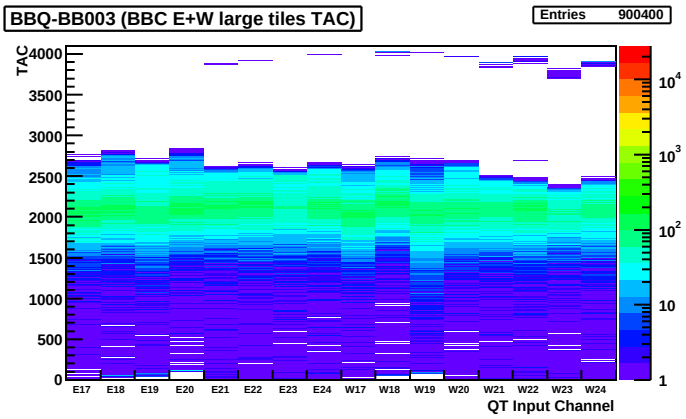

BBQ-BB001 (BBC east small tiles ADC)
Entries 900400
ADC
QT Input Channel

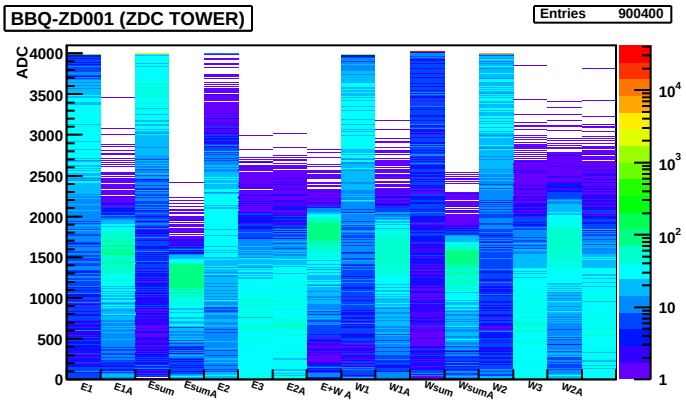

BBQ-BB001 (BBC east small tiles TAC)
Entries 900400
TAC
QT Input Channel

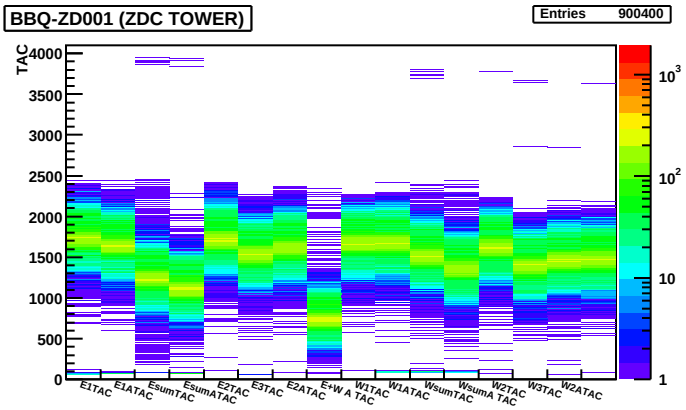

BBQ-BB002 (BBC west small tiles ADC)
Entries 900400
ADC
QT Input Channel

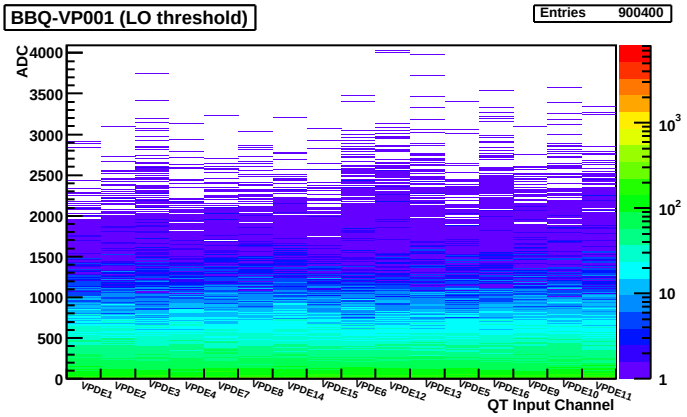

BBQ-BB002 (BBC west small tiles TAC)
Entries 900400
TAC
QT Input Channel

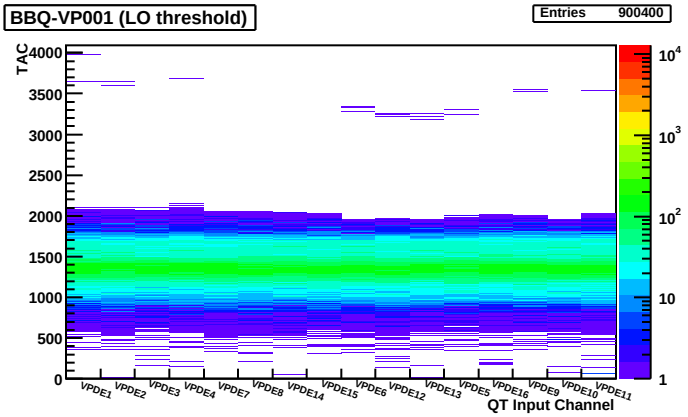

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 900400
ADC
QT Input Channel

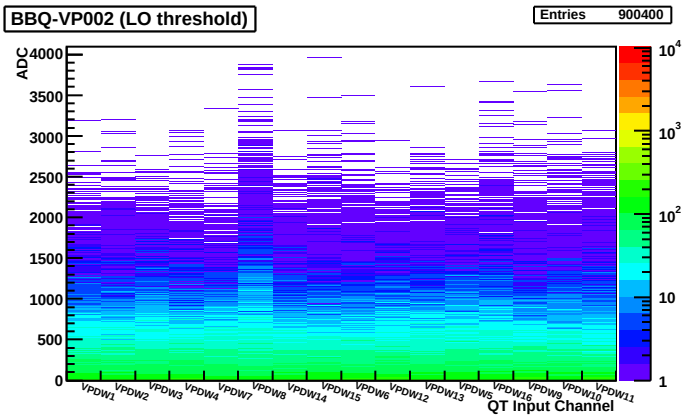

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 900400
TAC
QT Input Channel

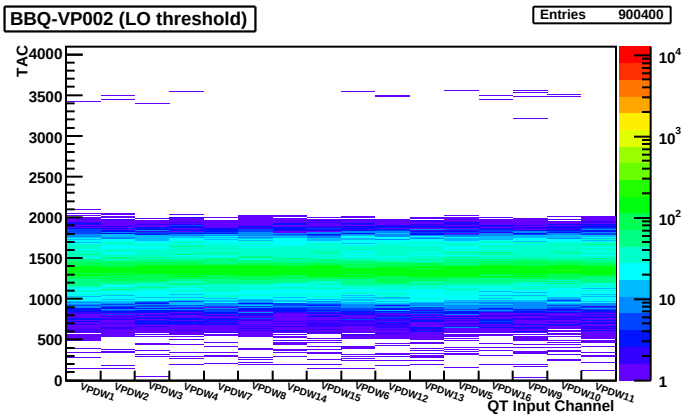

BBQ-ZD001 (ZDC TOWER)
Entries 900400
ADC

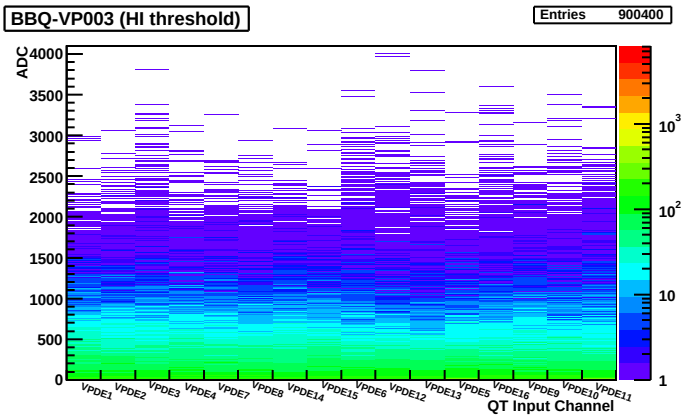

BBQ-ZD001 (ZDC TOWER)
Entries 900400
TAC

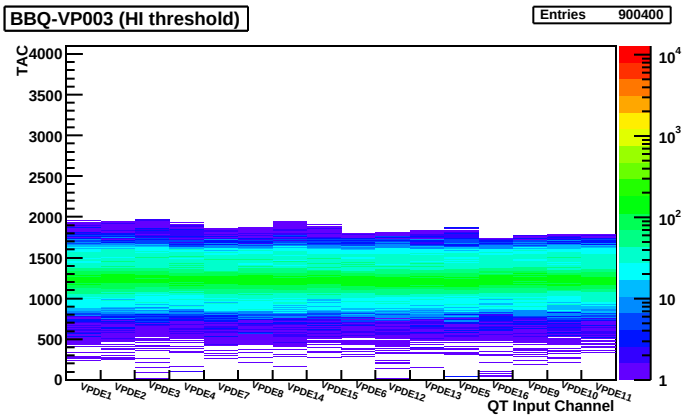

BBQ-VP001 (LO threshold)
Entries 900400
ADC
QT Input Channel

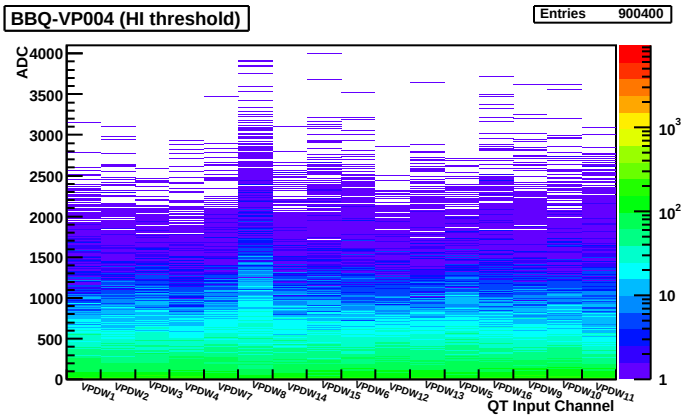

BBQ-VP001 (LO threshold)
Entries 900400
TAC
QT Input Channel

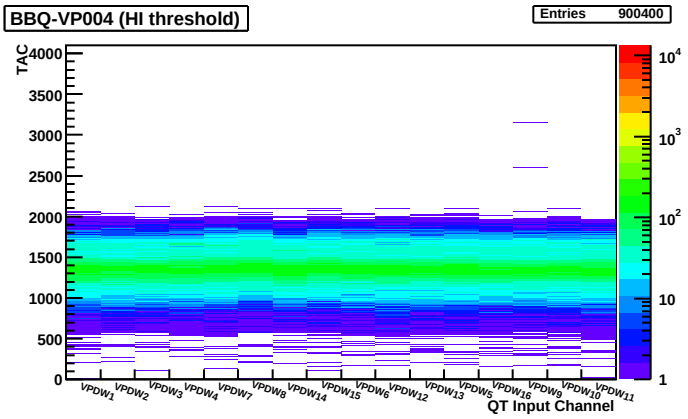

BBQ-VP002 (LO threshold)
Entries 900400
ADC
QT Input Channel

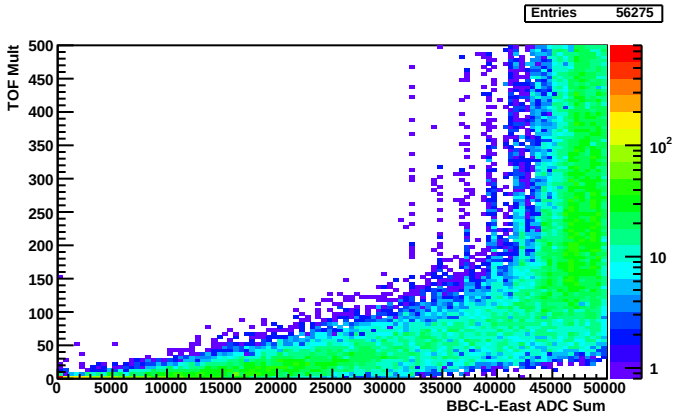

BBQ-VP002 (LO threshold)
Entries 900400
TAC
QT Input Channel

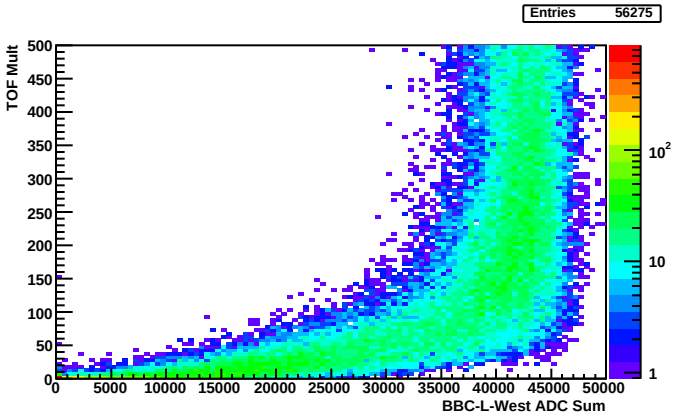

BBQ-VP003 (HI threshold)
Entries 900400
ADC
QT Input Channel

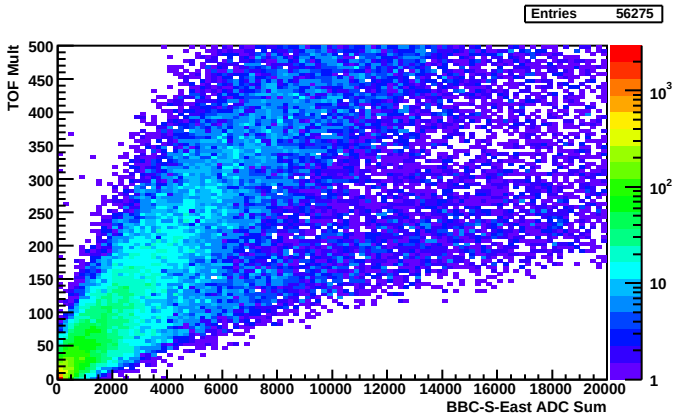

BBQ-VP003 (HI threshold)
Entries 900400
TAC
QT Input Channel

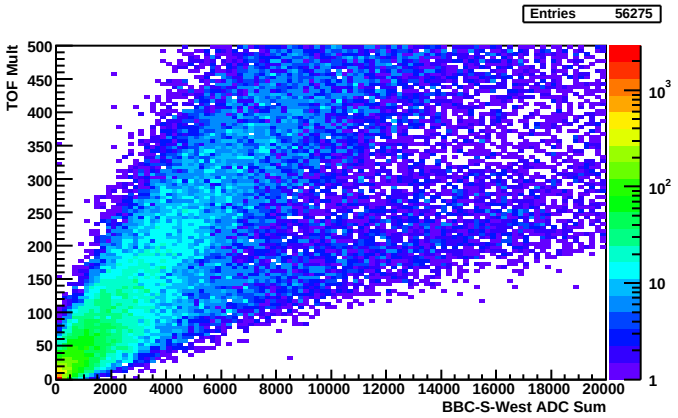

BBQ-VP004 (HI threshold)
Entries 900400
ADC
QT Input Channel

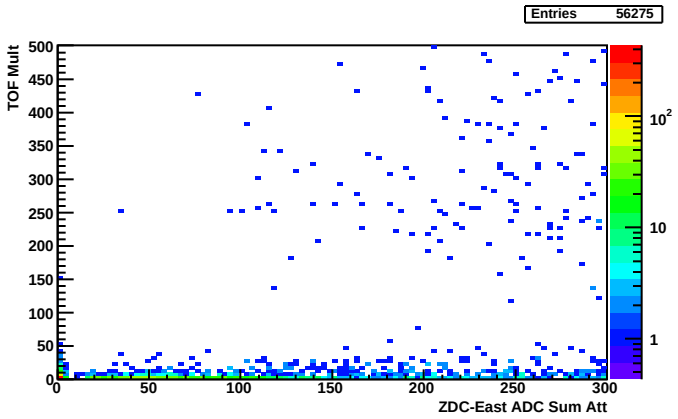

BBQ-VP004 (HI threshold)
Entries 900400
TAC
QT Input Channel

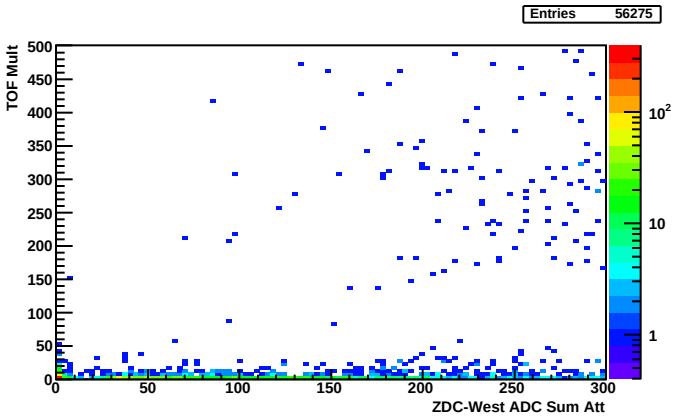

Entries 56275
TOF Mult
BBC-L-East ADC Sum

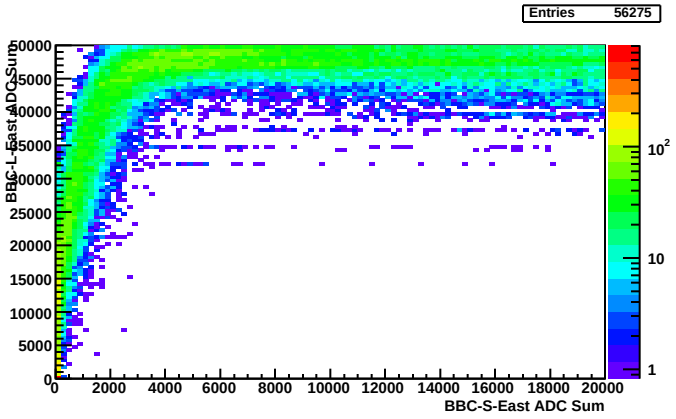

Entries 56275
TOF Mult
BBC-L-West ADC Sum

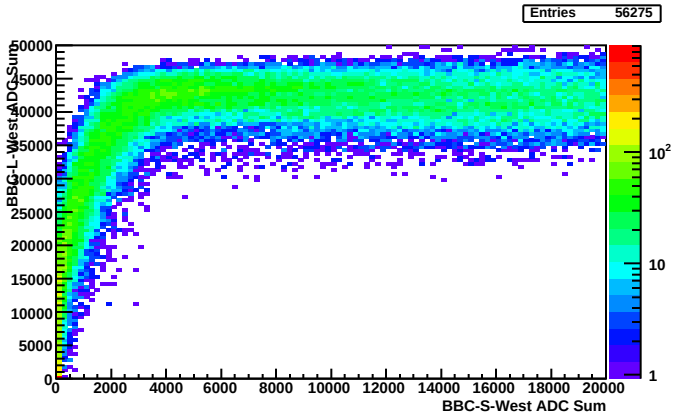

Entries 56275
TOF Mult
BBC-S-East ADC Sum

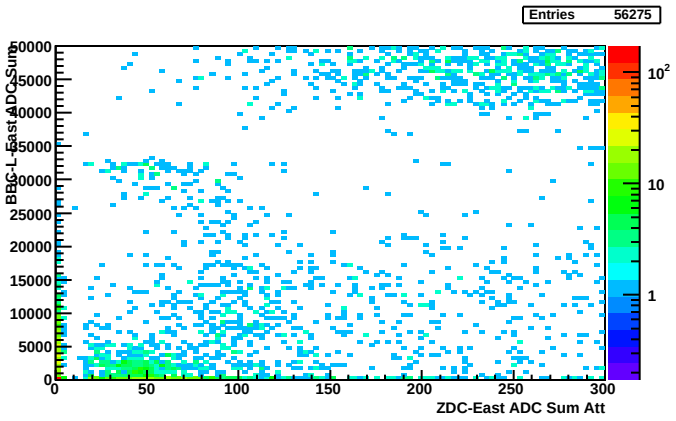

Entries 56275
TOF Mult
BBC-S-West ADC Sum

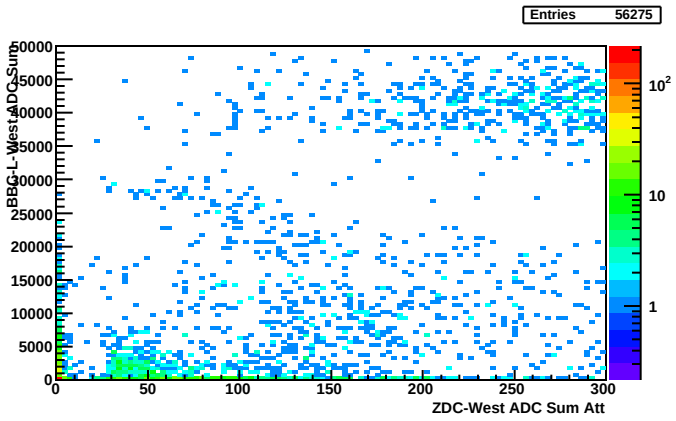

Entries 56275
TOF Mult
ZDC-East ADC Sum Att


Entries 56275
TOF Mult
ZDC-West ADC Sum Att


Entries 56275
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 56275
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 56275
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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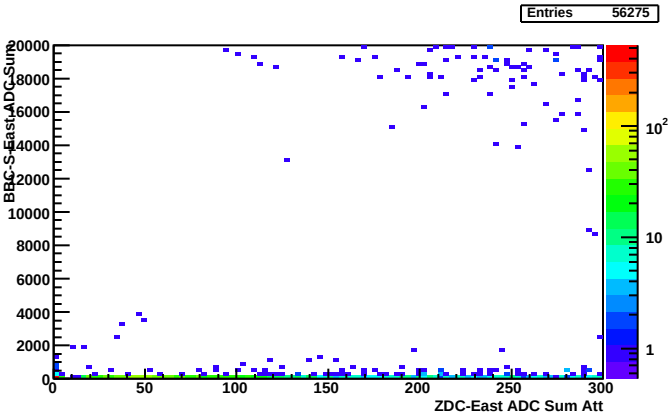
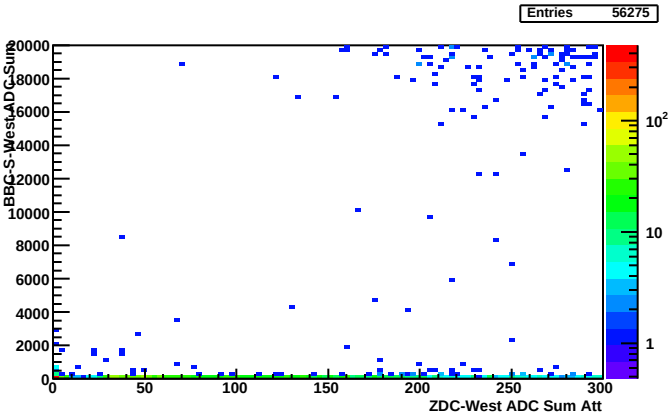
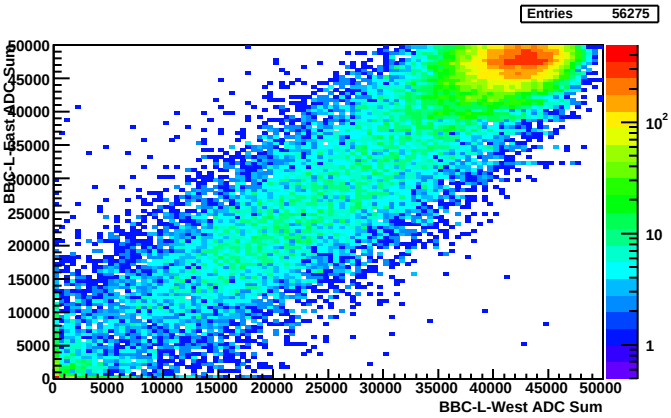
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 56275
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 56275
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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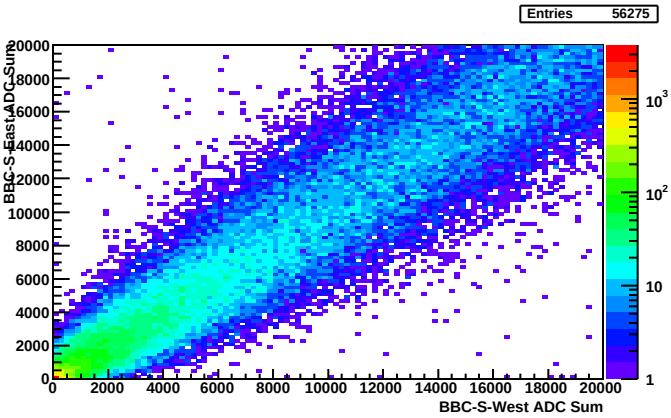
BBC-L-East ADC Sum
BBC-L-West ADC Sum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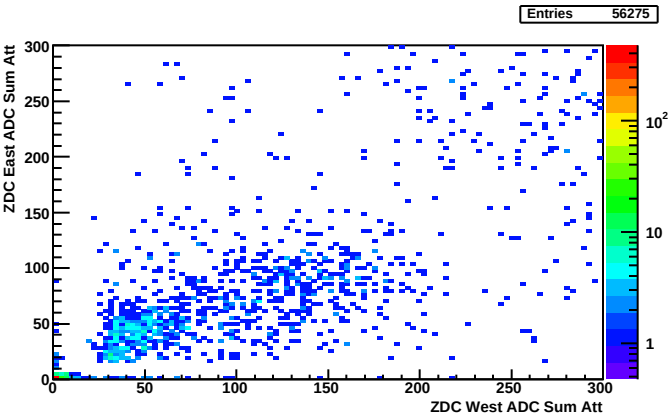
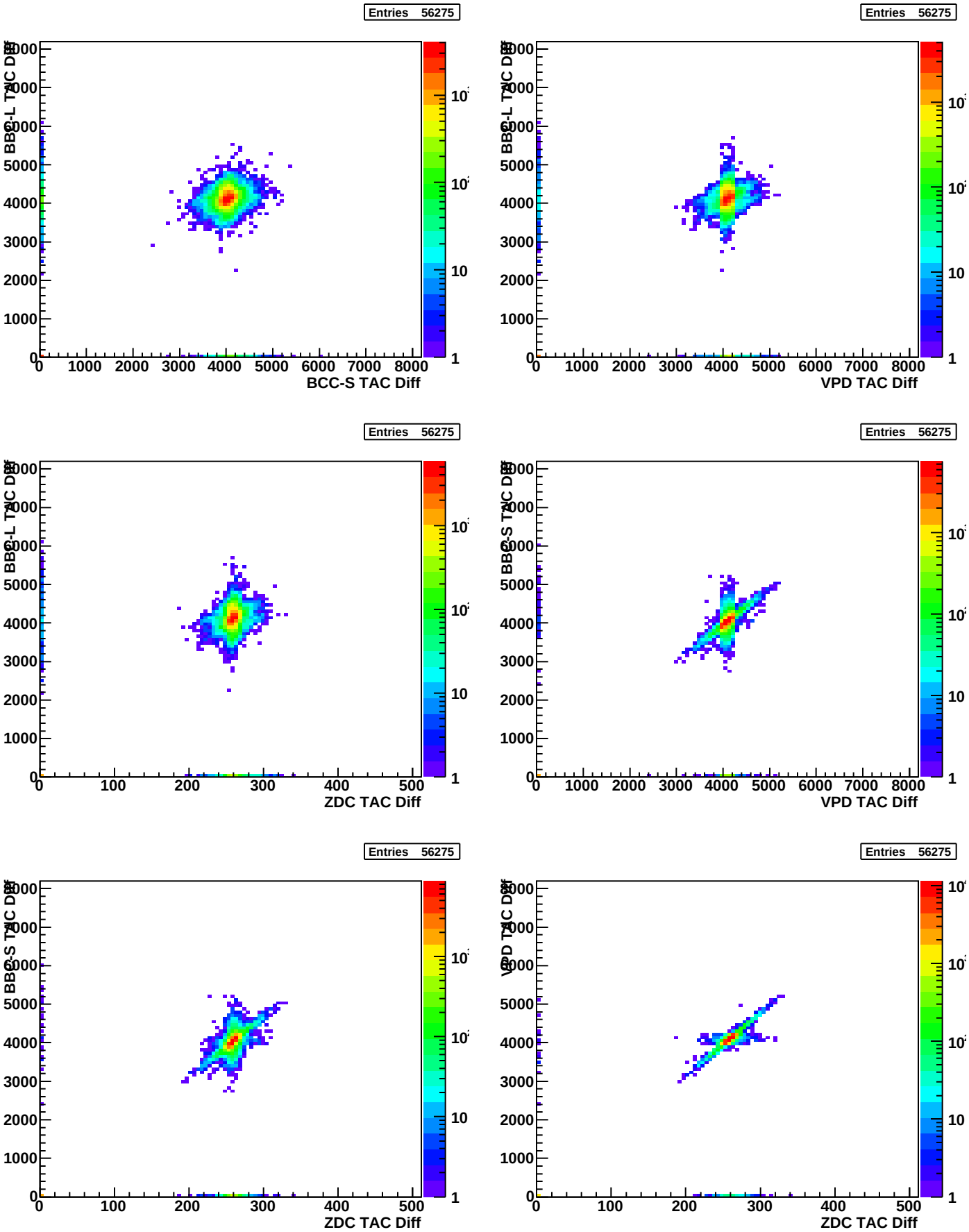
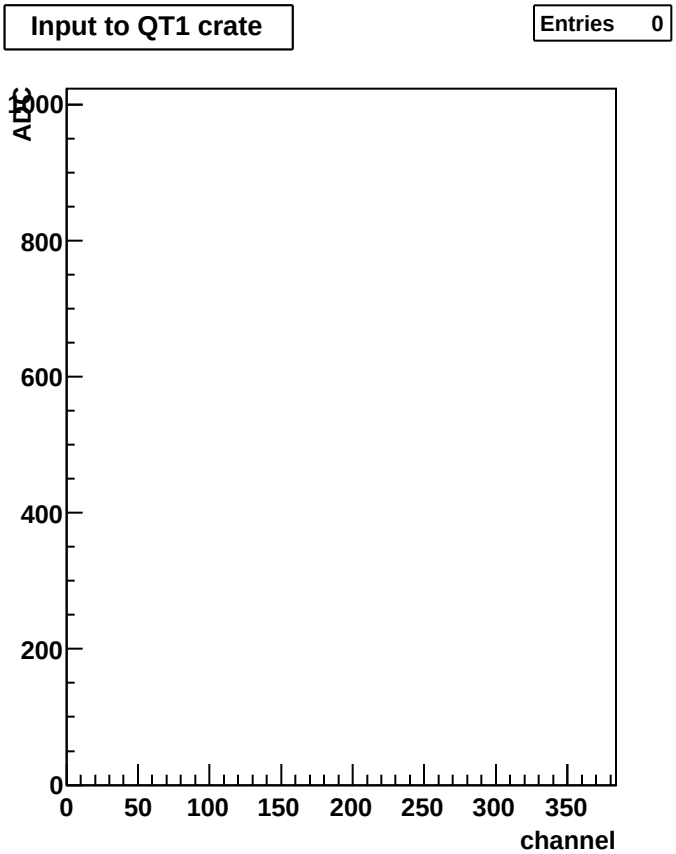
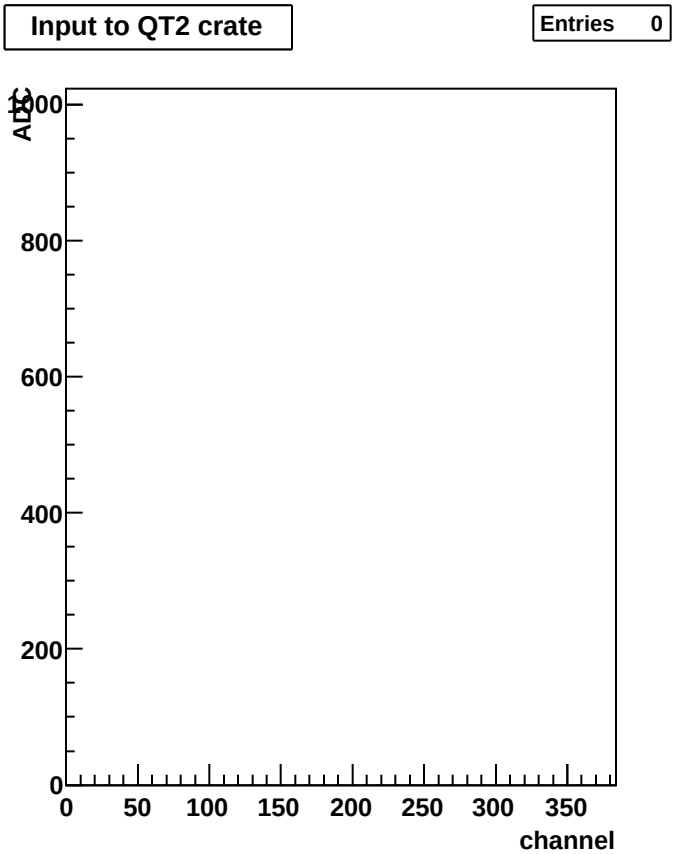
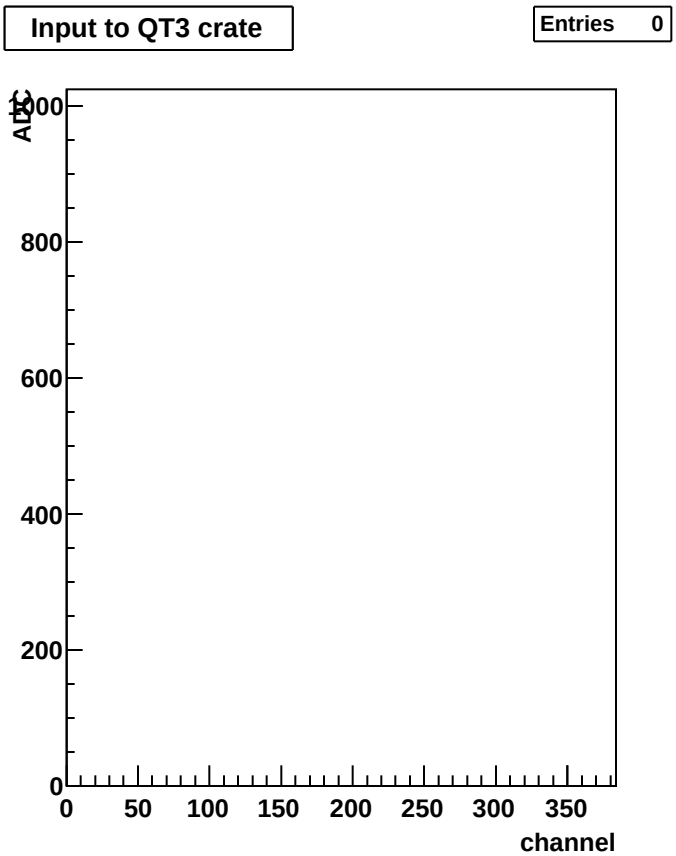
BBC-S-East ADC Sum
BBC-S-West ADC Sum

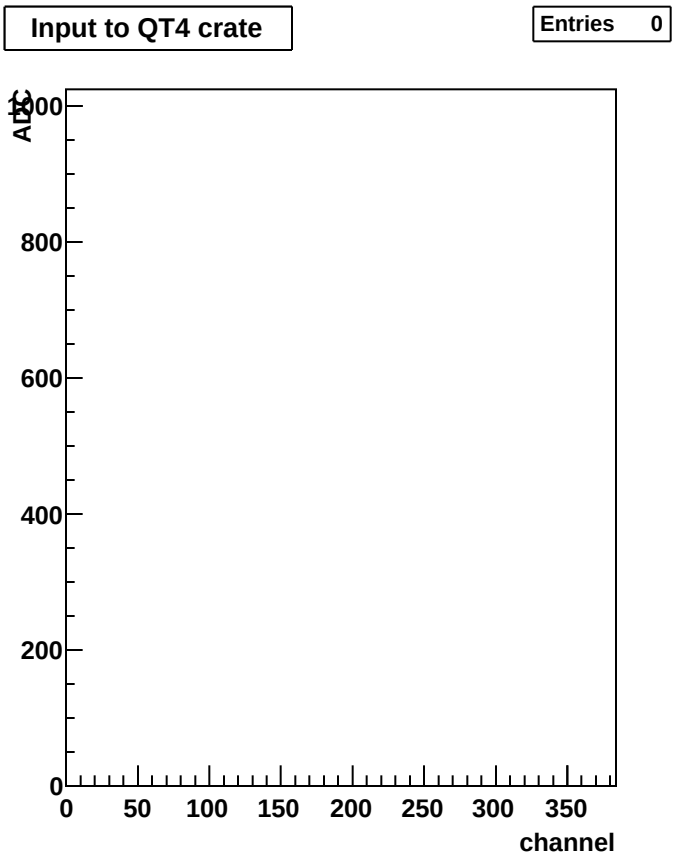

Entries 56275
ZDC East ADC Sum Att
ZDC West ADC Sum Att

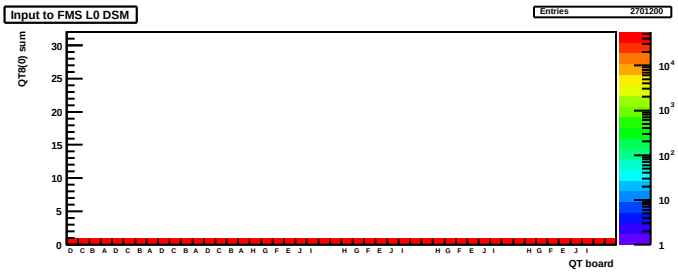

Entries 56275
BBC-L TAC Diff
BCC-S TAC Diff
Entries 56275
BBC-L TAC Diff
VPD TAC Diff
Entries 56275
BBC-L TAC Diff
ZDC TAC Diff
Entries 56275
BBC-S TAC Diff
VPD TAC Diff
Entries 56275
BBC-S TAC Diff
ZDC TAC Diff
Entries 56275
VPD TAC Diff
ZDC TAC Diff

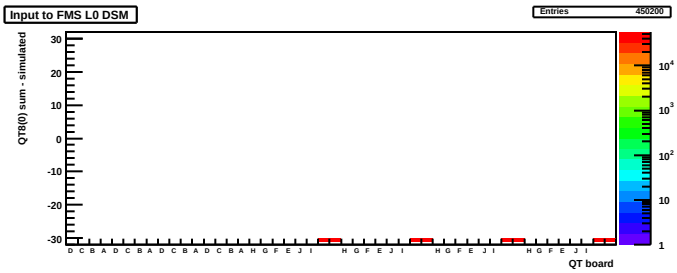

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

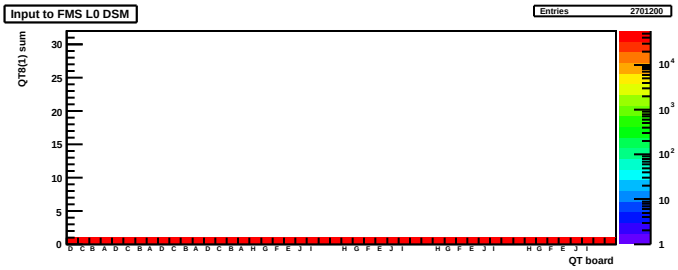

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

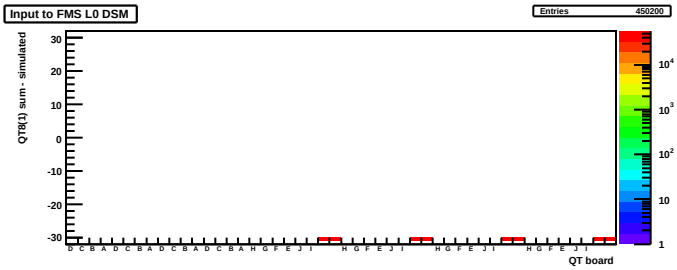

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

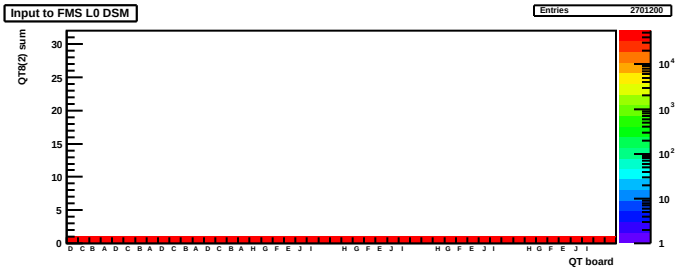

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

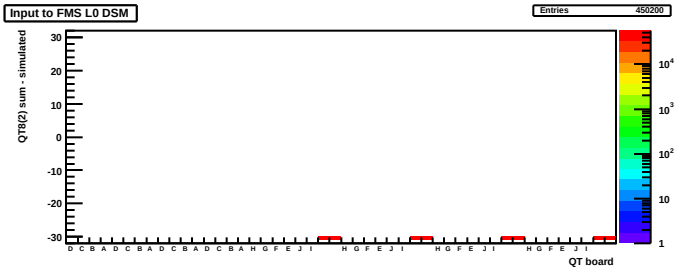

Input to FMS L0 DSM
Entries 2701200
QT8(0) sum
QT board

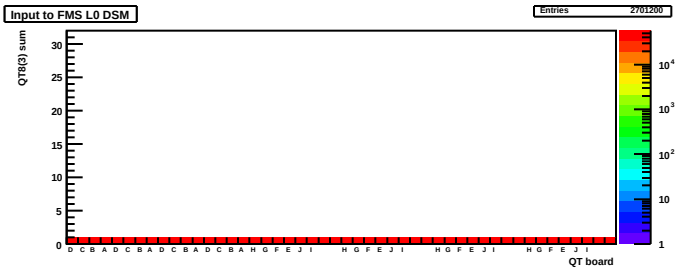

Input to FMS L0 DSM
Entries 450200
QT8(0) sum - simulated
QT board

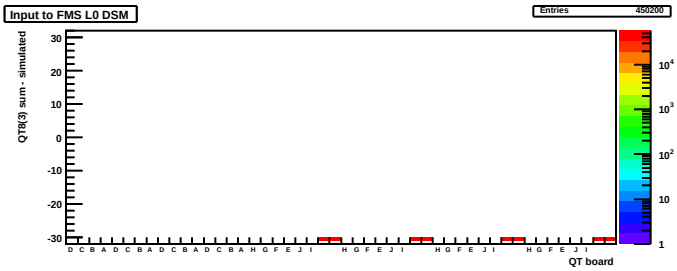

Input to FMS L0 DSM
Entries 2701200
QT8(1) sum
QT board

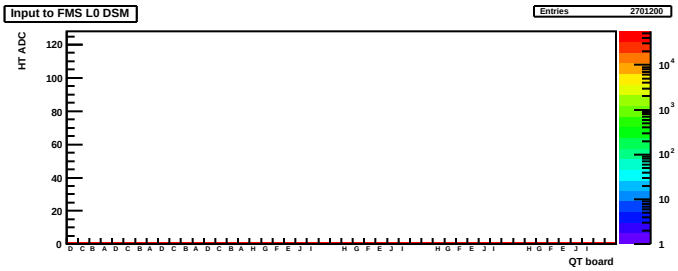

Input to FMS L0 DSM
Entries 450200
QT8(1) sum - simulated
QT board

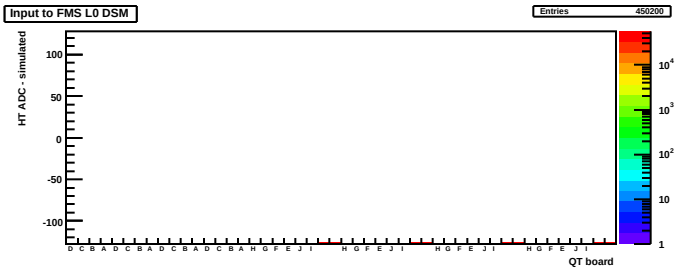

Input to FMS L0 DSM
Entries 2701200
QT8(2) sum
QT board

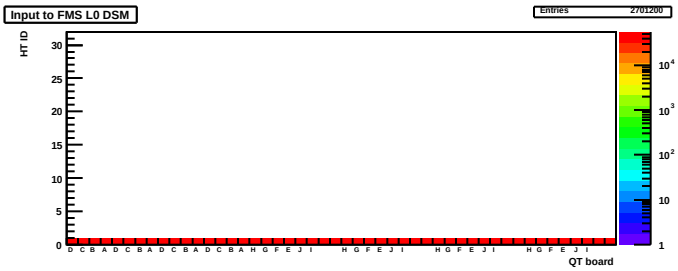

Input to FMS L0 DSM
Entries 450200
QT8(2) sum - simulated
QT board

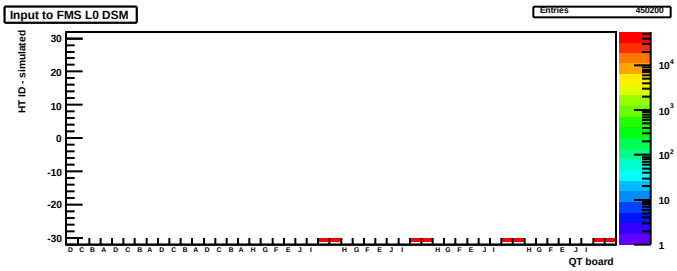

Input to FMS L0 DSM
Entries 2701200
QT8(3) sum
QT board

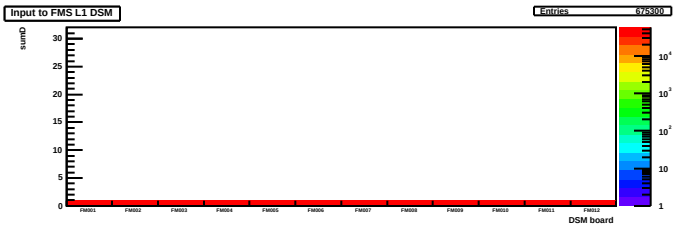

Input to FMS L0 DSM
Entries 450200
QT8(3) sum - simulated
QT board

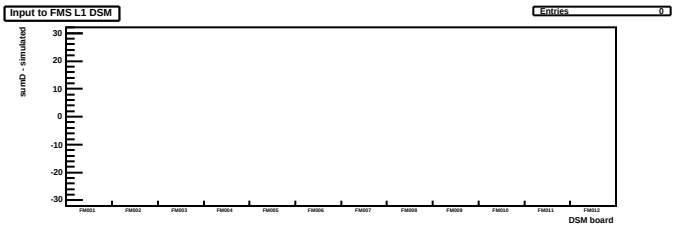

Input to FMS L0 DSM
Entries 2701200
HT ADC
QT board

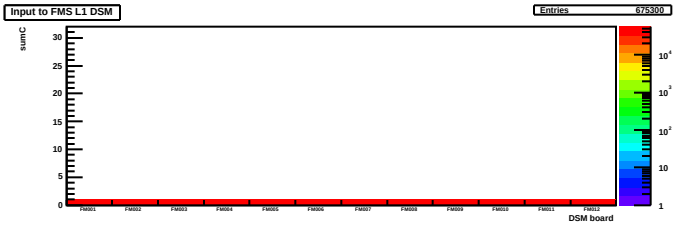

Input to FMS L0 DSM
Entries 450200
HT ADC - simulated
QT board

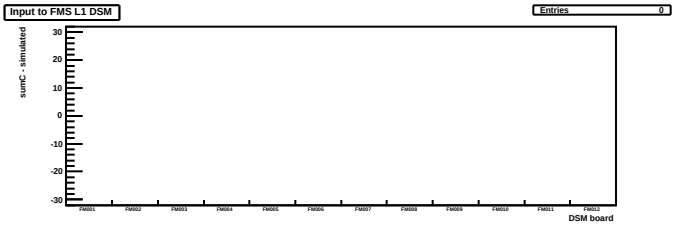

Input to FMS L0 DSM
Entries 2701200
HT ID
QT board

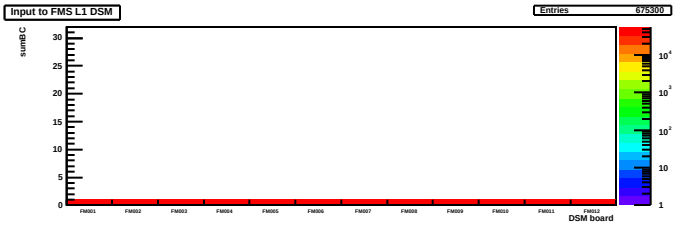

Input to FMS L0 DSM
Entries 450200
HT ID - simulated
QT board

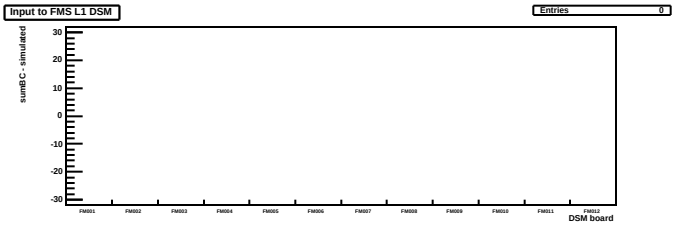

Input to FMS L1 DSM
Entries 675300
sumD
DSM board

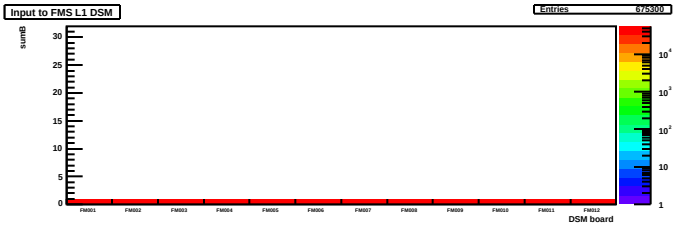

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

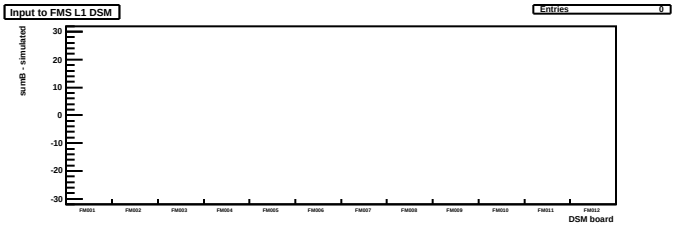

Input to FMS L1 DSM
Entries 675300
sumC
DSM board

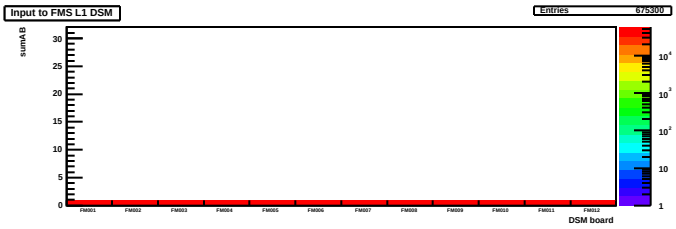

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

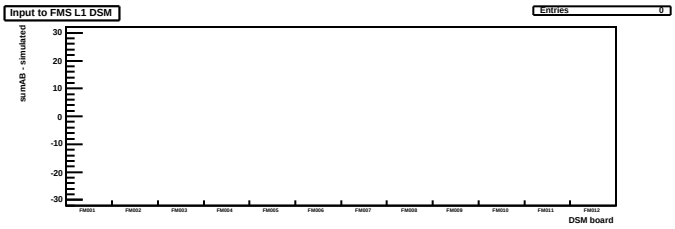

Input to FMS L1 DSM
Entries 675300
sumBC
DSM board

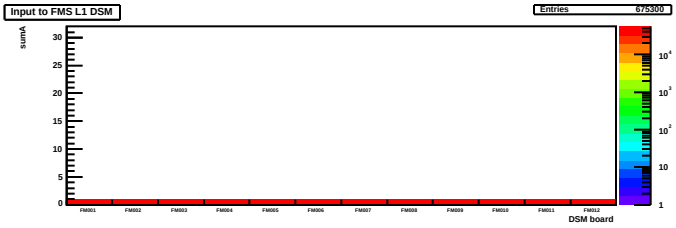

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

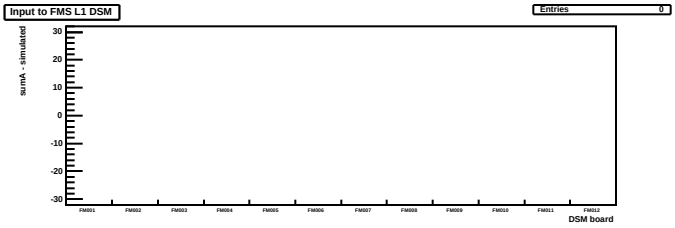

Input to FMS L1 DSM
Entries 675300
sumB
DSM board

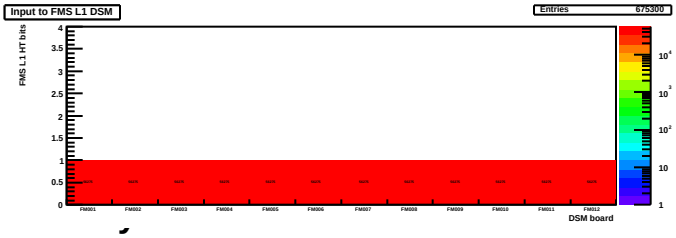

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

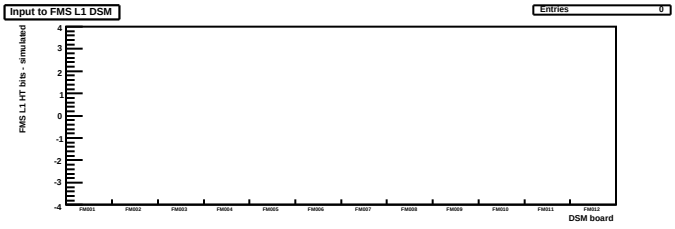

Input to FMS L1 DSM
Entries 675300
sumAB
DSM board

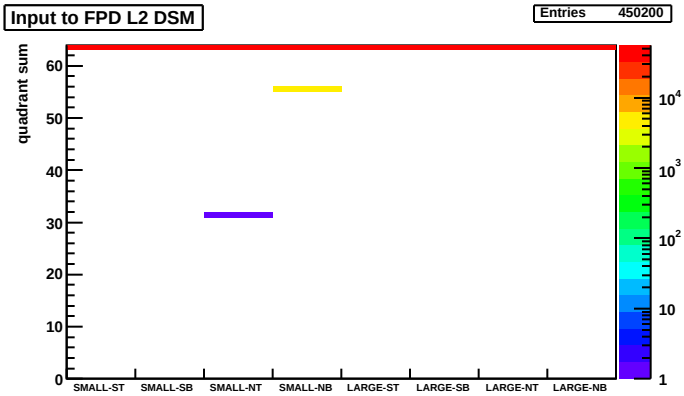

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

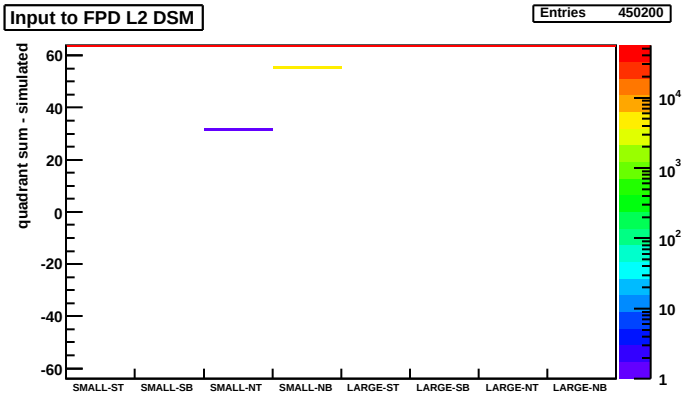

Input to FMS L1 DSM
Entries 675300
sumA
DSM board

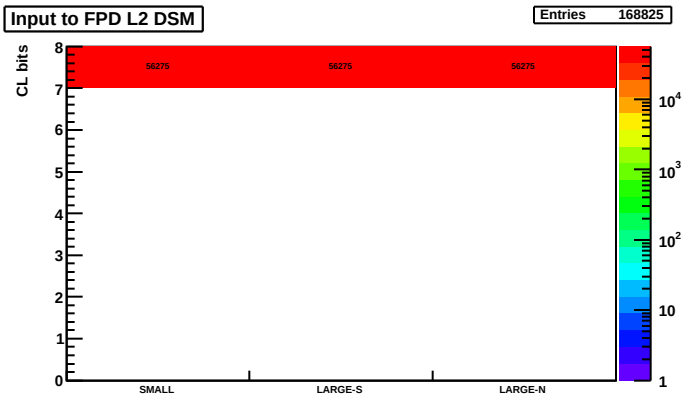

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

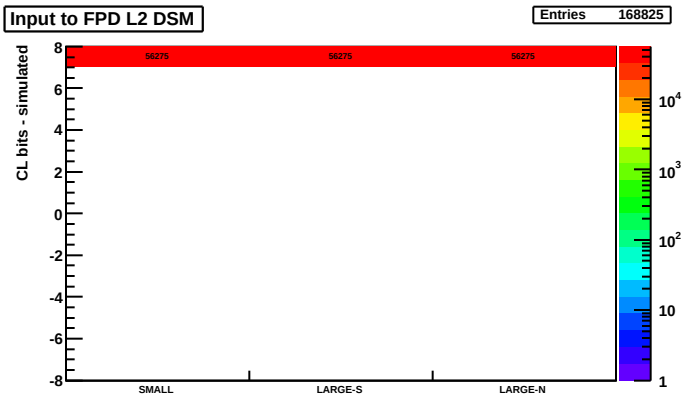

Input to FMS L1 DSM
Entries 675300
FMS L1 HT bits
DSM board

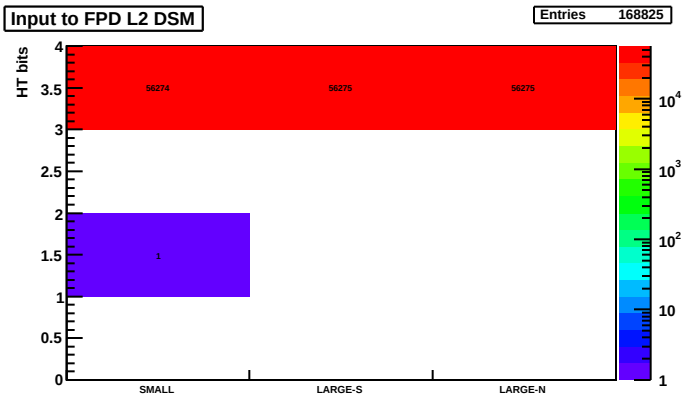

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

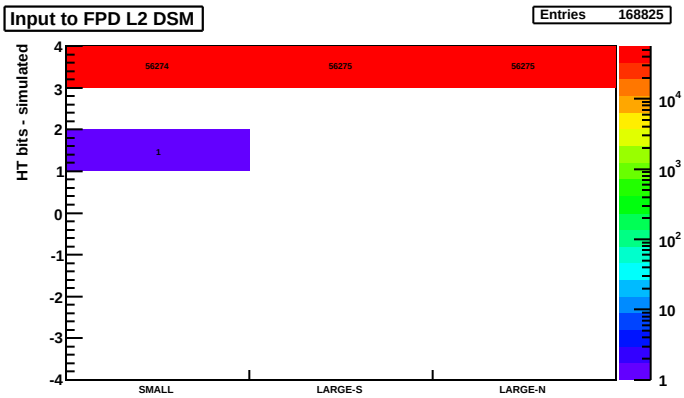

Input to FPD L2 DSM
Entries 450200
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

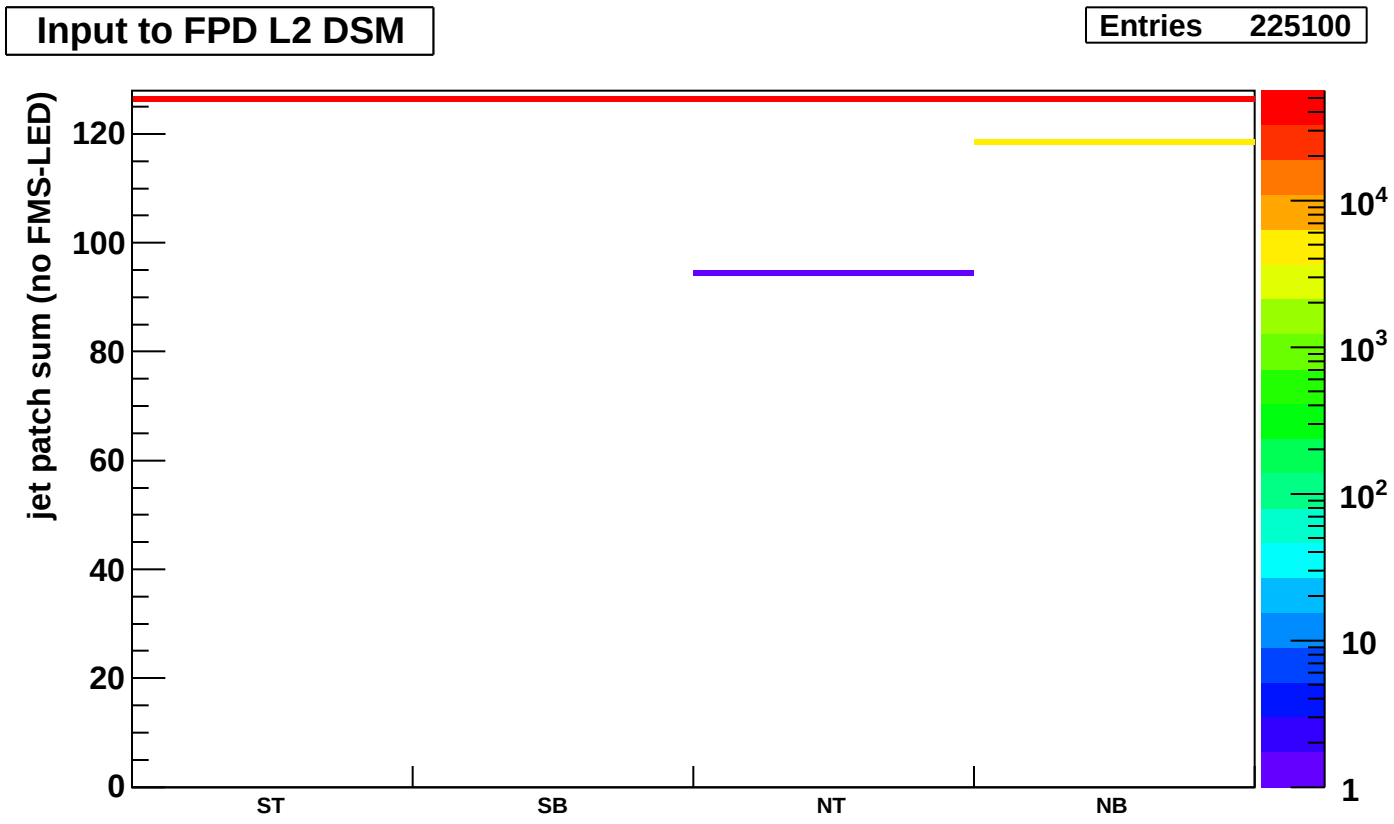

Input to FPD L2 DSM
Entries 450200
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

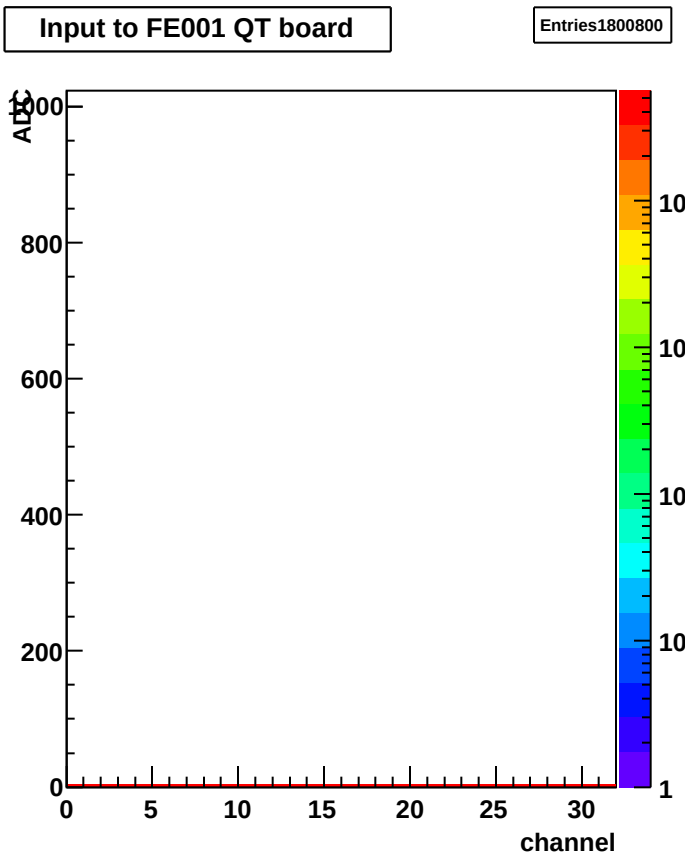

Input to FPD L2 DSM
Entries 168825
CL bits
56275
56275
56275
SMALL
LARGE-S
LARGE-N

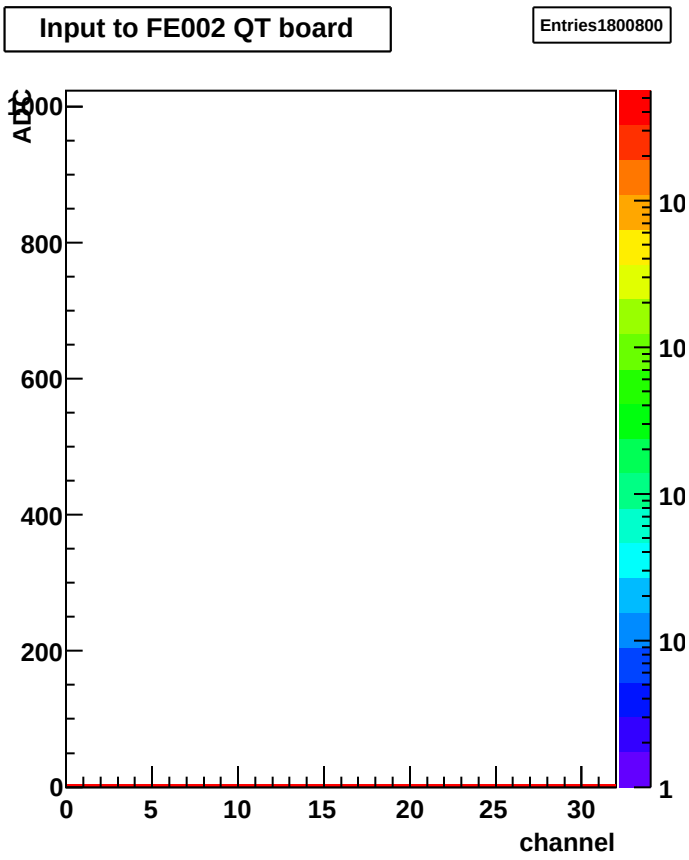

Input to FPD L2 DSM
Entries 168825
CL bits - simulated
56275
56275
56275
SMALL
LARGE-S
LARGE-N

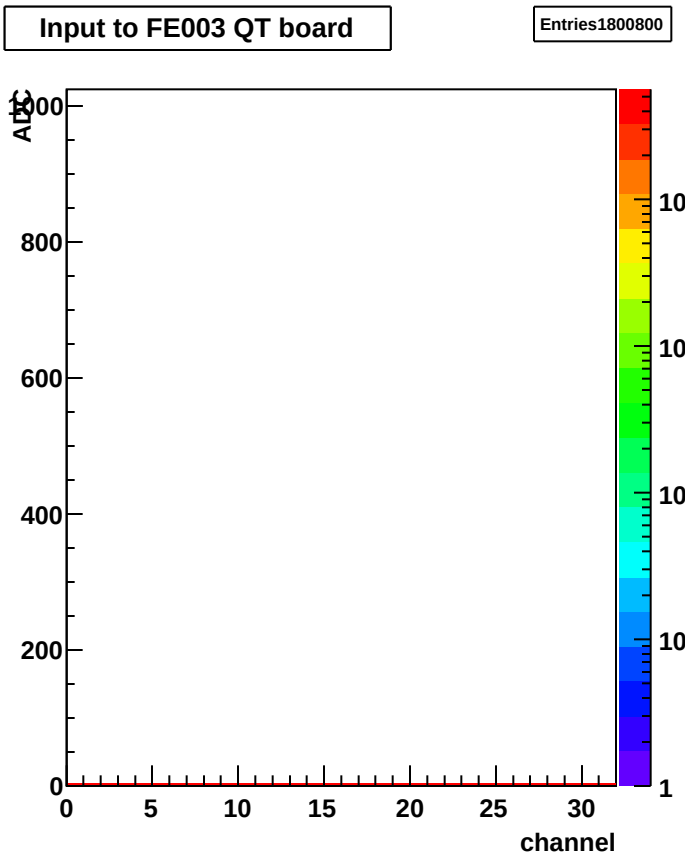

Input to FPD L2 DSM
Entries 168825
HT bits
56274
56275
56275
1
SMALL
LARGE-S
LARGE-N

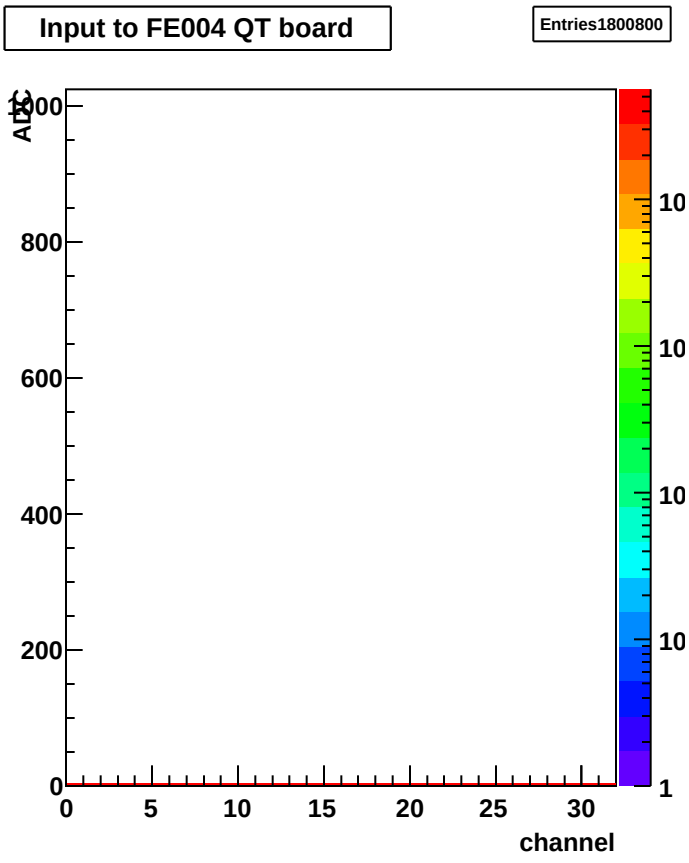

Input to FPD L2 DSM
Entries 168825
HT bits - simulated
56274
56275
56275
1
SMALL
LARGE-S
LARGE-N

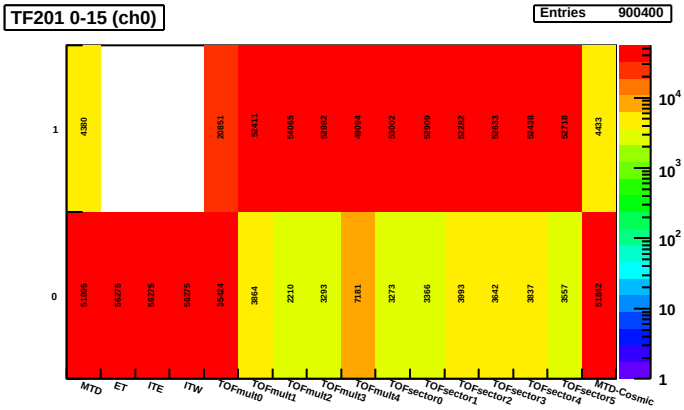

Input to FPD L2 DSM
Entries 225100
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

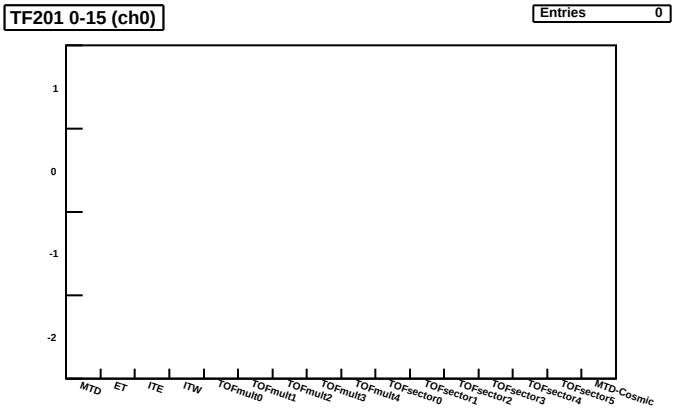

Input to FE001 QT board
Entries1800800
ADC
channel

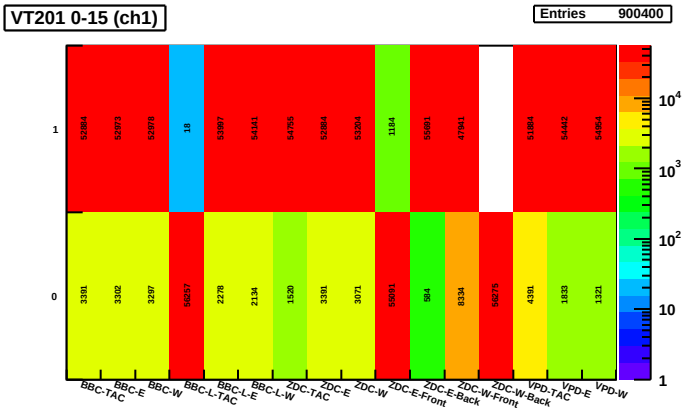

Input to FE002 QT board
Entries1800800
ADC
channel

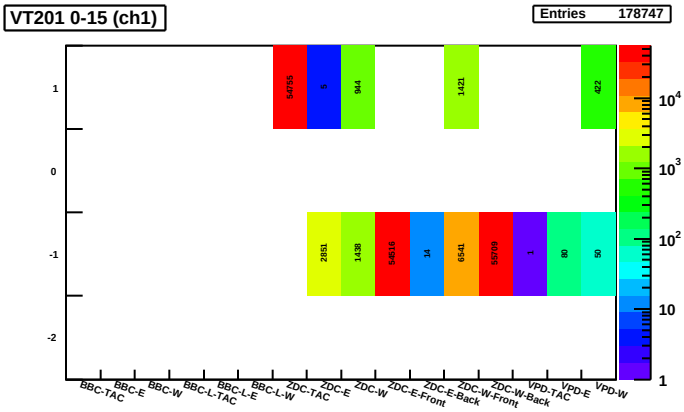

Input to FE003 QT board
Entries1800800
ADC
channel

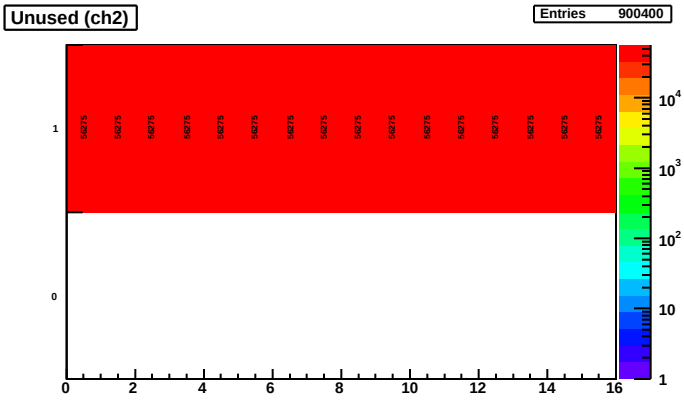

Input to FE004 QT board
Entries1800800
ADC
channel

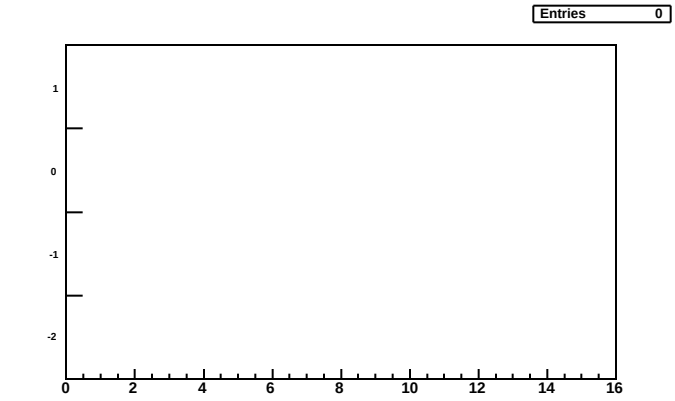

TF201 0-15 (ch0)
Entries 900400
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

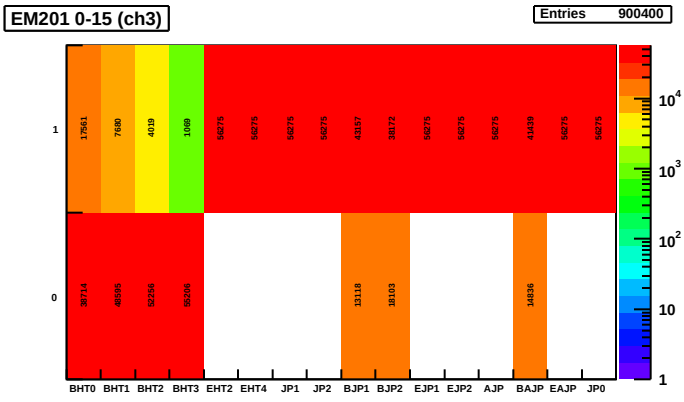

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

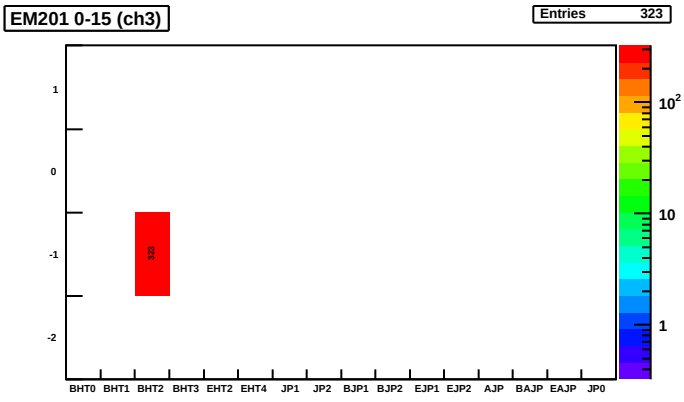

VT201 0-15 (ch1)
Entries 900400
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 178747
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

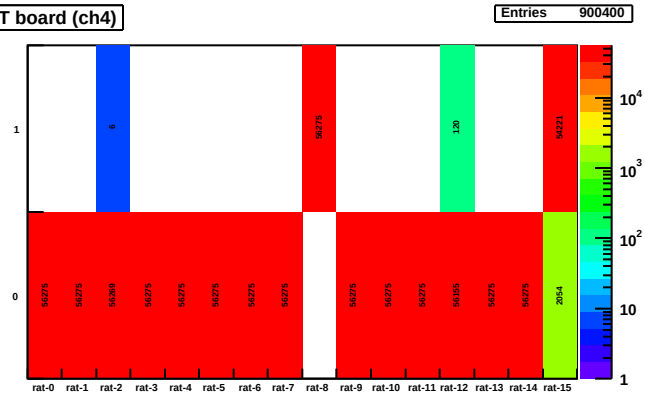

Unused (ch2)
Entries 900400

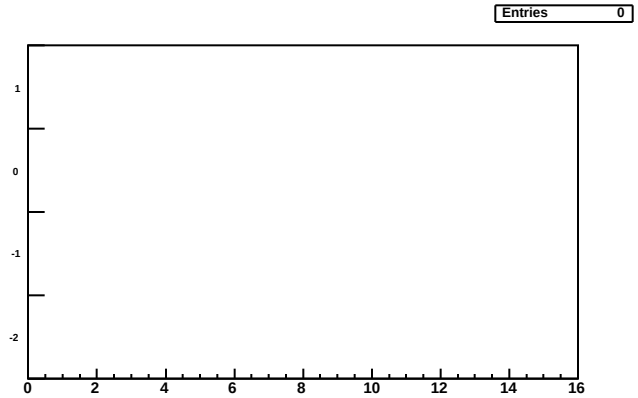

Entries 0


EM201 0-15 (ch3)
Entries 900400
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

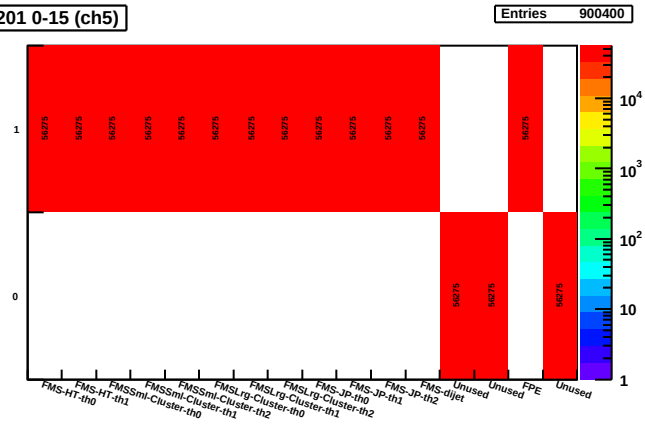

EM201 0-15 (ch3)
Entries 323
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

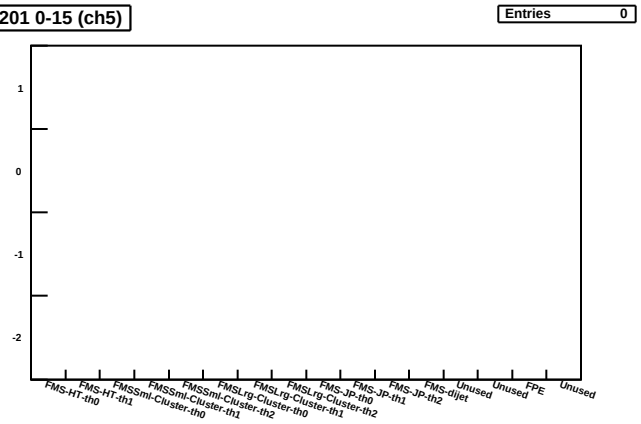




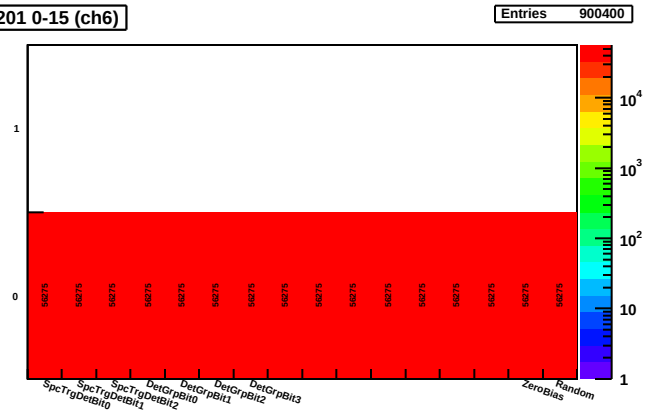
FP201 0-15 (ch5)

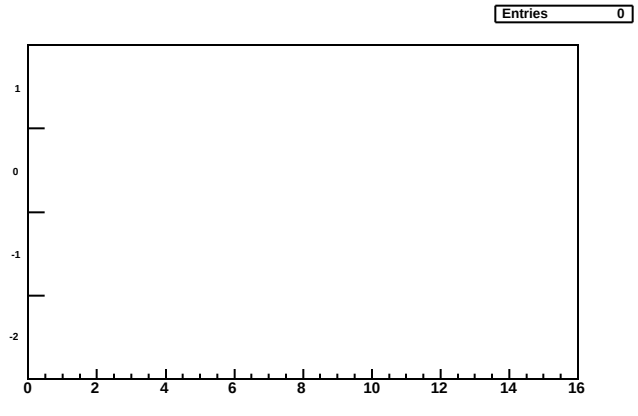


FP201 0-15 (ch5)

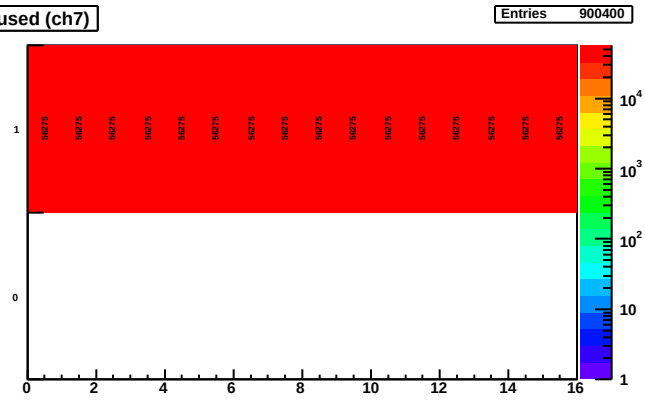


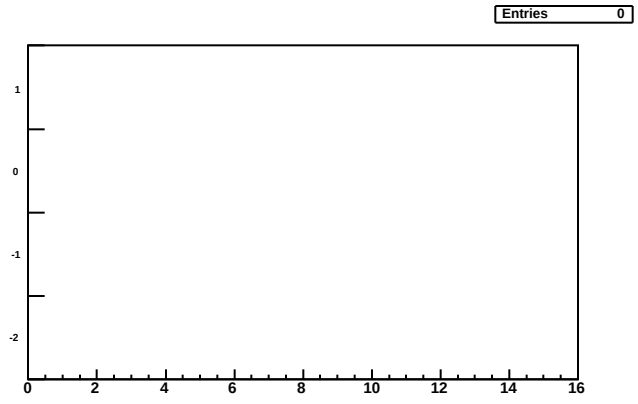
ST201 0-15 (ch6)

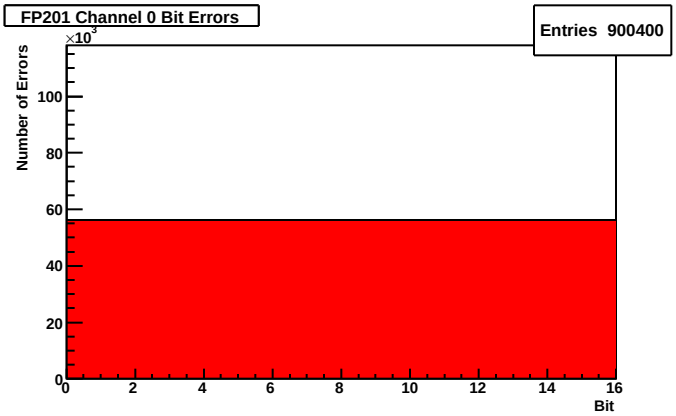


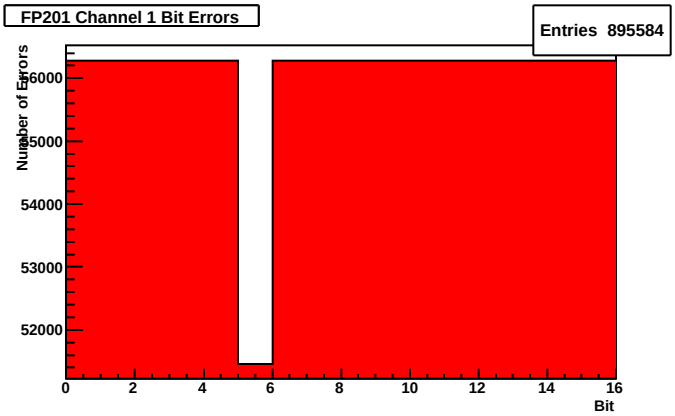


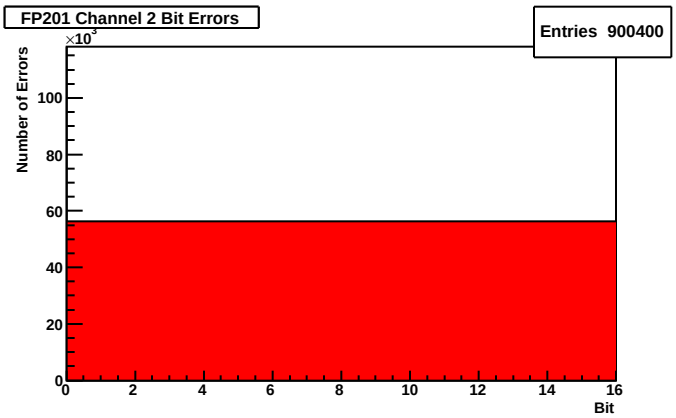
Unused (ch7)

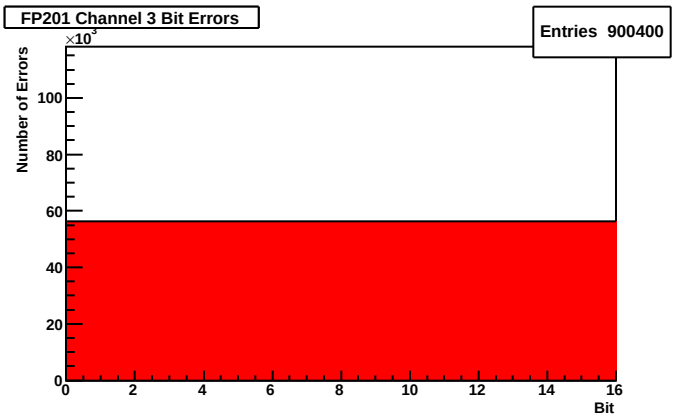


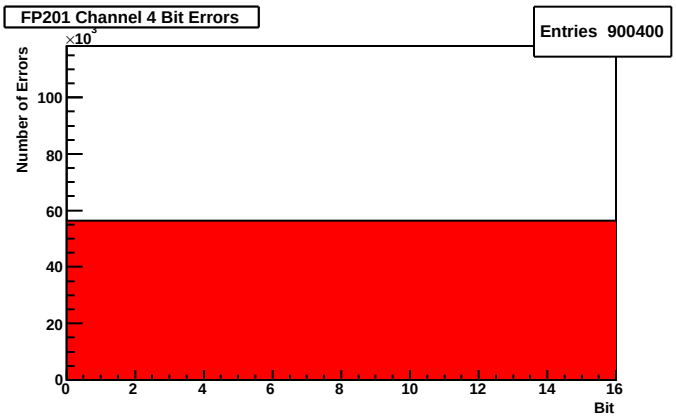


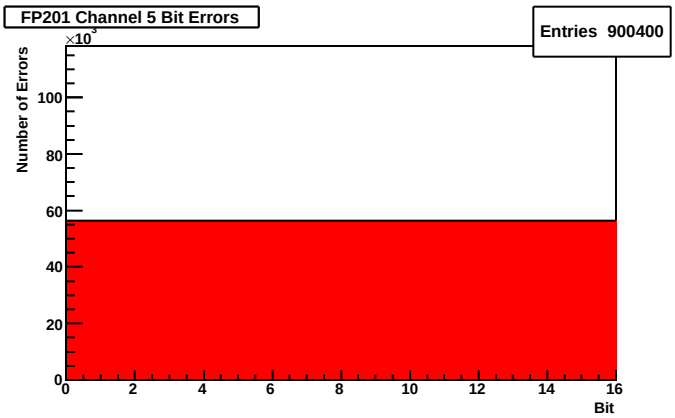

FP201 Channel 0 Bit Errors
Entries 900400
Number of Errors
×10³
Bit

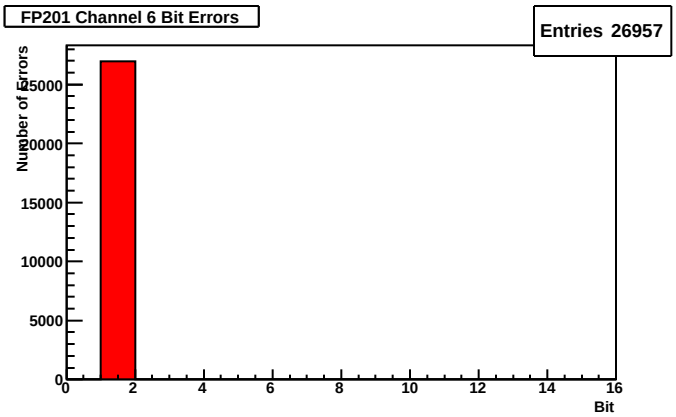

FP201 Channel 1 Bit Errors
Entries 895584
Number of Errors
Bit

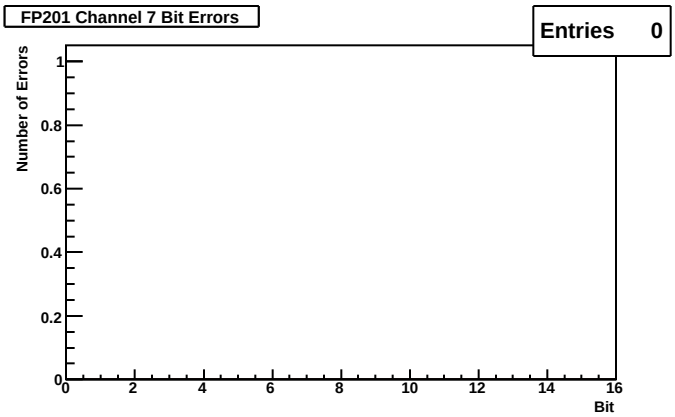

FP201 Channel 2 Bit Errors
Entries 900400
Number of Errors
×10³
Bit

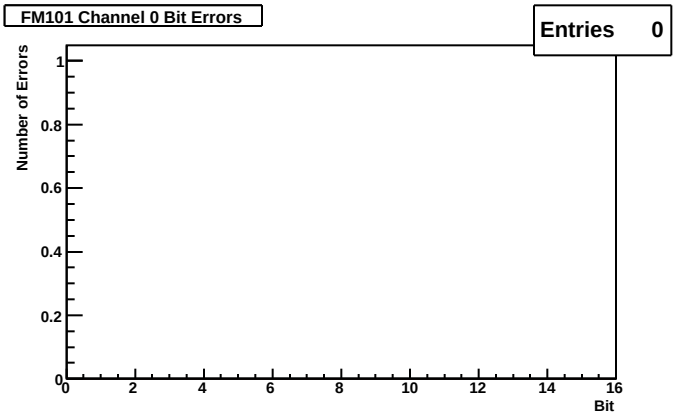

FP201 Channel 3 Bit Errors
Entries 900400
Number of Errors
×10³
Bit

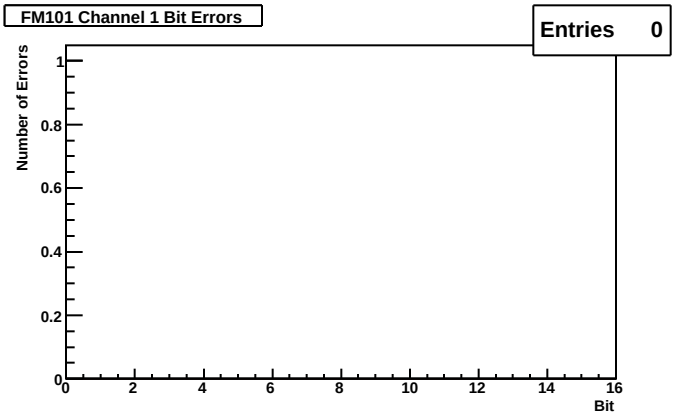

FP201 Channel 4 Bit Errors
Entries 900400
Number of Errors
×10³
Bit

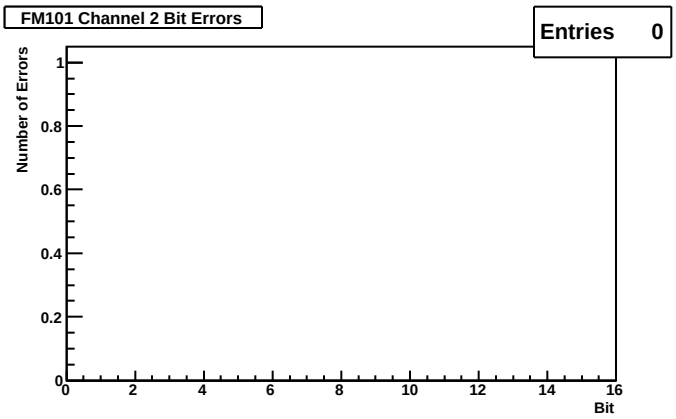

FP201 Channel 5 Bit Errors
Entries 900400
Number of Errors
×10³
Bit

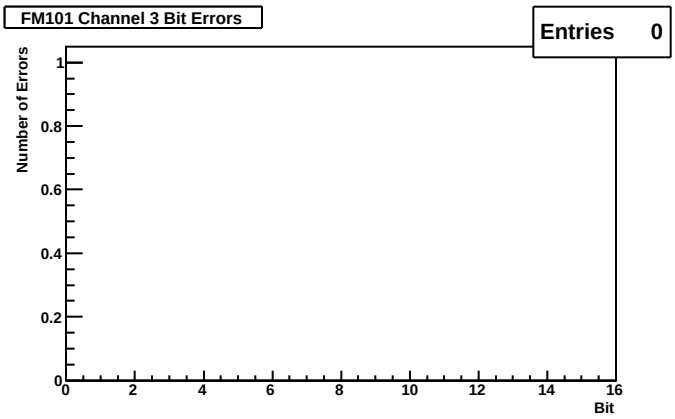

FP201 Channel 6 Bit Errors
Entries 26957
Number of Errors
Bit

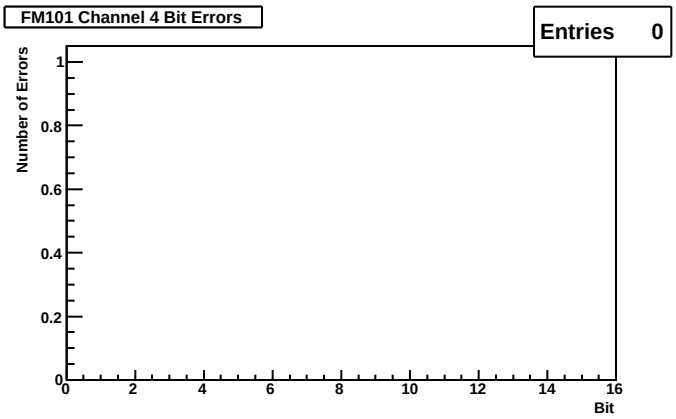

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

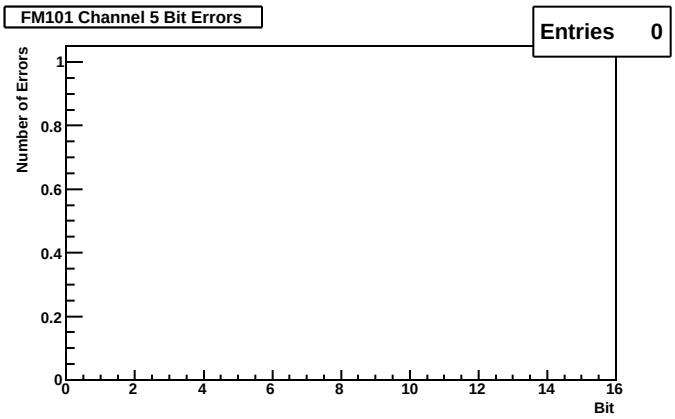

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

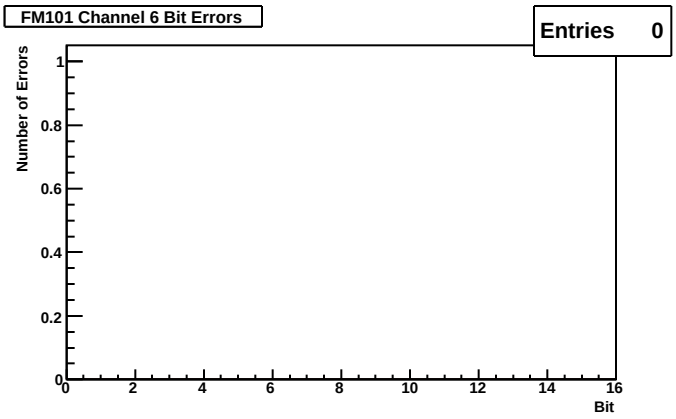

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

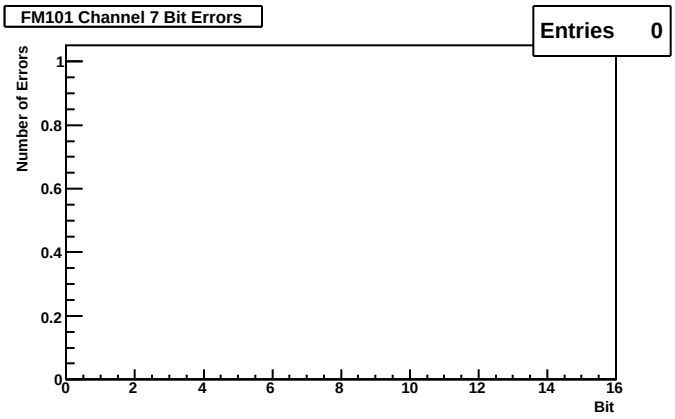

FM101 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

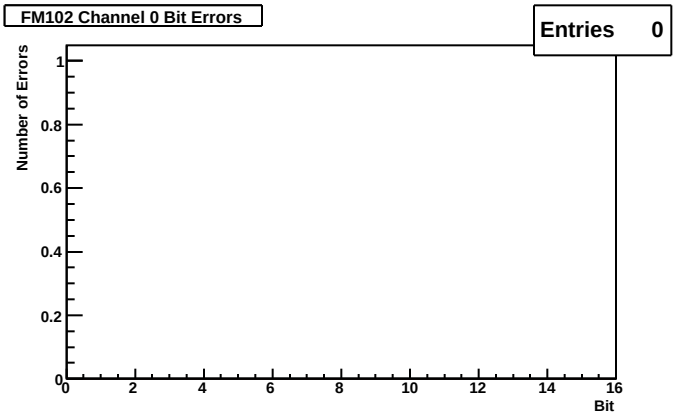

FM101 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

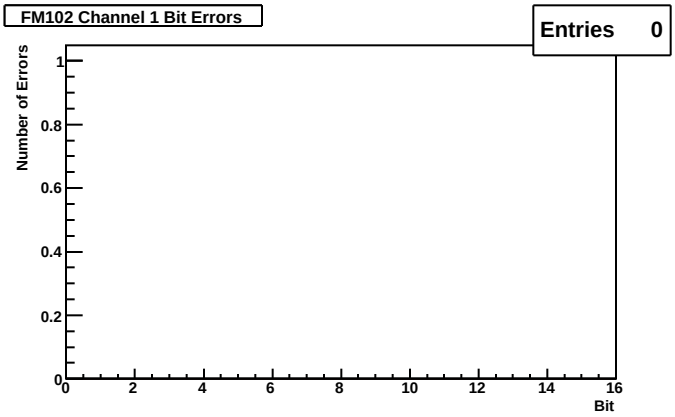

FM101 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

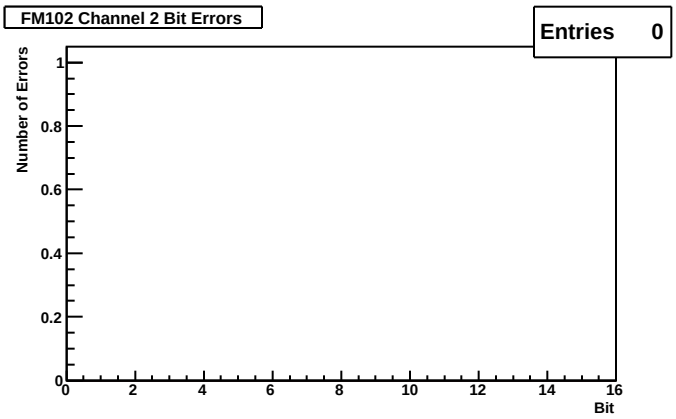

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

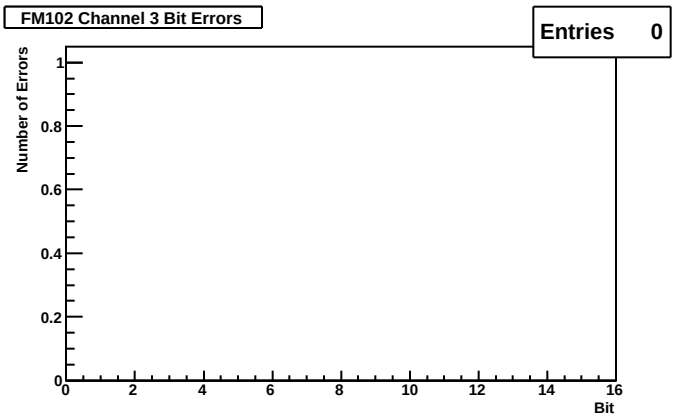

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

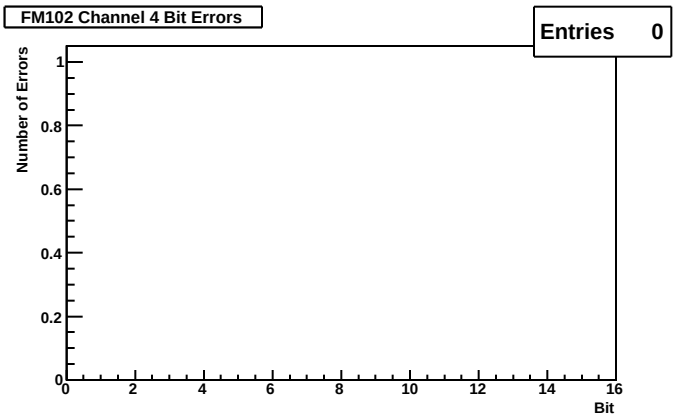

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

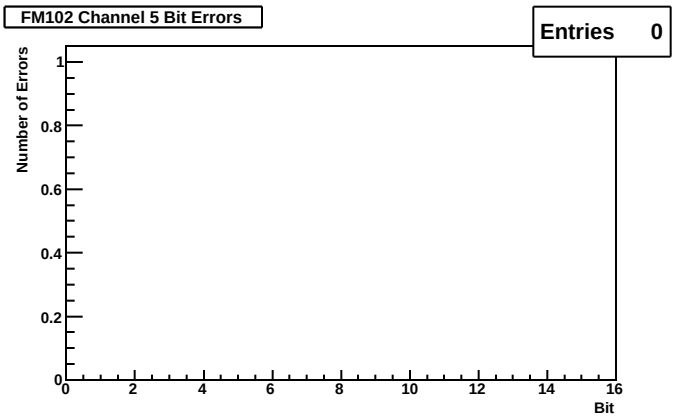

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

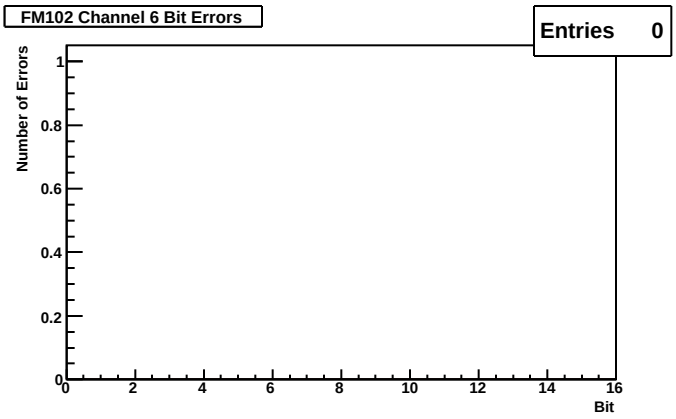

FM102 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

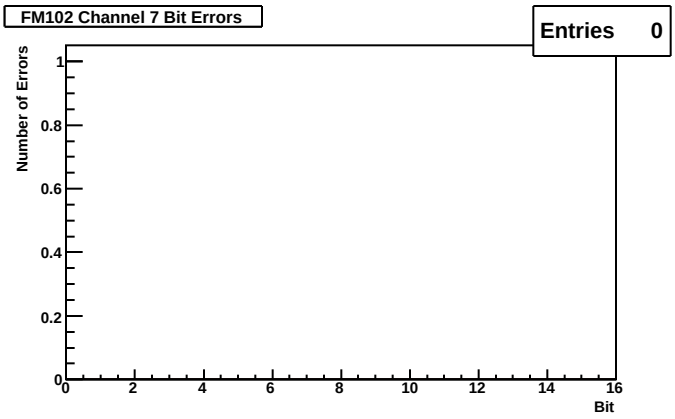

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

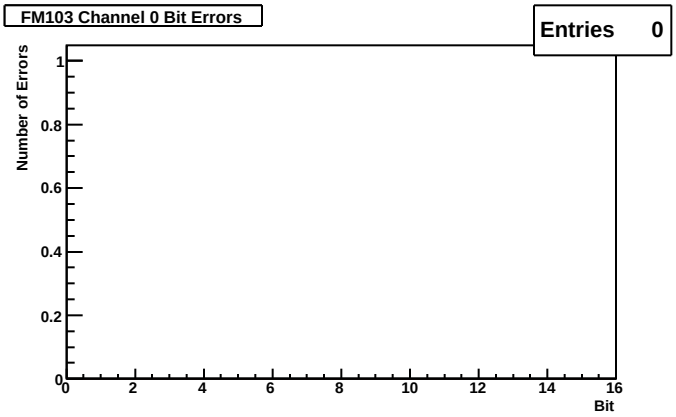

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

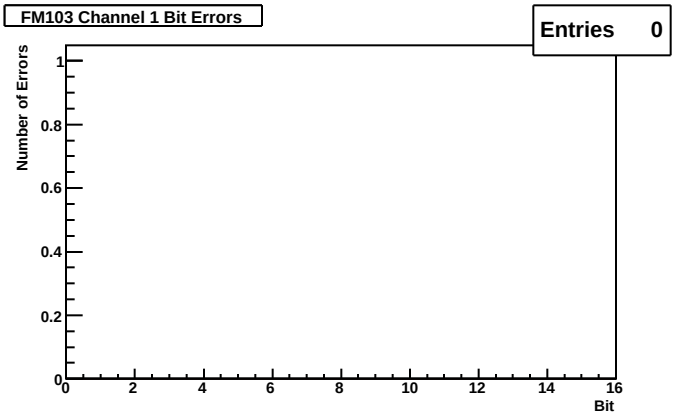

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

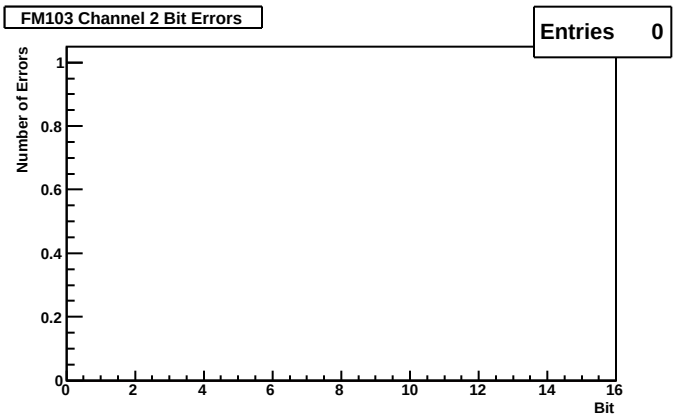

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

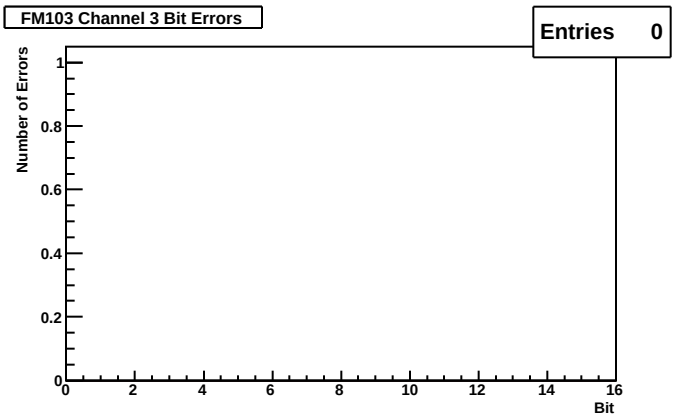

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

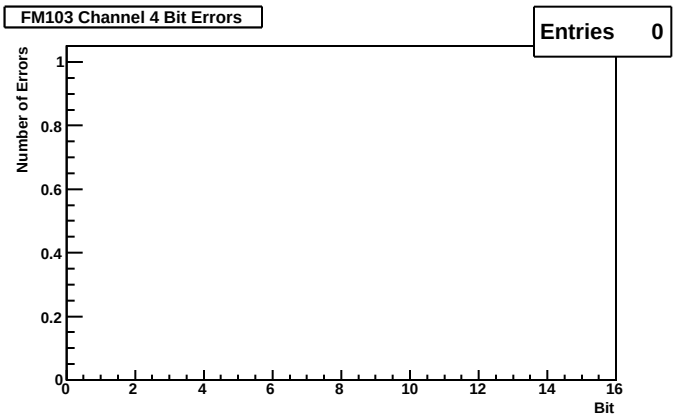

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

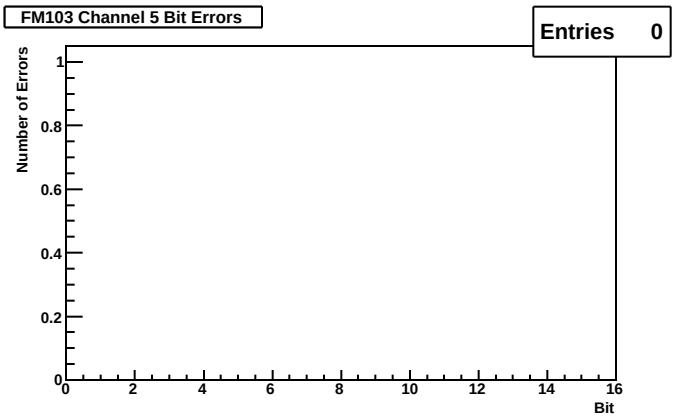

FM103 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

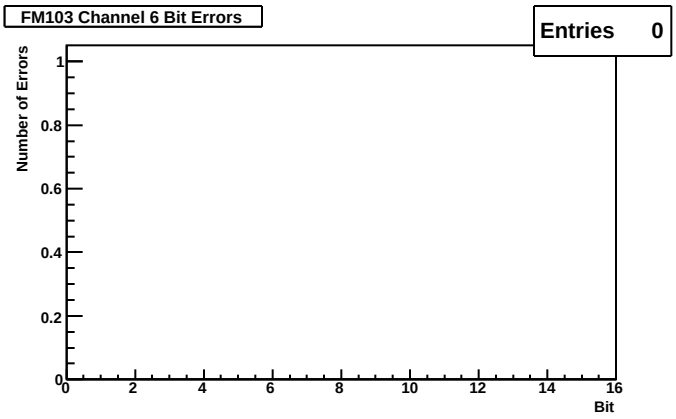

FM103 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

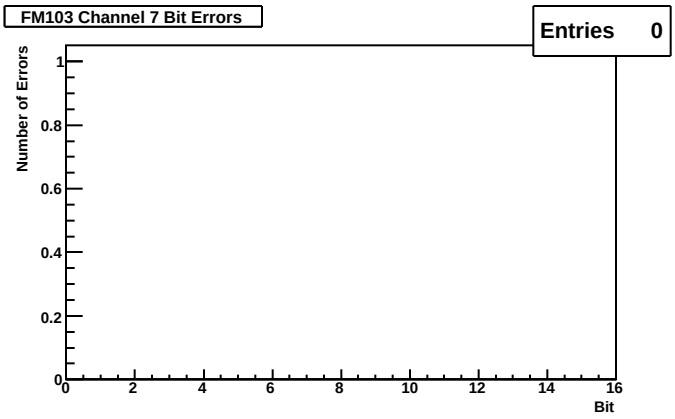

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

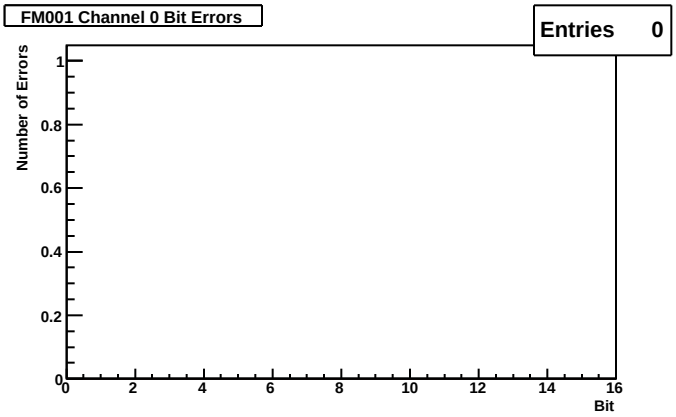

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

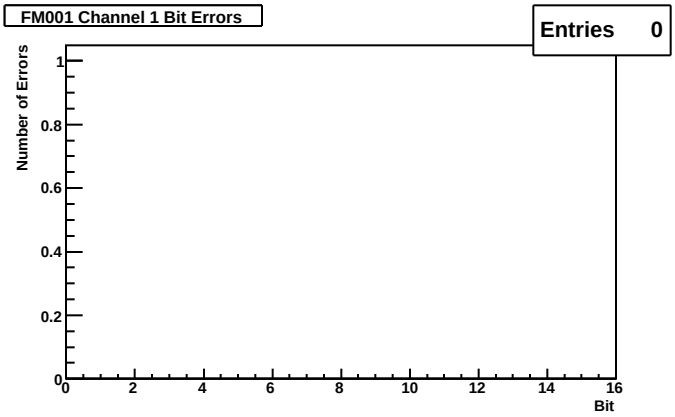

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

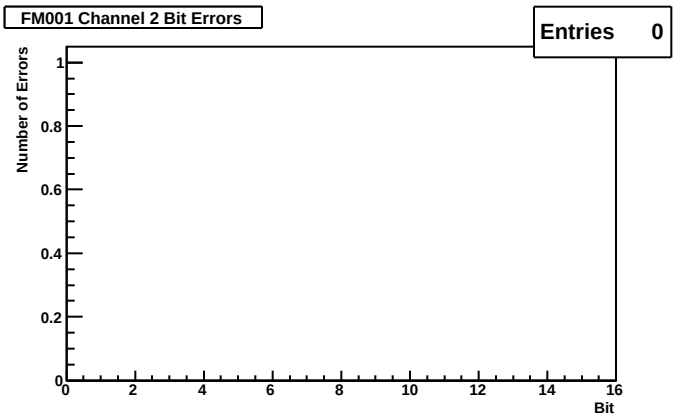

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

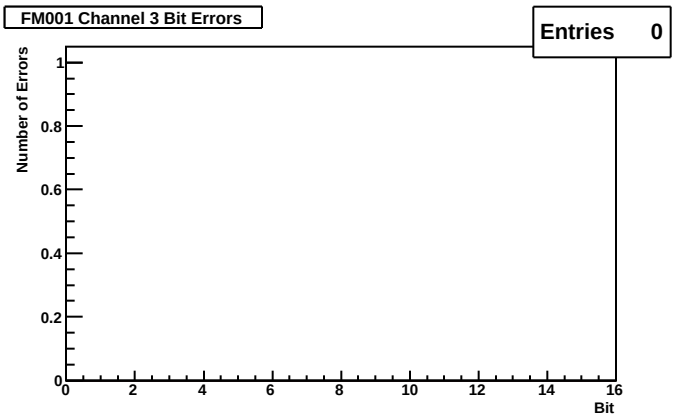

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

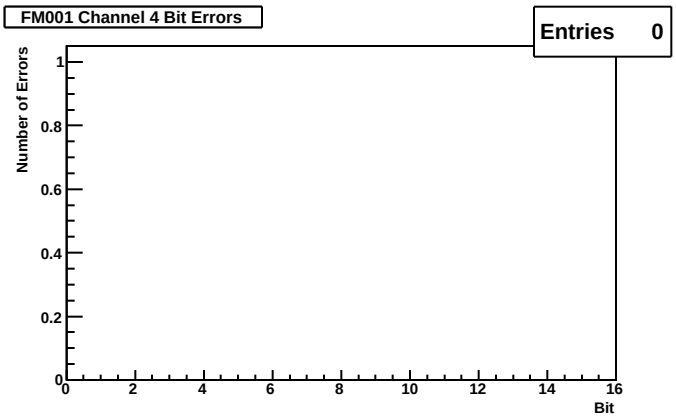

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

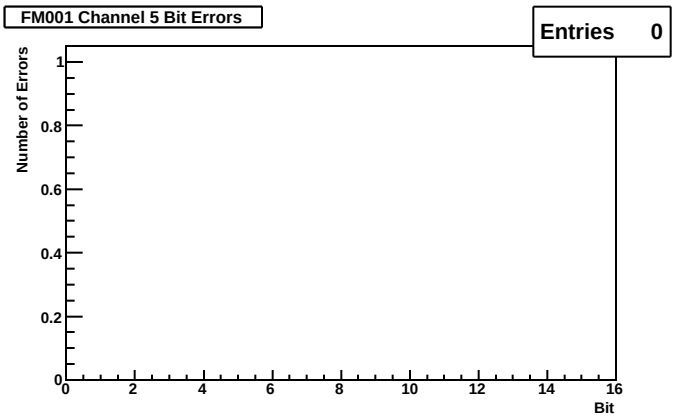

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

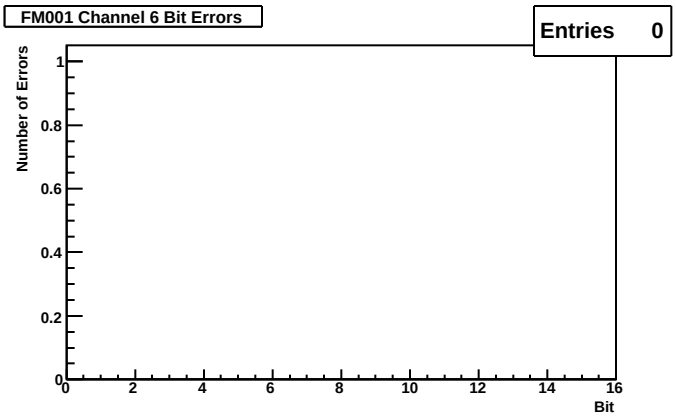

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

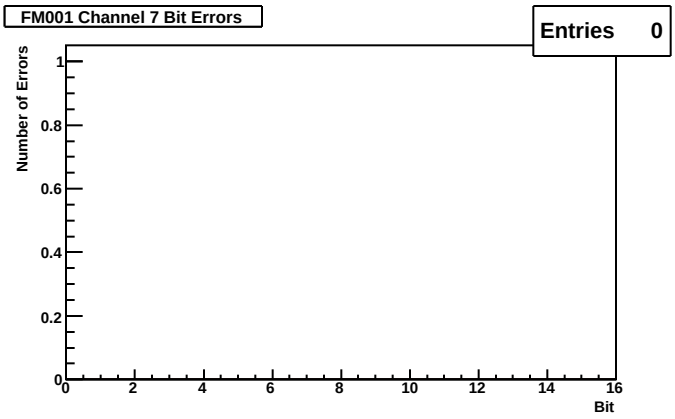

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

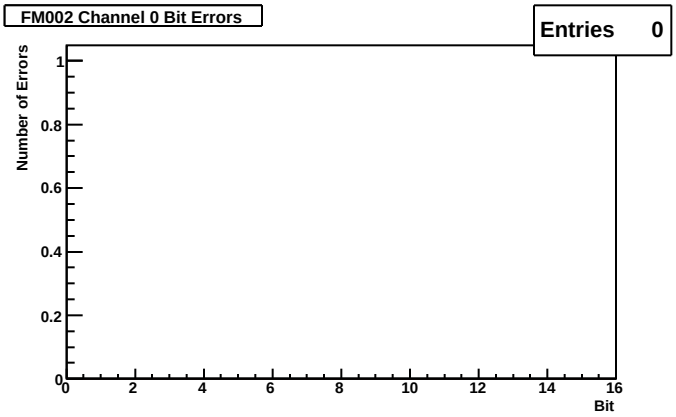

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

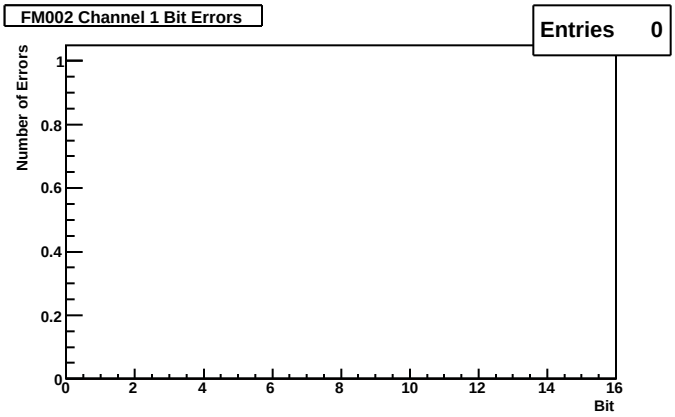

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

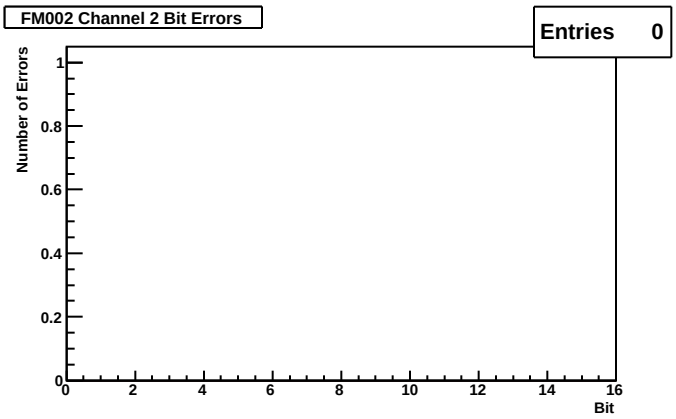

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

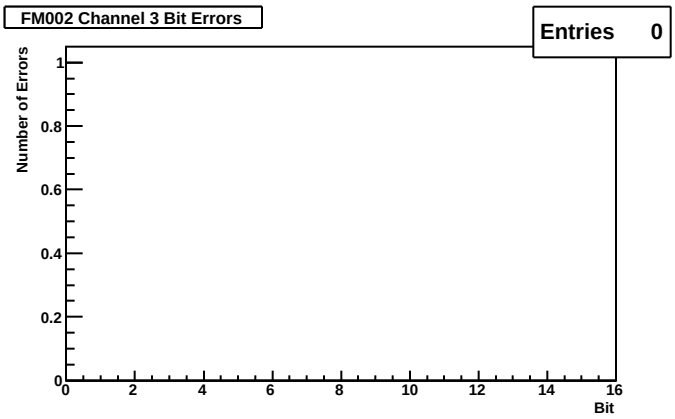

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

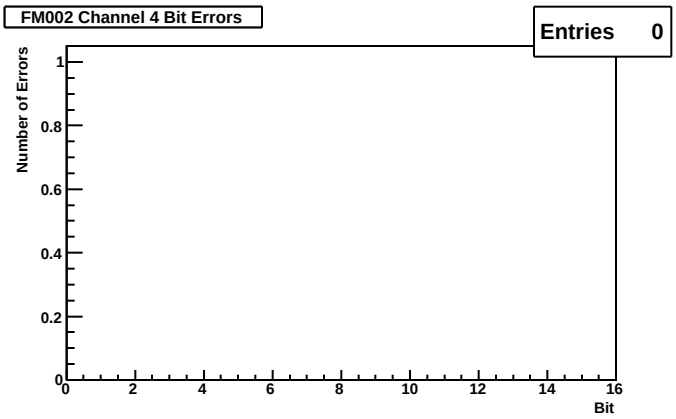

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

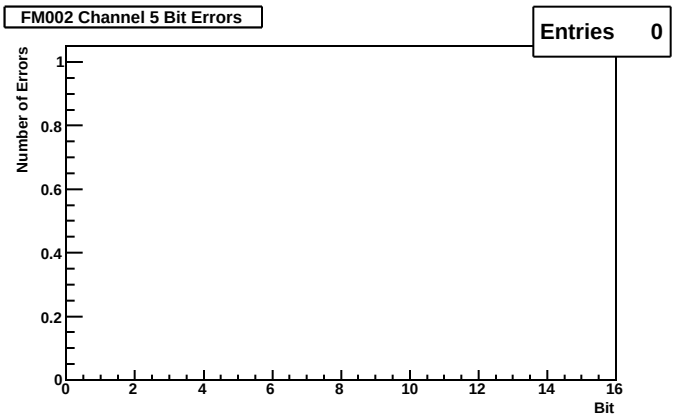

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

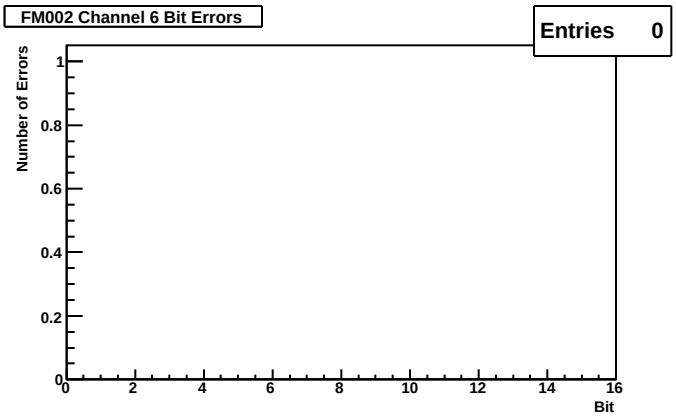

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

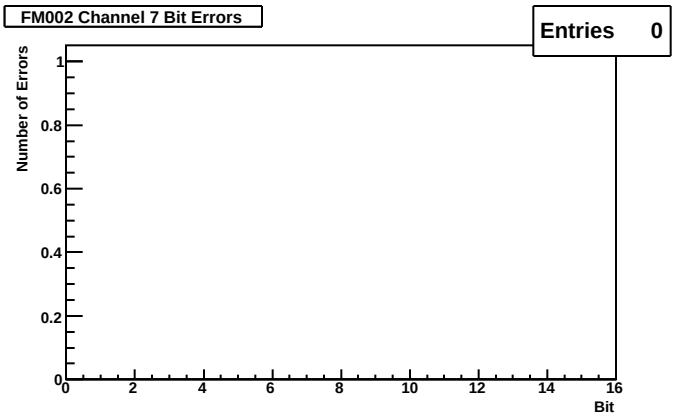

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

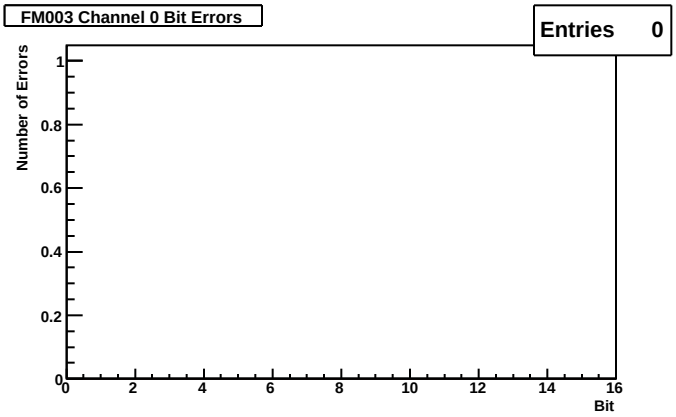

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

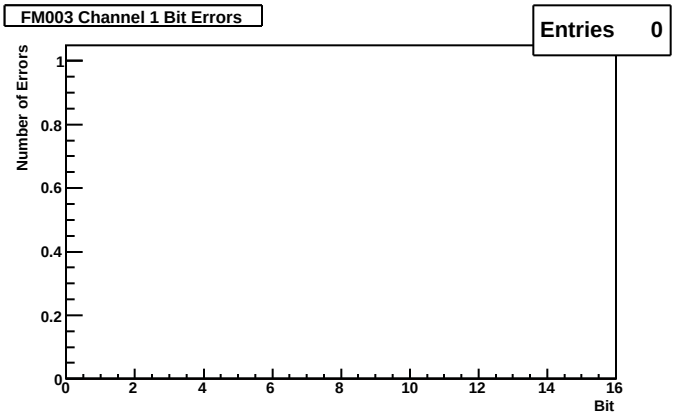

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

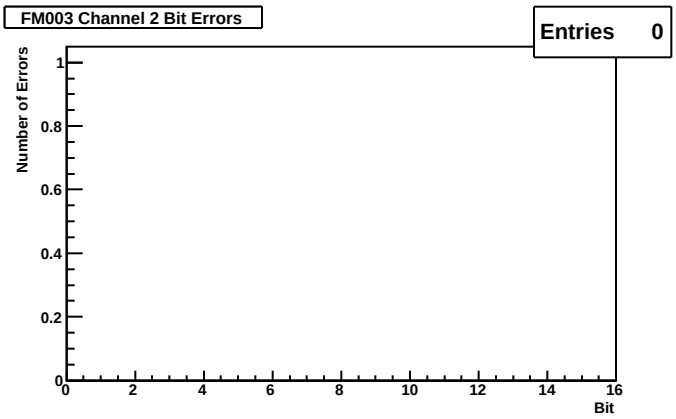

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

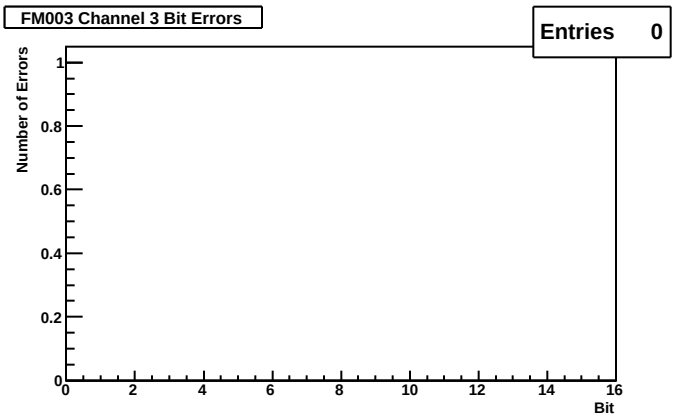

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

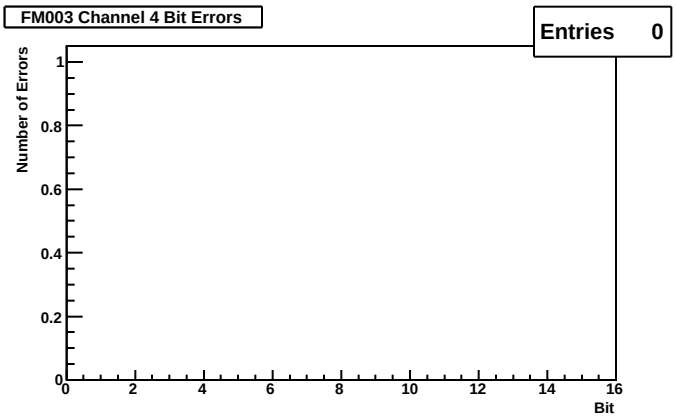

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

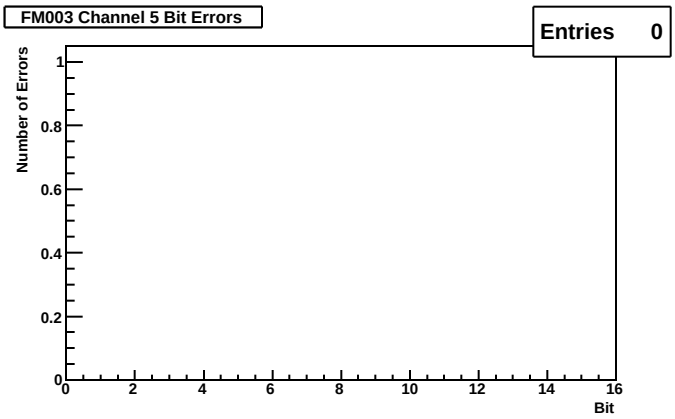

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

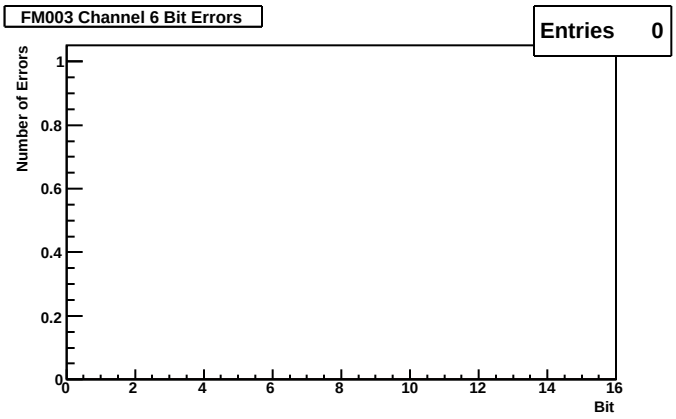

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

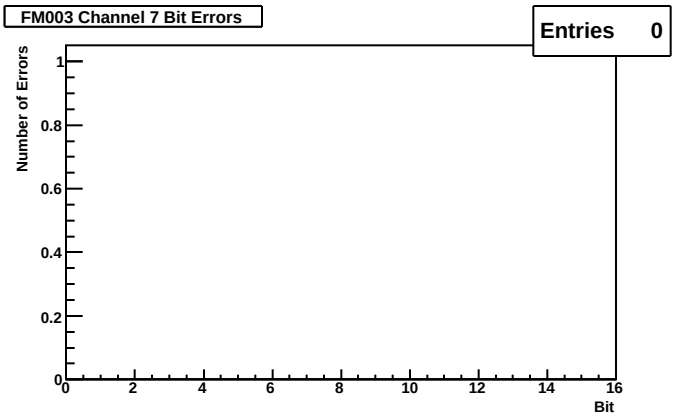

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

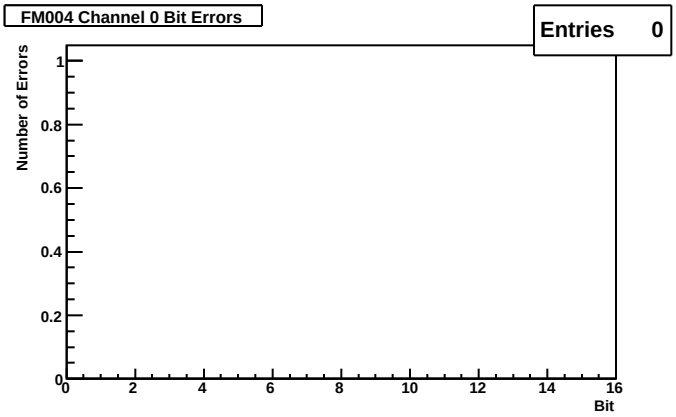

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

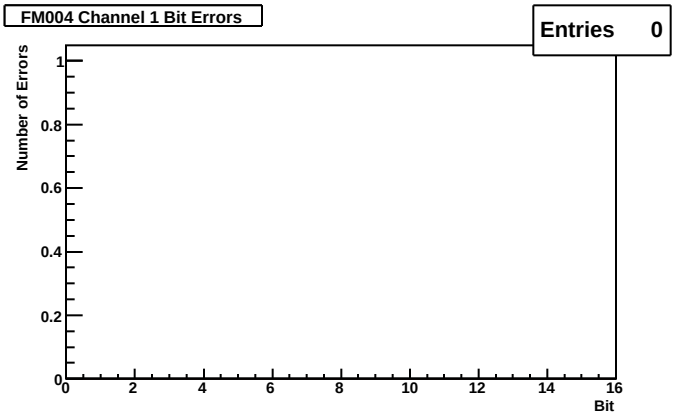

FM003 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

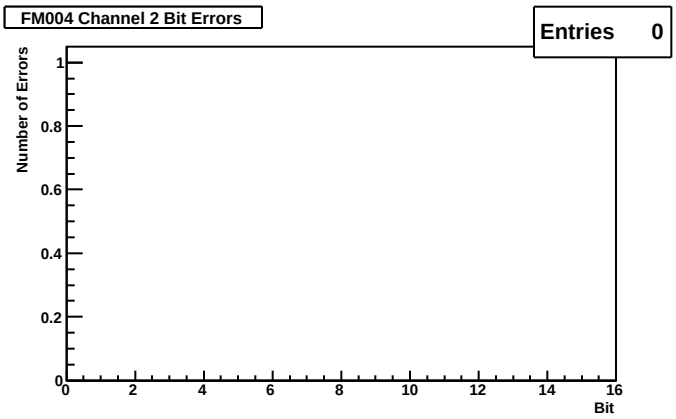

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

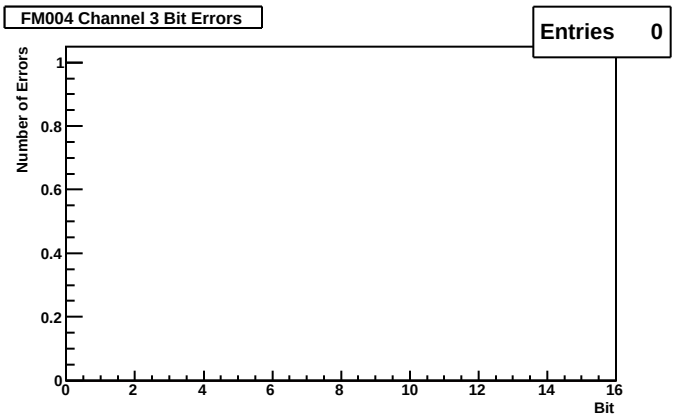

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

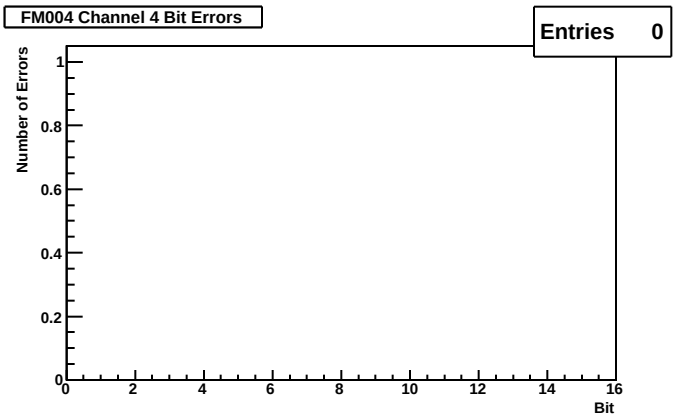

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

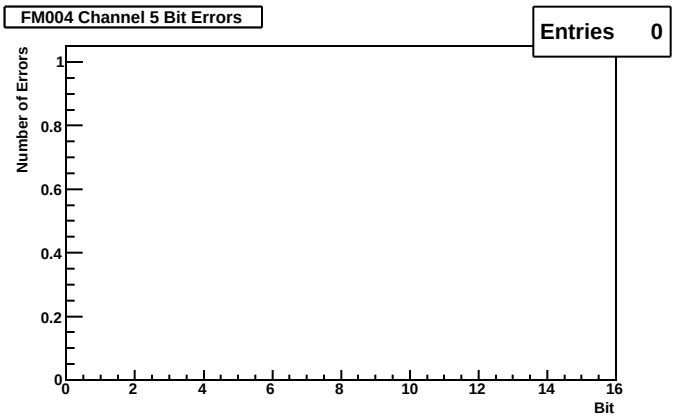

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

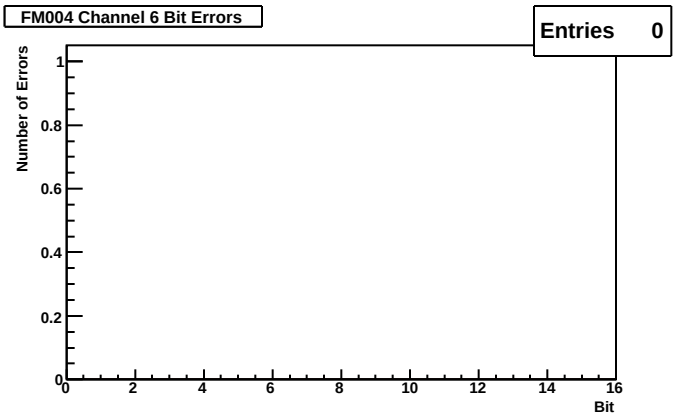

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

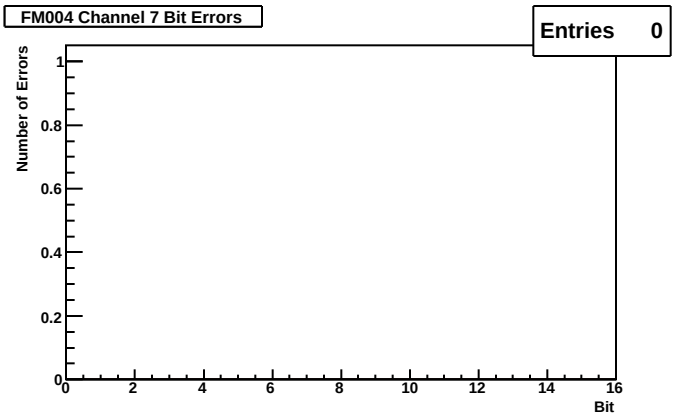

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

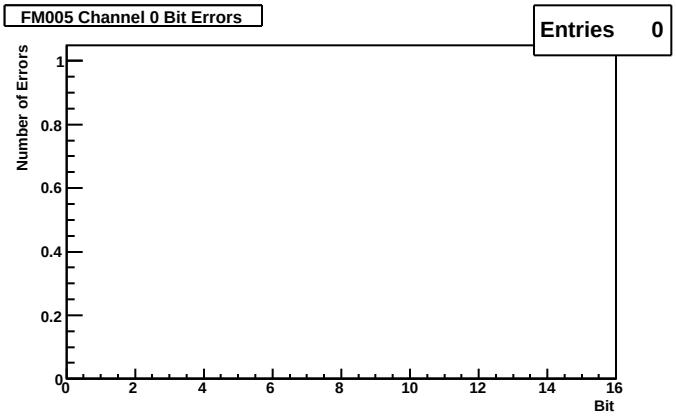

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

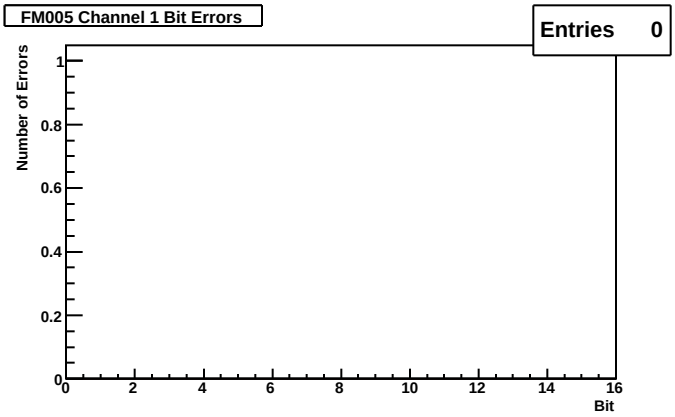

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

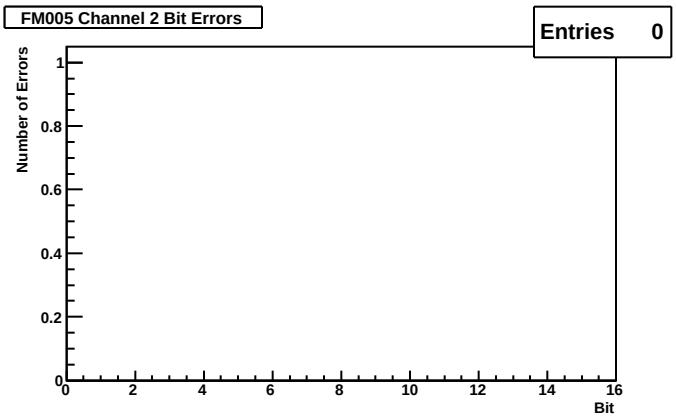

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

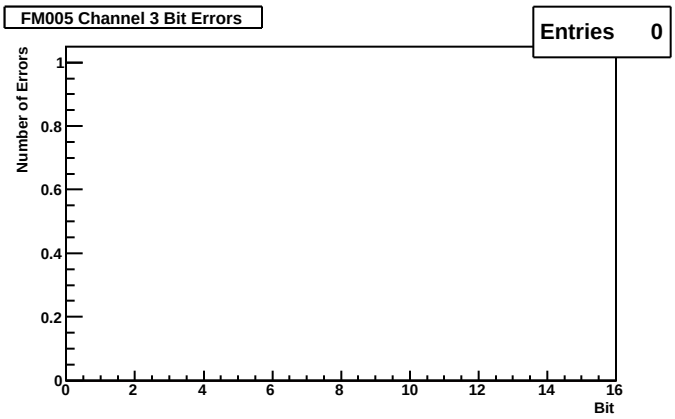

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

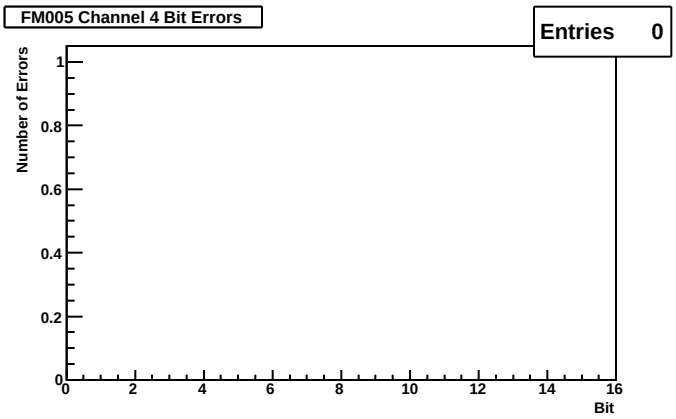

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

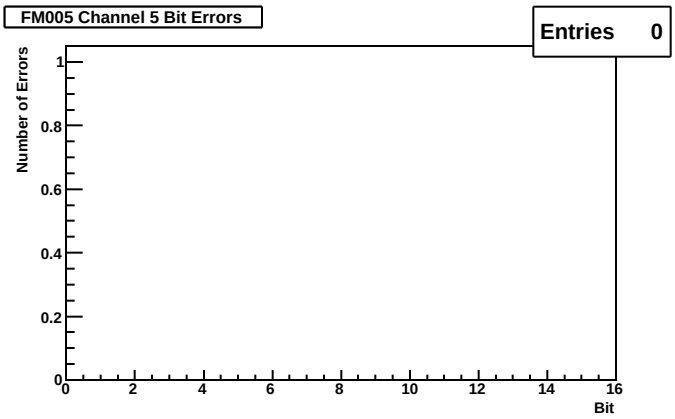

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

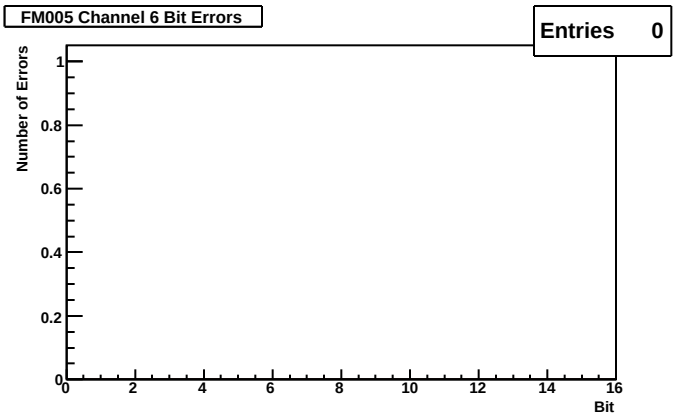

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

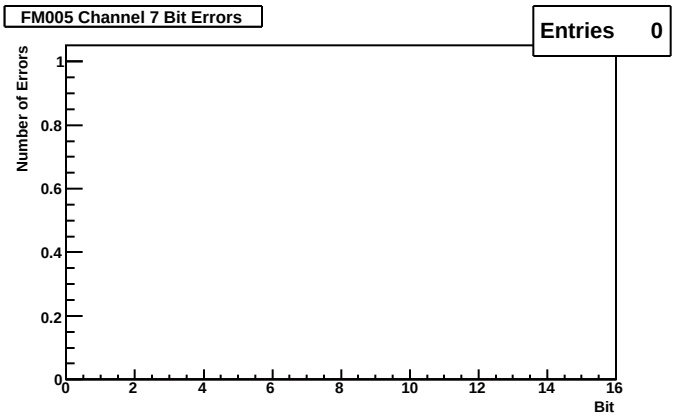

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

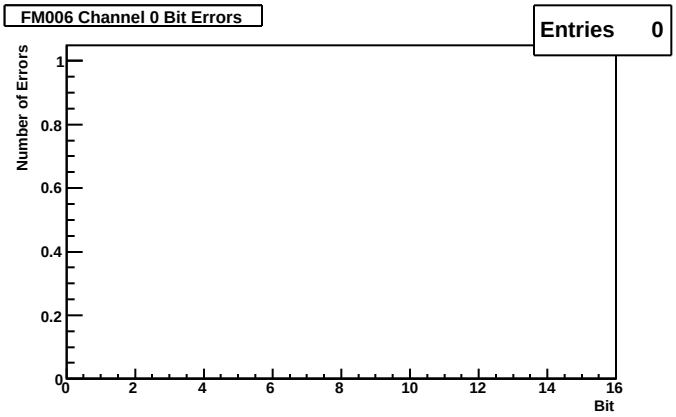

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

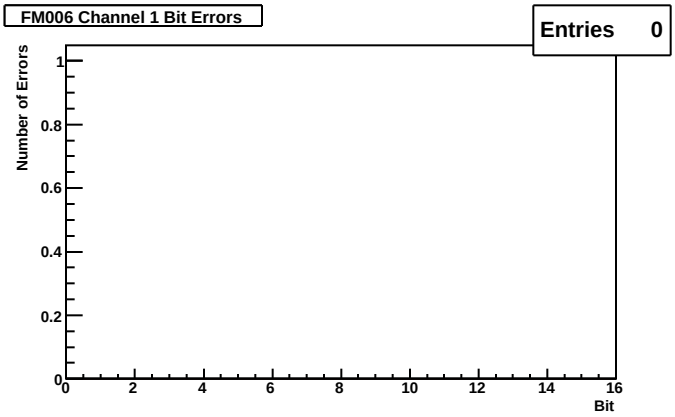

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

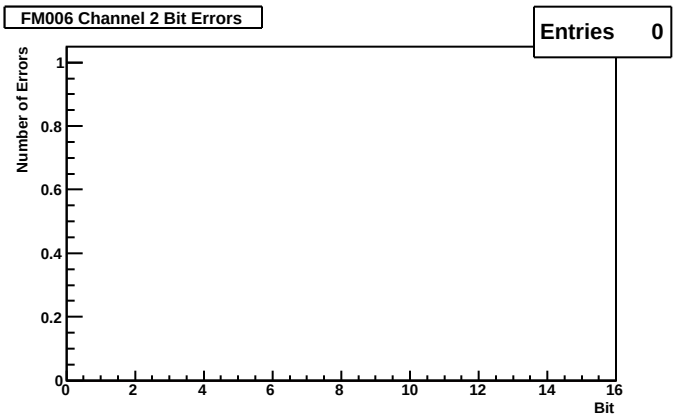

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

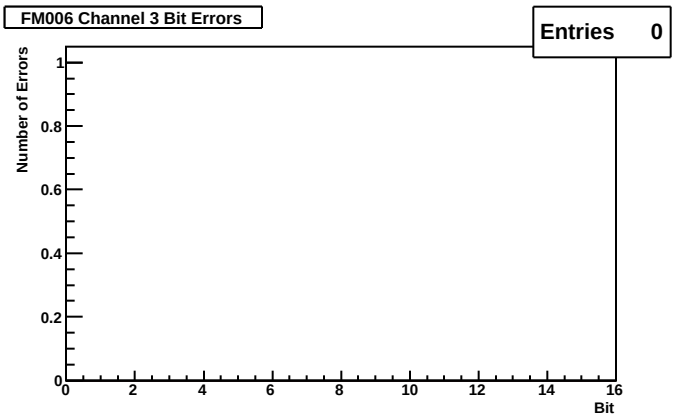

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

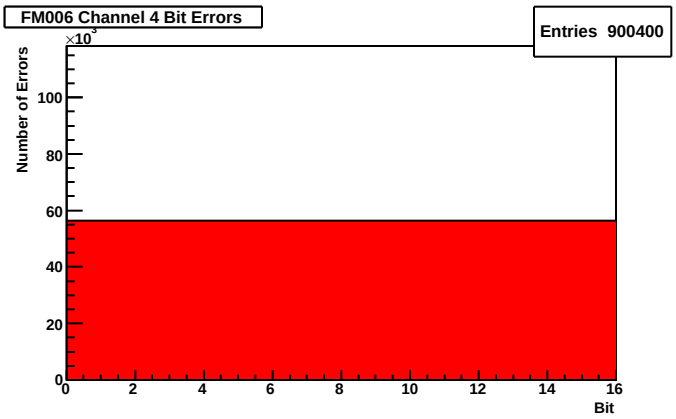

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

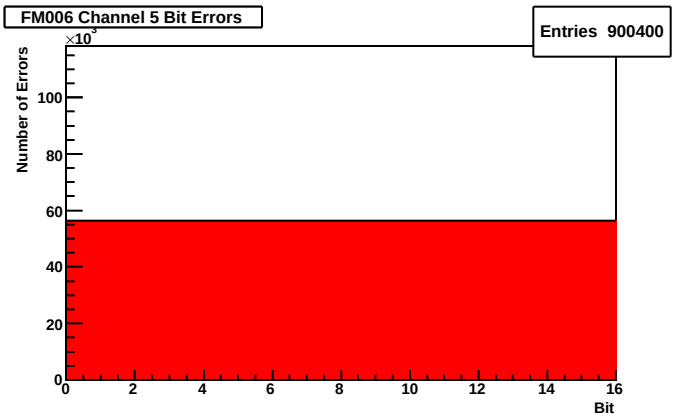

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

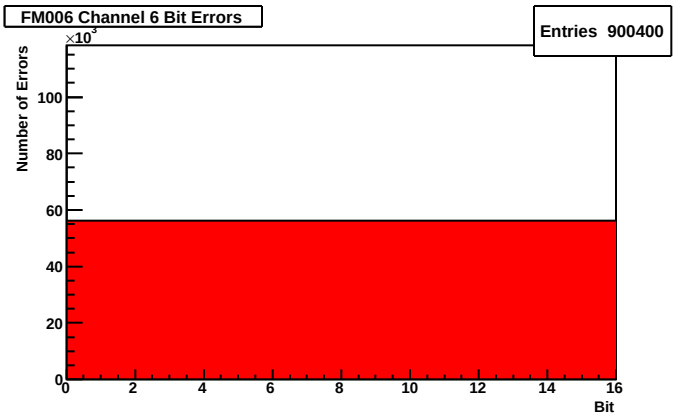

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

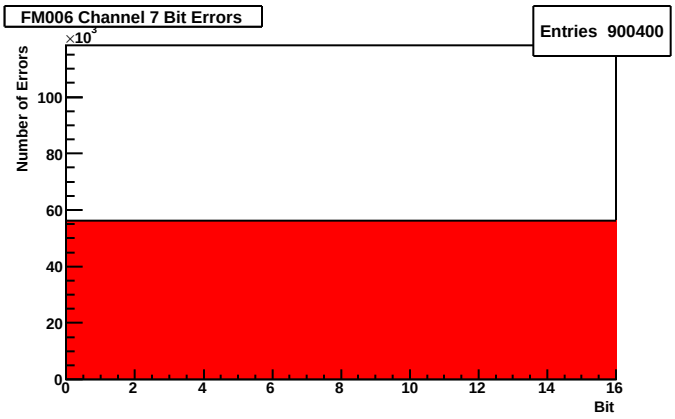

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

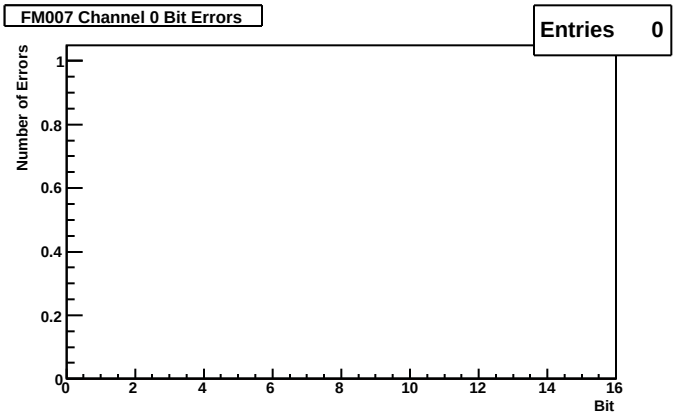

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

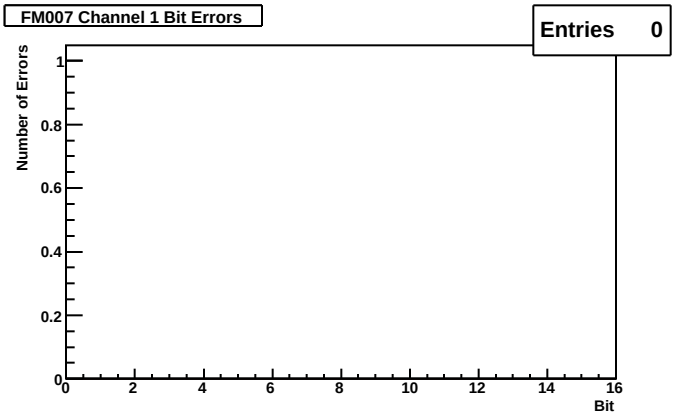

FM006 Channel 4 Bit Errors
Entries 900400
Number of Errors
Bit

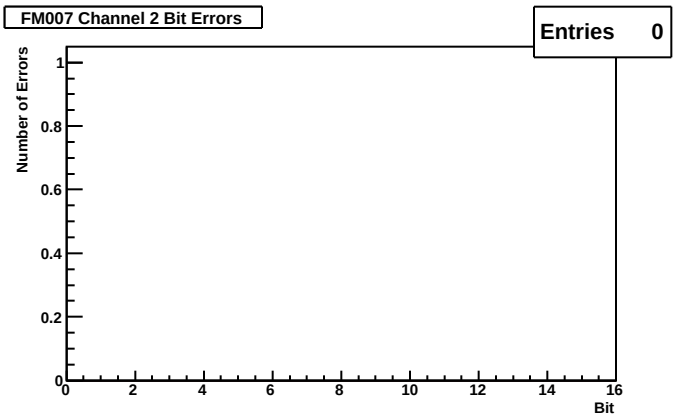

FM006 Channel 5 Bit Errors
Entries 900400
Number of Errors
Bit

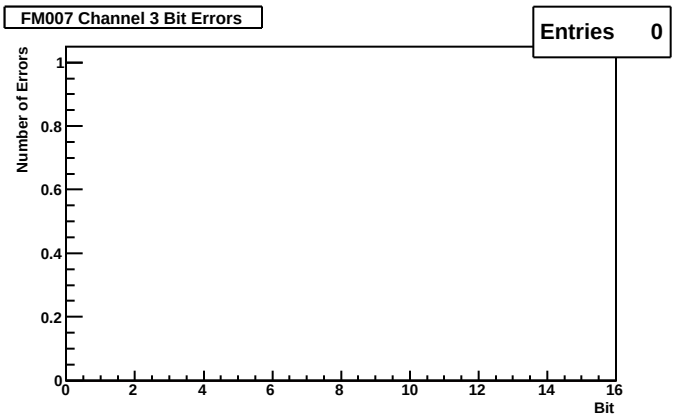

FM006 Channel 6 Bit Errors
Entries 900400
Number of Errors
Bit

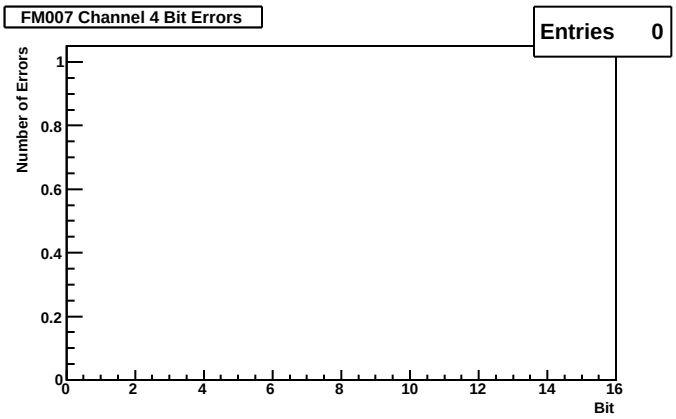

FM006 Channel 7 Bit Errors
Entries 900400
Number of Errors
Bit

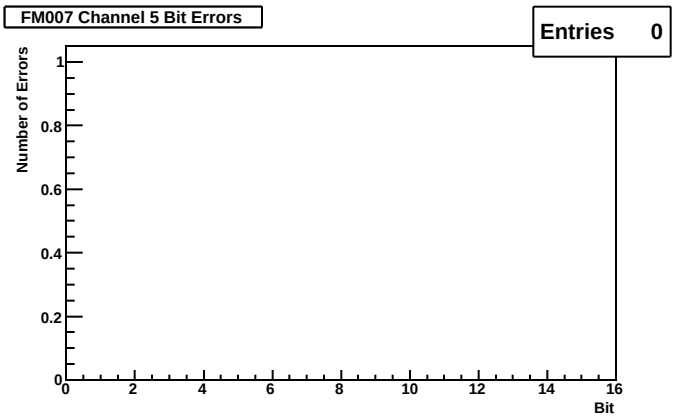

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

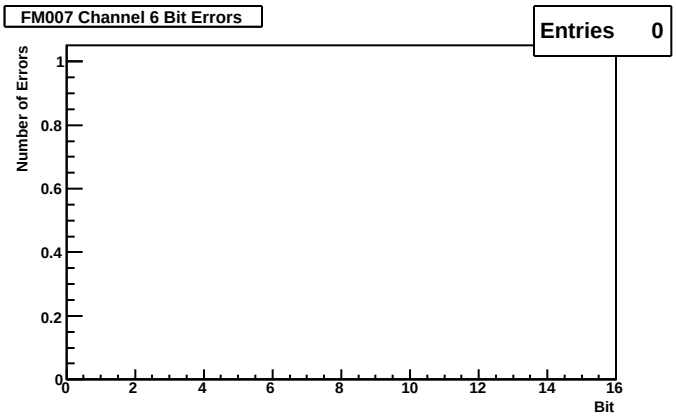

FM007 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

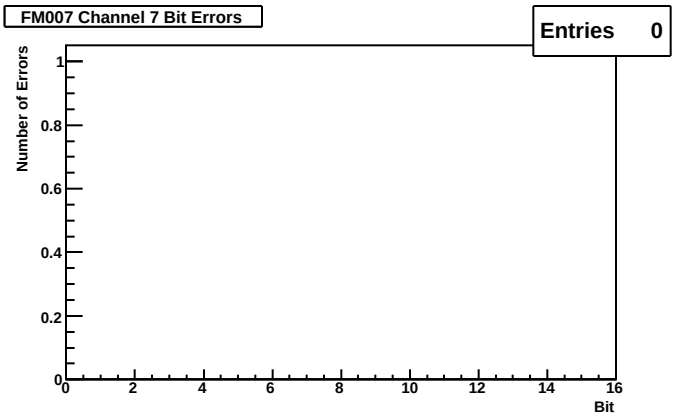

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

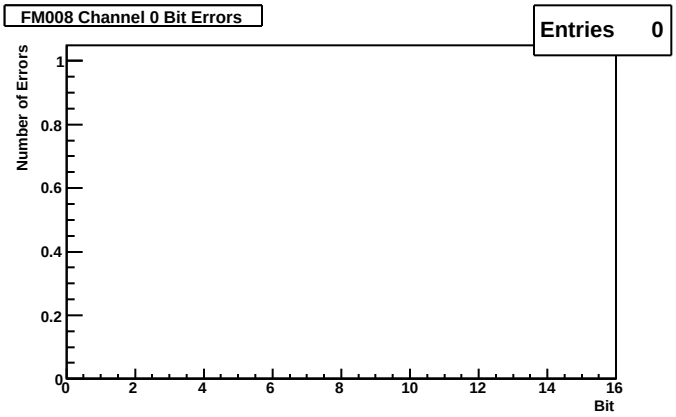

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

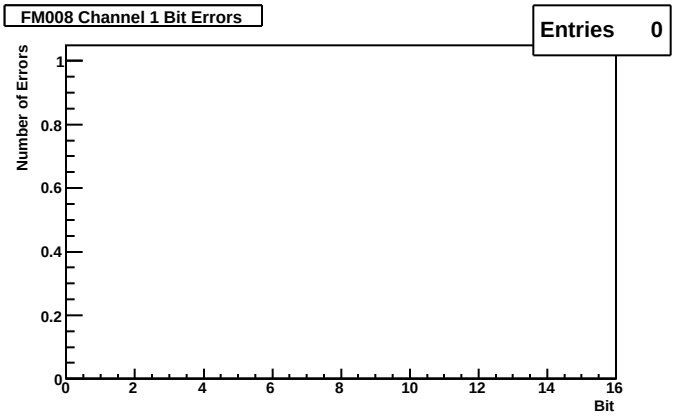

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

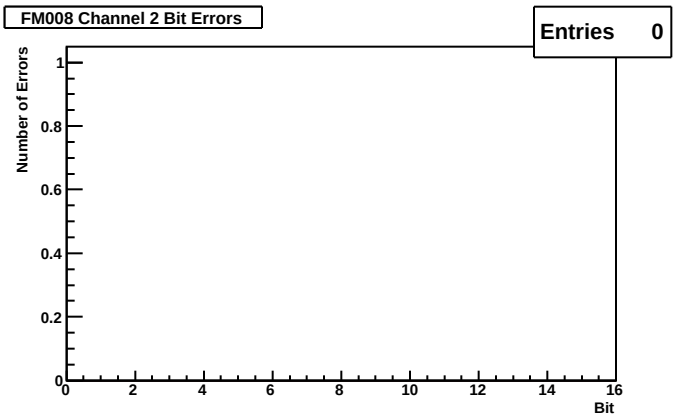

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

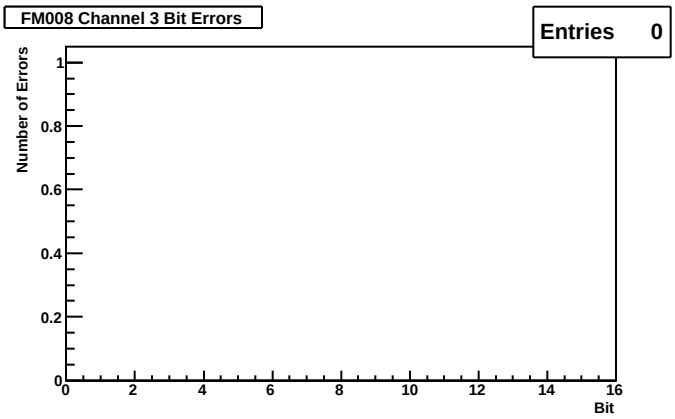

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

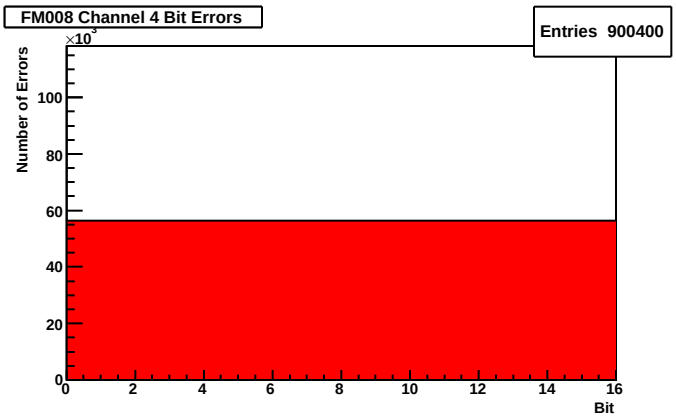

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

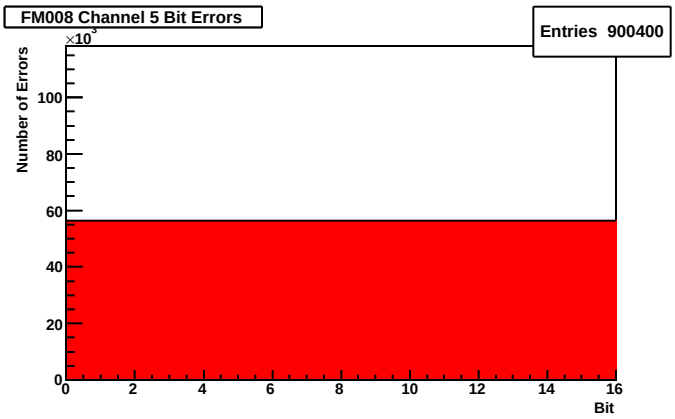

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

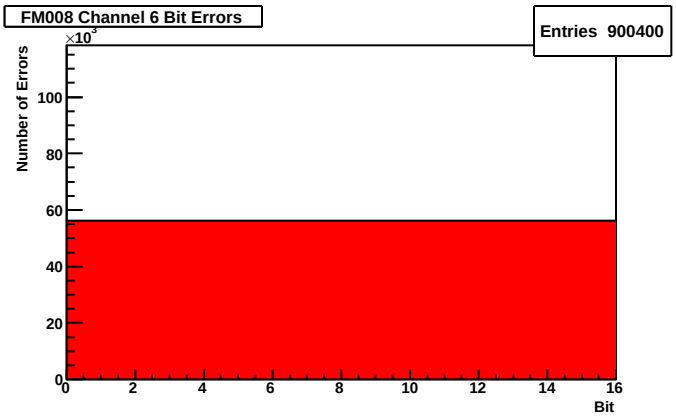

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

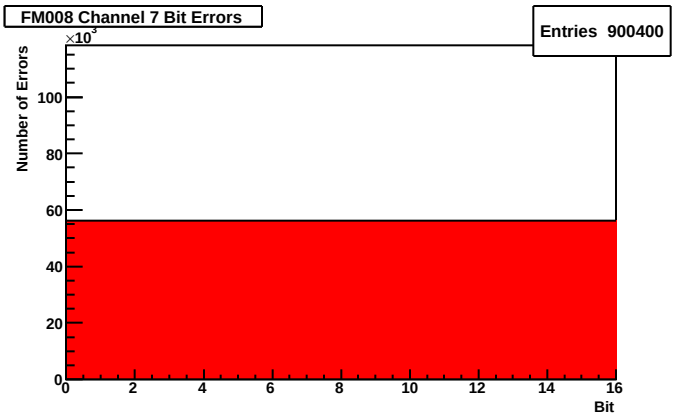

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

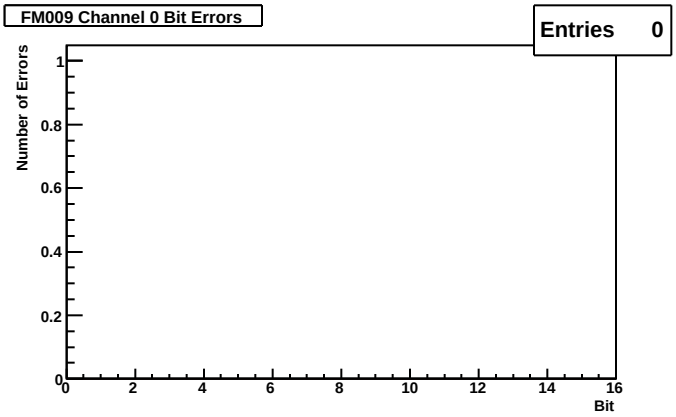

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

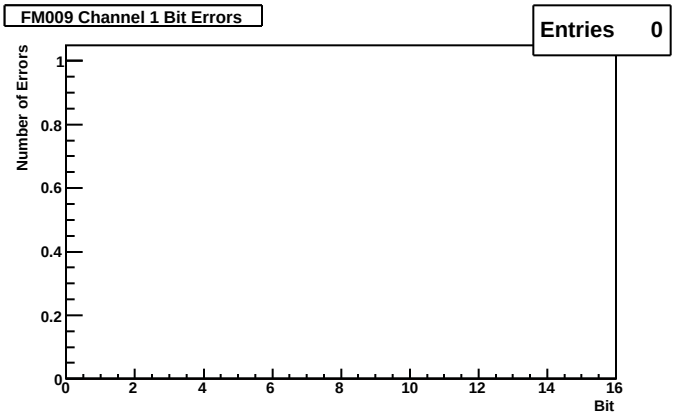

FM008 Channel 4 Bit Errors
Entries 900400
Number of Errors
Bit

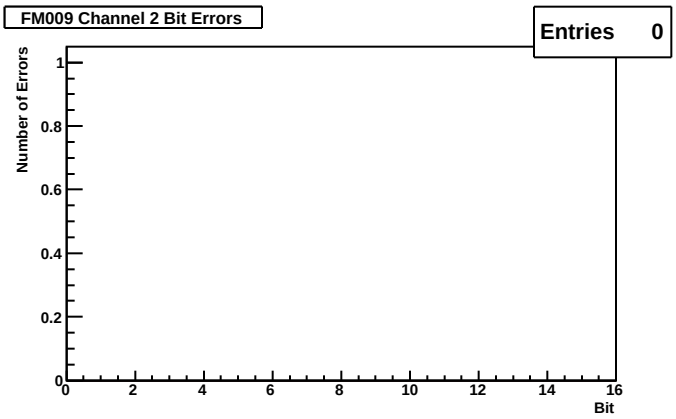

FM008 Channel 5 Bit Errors
Entries 900400
Number of Errors
Bit

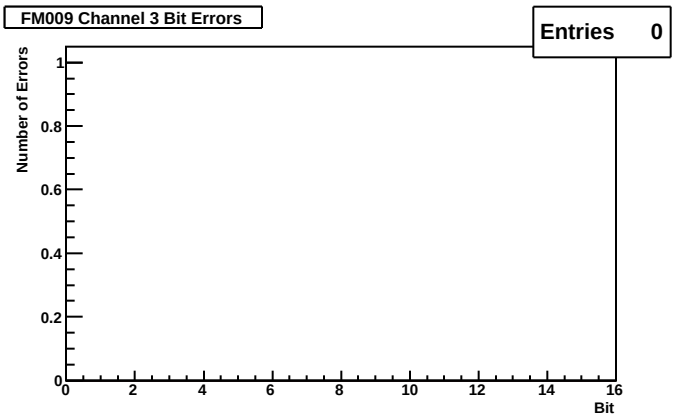

FM008 Channel 6 Bit Errors
Entries 900400
Number of Errors
Bit

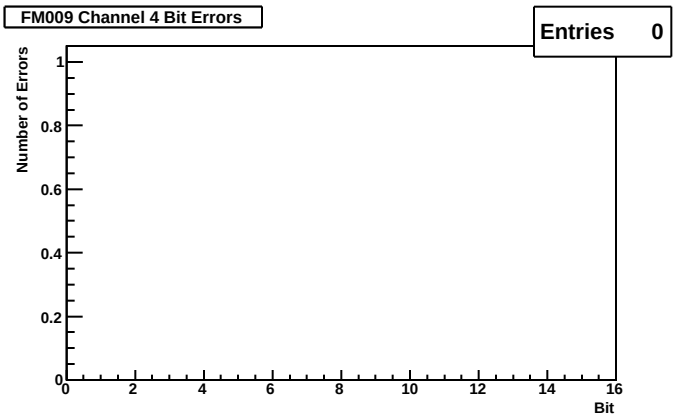

FM008 Channel 7 Bit Errors
Entries 900400
Number of Errors
Bit

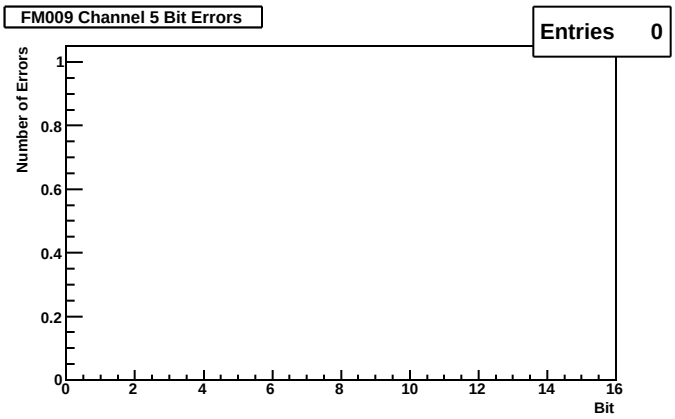

FM009 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

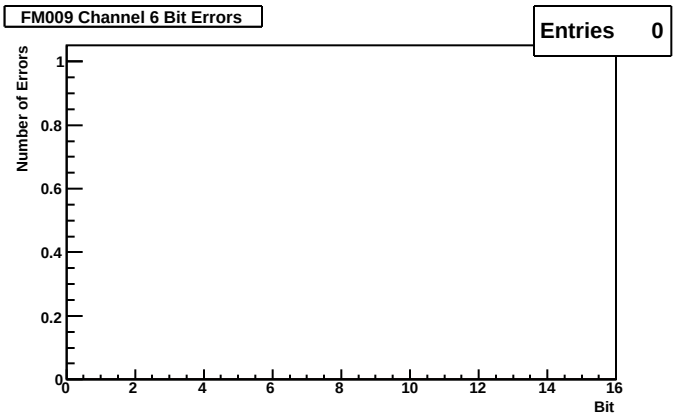

FM009 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

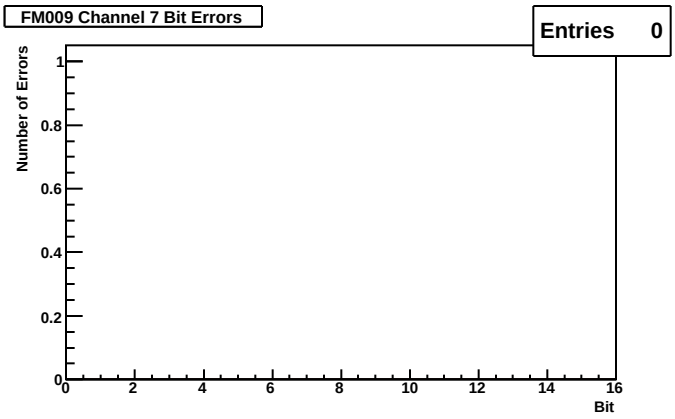

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

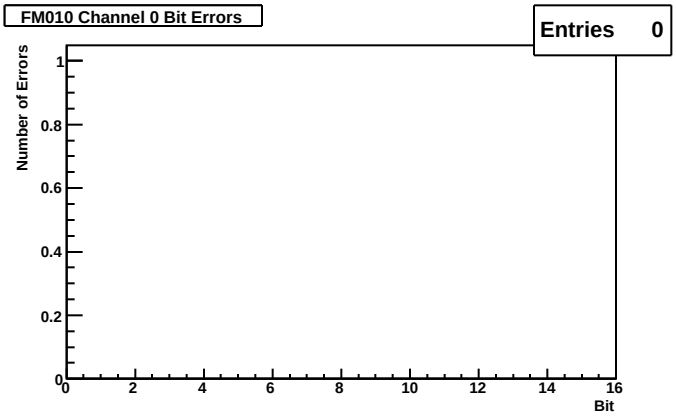

FM009 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

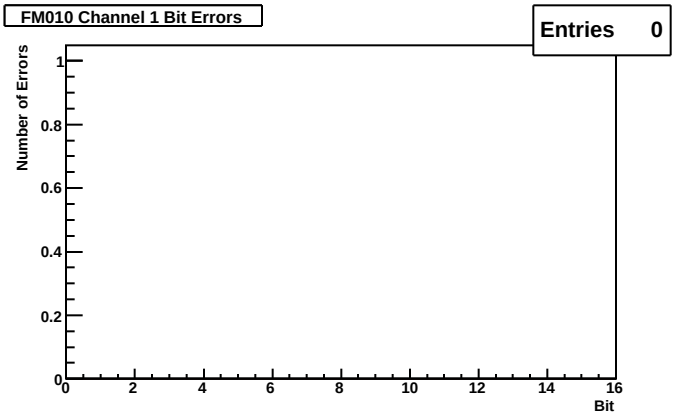

FM009 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

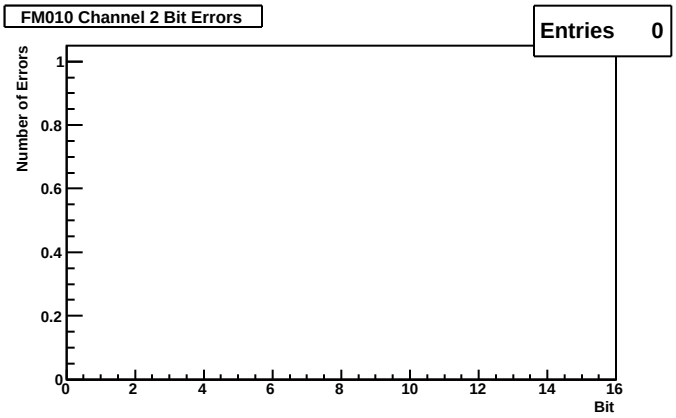

FM009 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

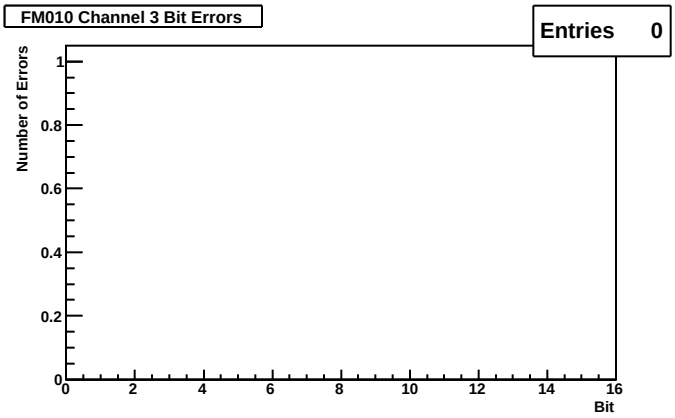

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

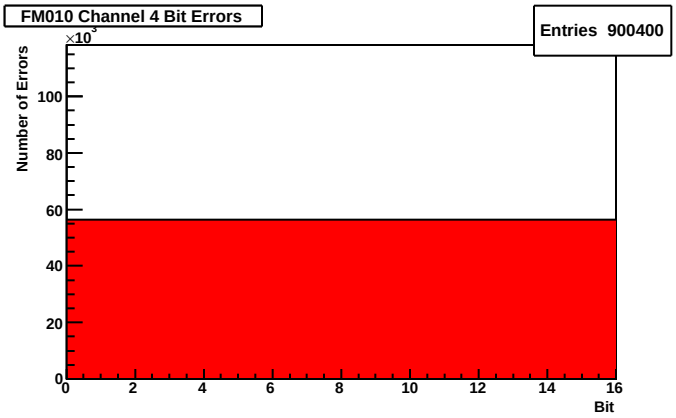

FM009 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

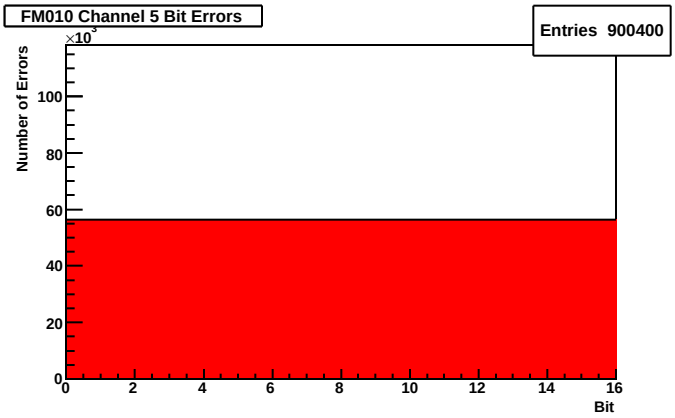

FM010 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

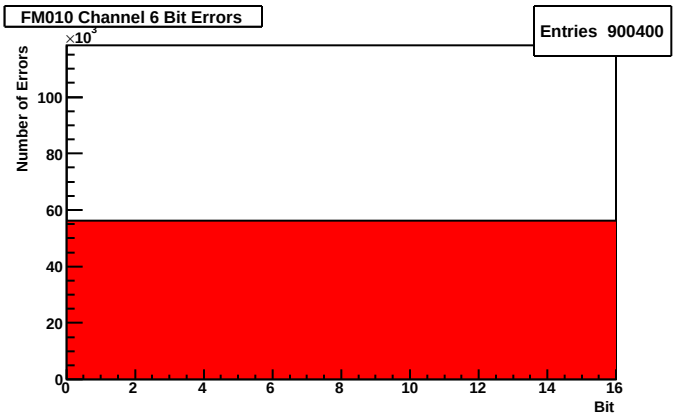

FM010 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

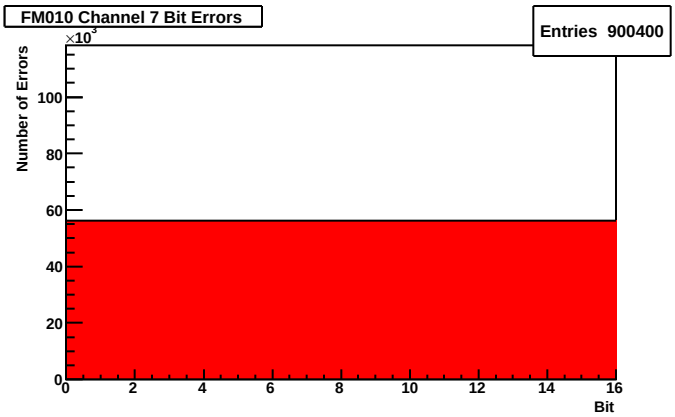

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

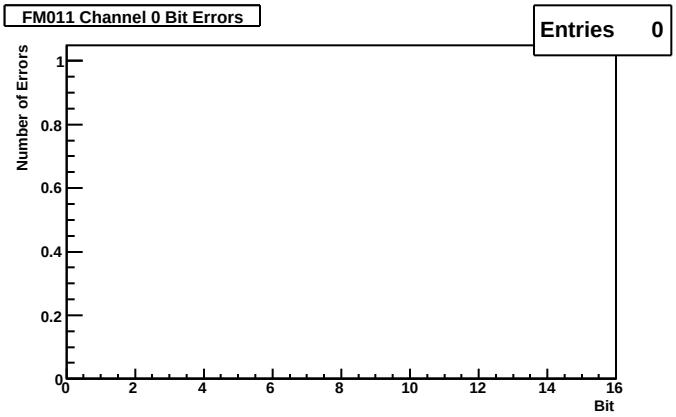

FM010 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

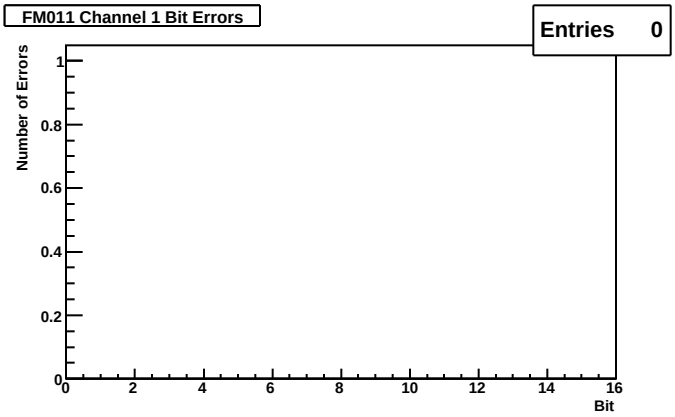

FM010 Channel 4 Bit Errors
Entries 900400
Number of Errors
Bit

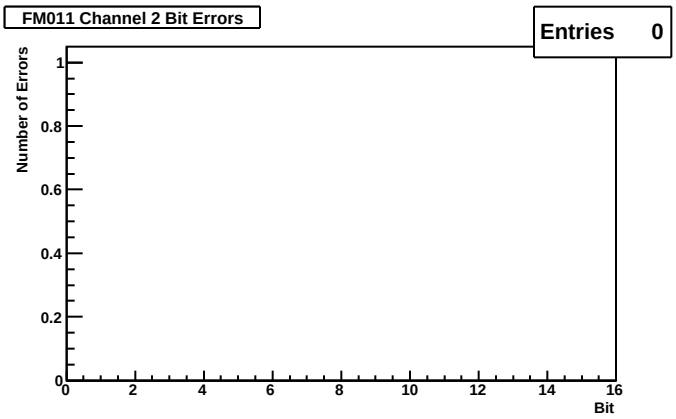

FM010 Channel 5 Bit Errors
Entries 900400
Number of Errors
Bit

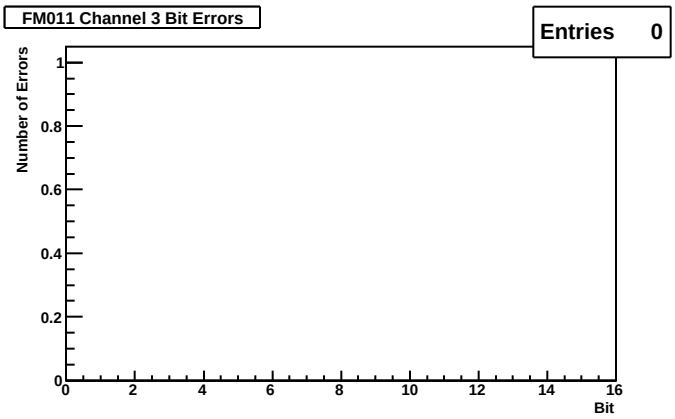

FM010 Channel 6 Bit Errors
Entries 900400
Number of Errors
Bit

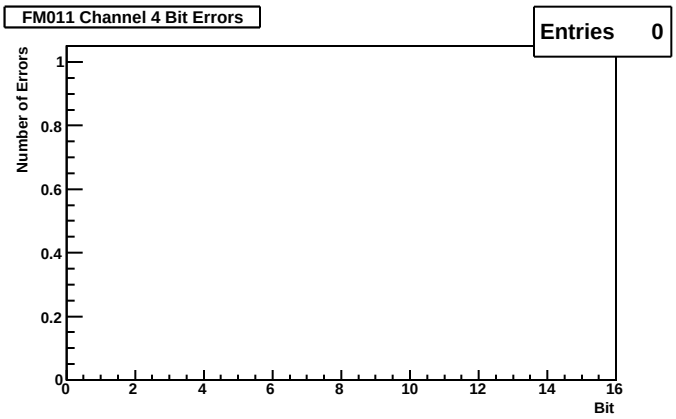

FM010 Channel 7 Bit Errors
Entries 900400
Number of Errors
Bit

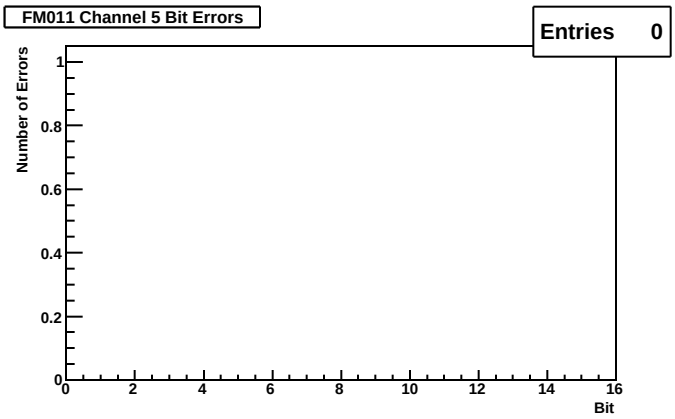

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

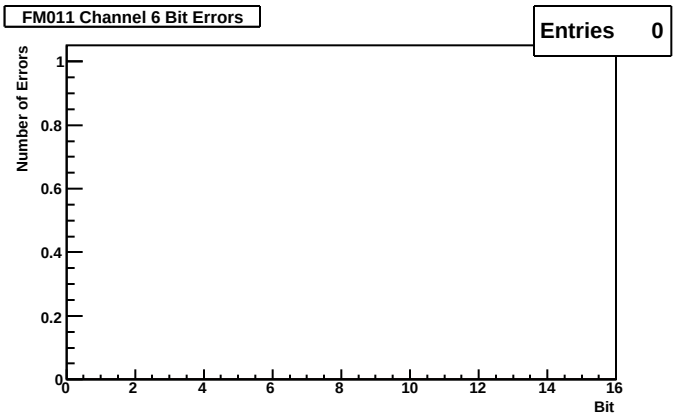

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

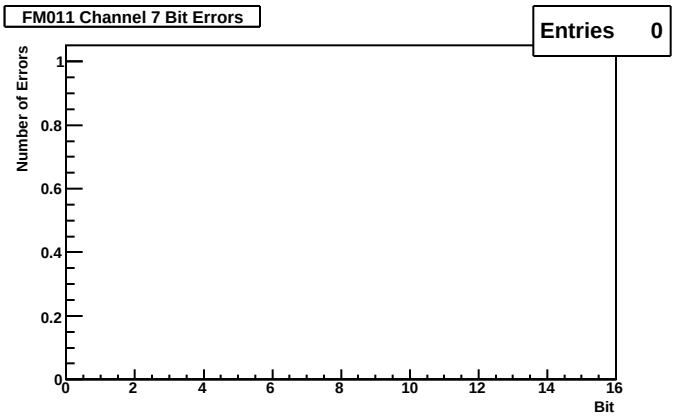

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

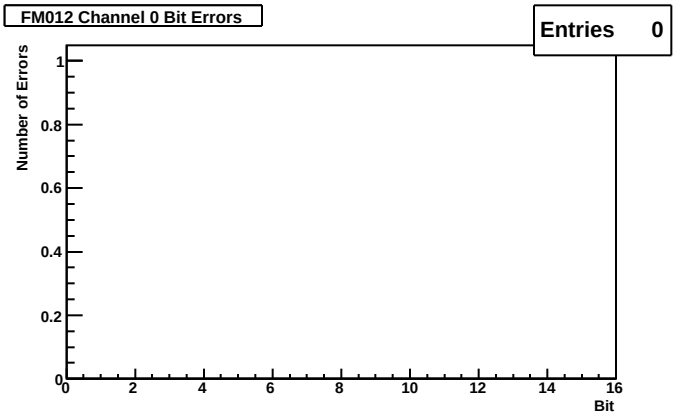

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

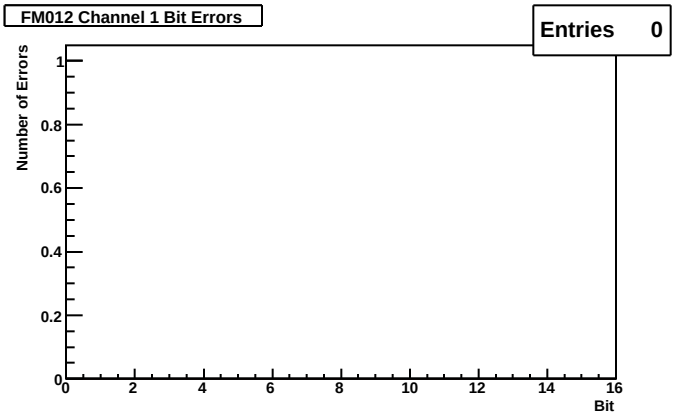

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

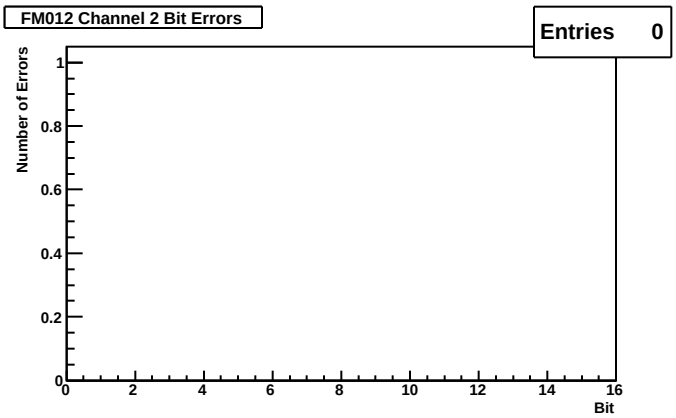

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

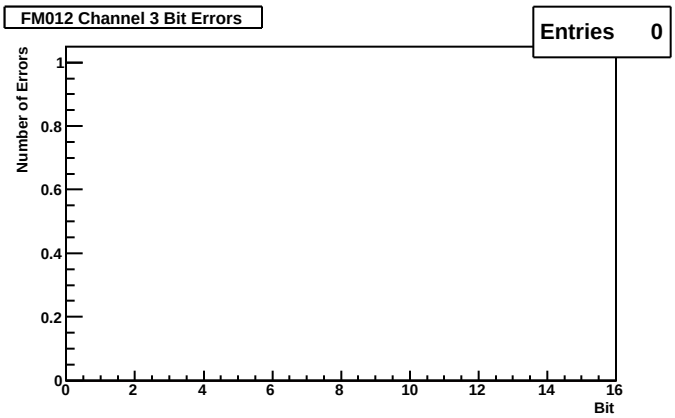

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

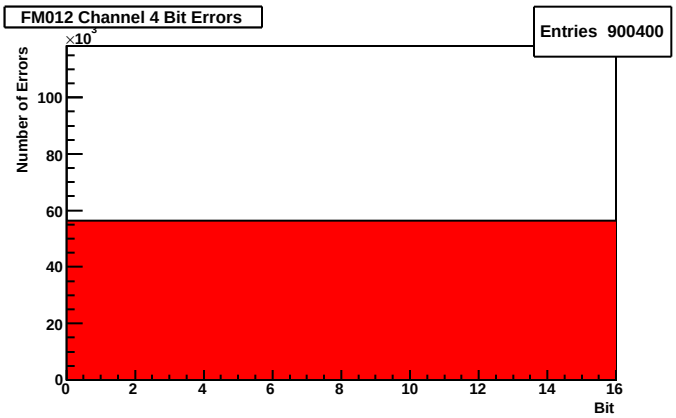

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

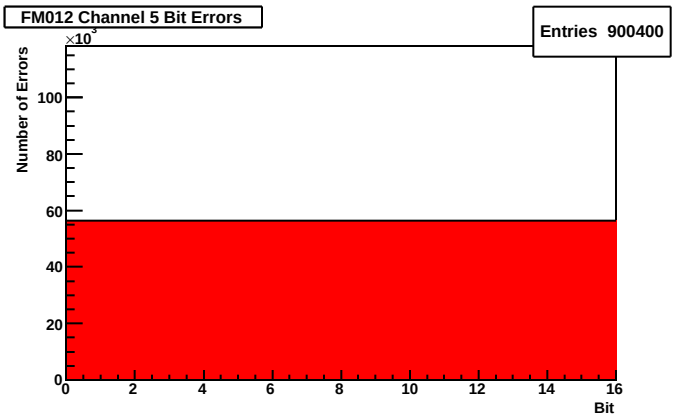

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

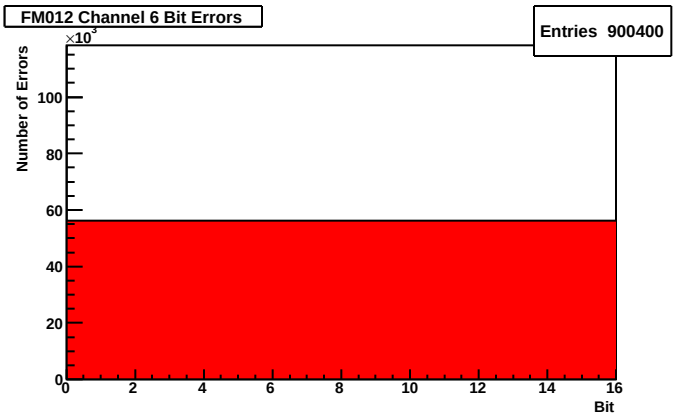

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

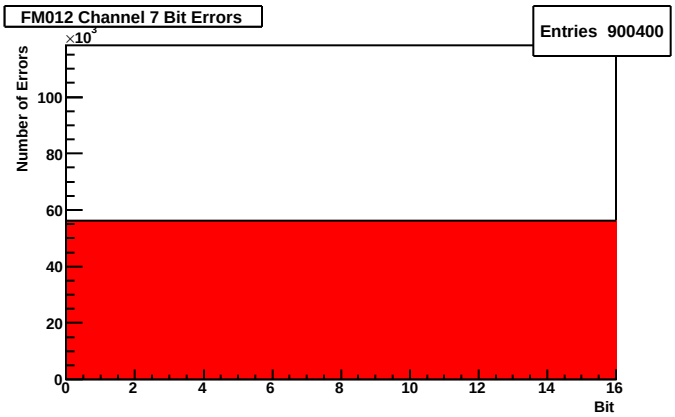

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 900400
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 900400
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 900400
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 900400
Number of Errors
Bit