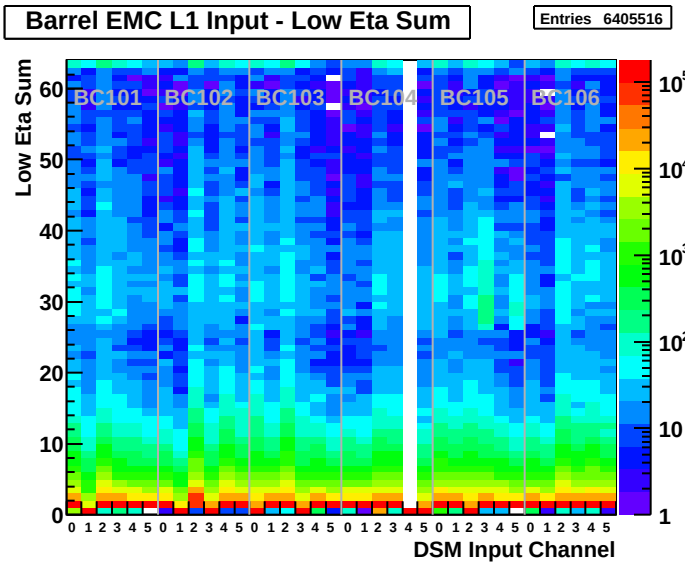
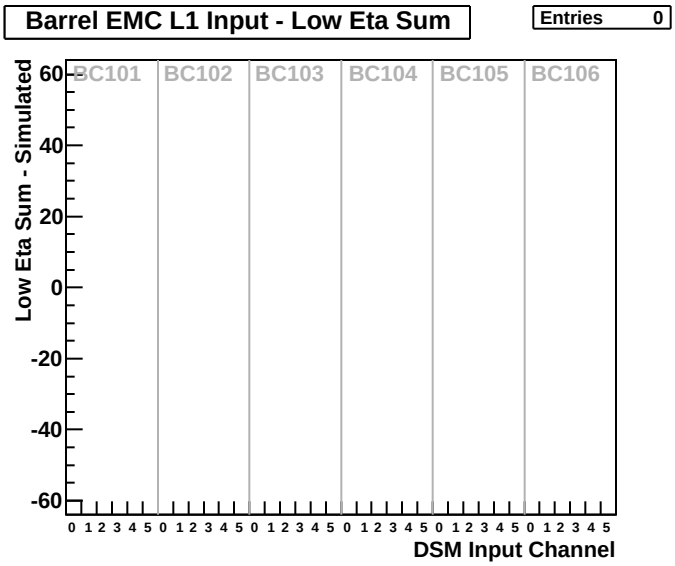
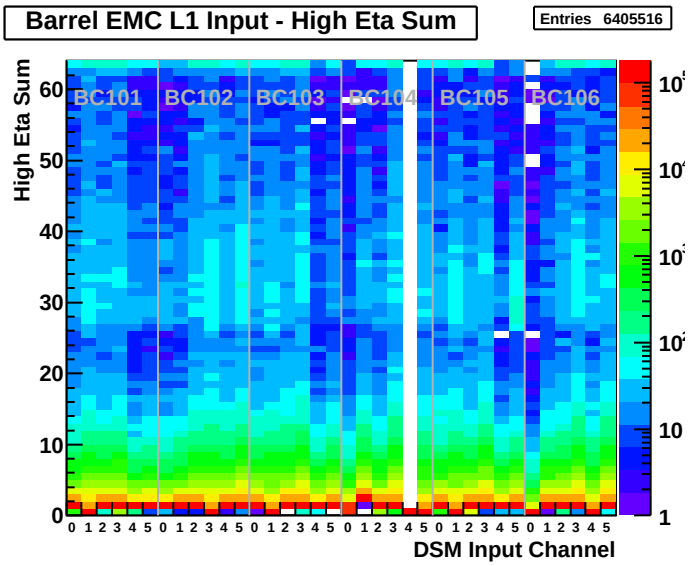
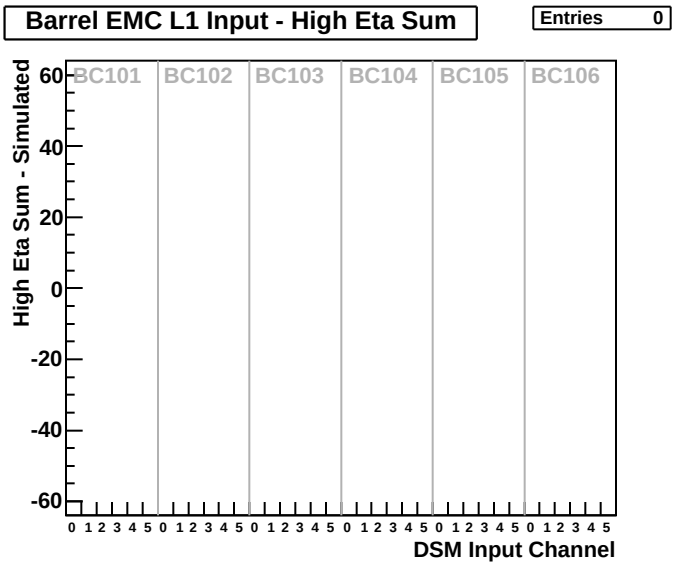
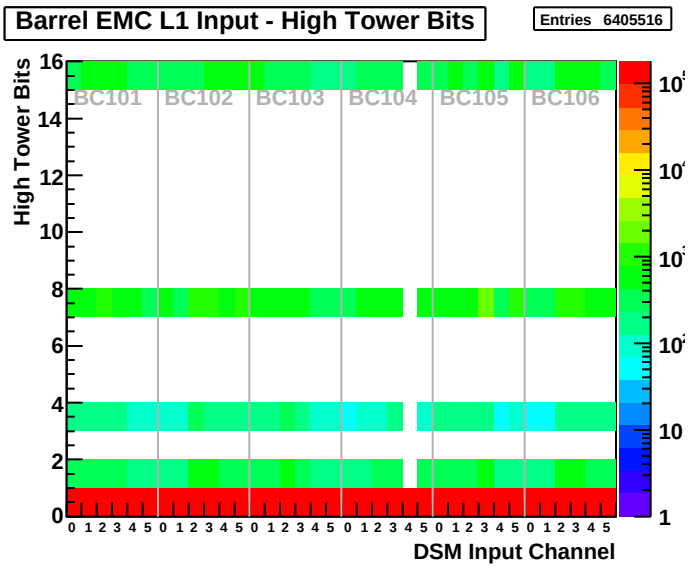
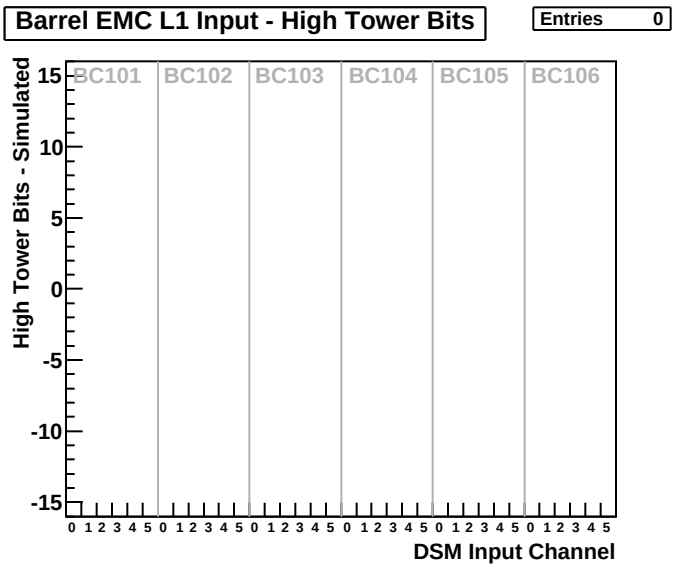

Barrel EMC L1 Input - Low Eta Sum
Entries 6405516
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

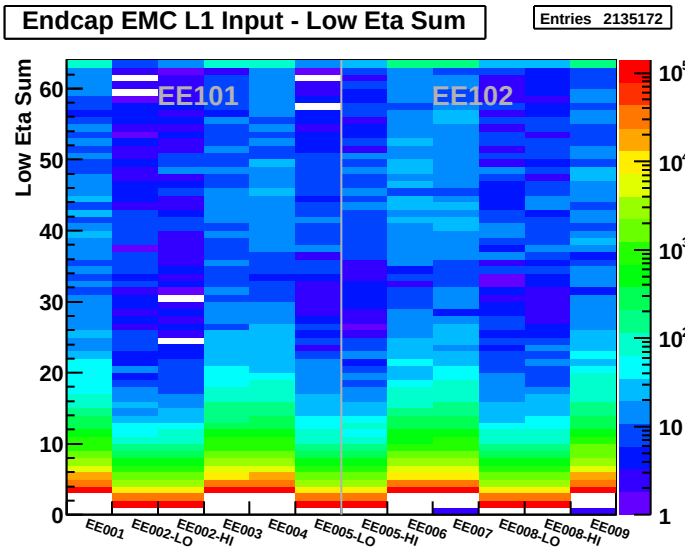

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

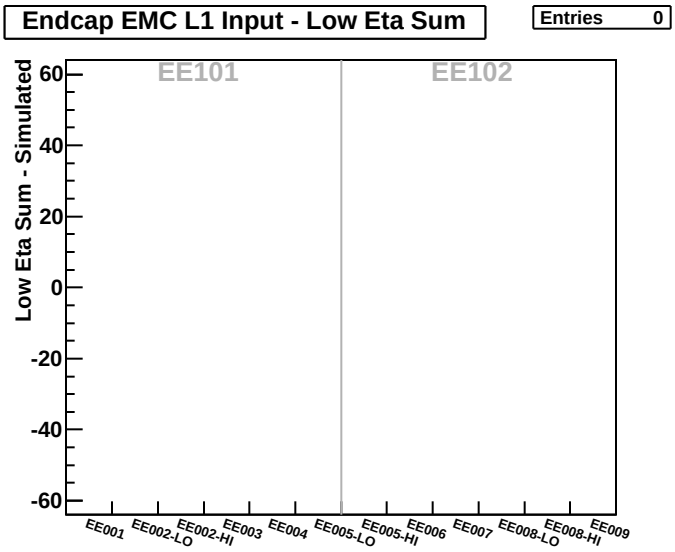

Barrel EMC L1 Input - High Eta Sum
Entries 6405516
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

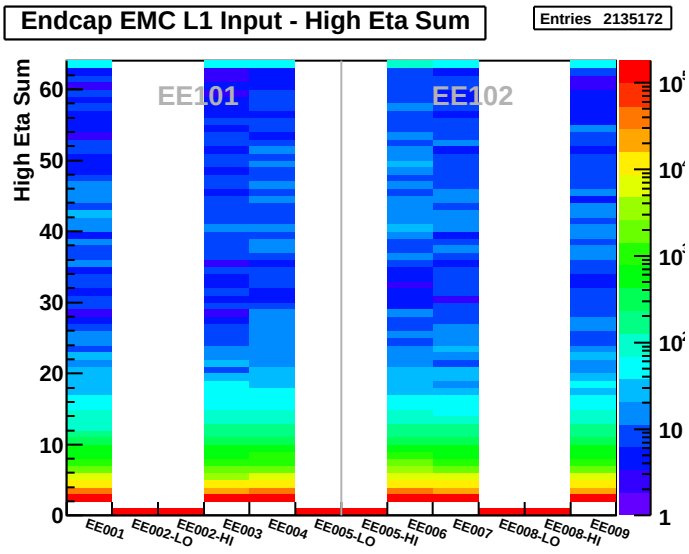

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

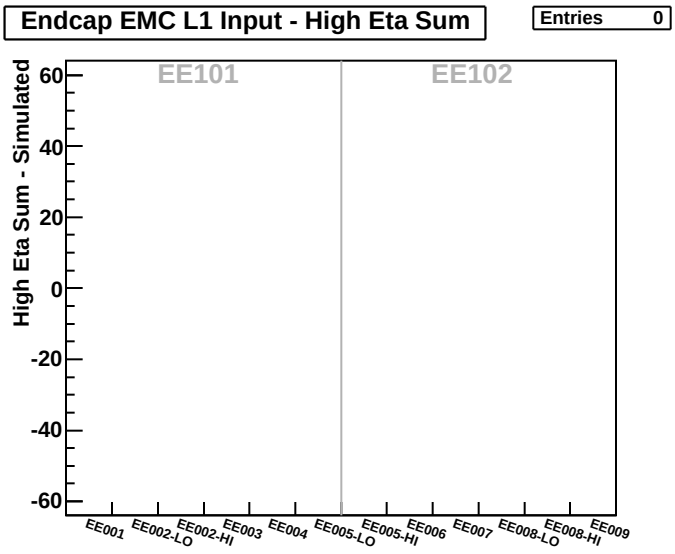

Barrel EMC L1 Input - High Tower Bits
Entries 6405516
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

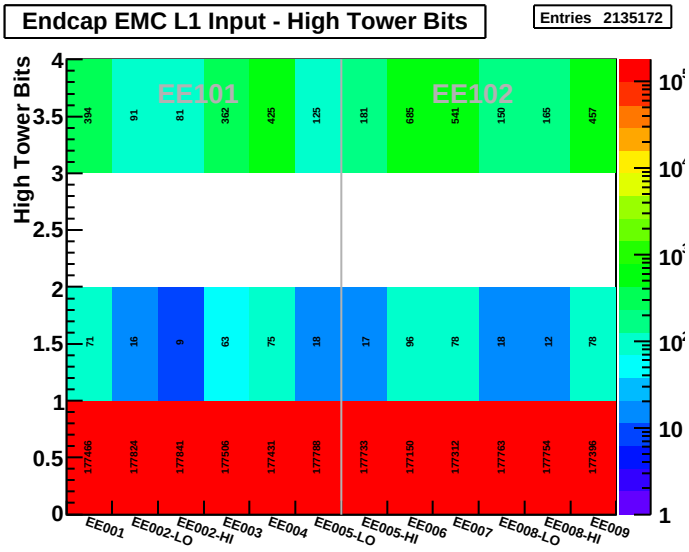

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

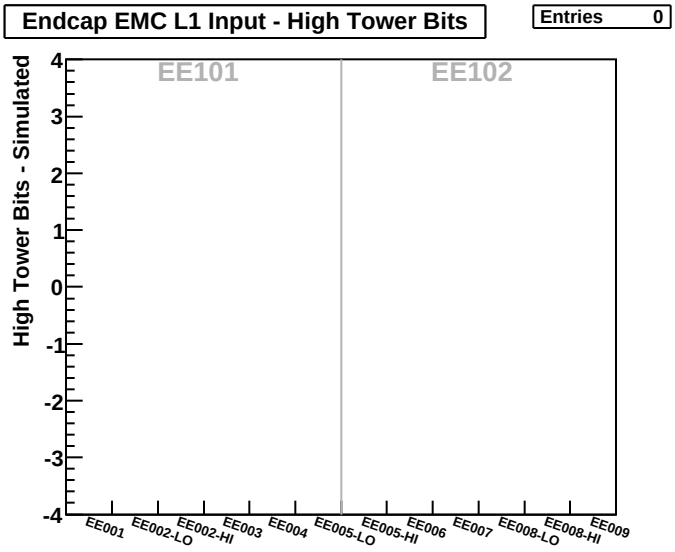

Endcap EMC L1 Input - Low Eta Sum
Entries 2135172
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

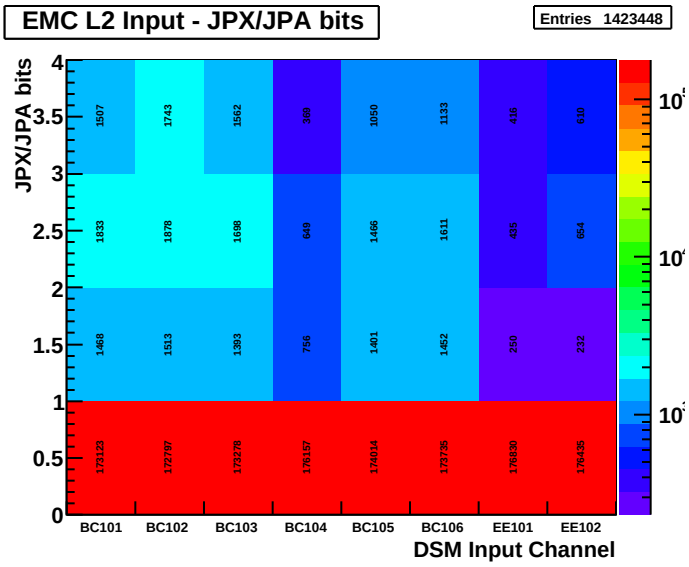

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

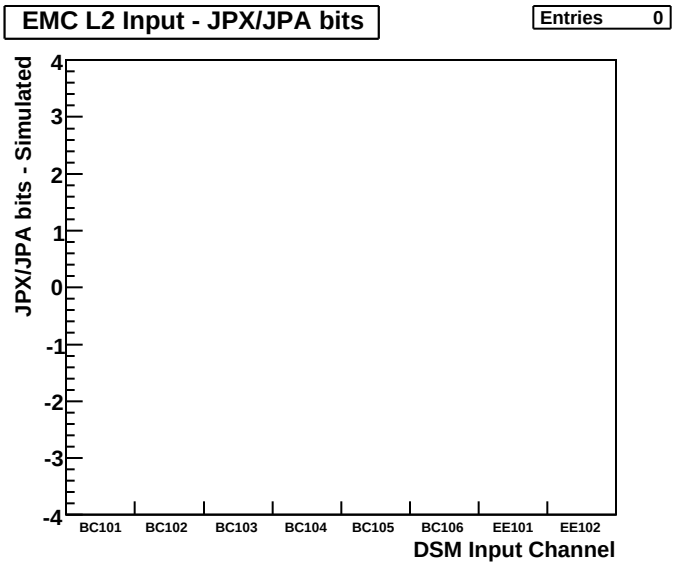

Endcap EMC L1 Input - High Eta Sum
Entries 2135172
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

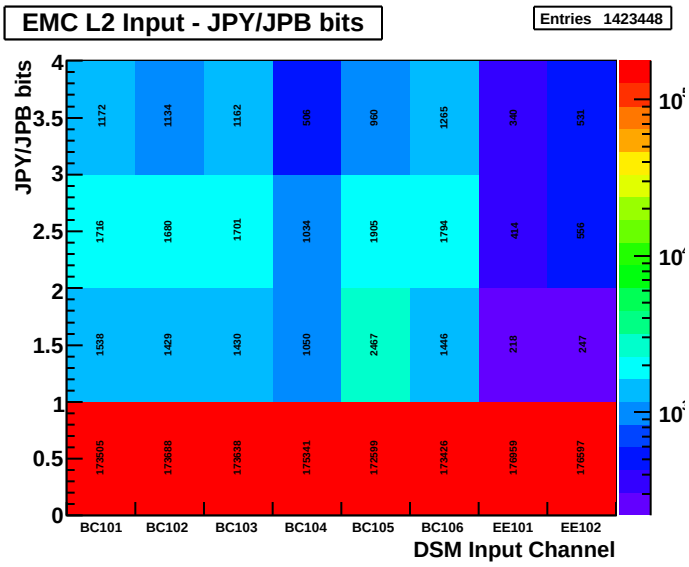

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

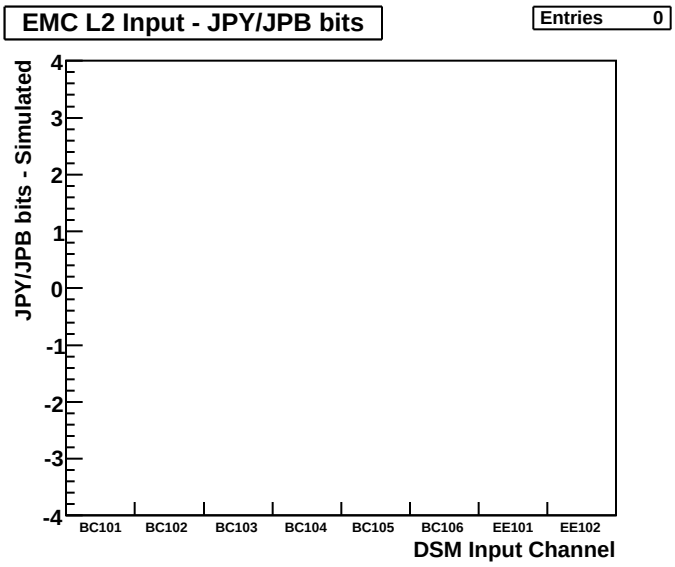

Endcap EMC L1 Input - High Tower Bits
Entries 2135172
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

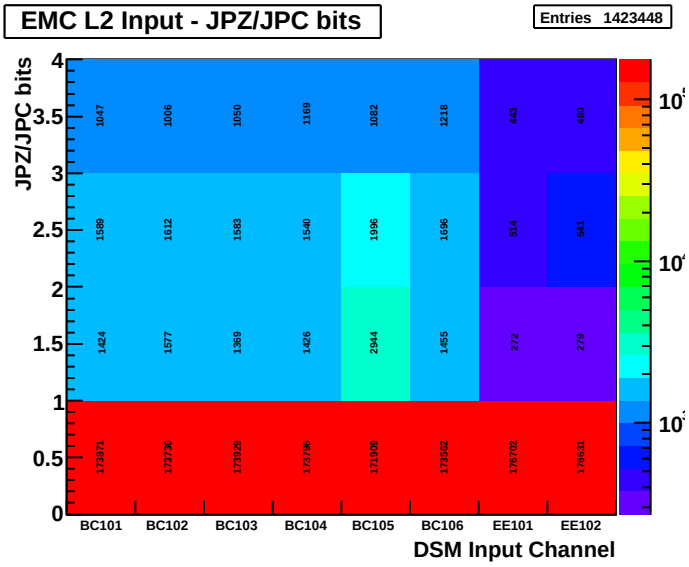

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

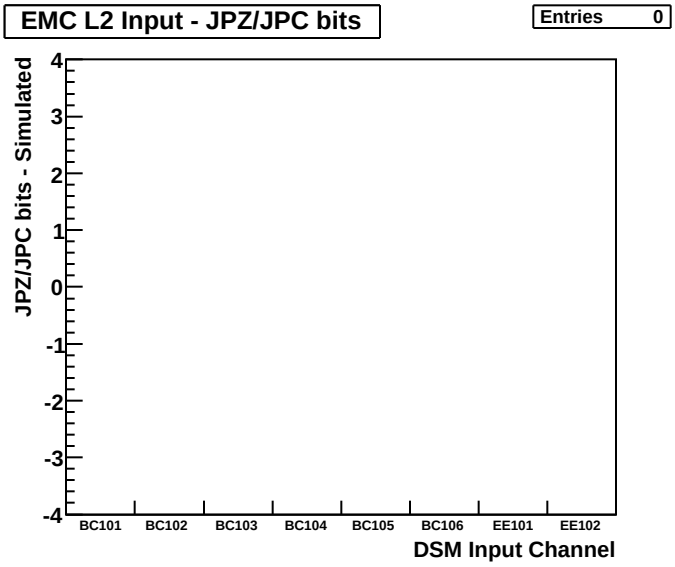

EMC L2 Input - JPX/JPA bits
Entries 1423448
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1507 1743 1562 369 1050 1133 416 610
1833 1878 1698 649 1466 1611 435 654
1468 1513 1393 756 1401 1452 250 232
173123 172797 172278 176157 174014 173735 176830 176435

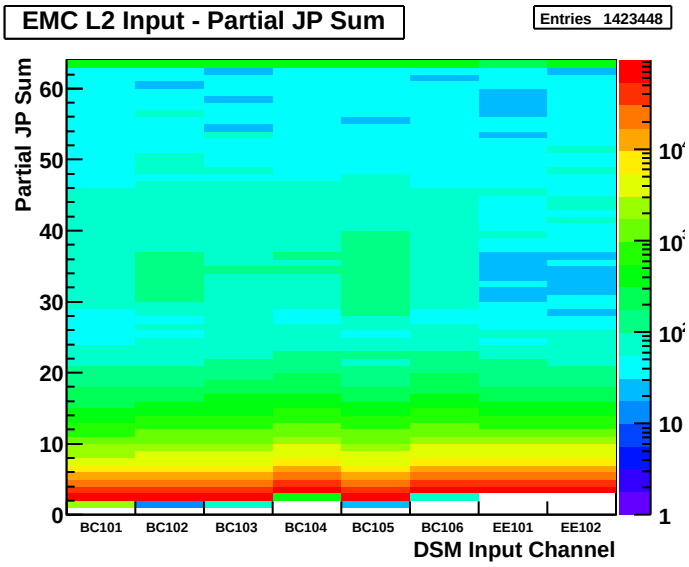

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

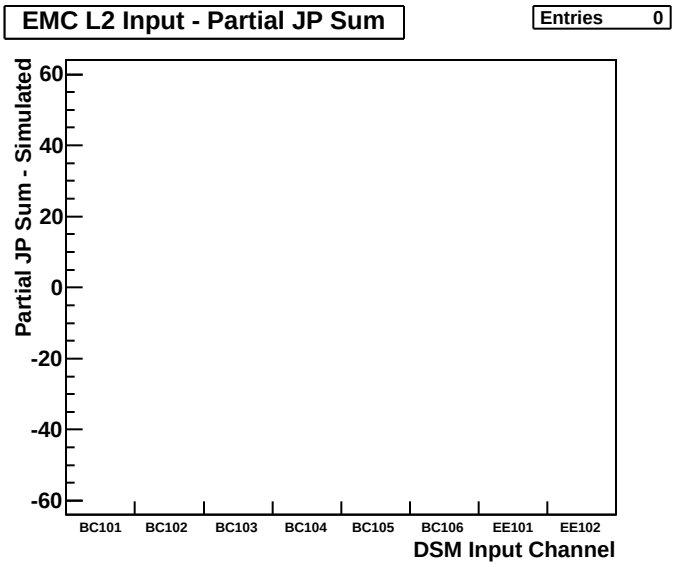

EMC L2 Input - JPY/JPB bits
Entries 1423448
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1172 1134 1162 506 960 1265 340 531
1716 1680 1701 1034 1905 1794 414 556
1538 1429 1430 1050 2467 1446 218 247
173505 173688 173638 175341 172599 173426 176959 176597

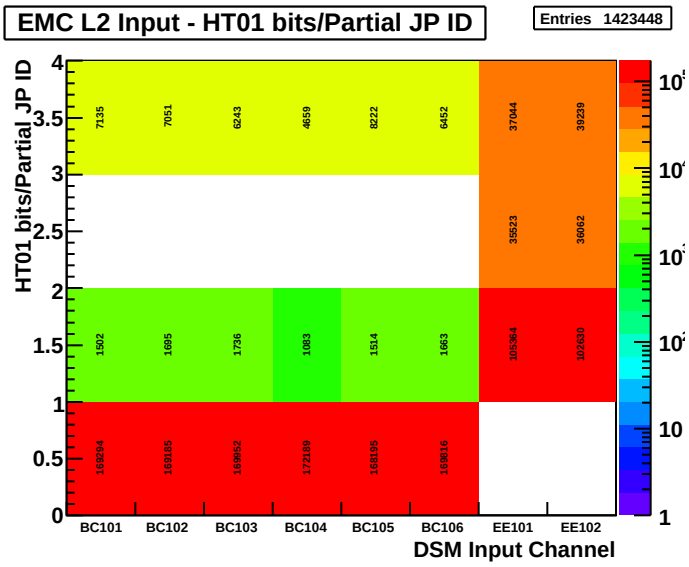

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

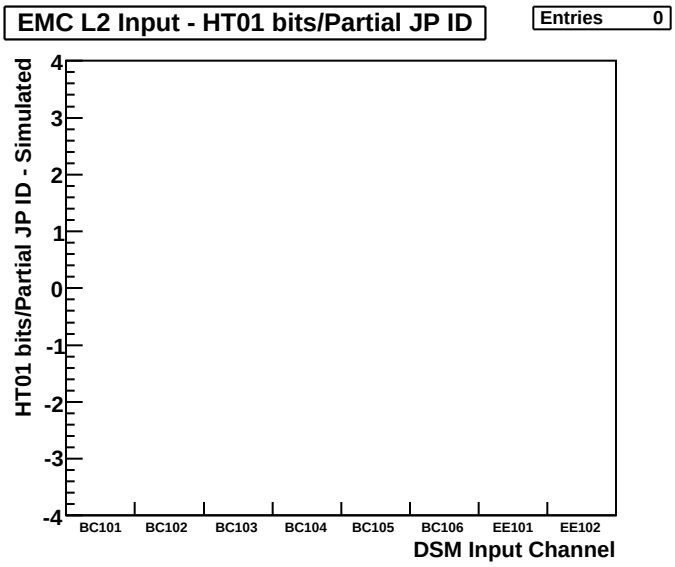

EMC L2 Input - JPZ/JPC bits
Entries 1423448
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1047 1006 1050 1169 1082 1218 443 480
1589 1612 1583 1540 1996 1696 514 541
1424 1577 1369 1426 2944 1465 272 279
173871 173736 173929 173796 171909 173562 176702 176631

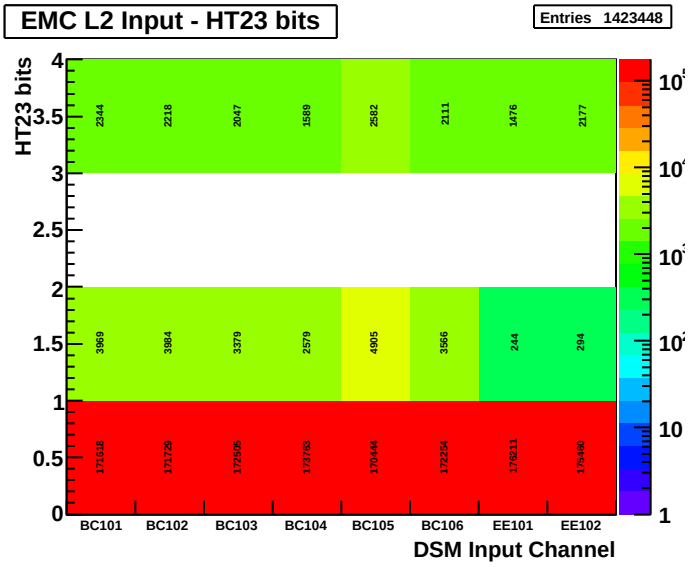

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

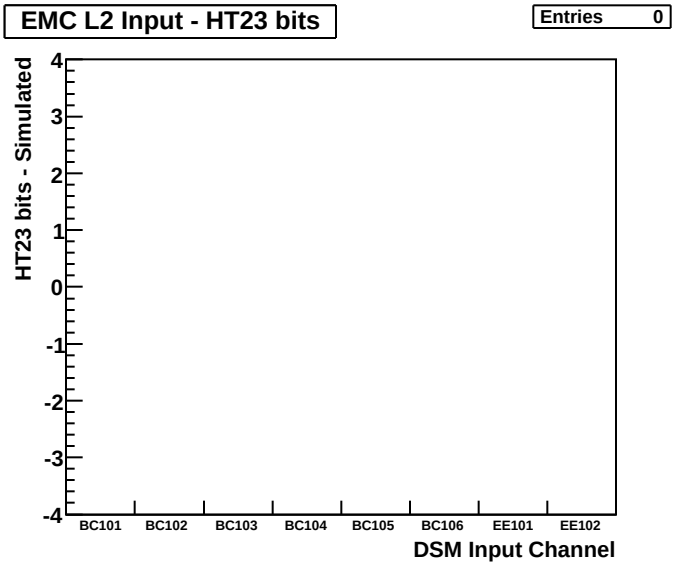

EMC L2 Input - Partial JP Sum
Entries 1423448
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

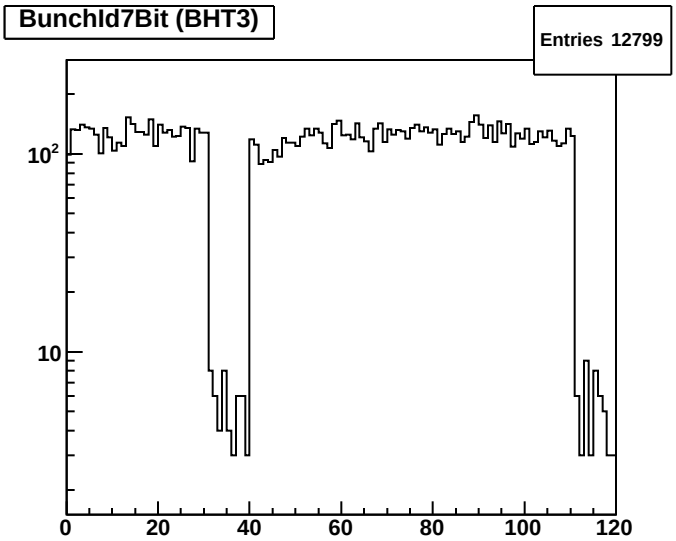

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

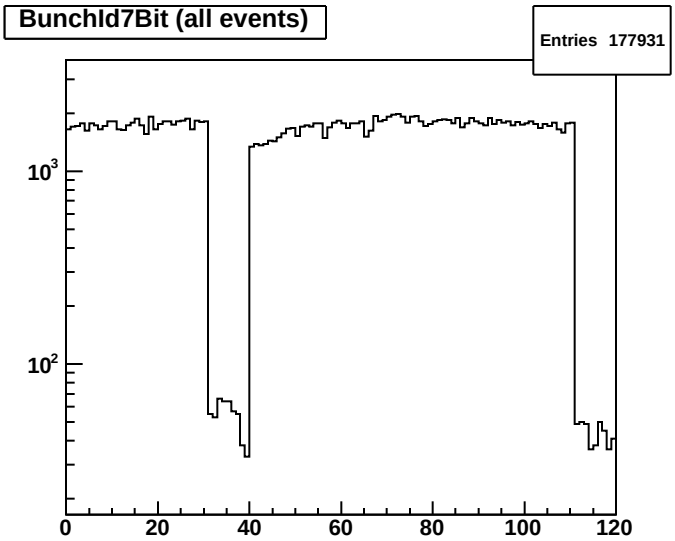

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1423448
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

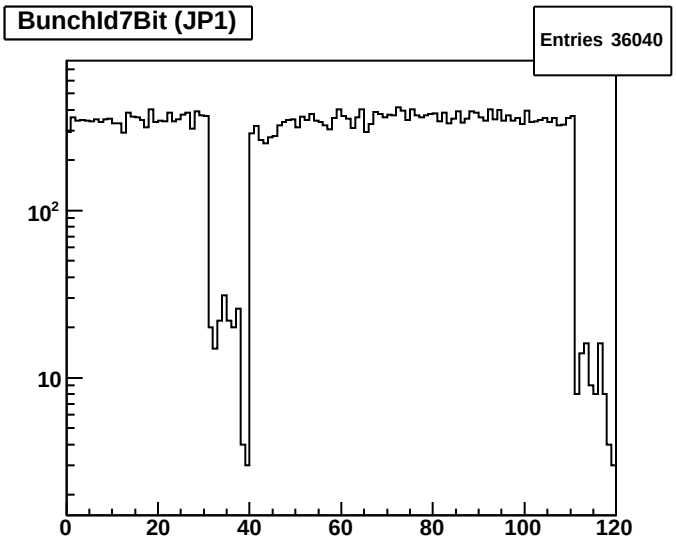

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

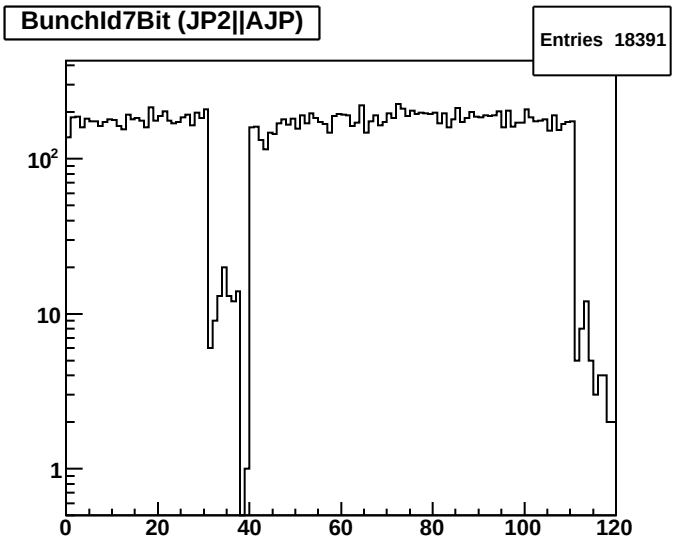

EMC L2 Input - HT23 bits
Entries 1423448
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

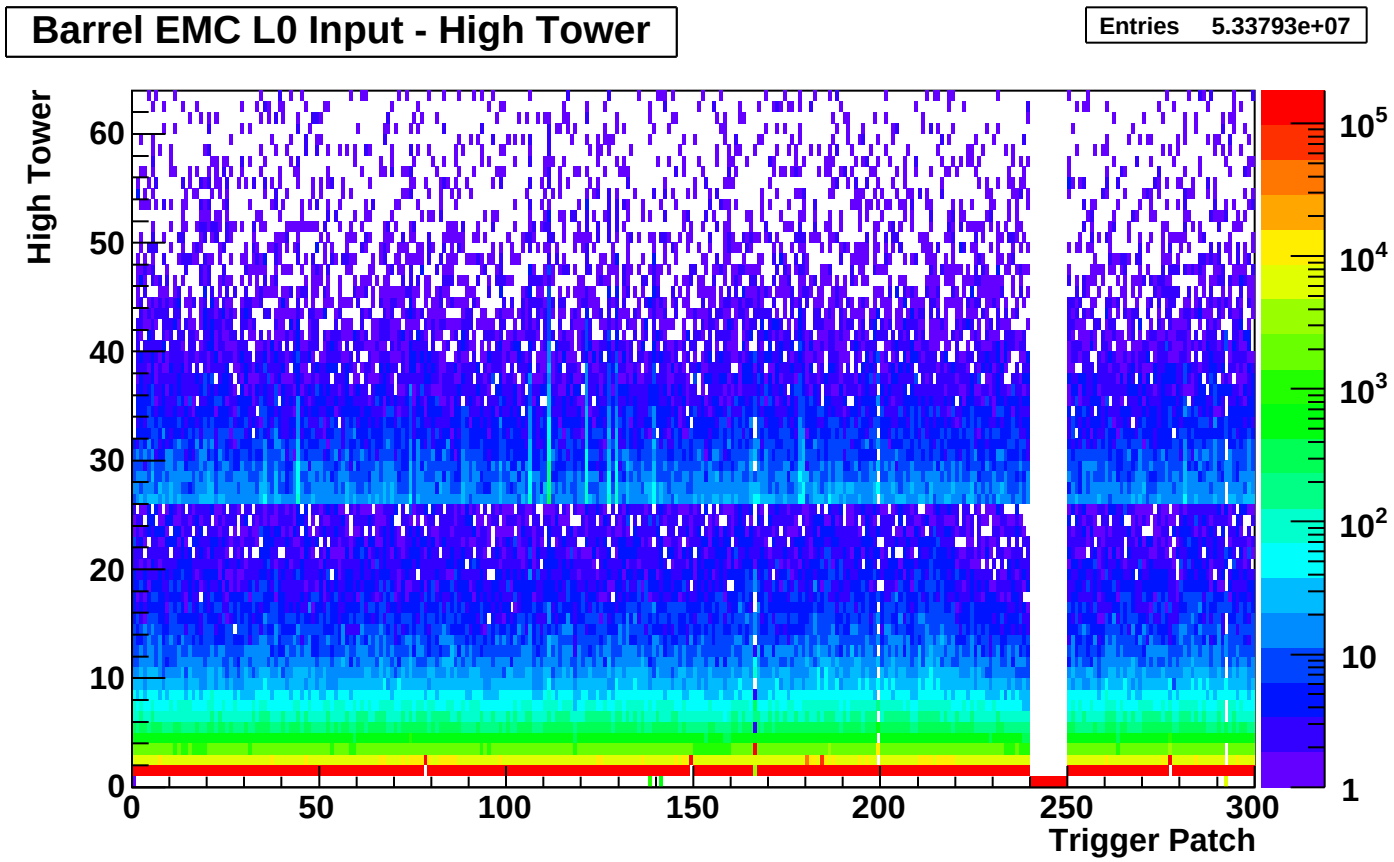

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

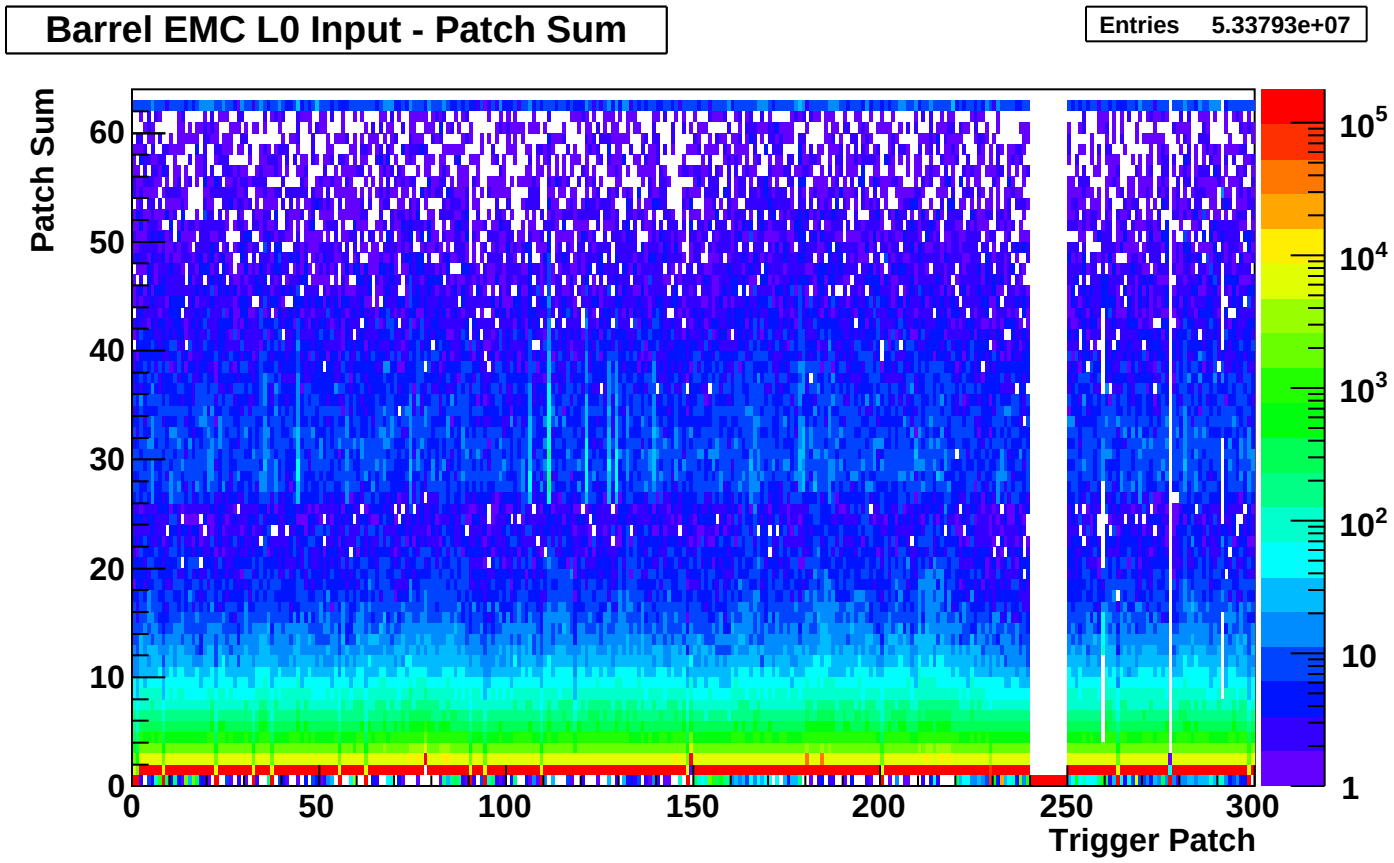

BunchId7Bit (BHT3)
Entries 12799
10^2
10
0
20
40
60
80
100
120

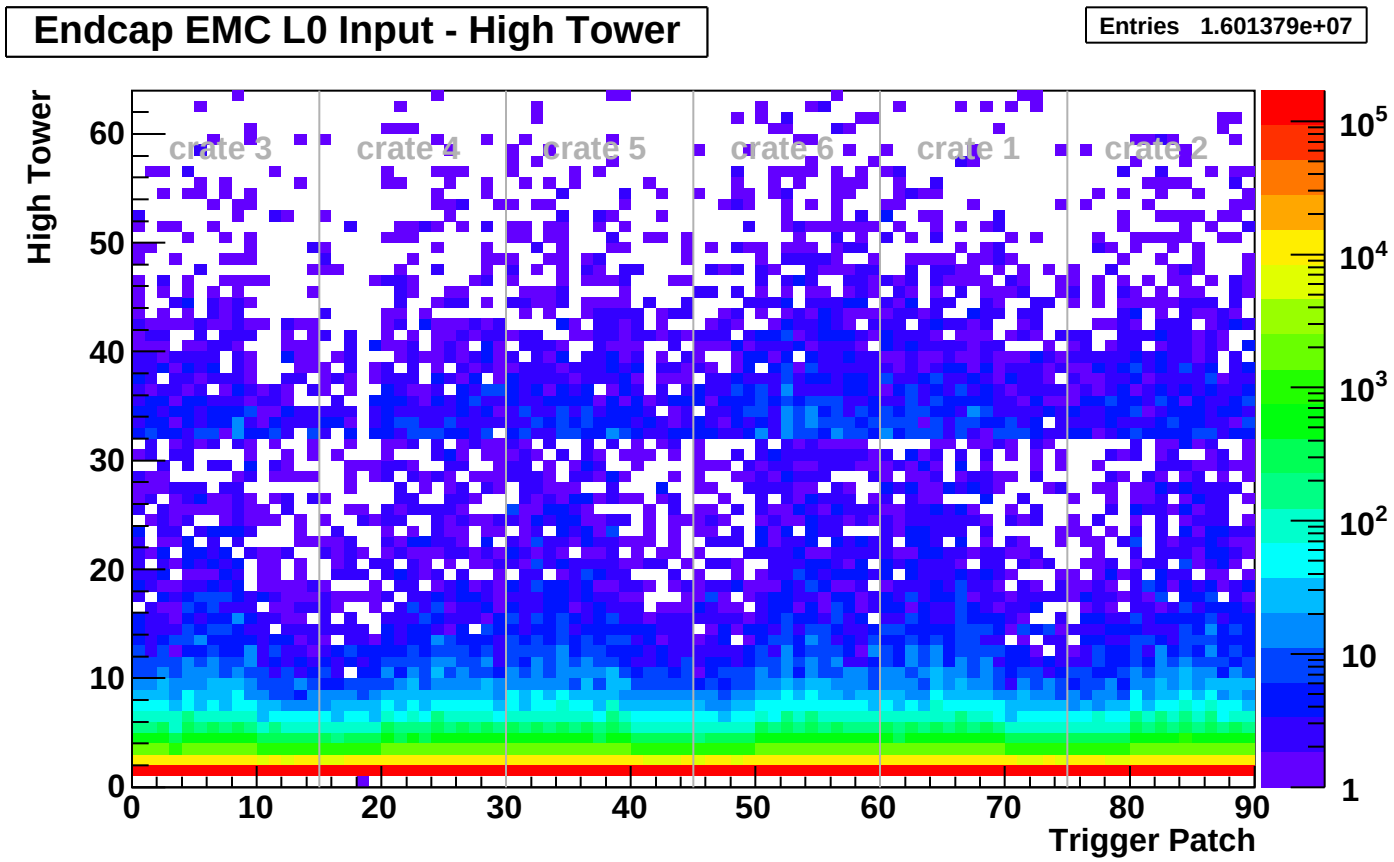

BunchId7Bit (all events)
Entries 177931
10^3
10^2
0
20
40
60
80
100
120

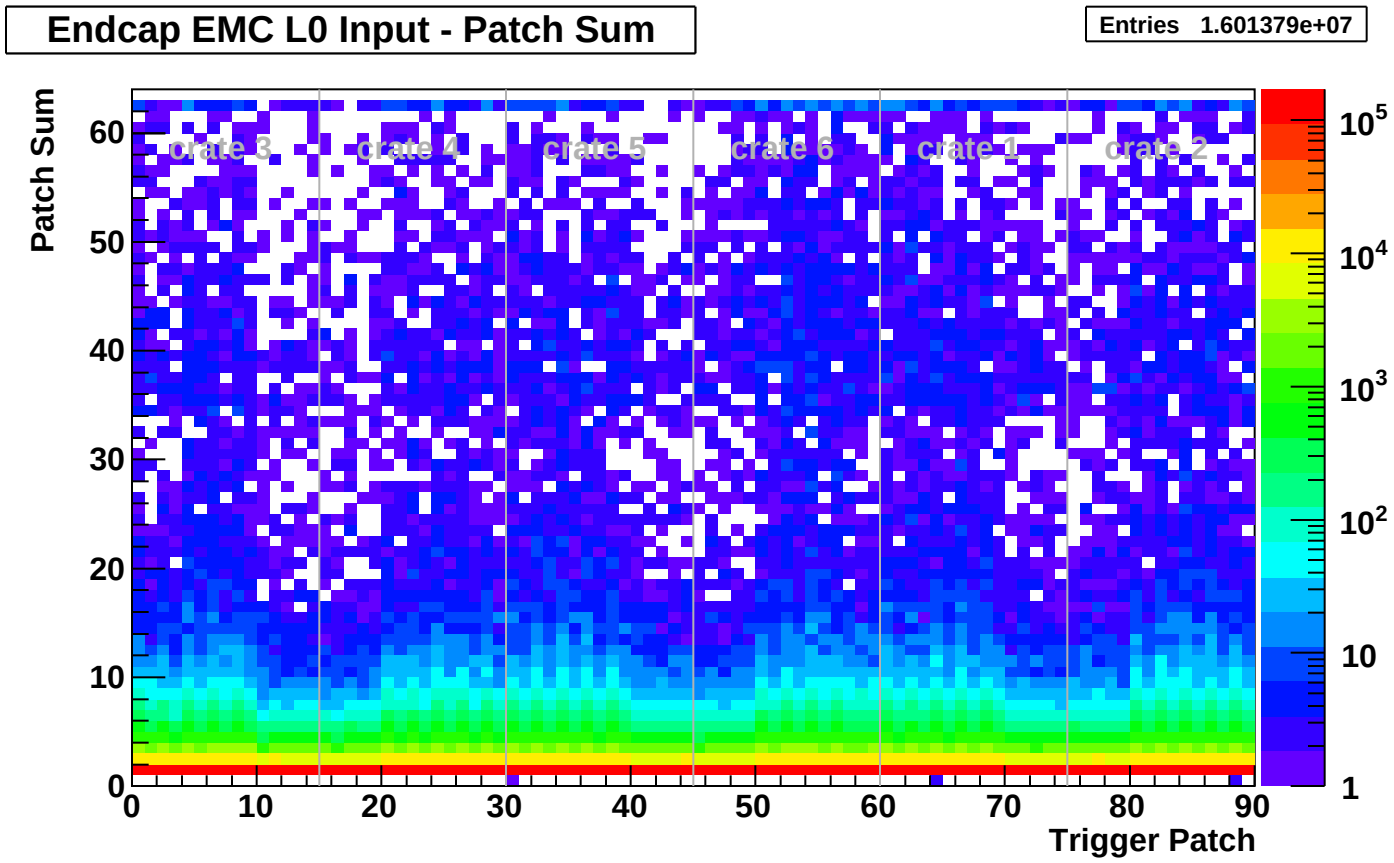

BunchId7Bit (JP1)
Entries 36040
10^2
10
0
20
40
60
80
100
120

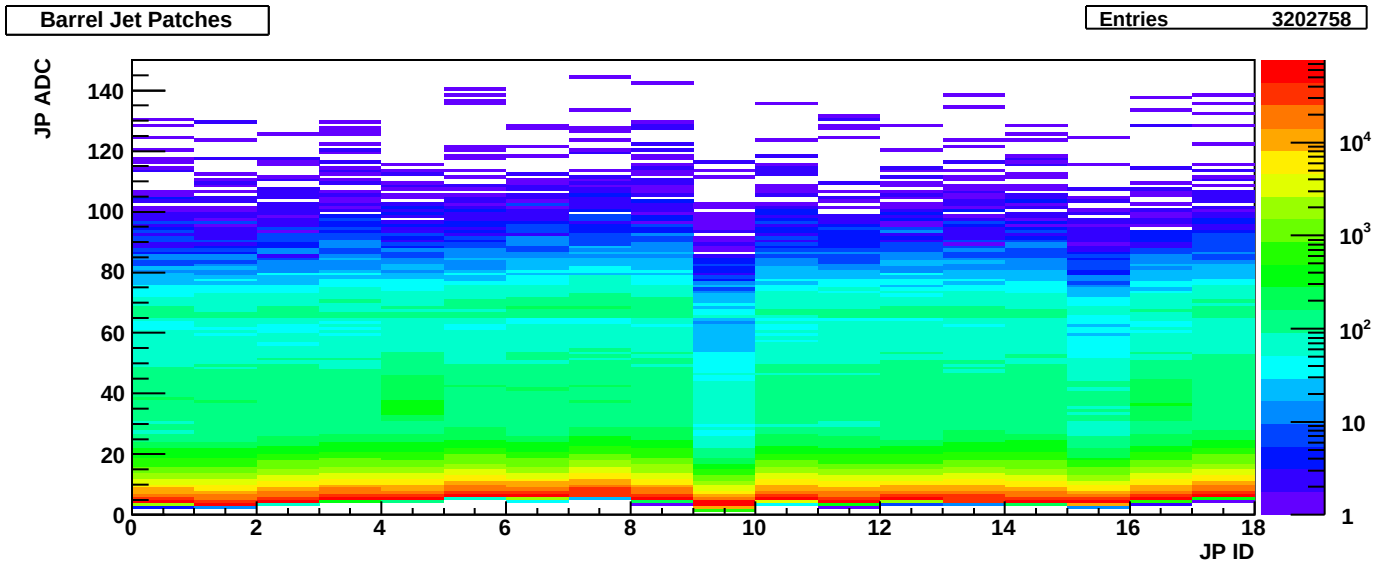

BunchId7Bit (JP2||AJP)
Entries 18391
10^2
10
1
0
20
40
60
80
100
120

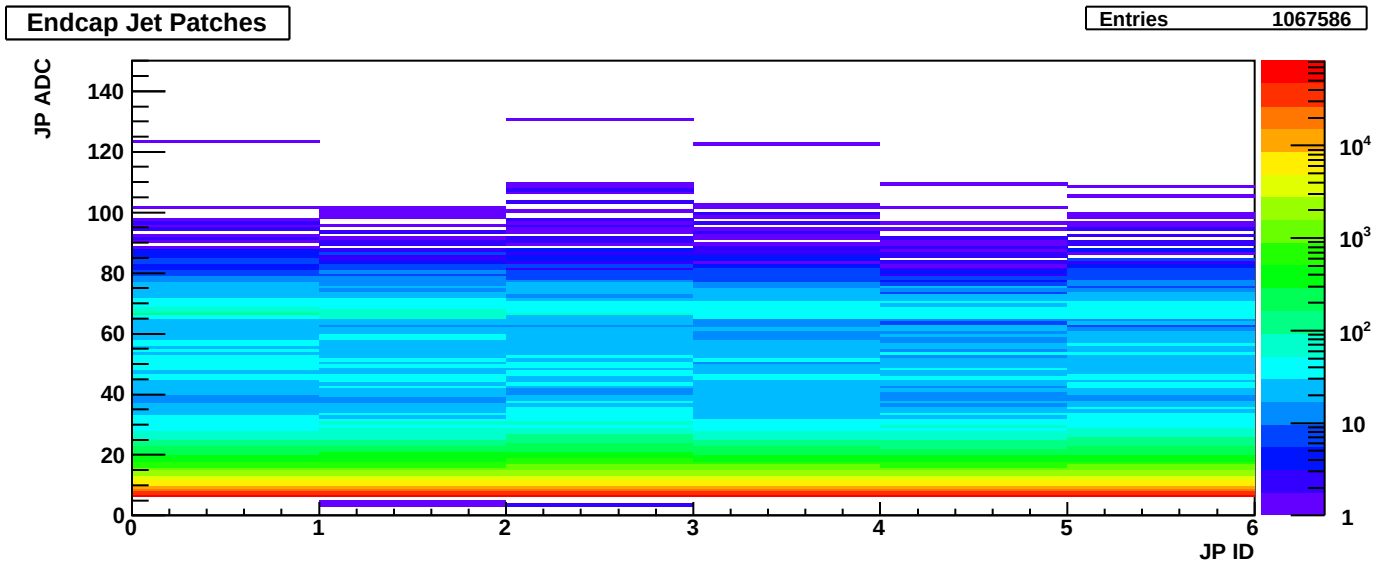

Barrel EMC L0 Input - High Tower
Entries 5.33793e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

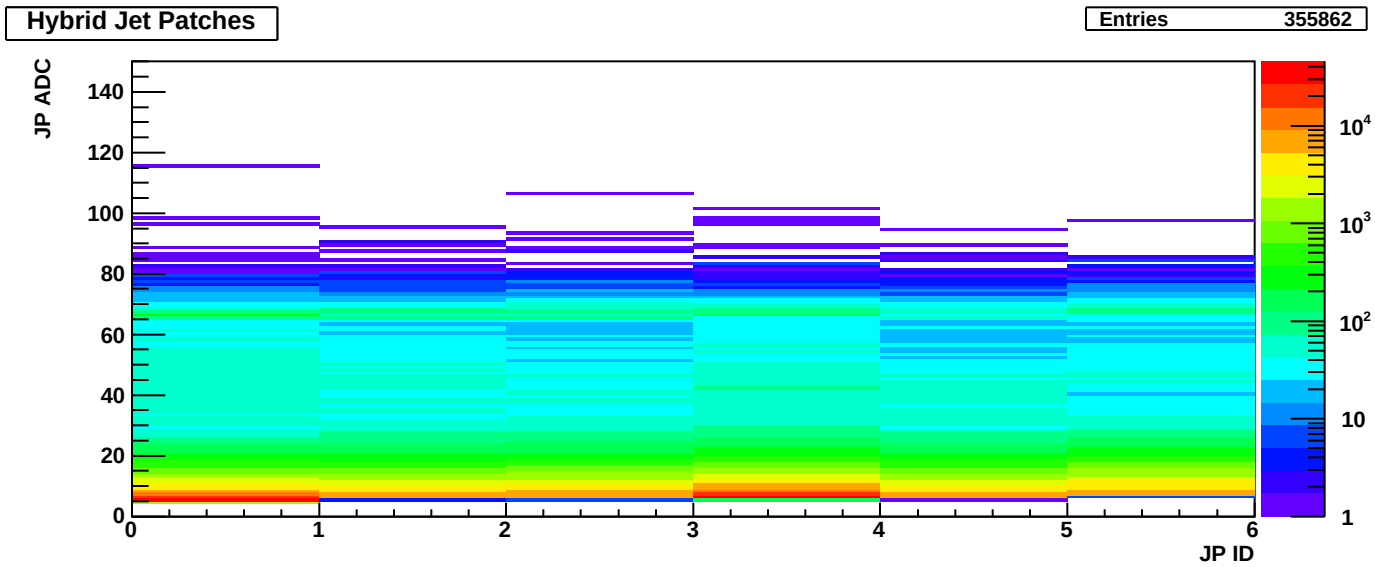

Barrel EMC L0 Input - Patch Sum
Entries 5.33793e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

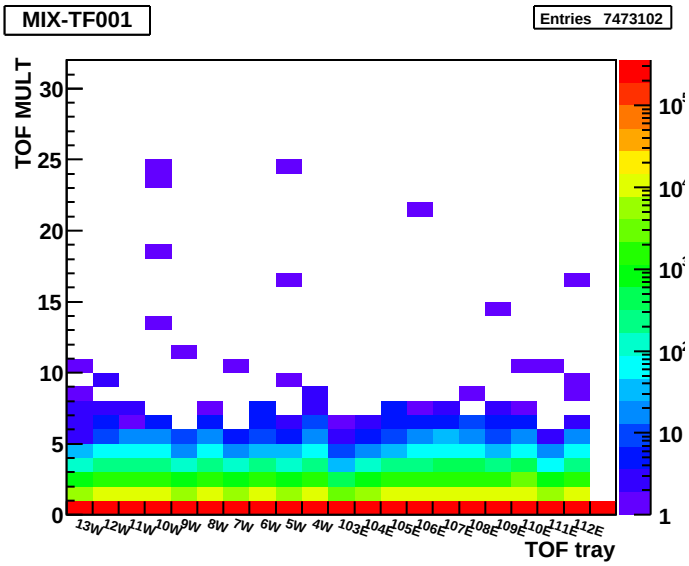

Endcap EMC L0 Input - High Tower
Entries 1.601379e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

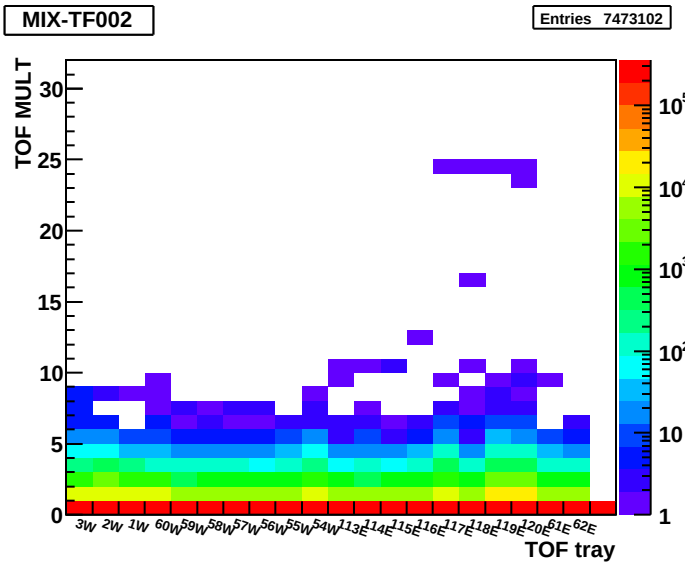

Endcap EMC L0 Input - Patch Sum
Entries 1.601379e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

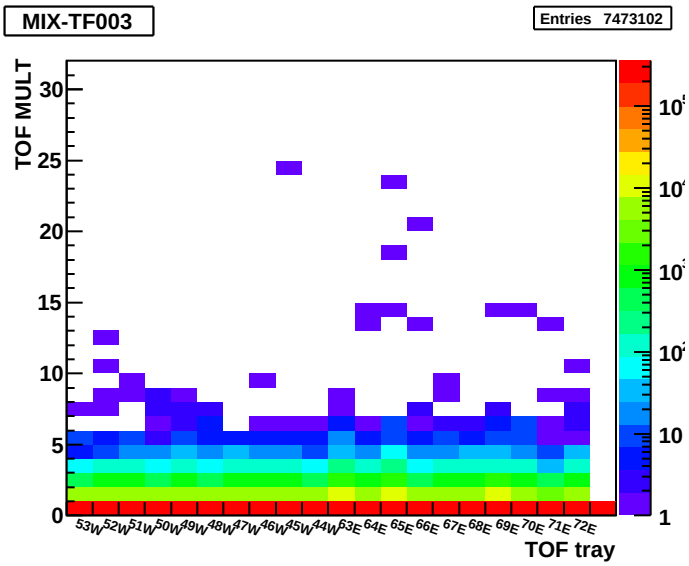

Barrel Jet Patches
Entries 3202758
JP ADC
JP ID

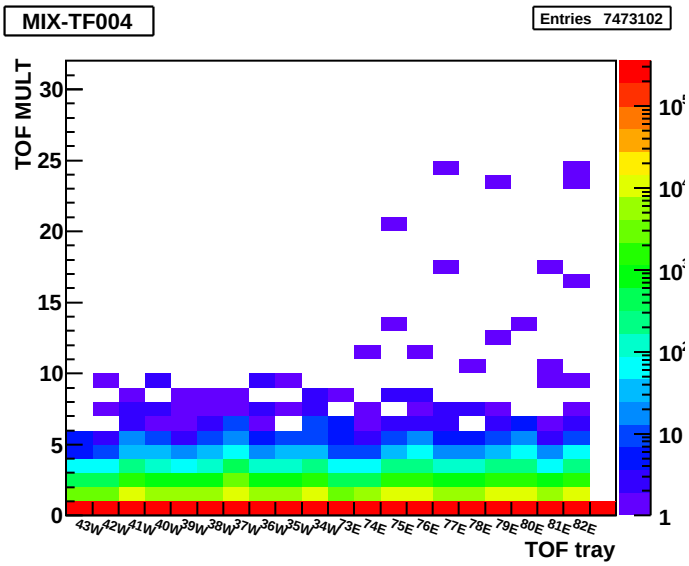

Endcap Jet Patches
Entries 1067586
JP ADC
JP ID

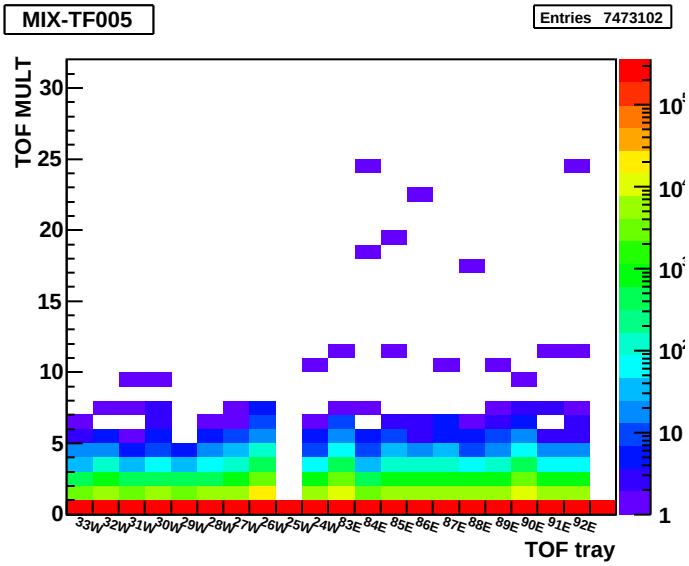

Hybrid Jet Patches
Entries 355862
JP ADC
JP ID

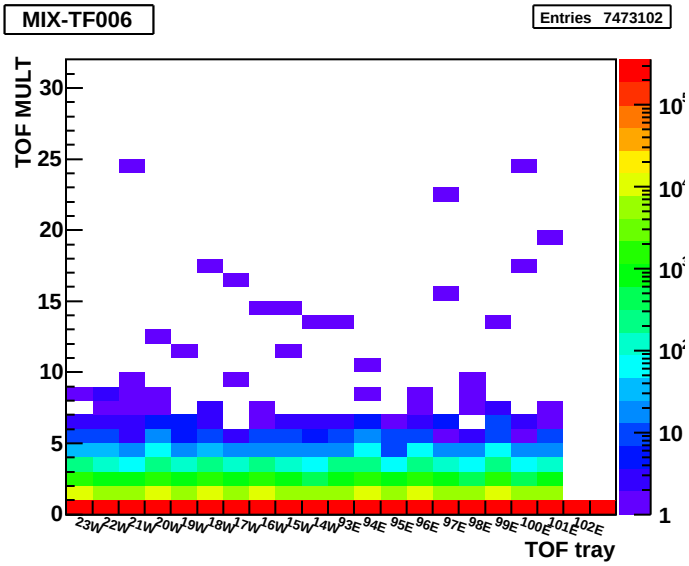

MIX-TF001
Entries 7473102
TOF MULT
TOF tray

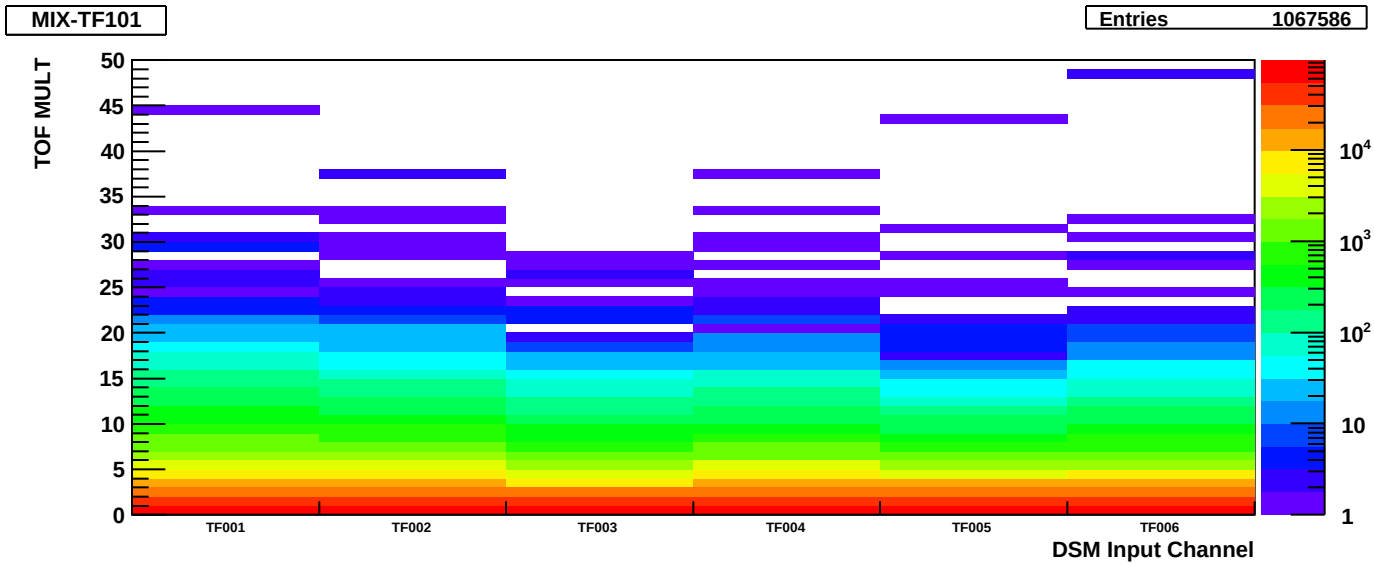

MIX-TF002
Entries 7473102
TOF MULT
TOF tray

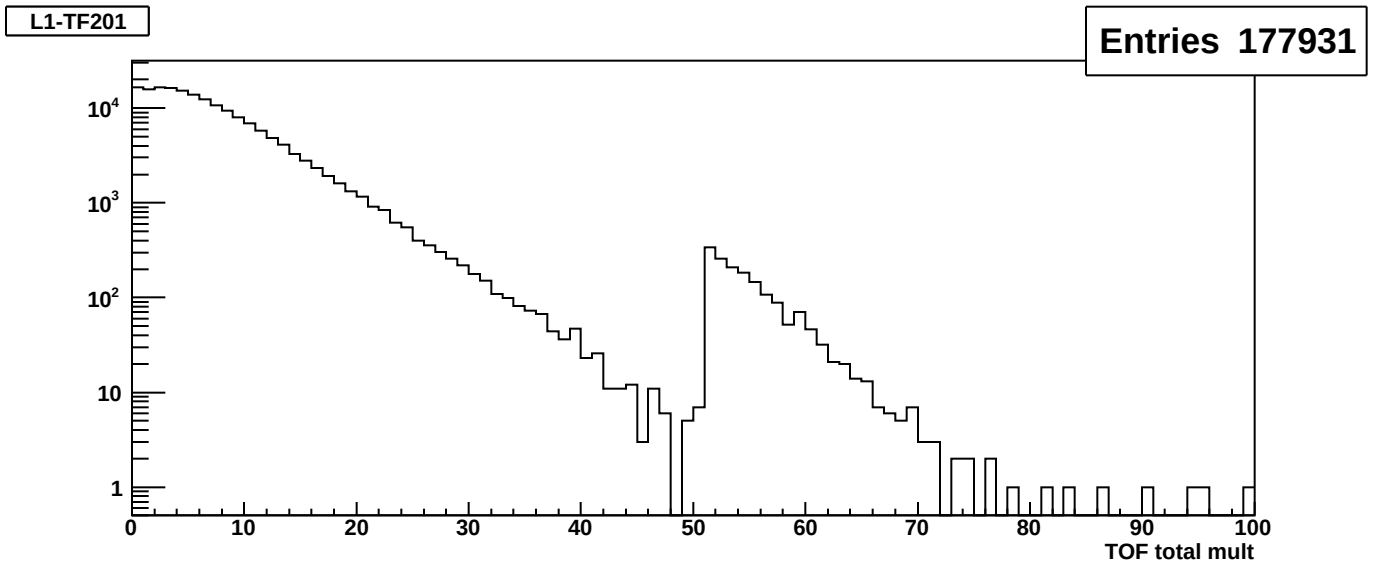

MIX-TF003
Entries 7473102
TOF MULT
TOF tray

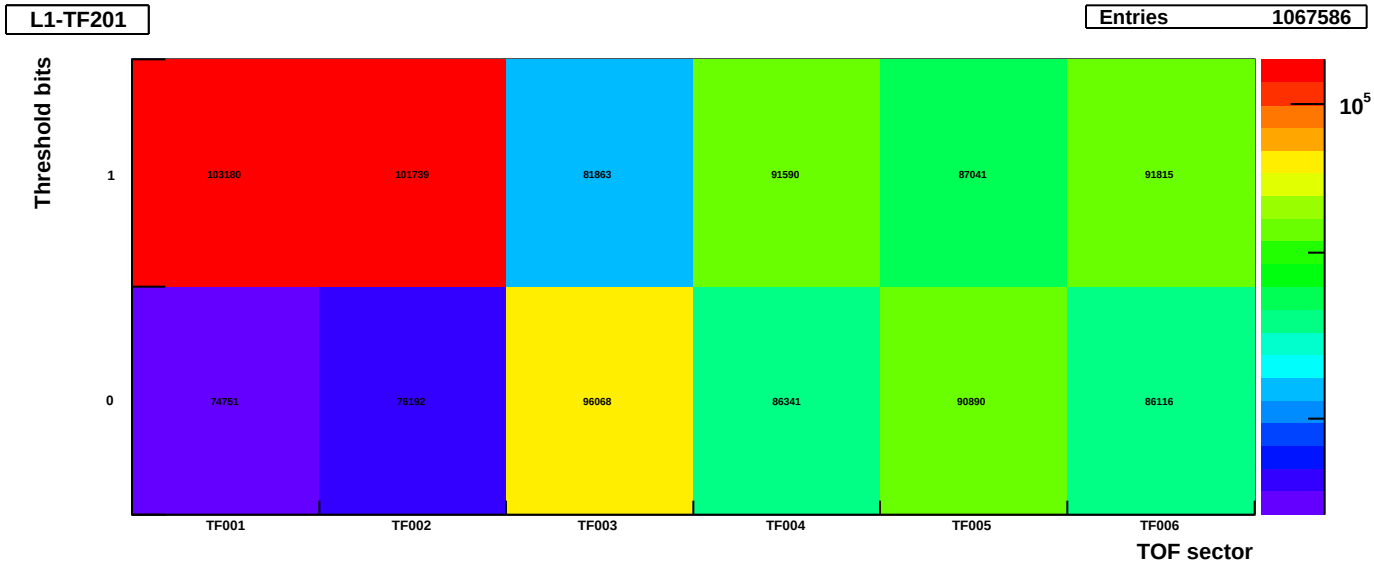

MIX-TF004
Entries 7473102
TOF MULT
TOF tray

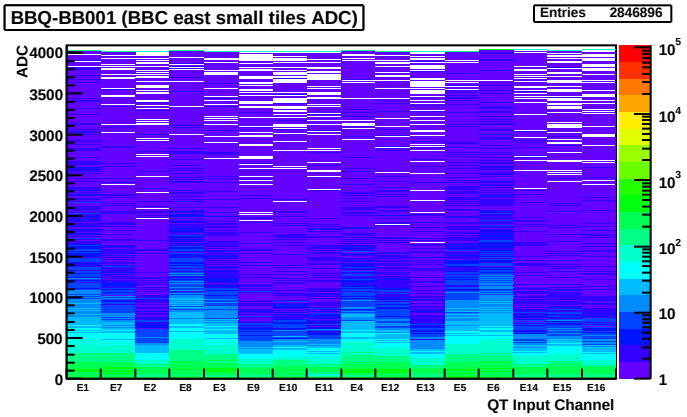

MIX-TF005
Entries 7473102
TOF MULT
TOF tray

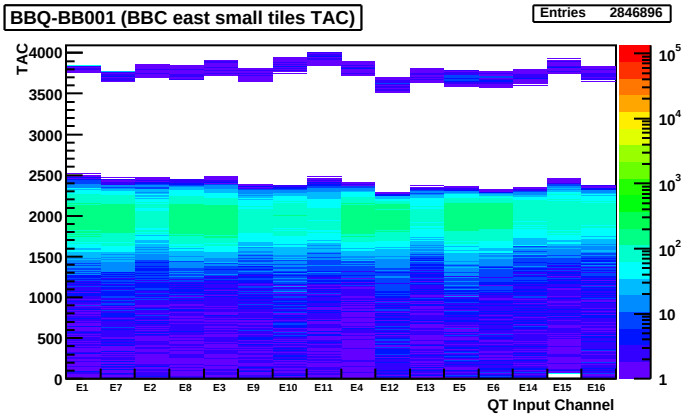

MIX-TF006
Entries 7473102
TOF MULT
TOF tray

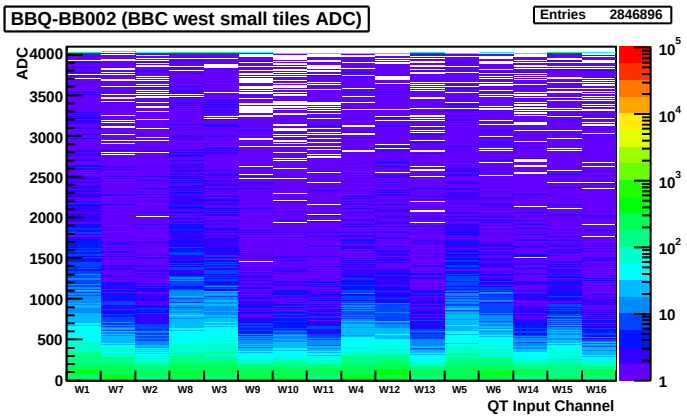

MIX-TF101
Entries 1067586
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

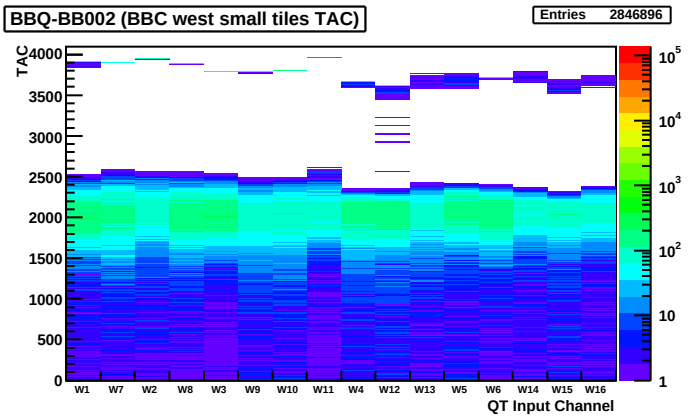

L1-TF201
Entries 177931
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

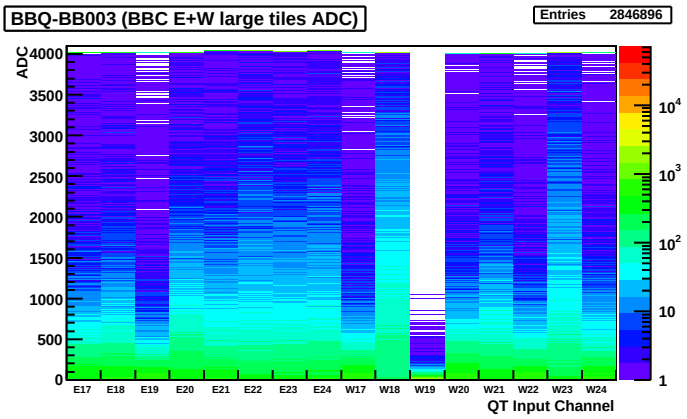

L1-TF201
Entries 1067586
Threshold bits
1
0
103180
101739
81863
91590
87041
91815
74751
76192
96068
86341
90890
86116
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

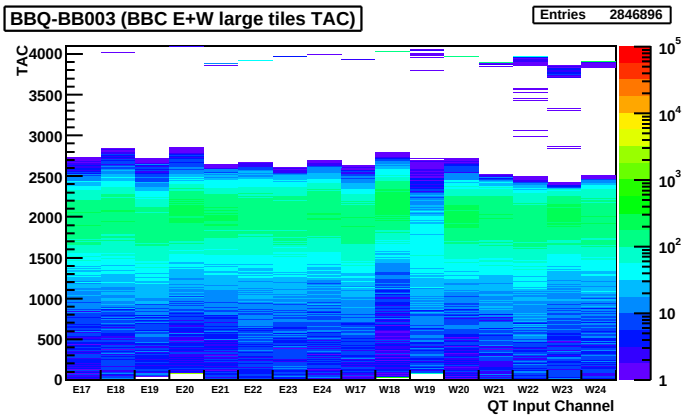

BBQ-BB001 (BBC east small tiles ADC)
Entries 2846896
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

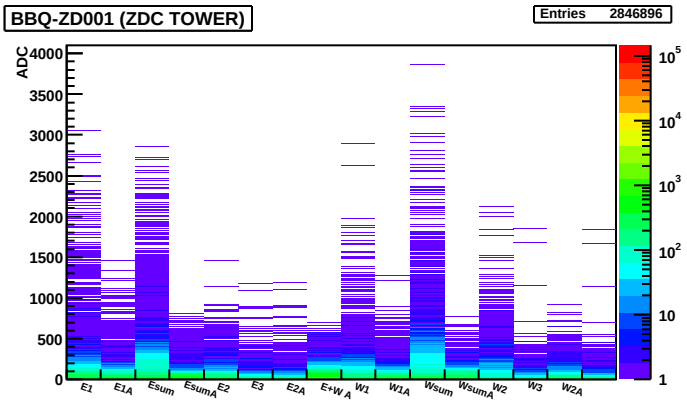

BBQ-BB001 (BBC east small tiles TAC)
Entries 2846896
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

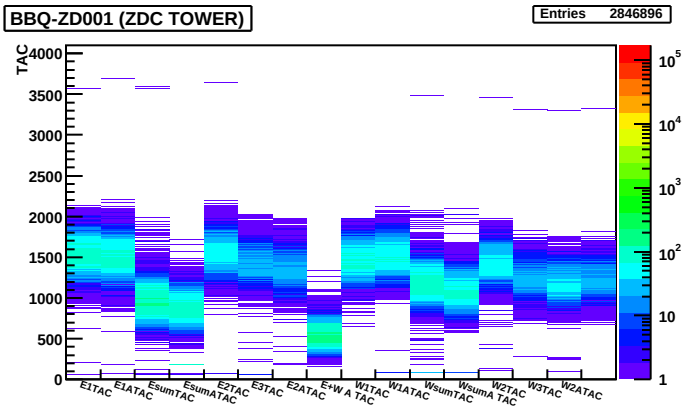

BBQ-BB002 (BBC west small tiles ADC)
Entries 2846896
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

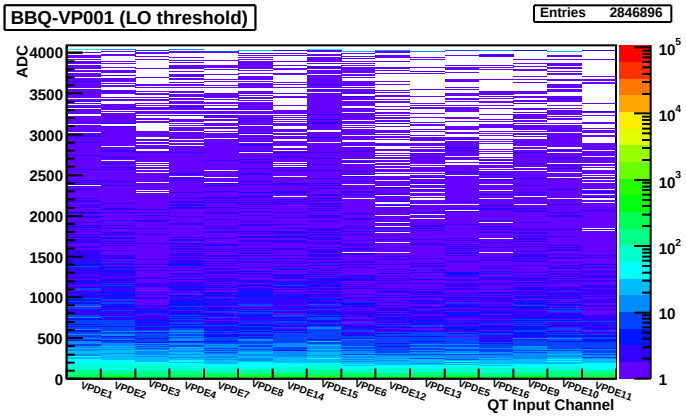

BBQ-BB002 (BBC west small tiles TAC)
Entries 2846896
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

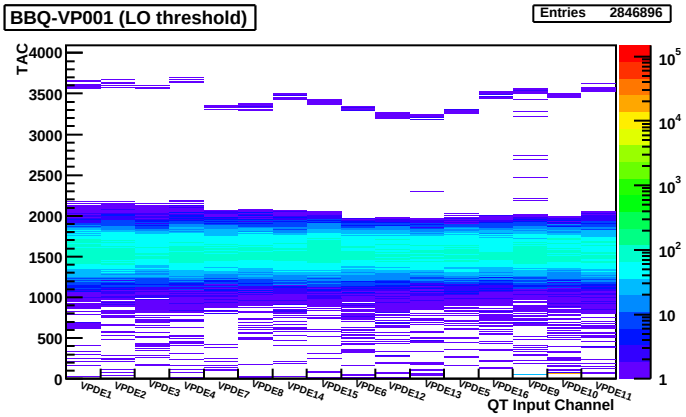

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2846896
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

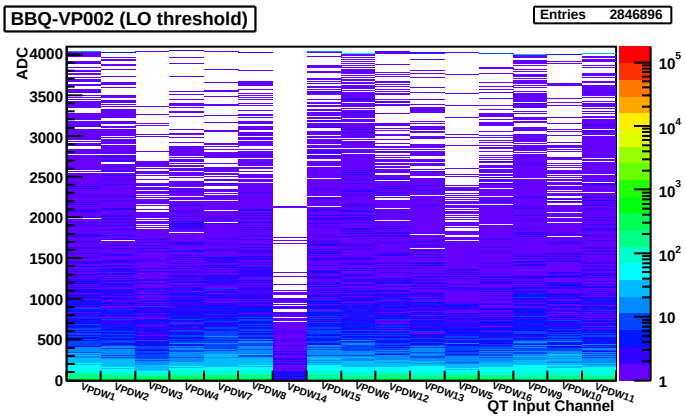

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2846896
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

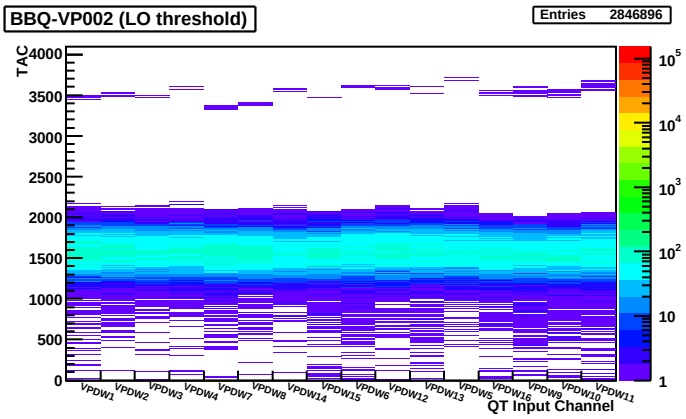

BBQ-ZD001 (ZDC TOWER)
Entries 2846896
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

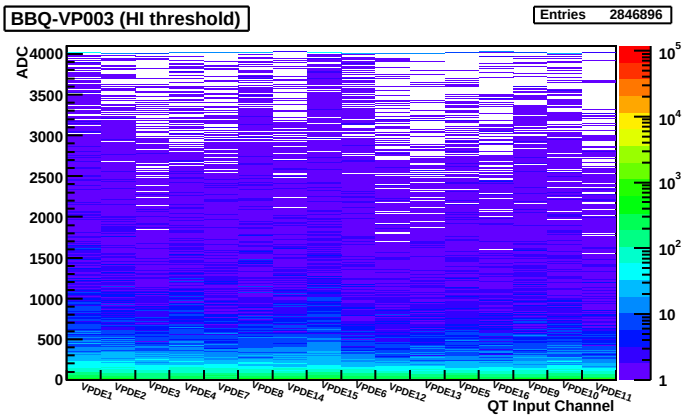

BBQ-ZD001 (ZDC TOWER)
Entries 2846896
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

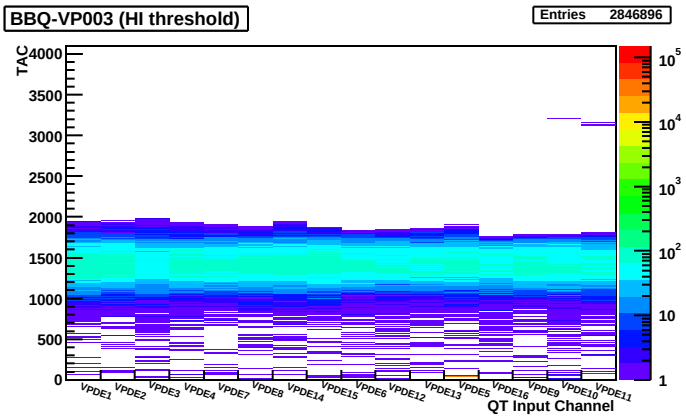

BBQ-VP001 (LO threshold)
Entries 2846896
ADC
QT Input Channel

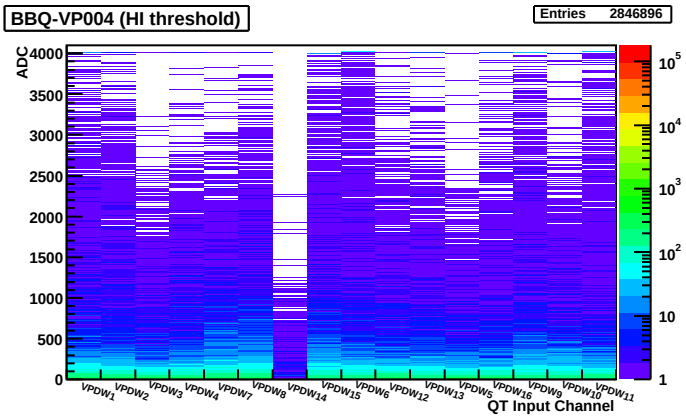

BBQ-VP001 (LO threshold)
Entries 2846896
TAC
QT Input Channel

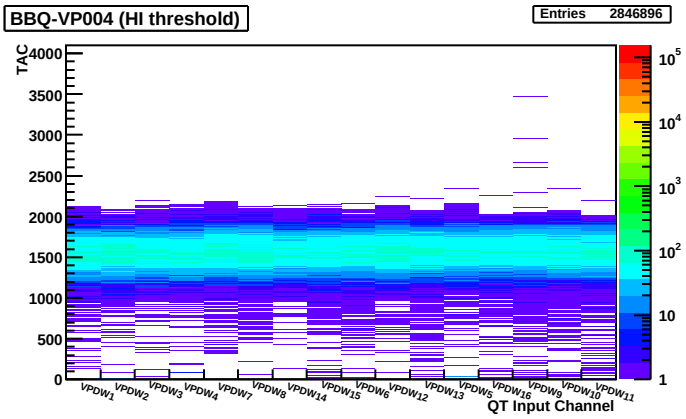

BBQ-VP002 (LO threshold)
Entries 2846896
ADC
QT Input Channel

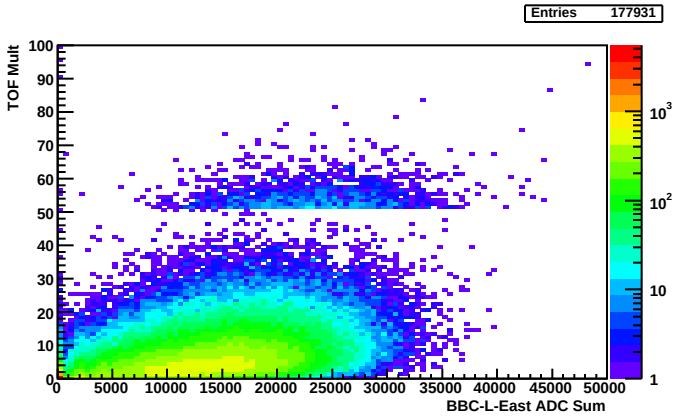

BBQ-VP002 (LO threshold)
Entries 2846896
TAC
QT Input Channel

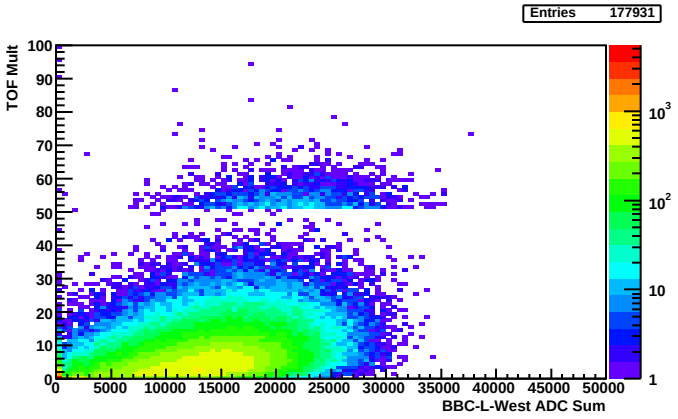

BBQ-VP003 (HI threshold)
Entries 2846896
ADC
QT Input Channel

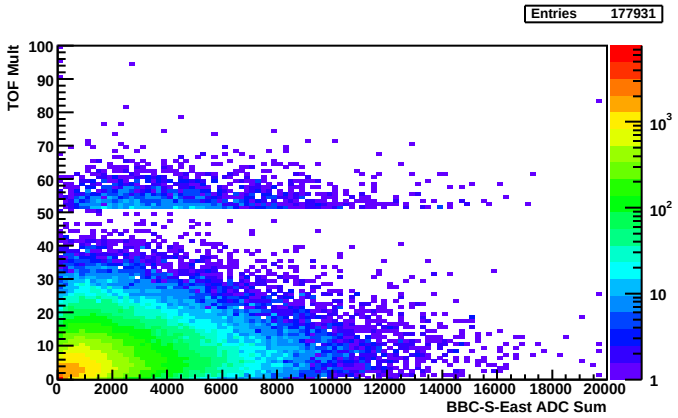

BBQ-VP003 (HI threshold)
Entries 2846896
TAC
QT Input Channel

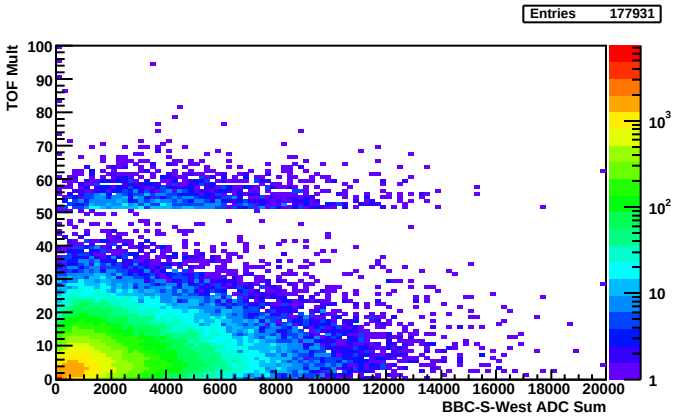

BBQ-VP004 (HI threshold)
Entries 2846896
ADC
QT Input Channel

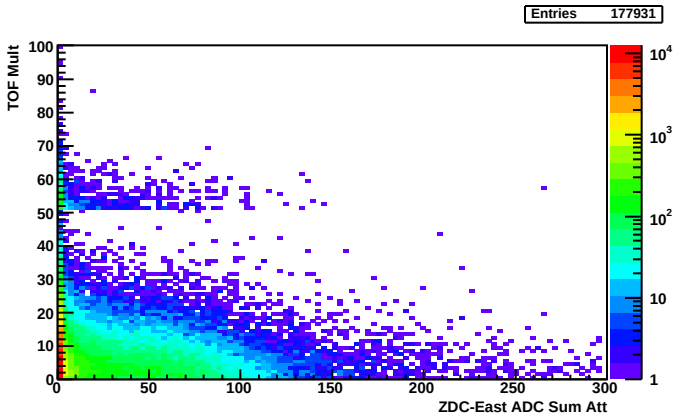

BBQ-VP004 (HI threshold)
Entries 2846896
TAC
QT Input Channel

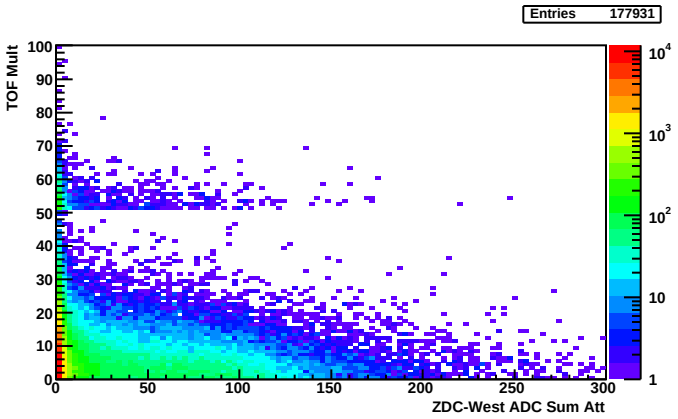

Entries 177931
TOF Mult
BBC-L-East ADC Sum

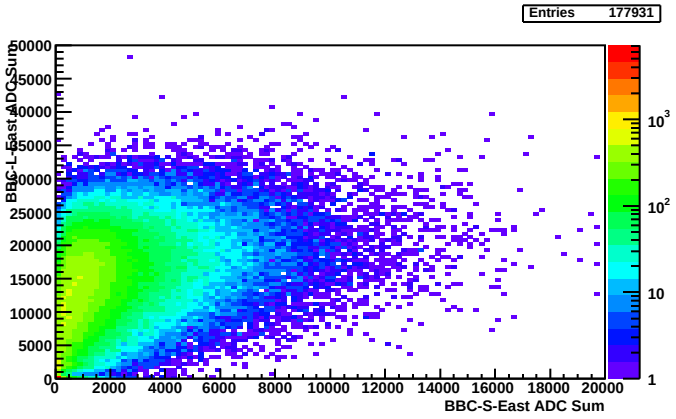

Entries 177931
TOF Mult
BBC-L-West ADC Sum

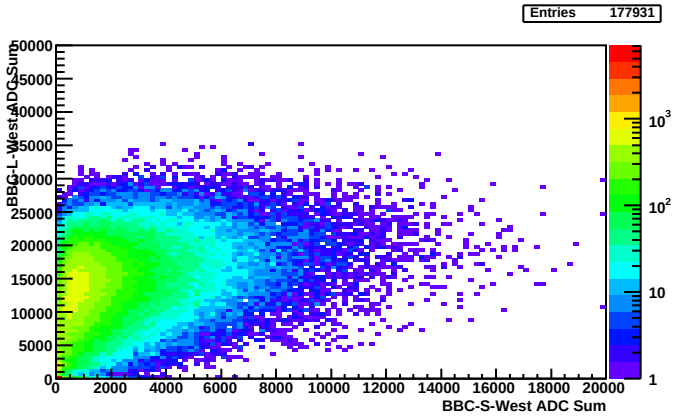

Entries 177931
TOF Mult
BBC-S-East ADC Sum

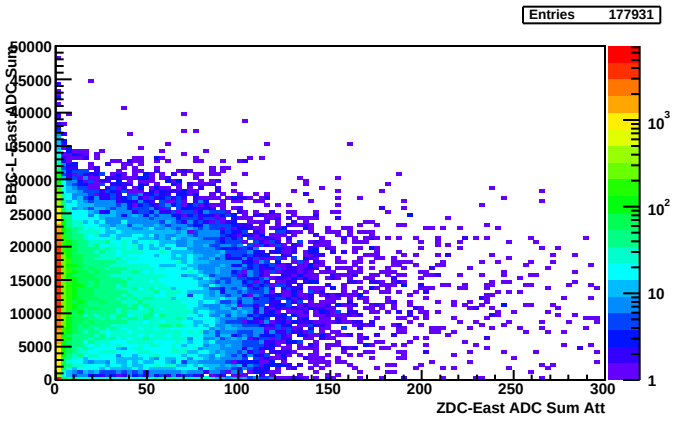

Entries 177931
TOF Mult
BBC-S-West ADC Sum

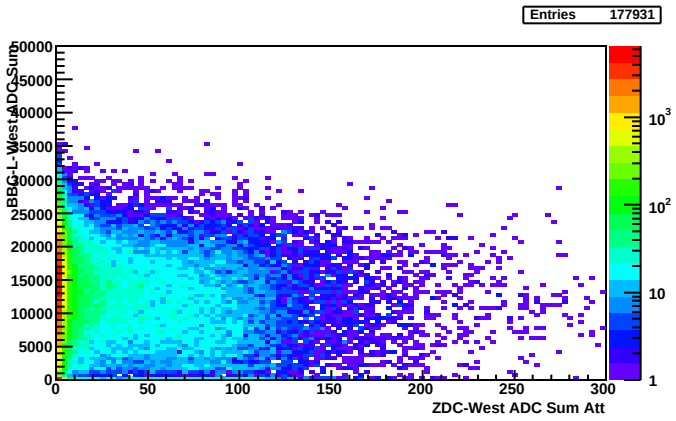

Entries 177931
TOF Mult
ZDC-East ADC Sum Att


Entries 177931
TOF Mult
ZDC-West ADC Sum Att


Entries 177931
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 177931
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 177931
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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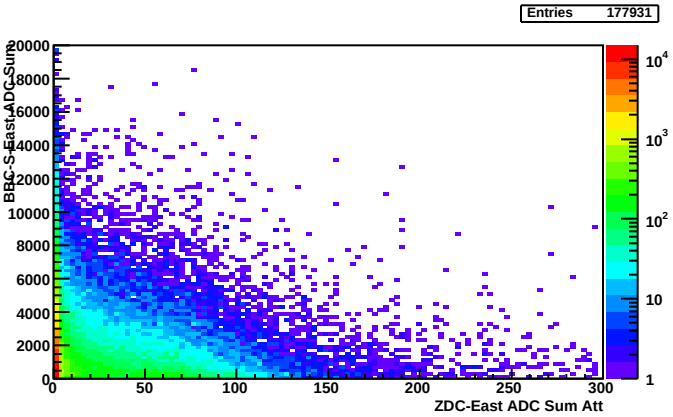
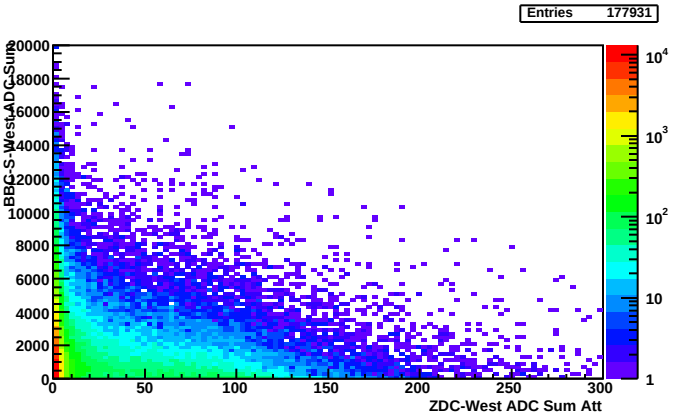
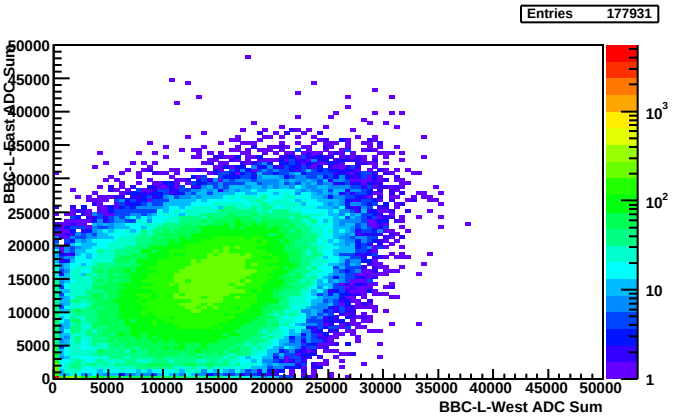
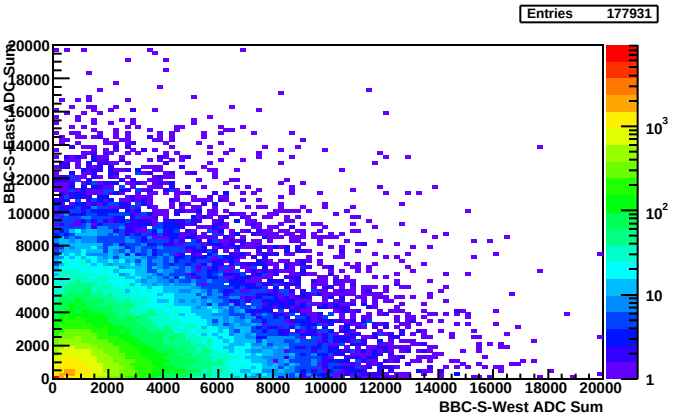
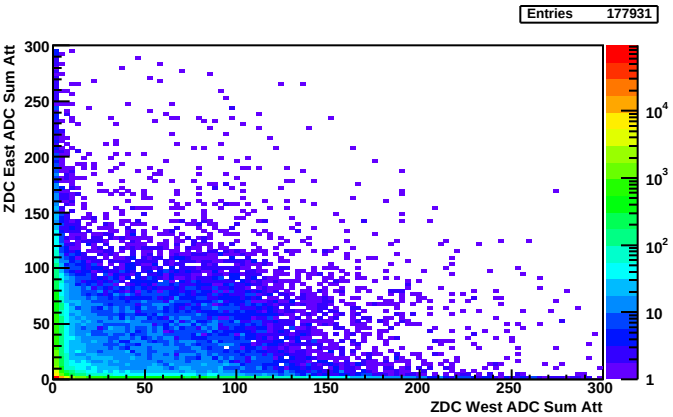
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

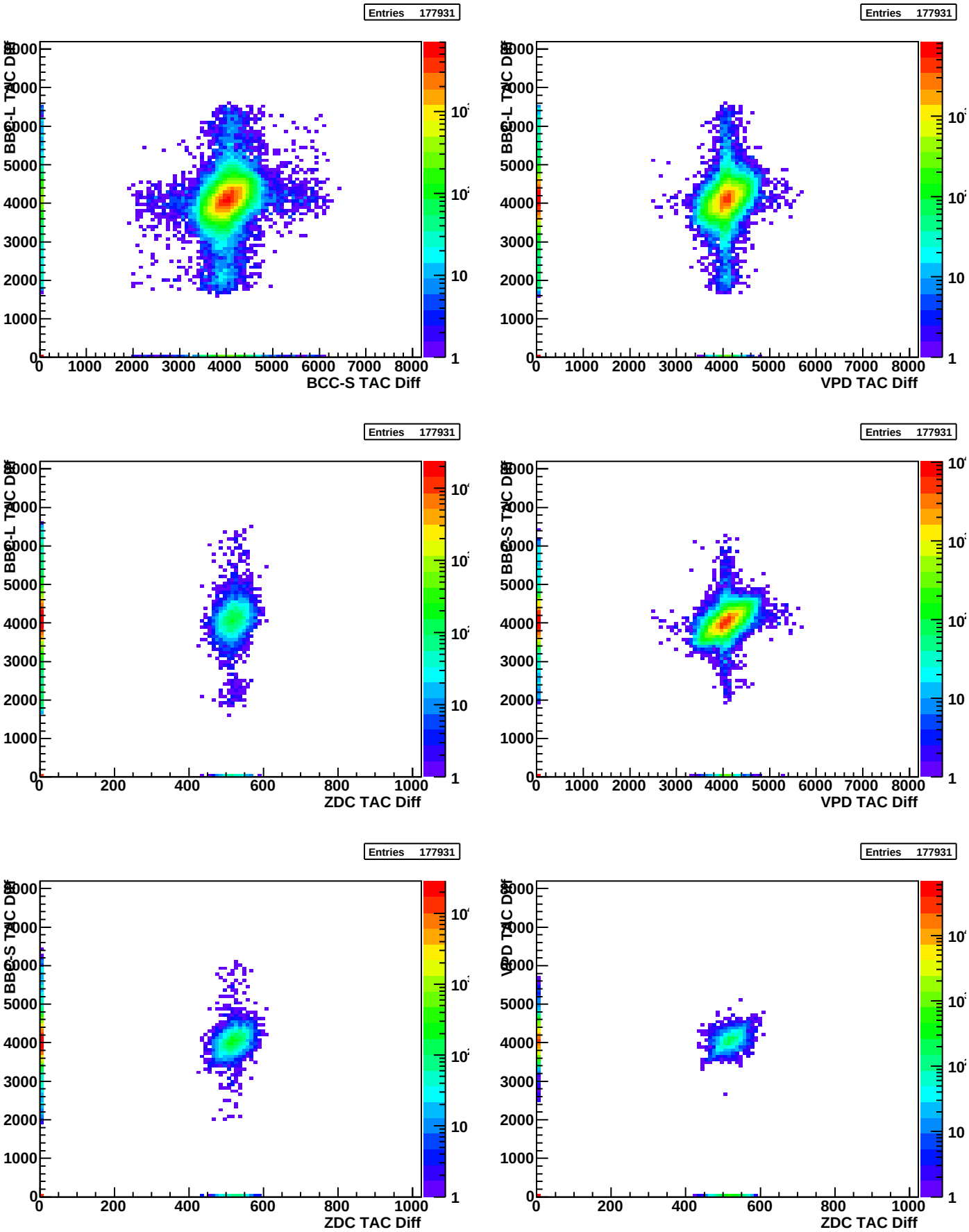

Entries 177931
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

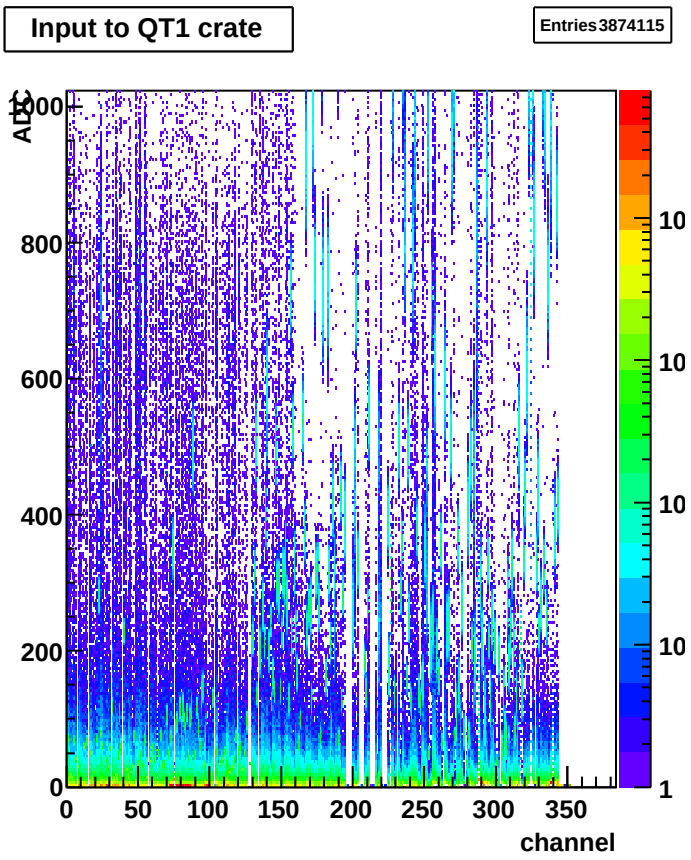

Entries 177931
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

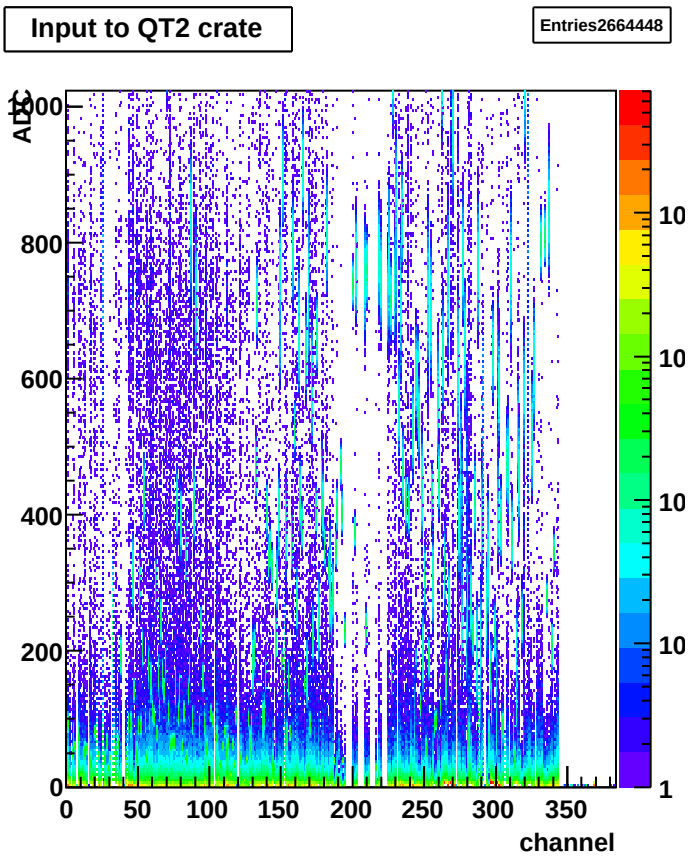

Entries 177931
BBC-L-East ADC Sum
BBC-L-West ADC Sum

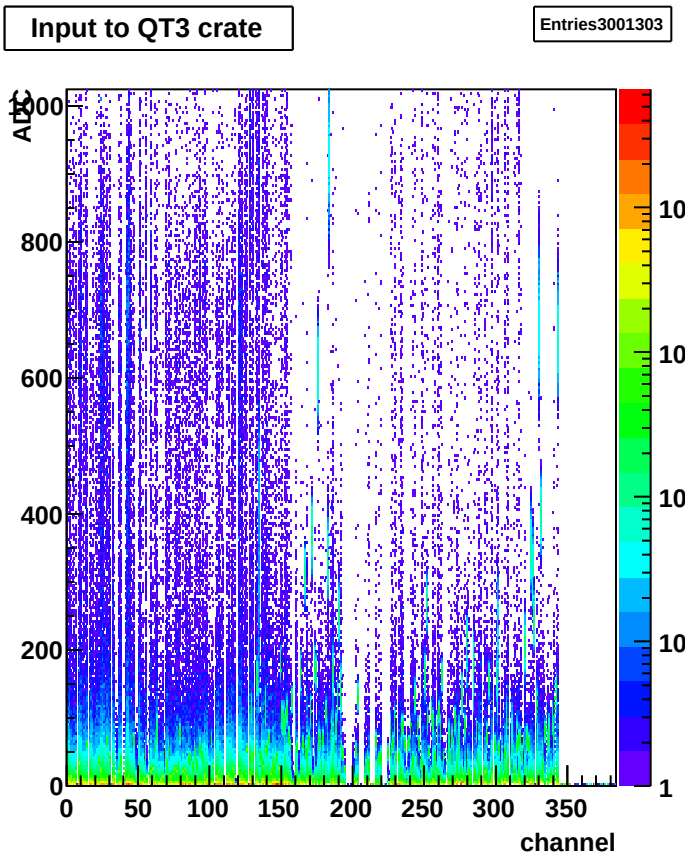

Entries 177931
BBC-S-East ADC Sum
BBC-S-West ADC Sum

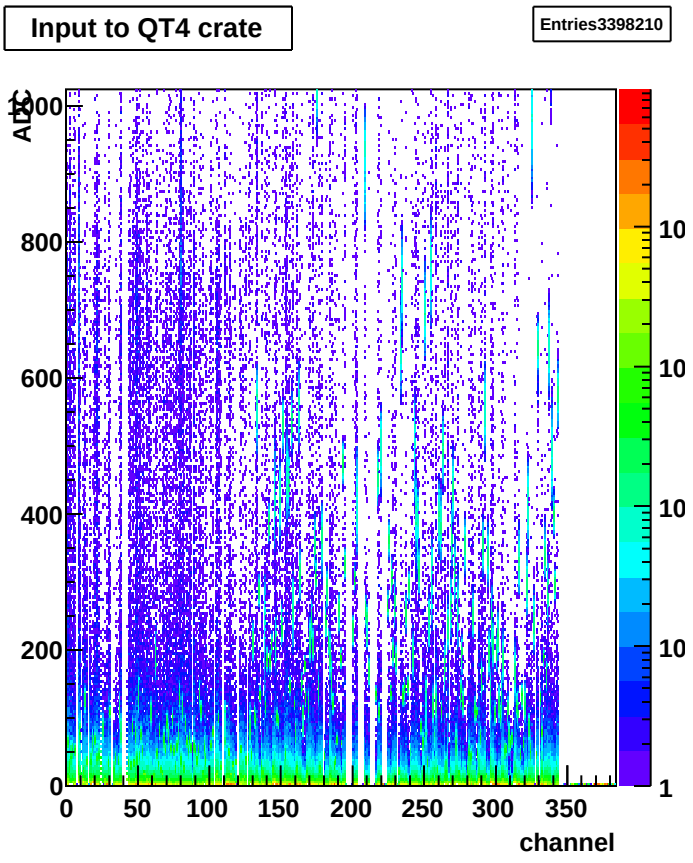

Entries 177931
ZDC East ADC Sum Att
ZDC West ADC Sum Att

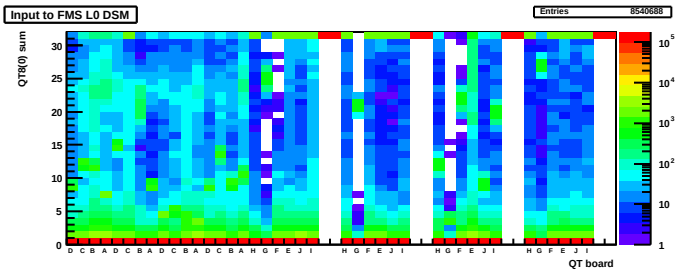

Entries 177931
BBC-L TAC Diff
BCC-S TAC Diff
Entries 177931
BBC-L TAC Diff
VPD TAC Diff
Entries 177931
BBC-L TAC Diff
ZDC TAC Diff
Entries 177931
BBC-S TAC Diff
VPD TAC Diff
Entries 177931
BBC-S TAC Diff
ZDC TAC Diff
Entries 177931
VPD TAC Diff
ZDC TAC Diff

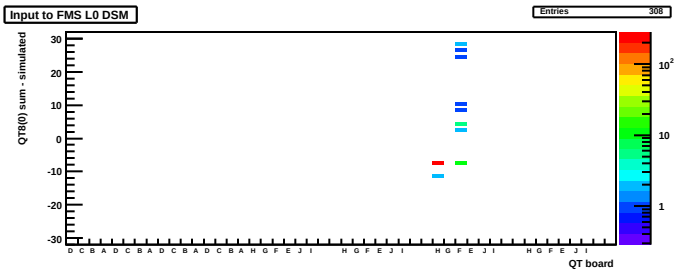

Input to QT1 crate
Entries3874115
ADC
channel

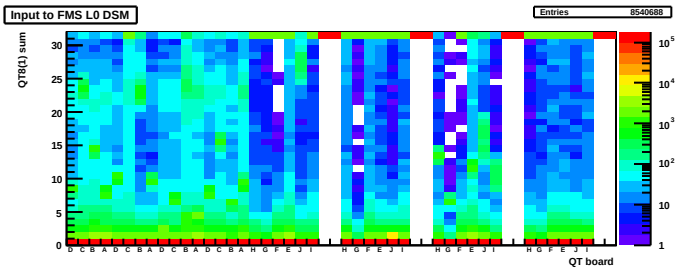

Input to QT2 crate
Entries2664448
ADC
channel

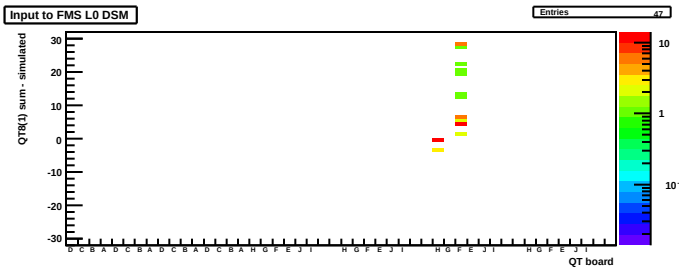

Input to QT3 crate
Entries3001303
ADC
channel

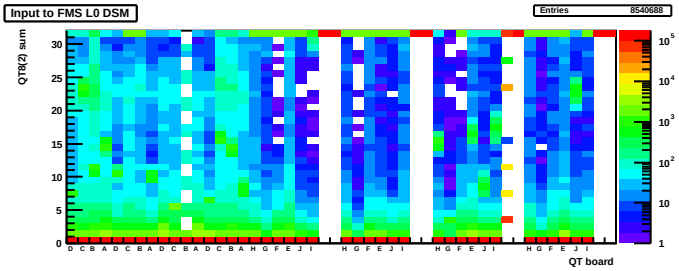

Input to QT4 crate
Entries3398210
ADC
channel

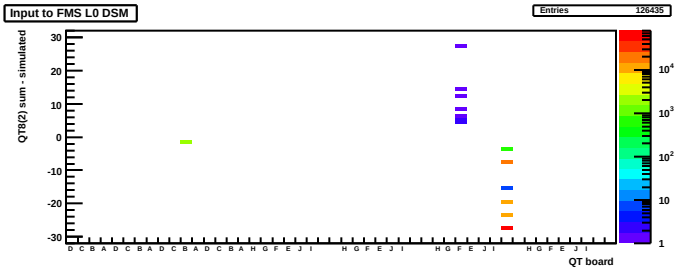

Input to FMS L0 DSM
Entries 8540688
QT8(0) sum
QT board

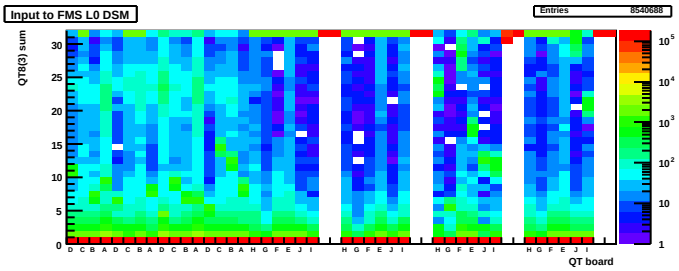

Input to FMS L0 DSM
Entries 308
QT8(0) sum - simulated
QT board

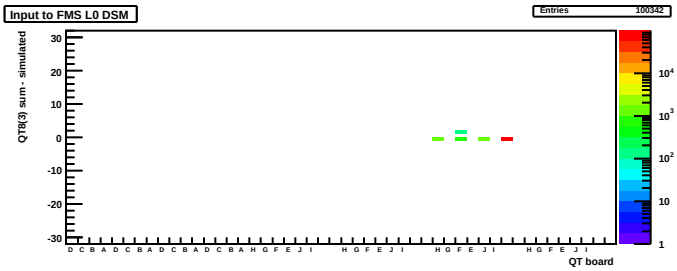

Input to FMS L0 DSM
Entries 8540688
QT8(1) sum
QT board

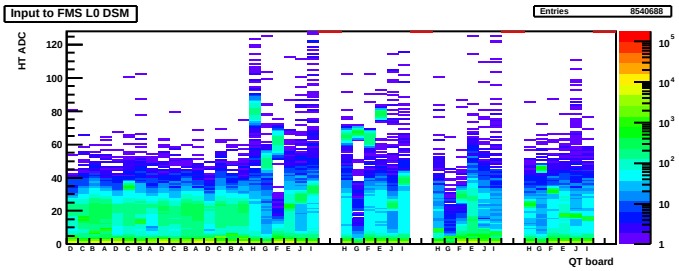

Input to FMS L0 DSM
Entries 47
QT8(1) sum - simulated
QT board

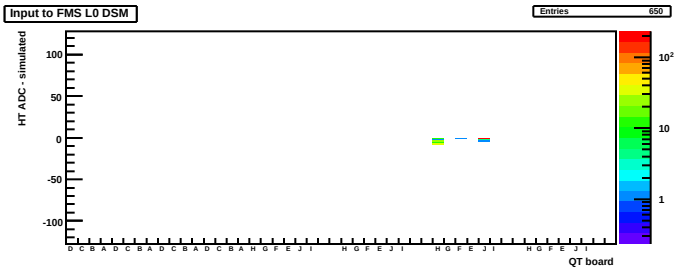

Input to FMS L0 DSM
Entries 8540688
QT8(2) sum
QT board

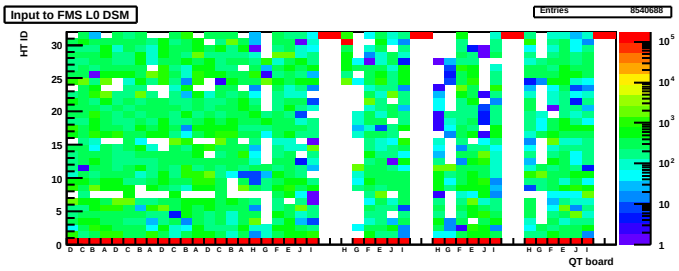

Input to FMS L0 DSM
Entries 126435
QT8(2) sum - simulated
QT board

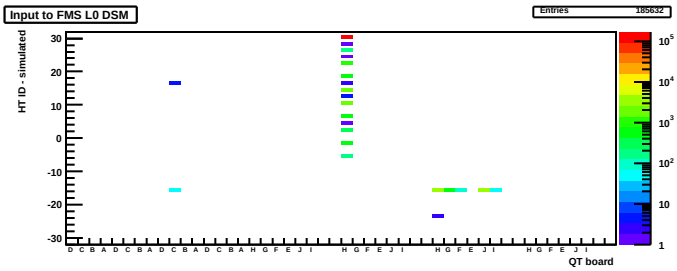

Input to FMS L0 DSM
Entries 8540688
QT8(3) sum
QT board

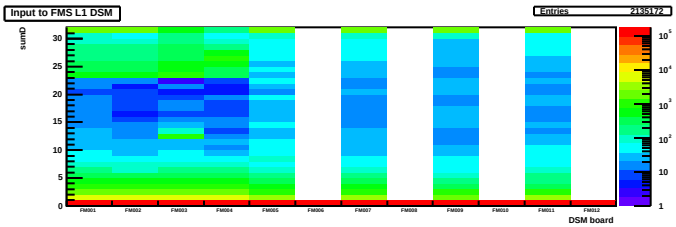

Input to FMS L0 DSM
Entries 100342
QT8(3) sum - simulated
QT board

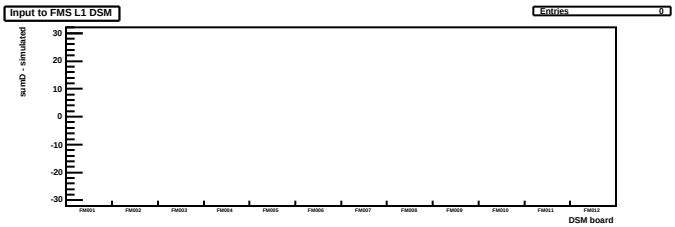

Input to FMS L0 DSM
Entries 8540688
HT ADC
QT board

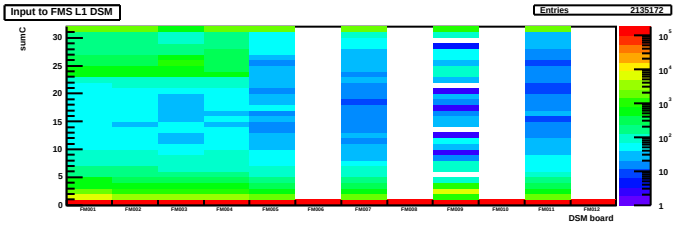

Input to FMS L0 DSM
Entries 650
HT ADC - simulated
QT board

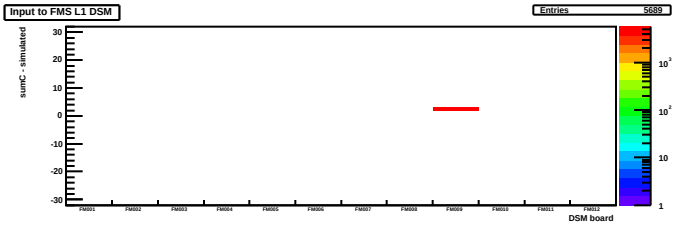

Input to FMS L0 DSM
Entries 8540688
HT ID
QT board

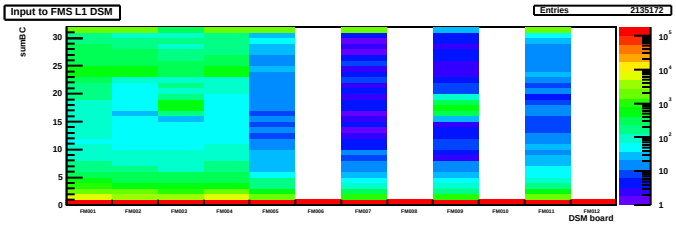

Input to FMS L0 DSM
Entries 185632
HT ID - simulated
QT board

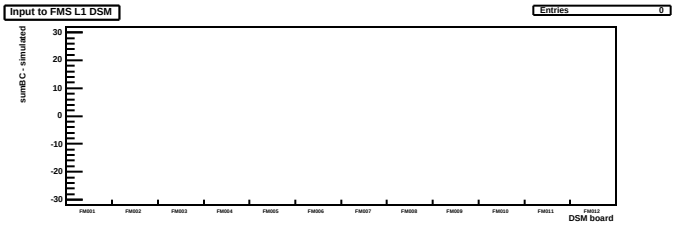

Input to FMS L1 DSM
Entries 2135172
sumD
DSM board

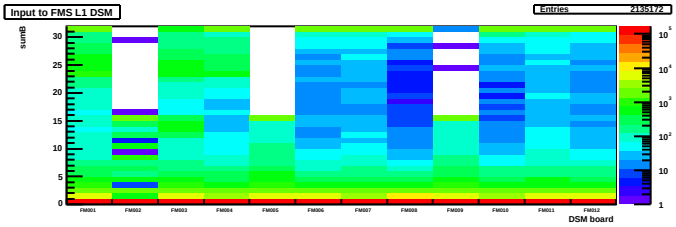

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

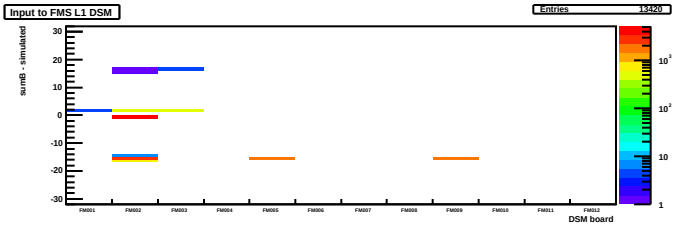

Input to FMS L1 DSM
Entries 2135172
sumC
DSM board

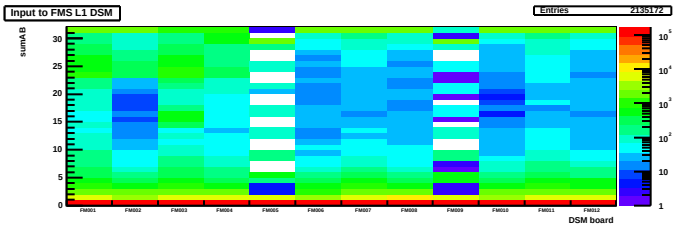

Input to FMS L1 DSM
Entries 5689
sumC - simulated
DSM board

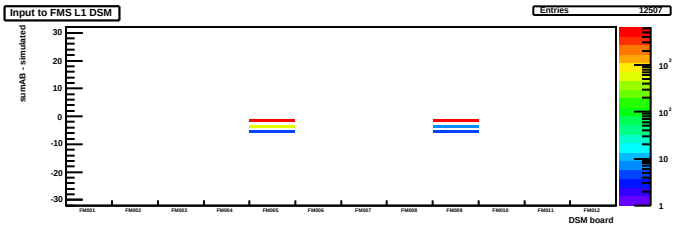

Input to FMS L1 DSM
Entries 2135172
sumBC
DSM board

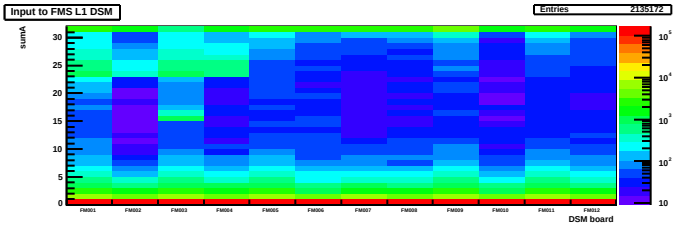

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

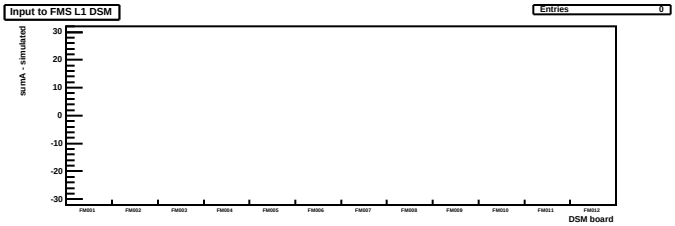

Input to FMS L1 DSM
Entries 2135172
sumB
DSM board

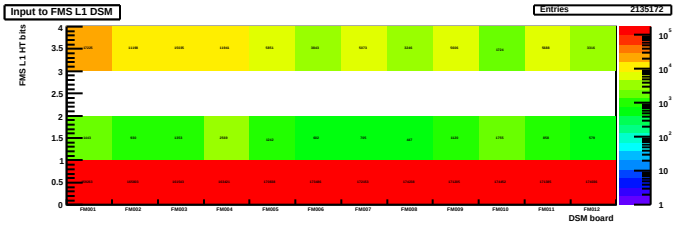

Input to FMS L1 DSM
Entries 13420
sumB - simulated
DSM board

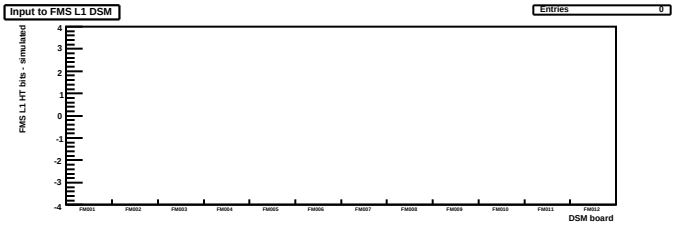

Input to FMS L1 DSM
Entries 2135172
sumAB
DSM board

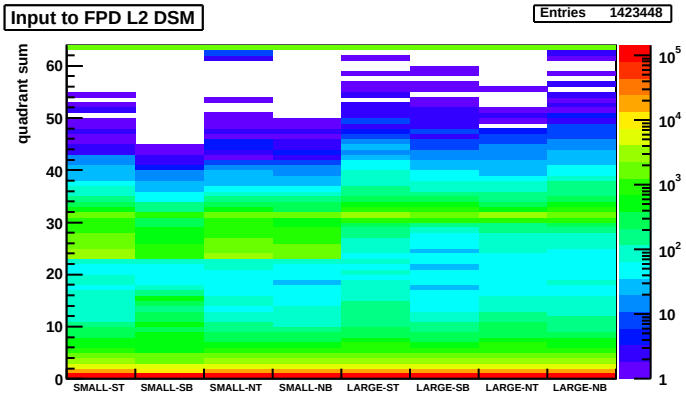

Input to FMS L1 DSM
Entries 12507
sumAB - simulated
DSM board

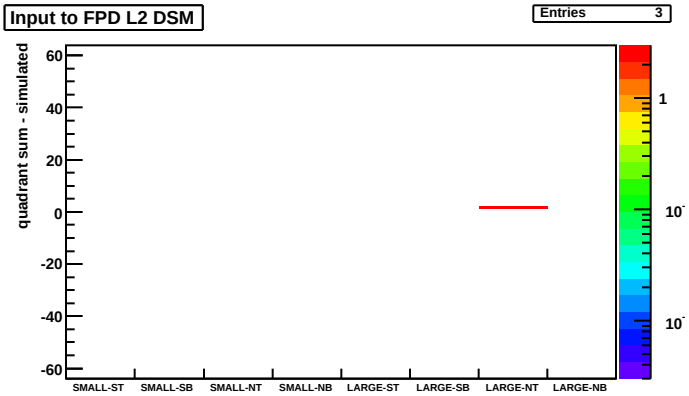

Input to FMS L1 DSM
Entries 2135172
sumA
DSM board

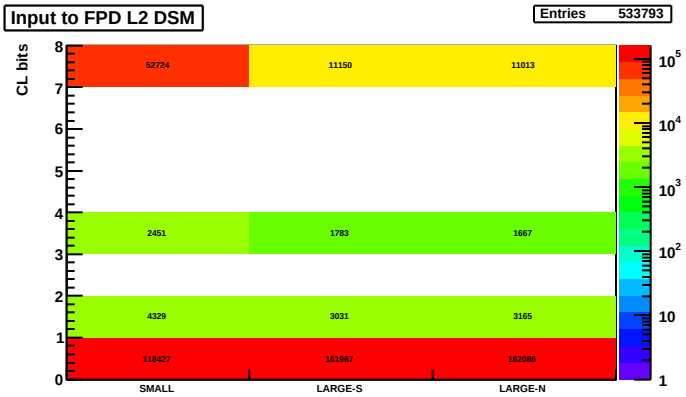

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

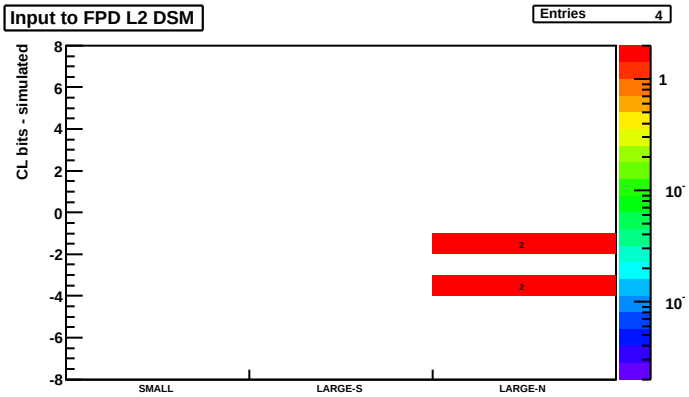

Input to FMS L1 DSM
Entries 2135172
FMS L1 HT bits
DSM board

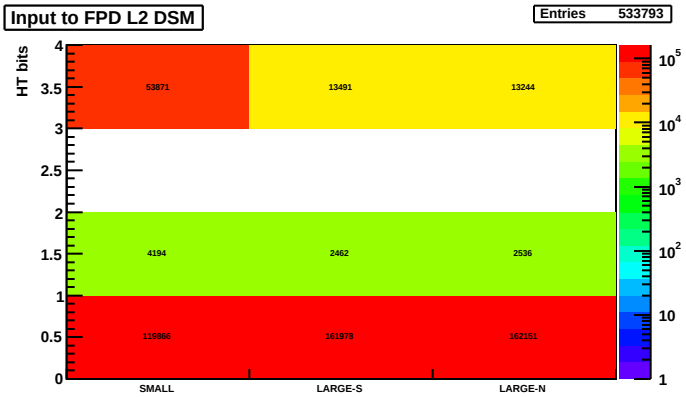

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

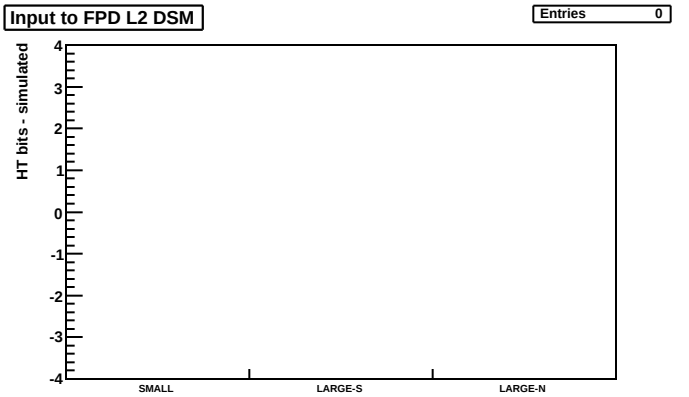

Input to FPD L2 DSM
Entries 1423448
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

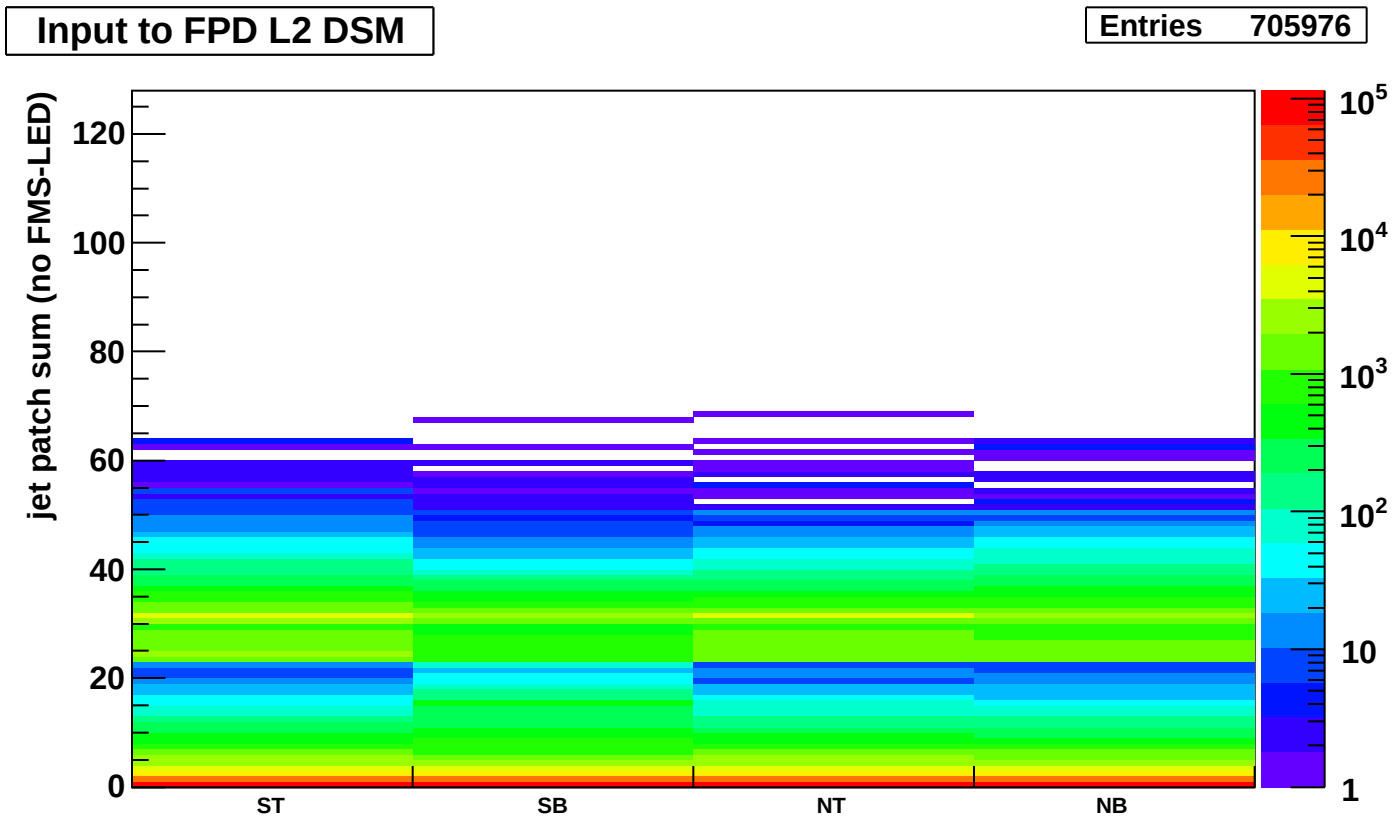

Input to FPD L2 DSM
Entries 3
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

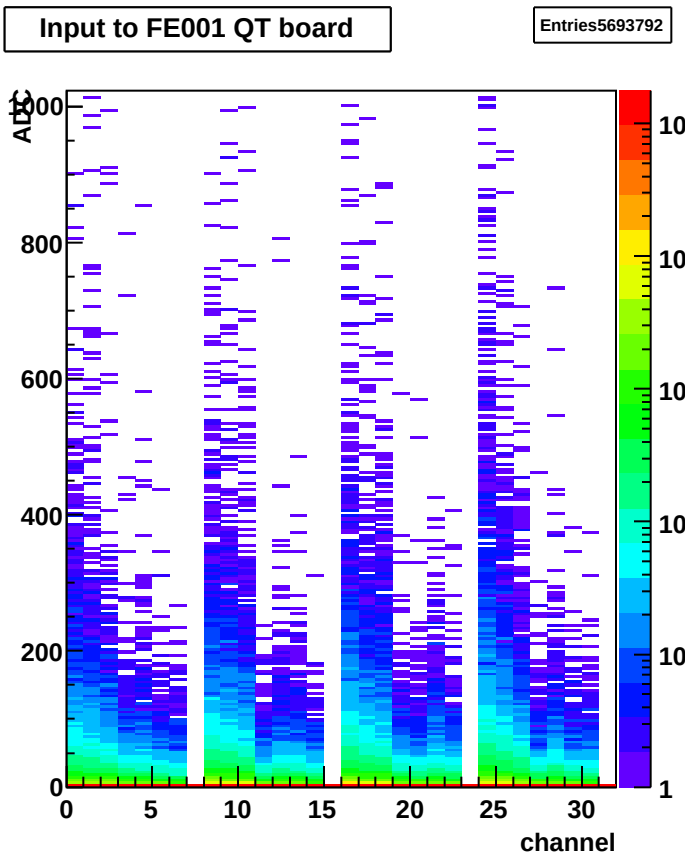

Input to FPD L2 DSM
Entries 533793
CL bits
52724
11150
11013
2451
1783
1667
4329
3031
3165
118427
161967
162086
SMALL
LARGE-S
LARGE-N

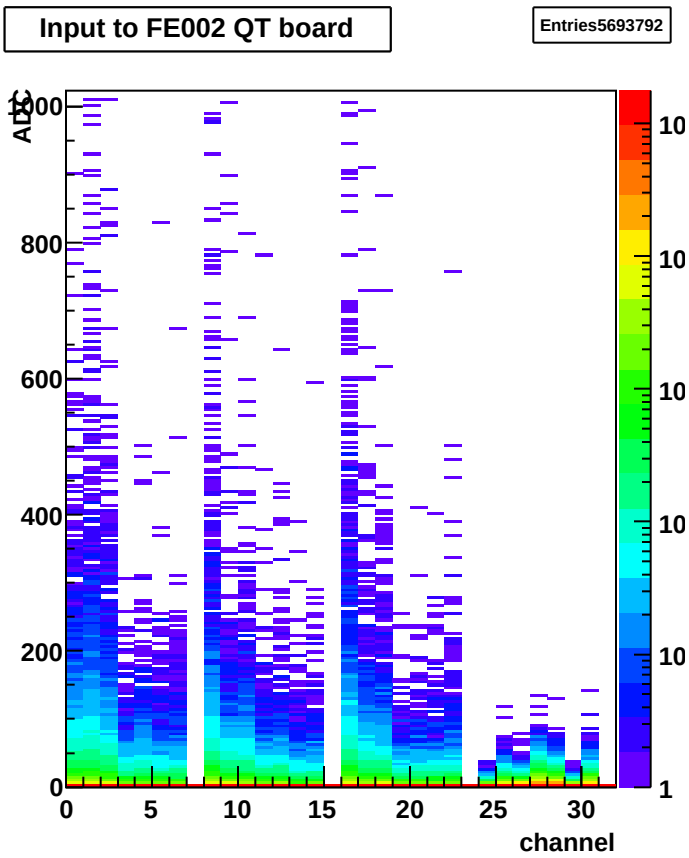

Input to FPD L2 DSM
Entries 4
CL bits - simulated
2
2
SMALL
LARGE-S
LARGE-N

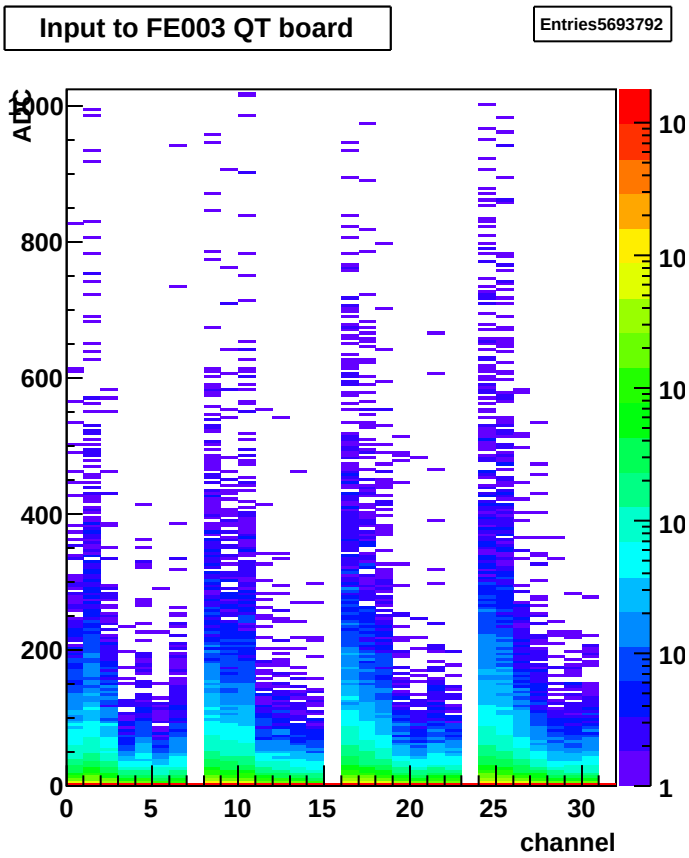

Input to FPD L2 DSM
Entries 533793
HT bits
53871
13491
13244
4194
2462
2536
119866
161978
162151
SMALL
LARGE-S
LARGE-N

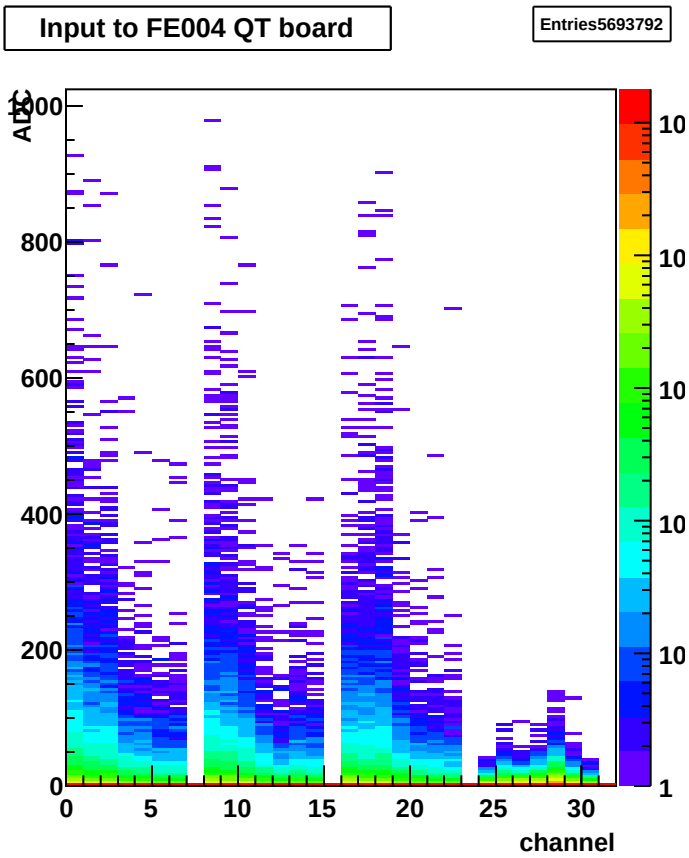

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

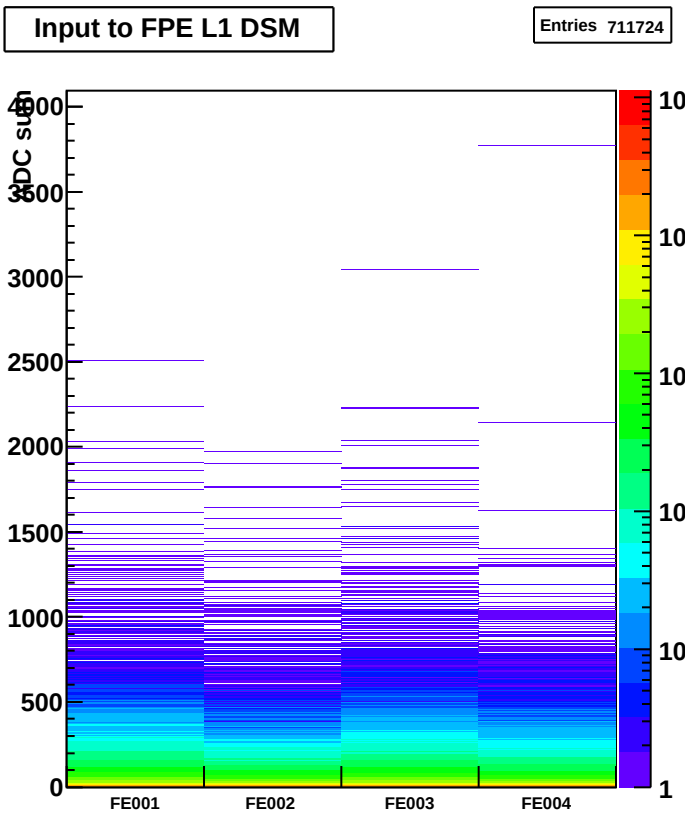

Input to FPD L2 DSM
Entries 705976
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

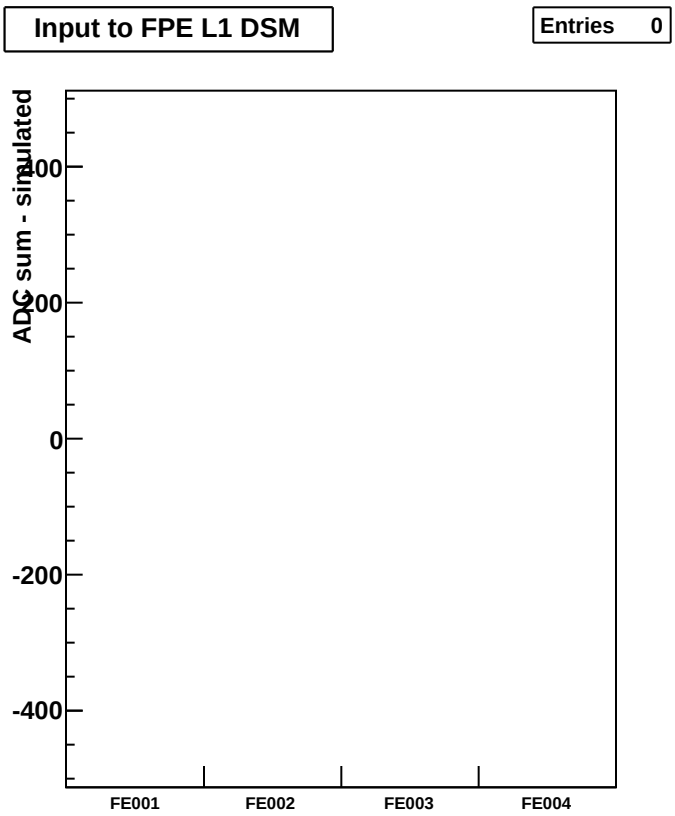

Input to FE001 QT board
Entries5693792
ADC
channel

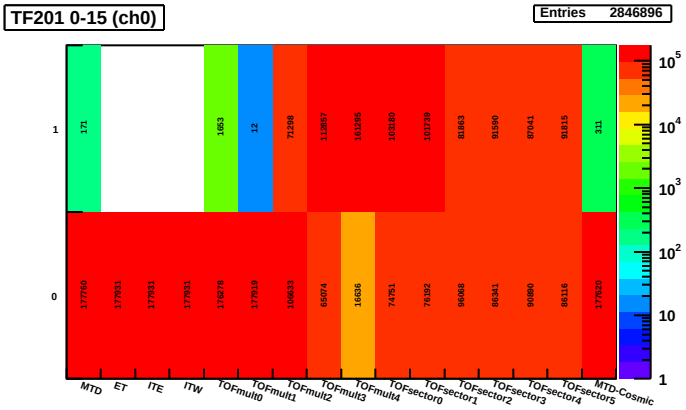

Input to FE002 QT board
Entries5693792
ADC
channel

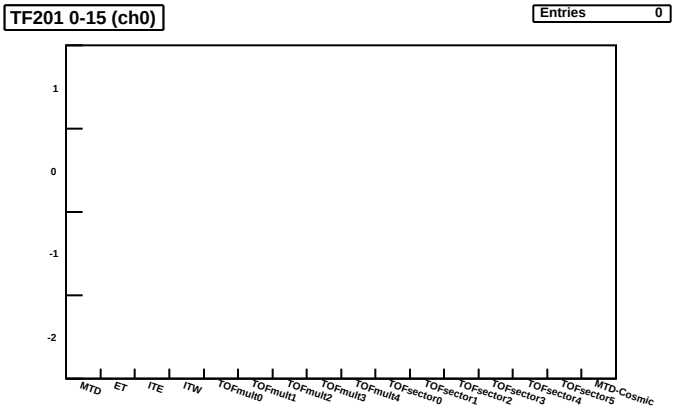

Input to FE003 QT board
Entries5693792
ADC
channel

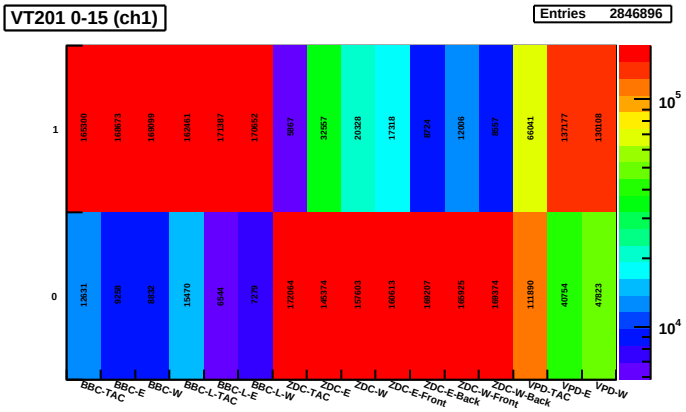

Input to FE004 QT board
Entries5693792
ADC
channel

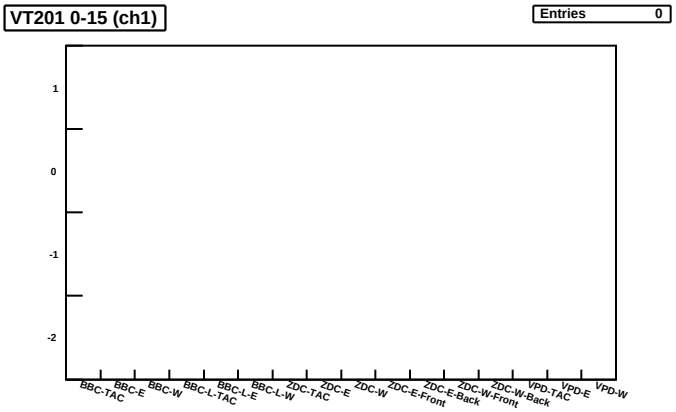

Input to FPE L1 DSM
Entries 711724
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

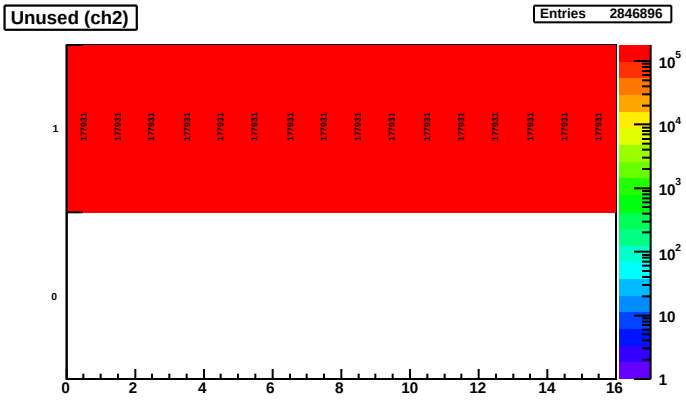

Input to FPE L1 DSM
Entries 0
ADC sum - simulated
100
0
-200
-400
FE001
FE002
FE003
FE004

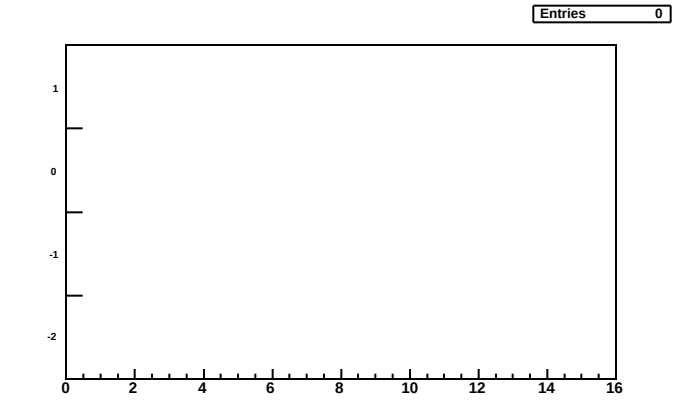

TF201 0-15 (ch0)
Entries 2846896
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

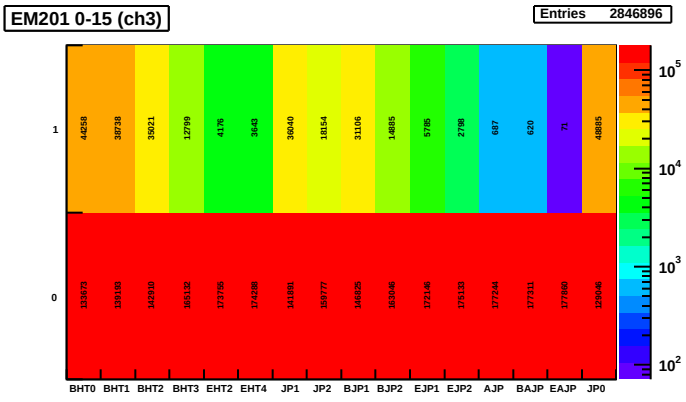

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

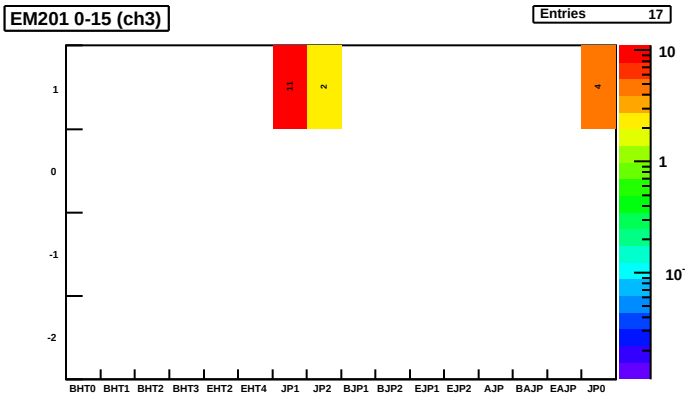

VT201 0-15 (ch1)
Entries 2846896
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

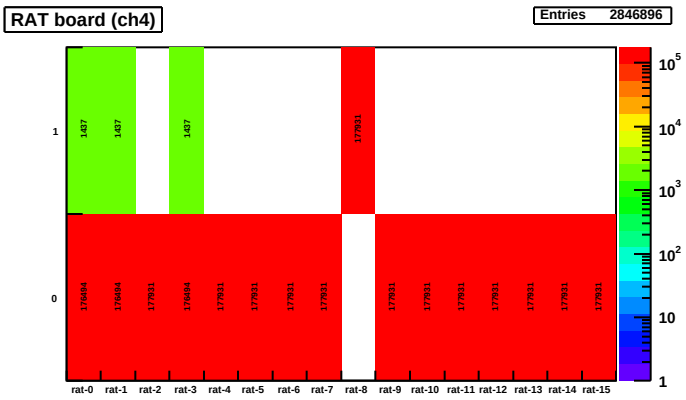

VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

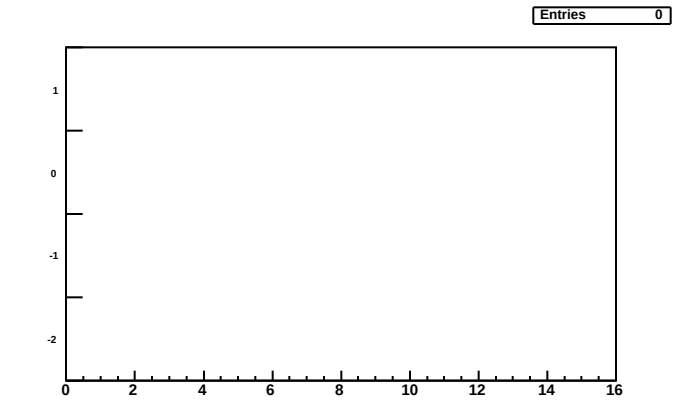

Unused (ch2)
Entries 2846896

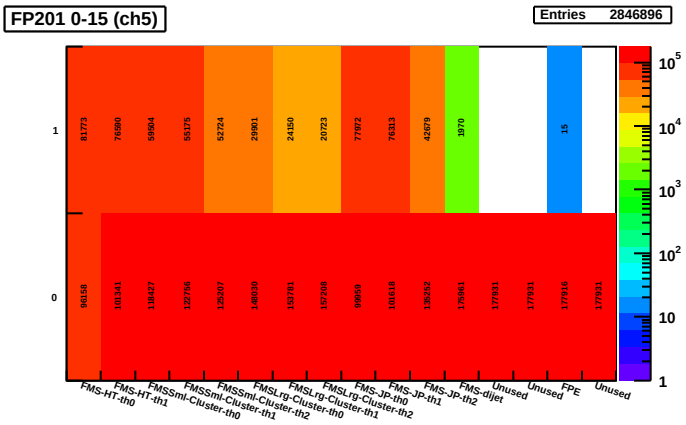

Entries 0

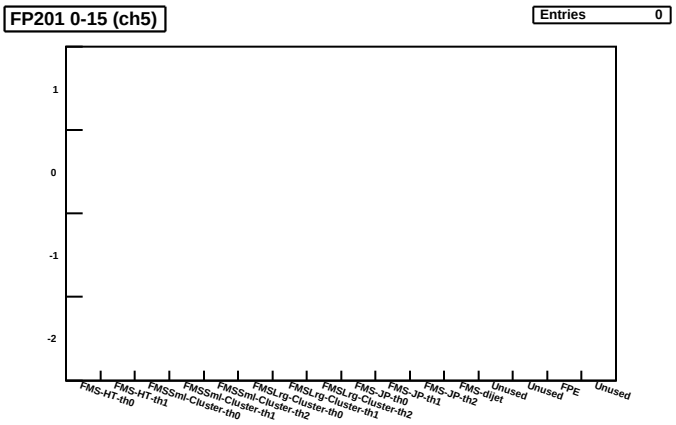

EM201 0-15 (ch3)
Entries 2846896
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

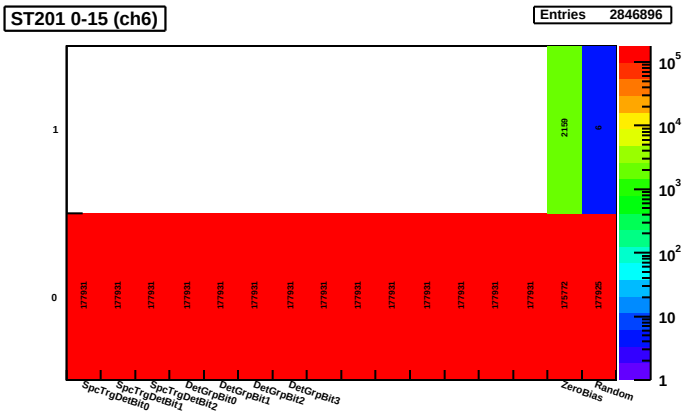

EM201 0-15 (ch3)
Entries 17
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

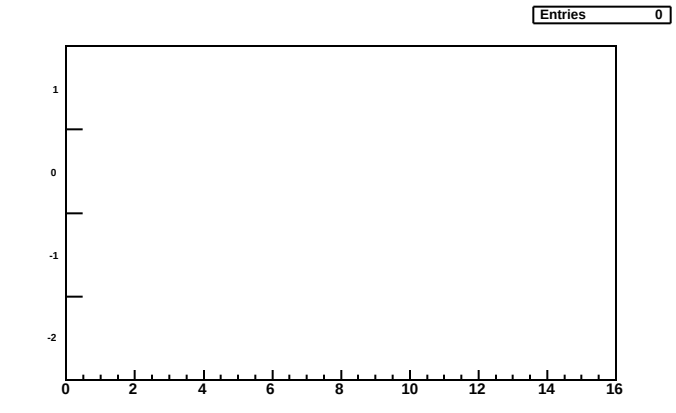

RAT board (ch4)
Entries 2846896
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

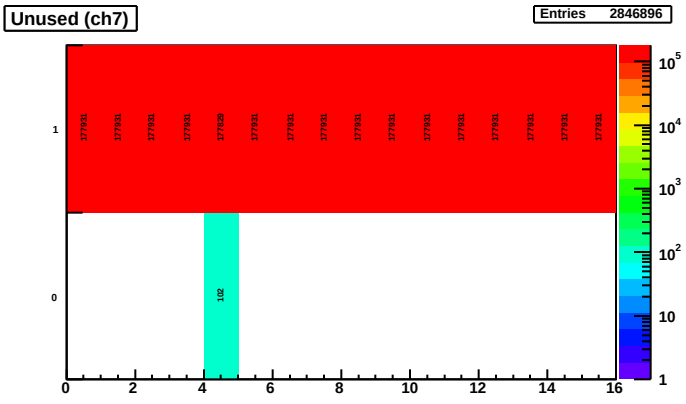

Entries 0

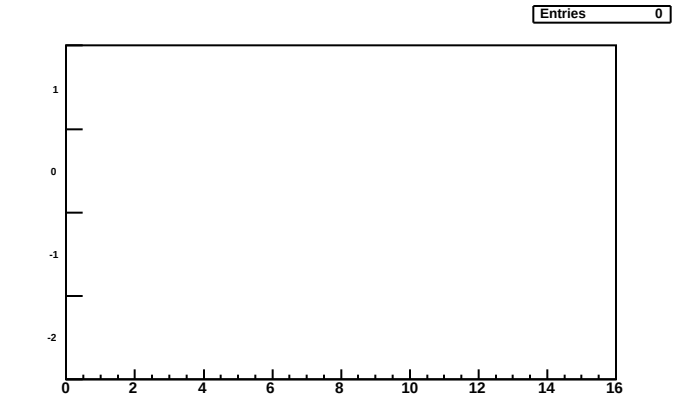

FP201 0-15 (ch5)
Entries 2846896
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused

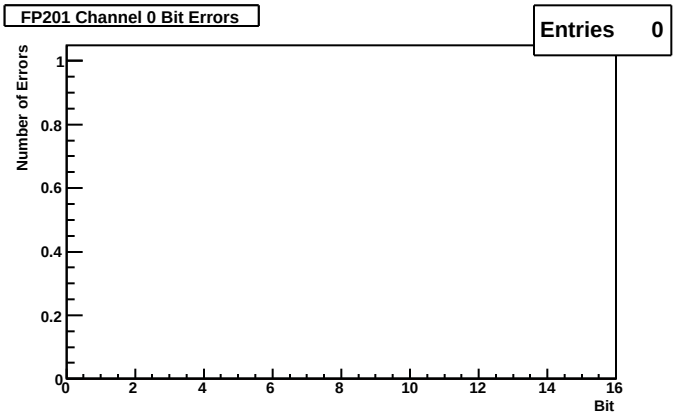

FP201 0-15 (ch5)
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused

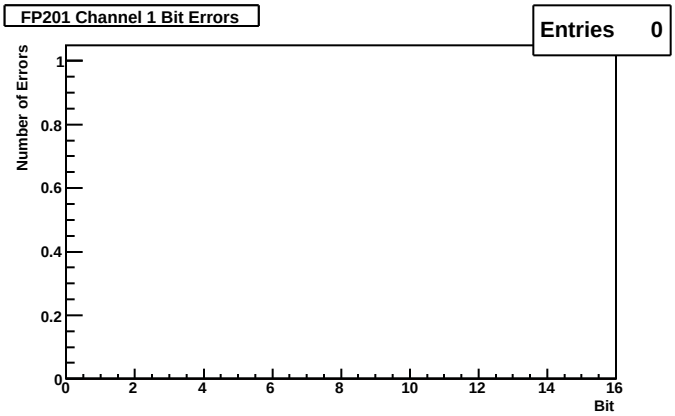

ST201 0-15 (ch6)
Entries 2846896
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

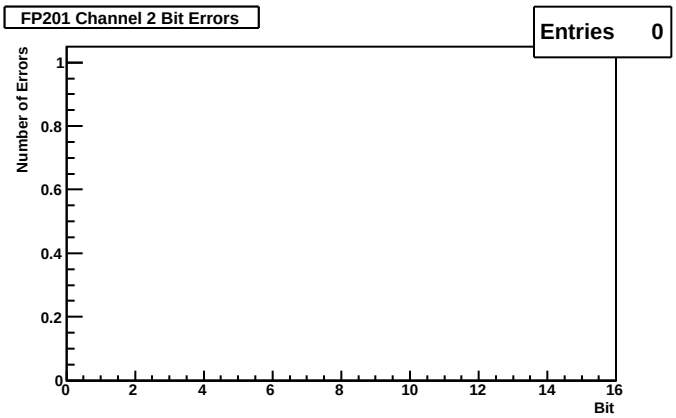

Entries 0

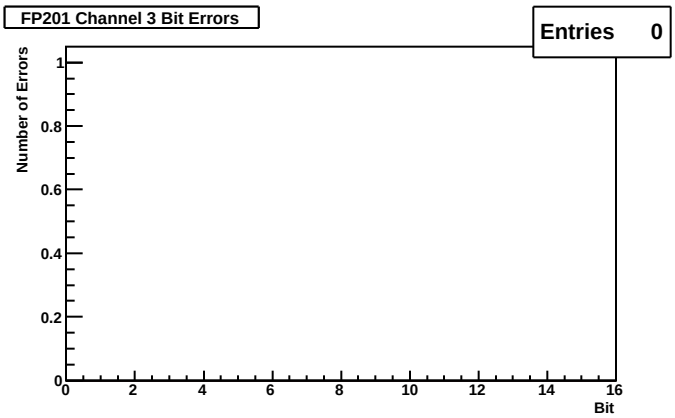

Unused (ch7)
Entries 2846896

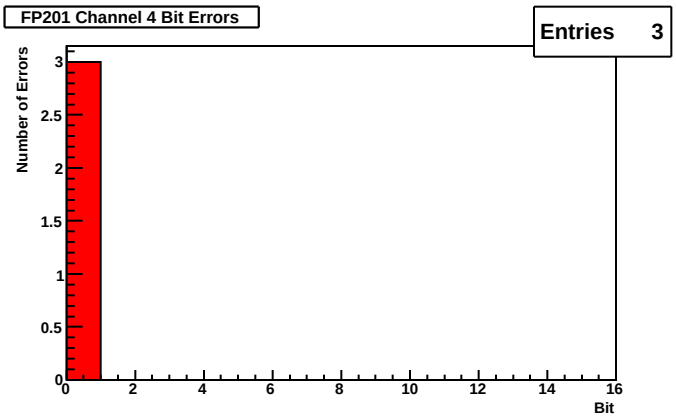

Entries 0

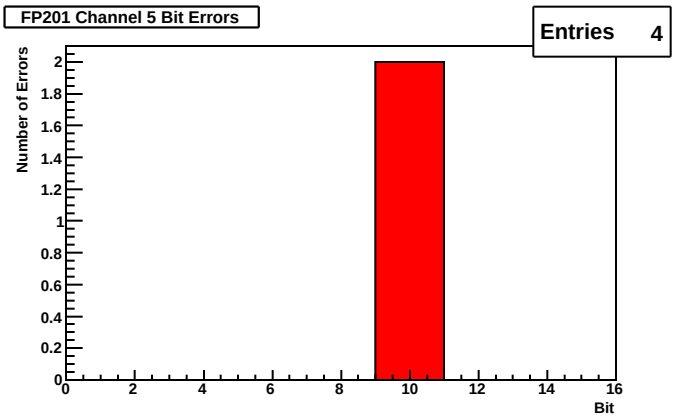

FP201 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

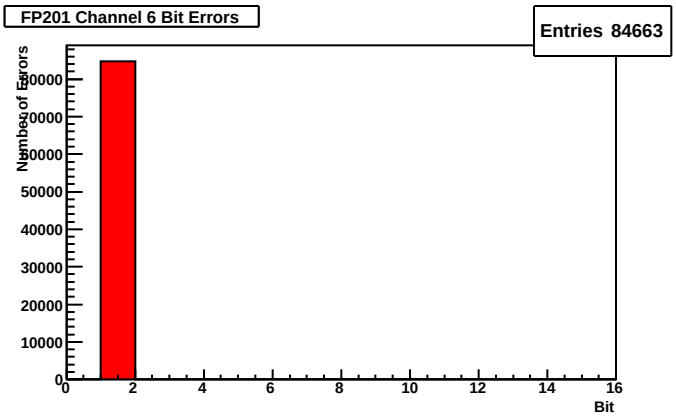

FP201 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

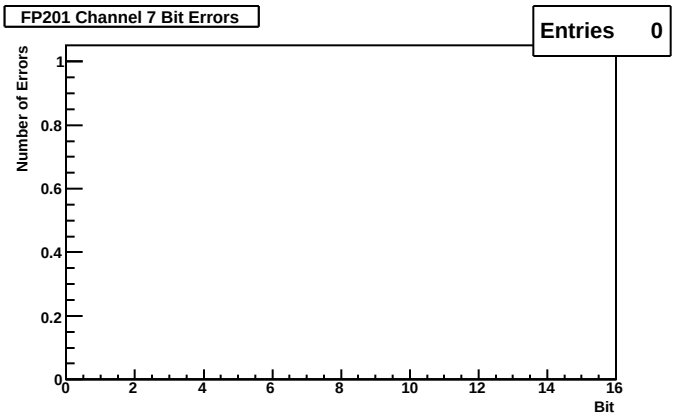

FP201 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

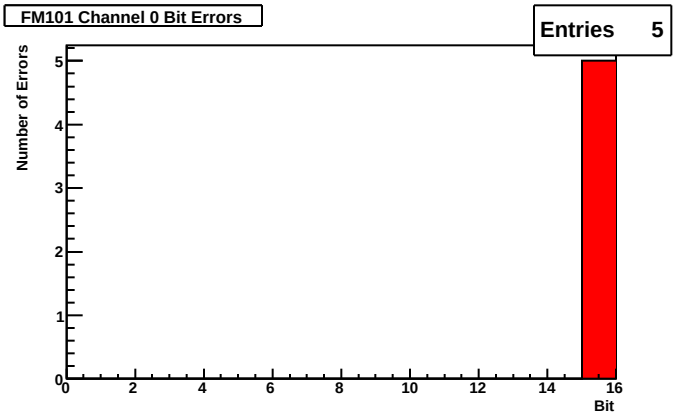

FP201 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

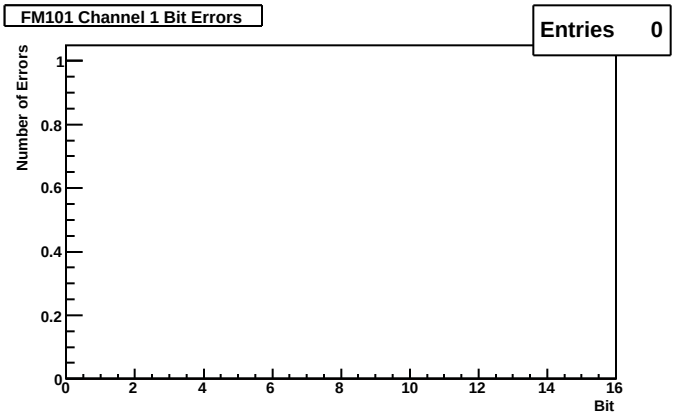

FP201 Channel 4 Bit Errors
Entries 3
Number of Errors
Bit

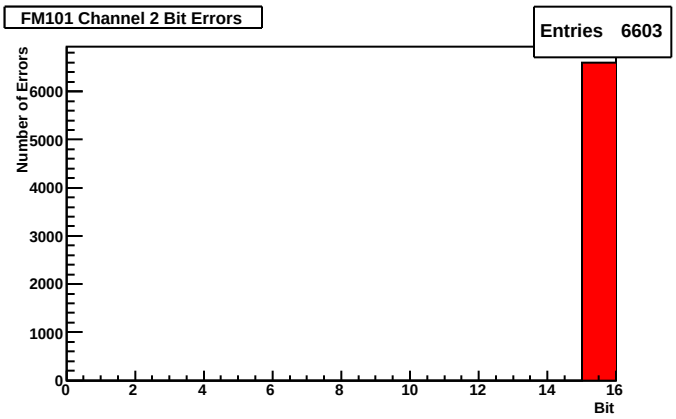

FP201 Channel 5 Bit Errors
Entries 4
Number of Errors
Bit

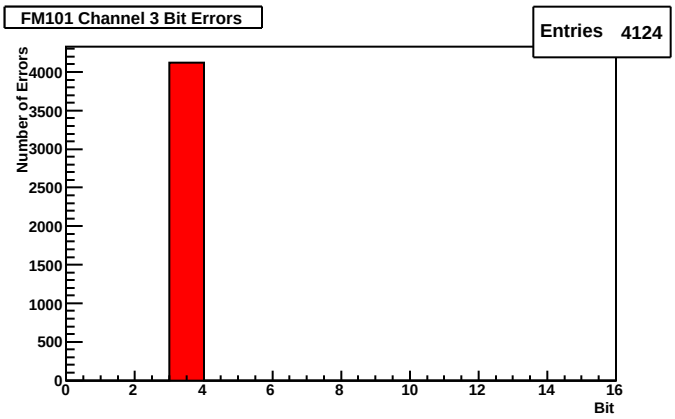

FP201 Channel 6 Bit Errors
Entries 84663
Number of Errors
Bit

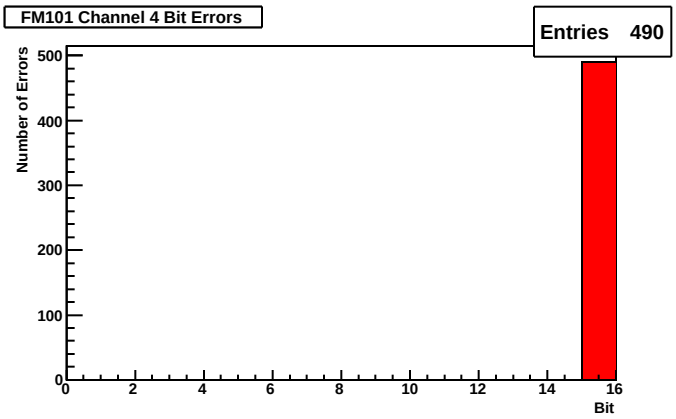

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

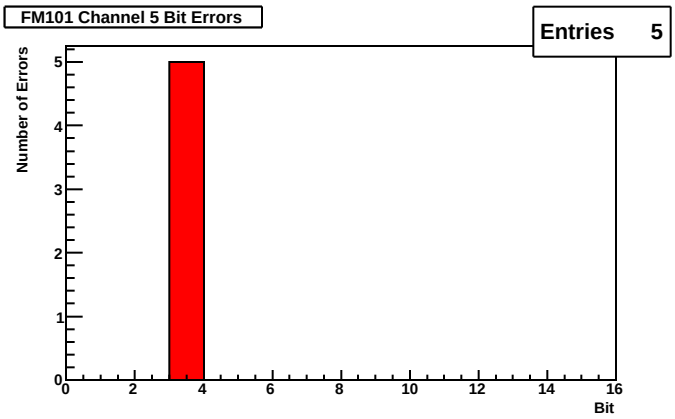

FM101 Channel 0 Bit Errors
Entries 5
Number of Errors
Bit

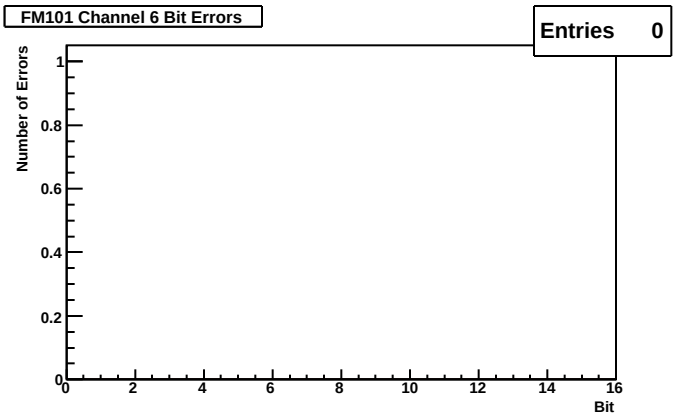

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

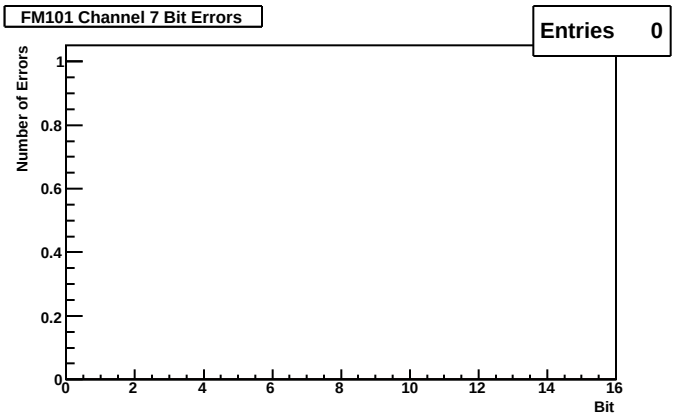

FM101 Channel 2 Bit Errors
Entries 6603
Number of Errors
Bit

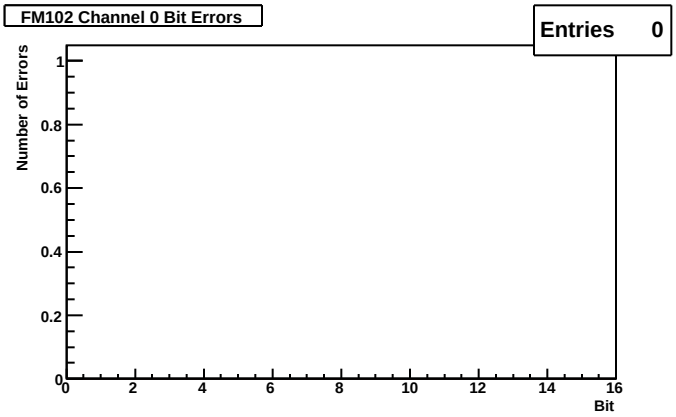

FM101 Channel 3 Bit Errors
Entries 4124
Number of Errors
Bit

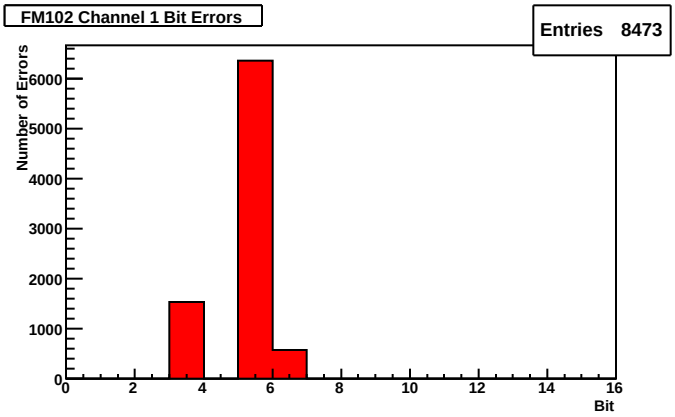

FM101 Channel 4 Bit Errors
Entries 490
Number of Errors
Bit

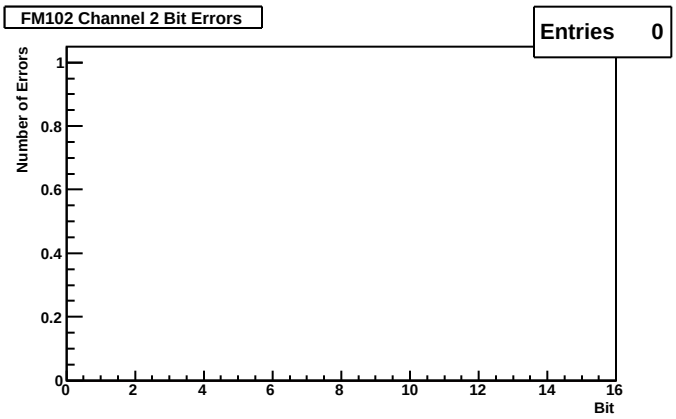

FM101 Channel 5 Bit Errors
Entries 5
Number of Errors
Bit

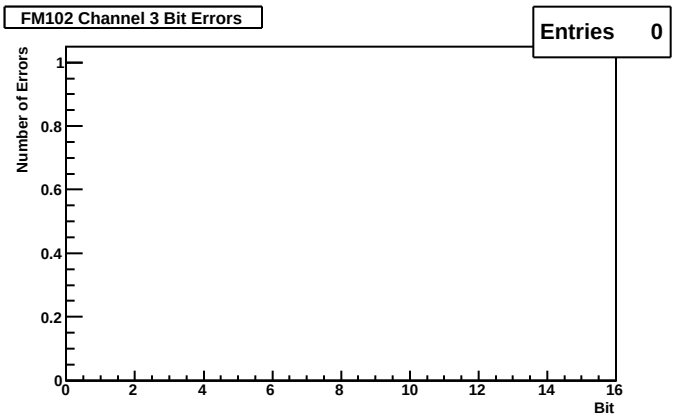

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

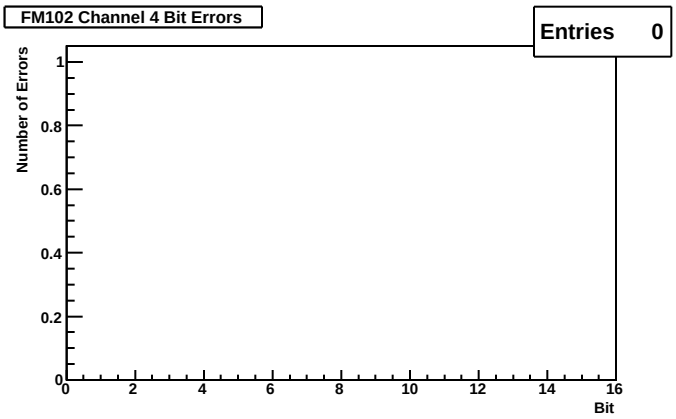

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

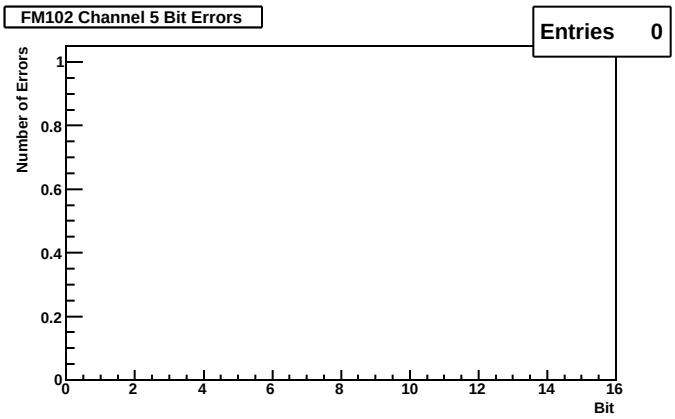

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

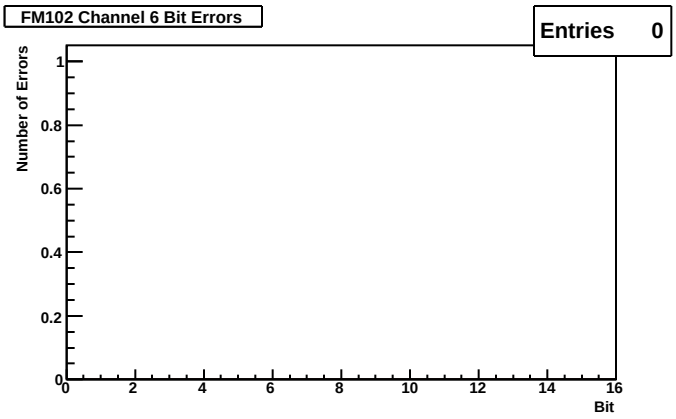

FM102 Channel 1 Bit Errors
Entries 8473
Number of Errors
Bit

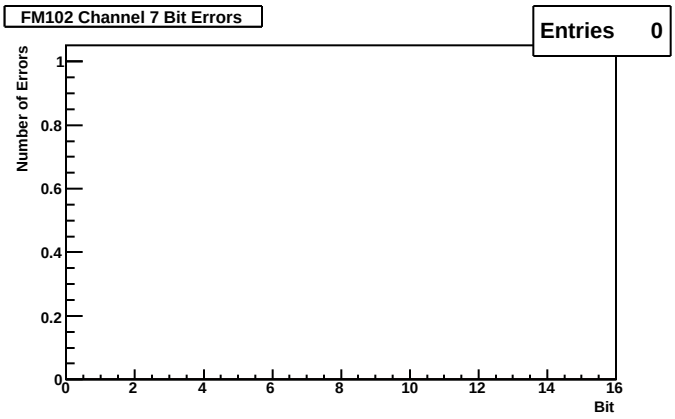

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

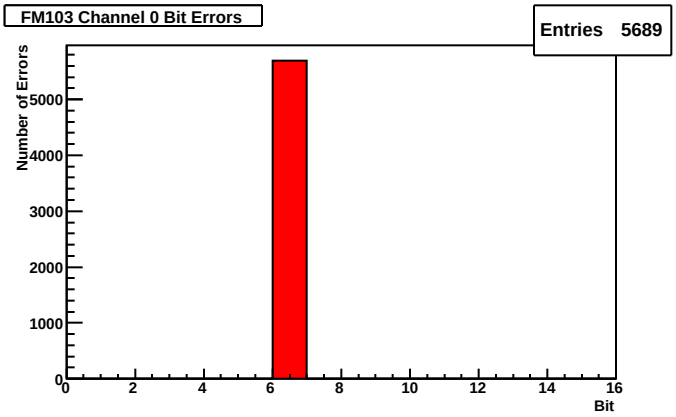

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

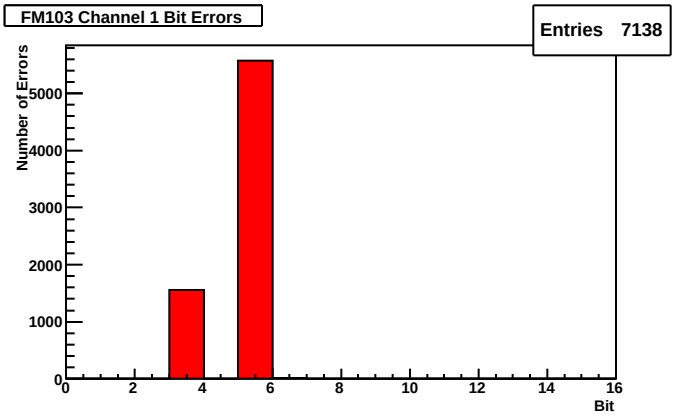

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

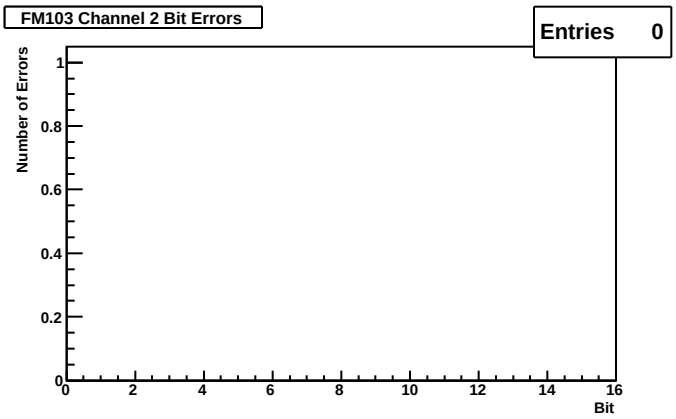

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

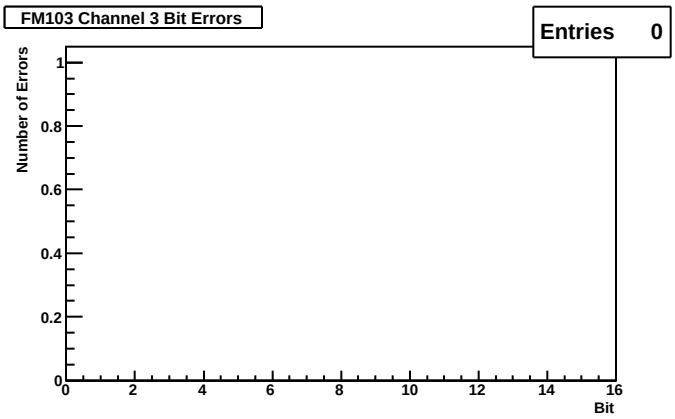

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

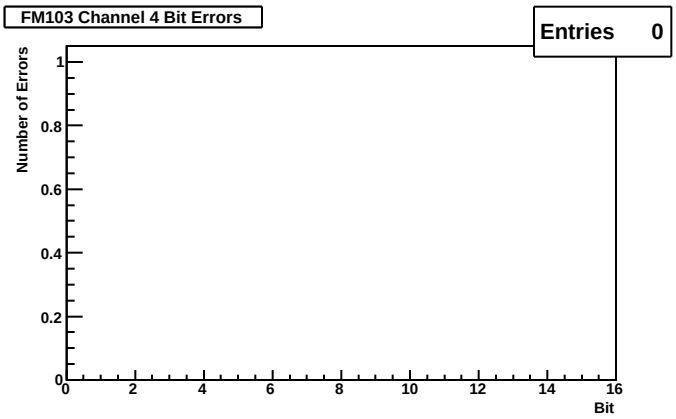

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

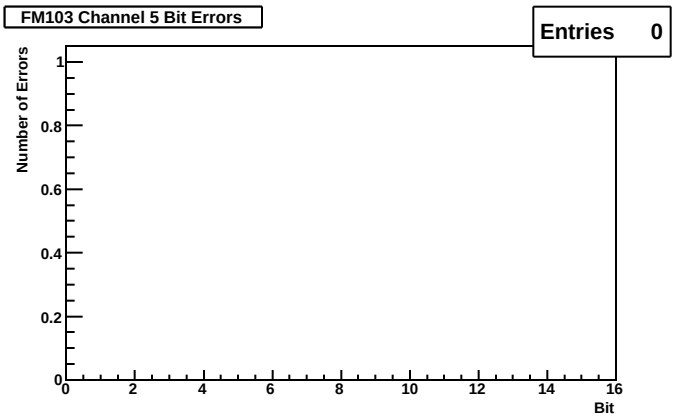

FM103 Channel 0 Bit Errors
Entries 5689
Number of Errors
Bit

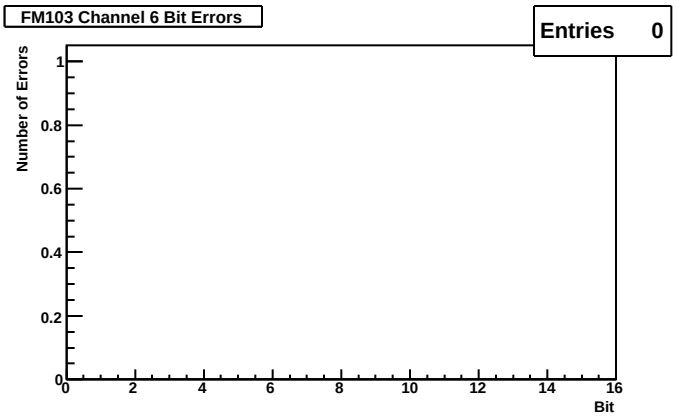

FM103 Channel 1 Bit Errors
Entries 7138
Number of Errors
Bit

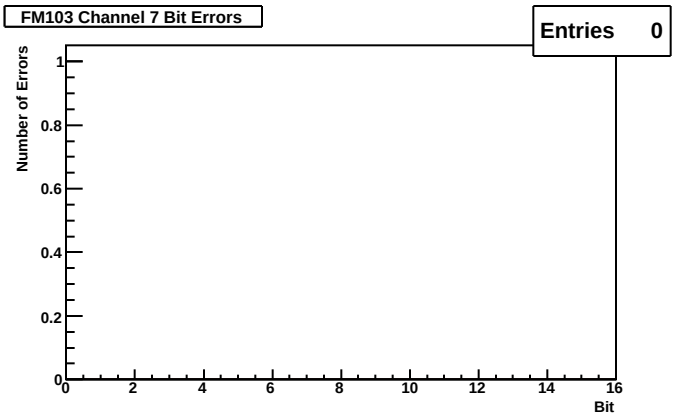

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

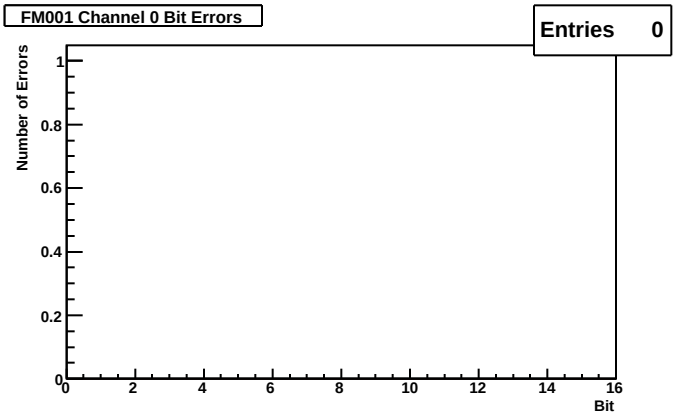

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

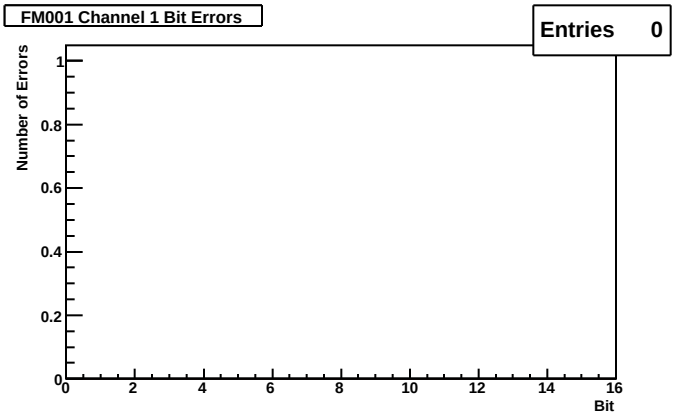

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

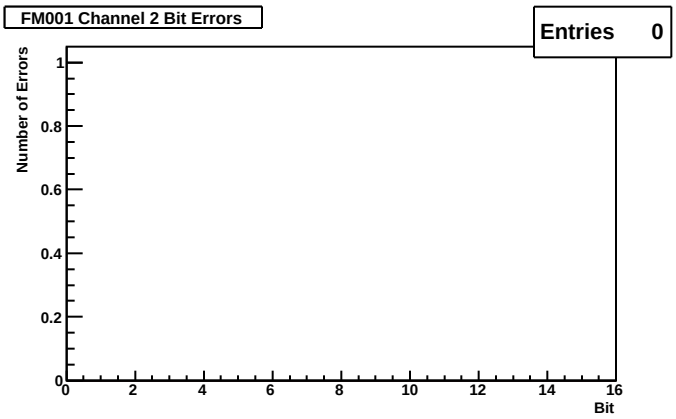

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

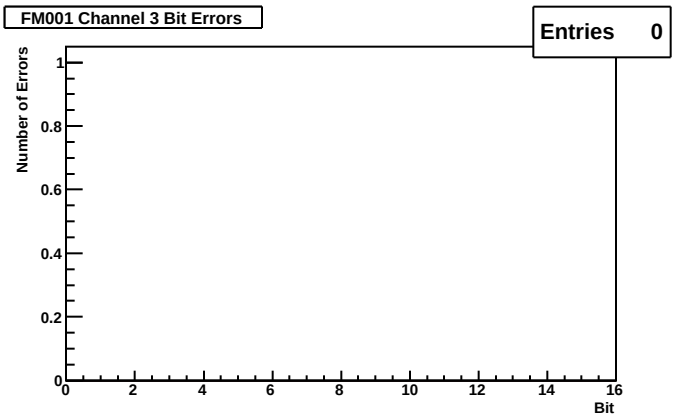

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

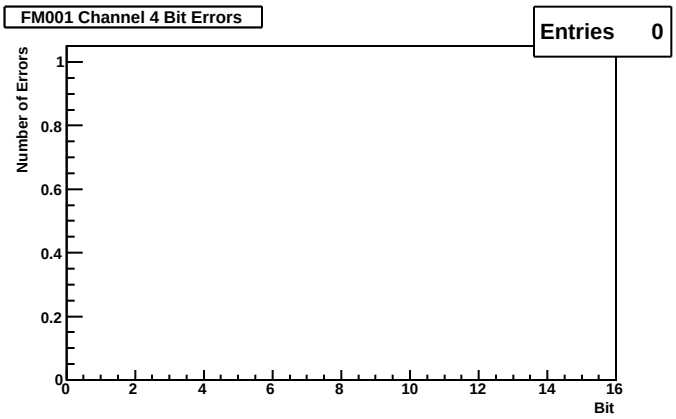

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

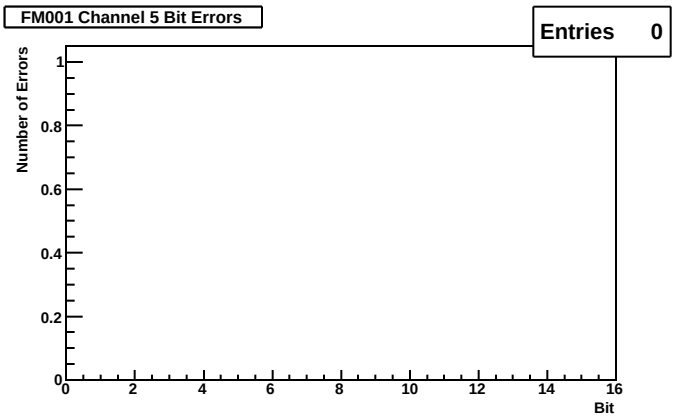

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

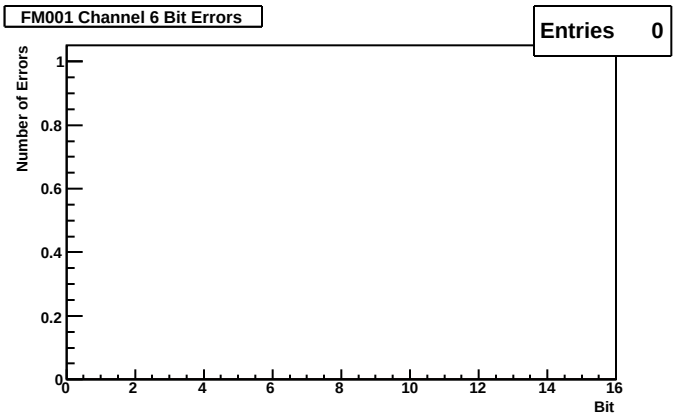

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

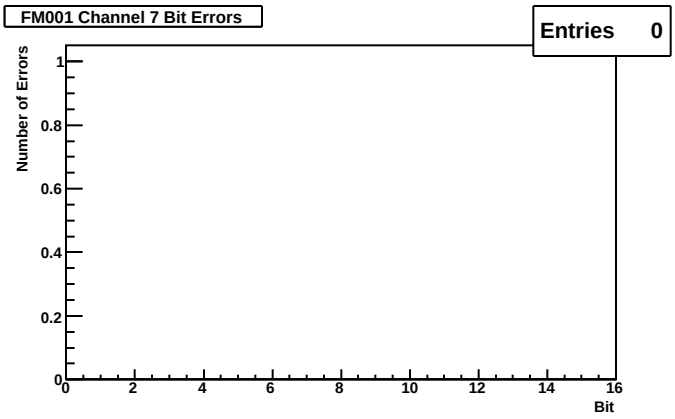

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

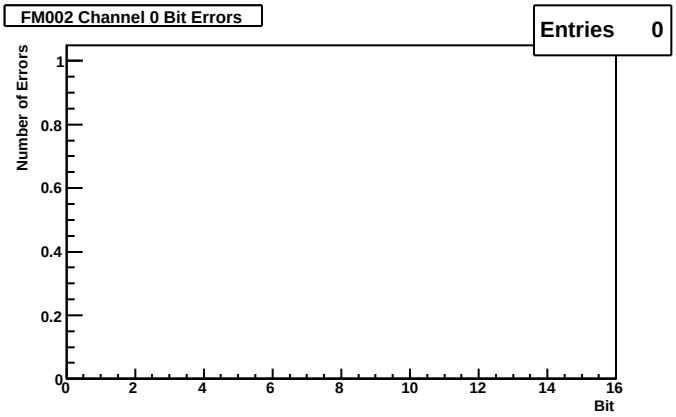

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

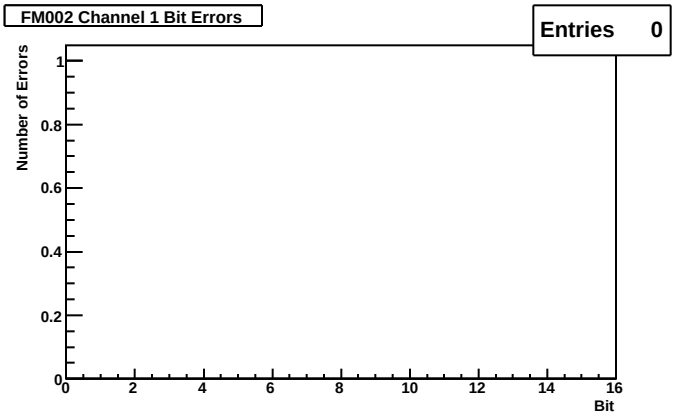

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

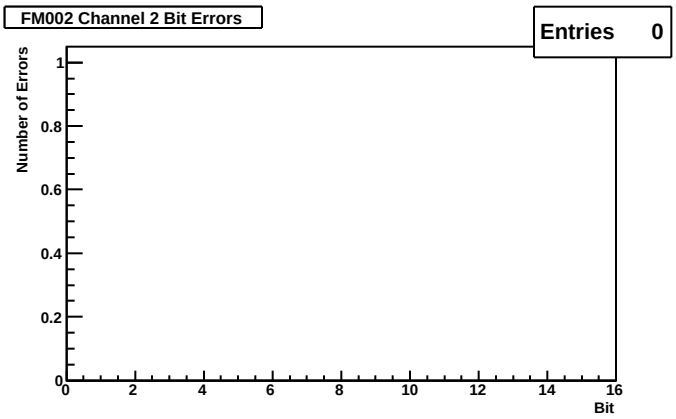

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

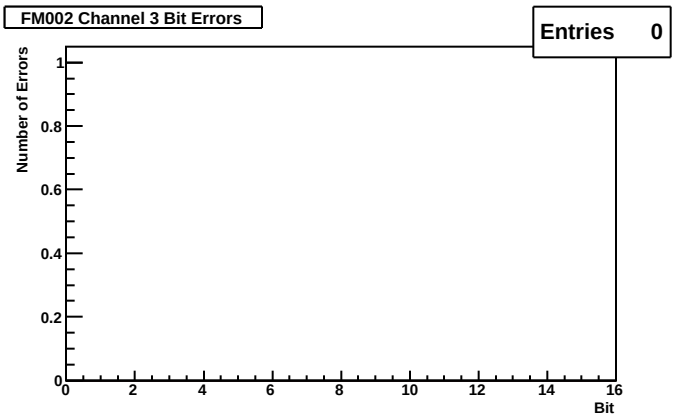

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

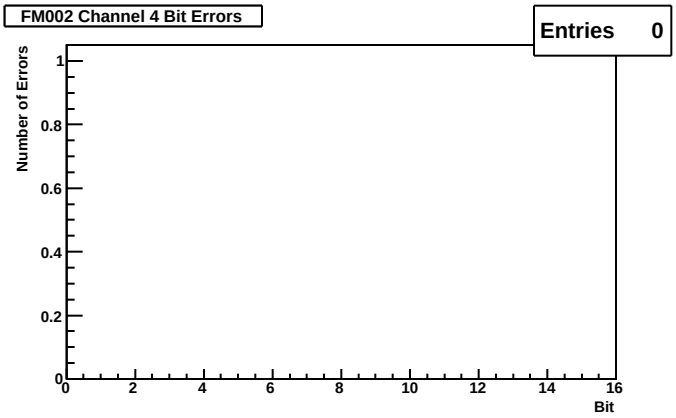

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

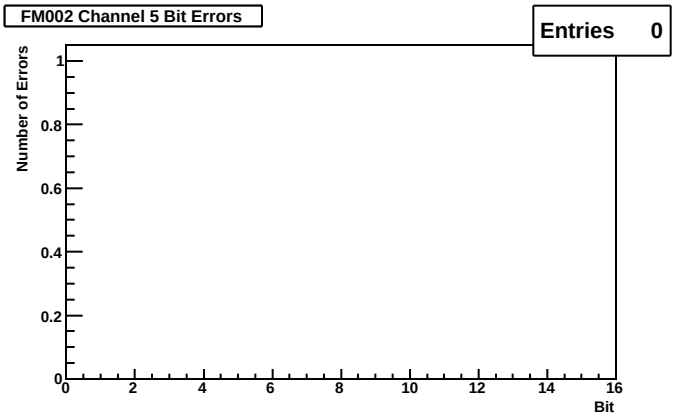

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

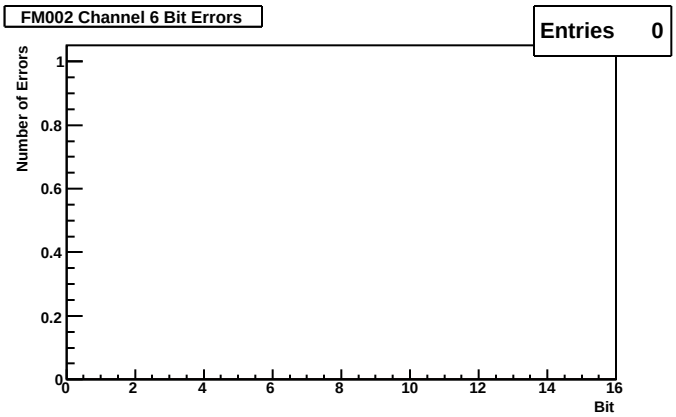

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

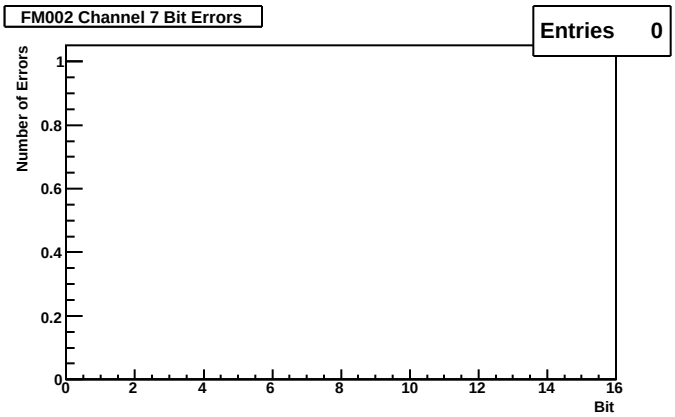

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

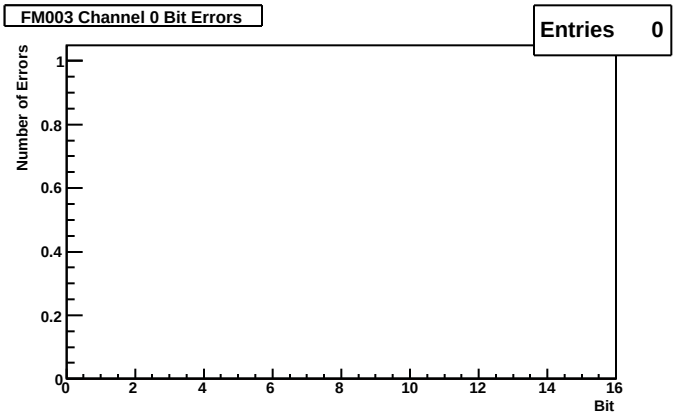

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

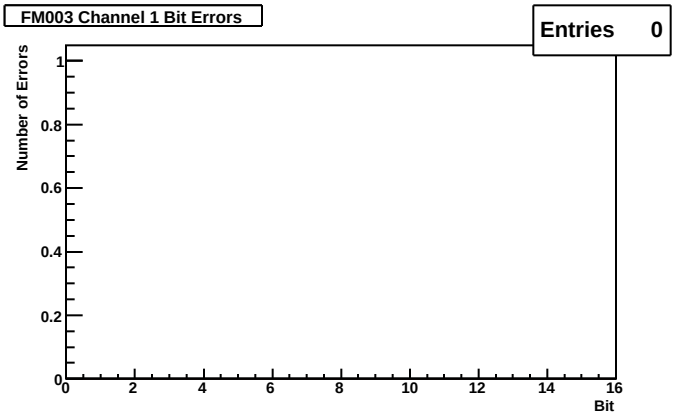

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

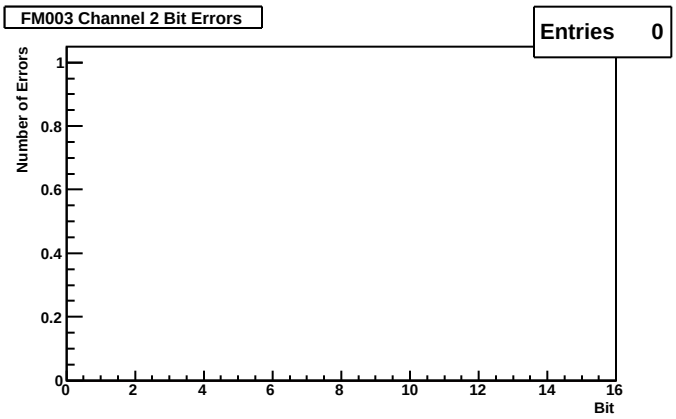

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

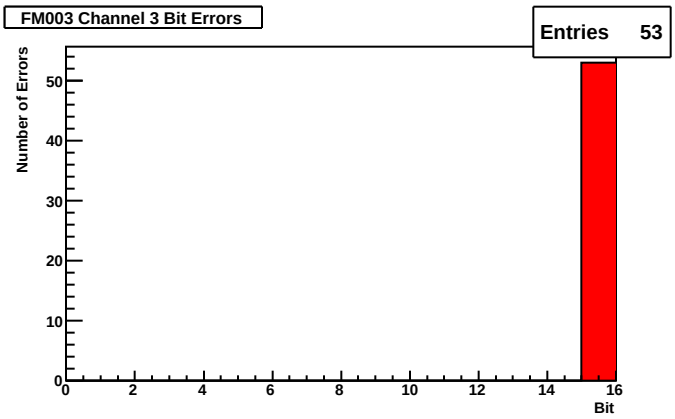

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

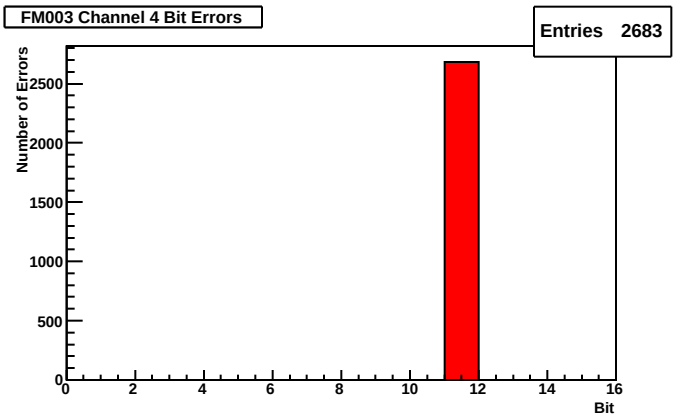

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

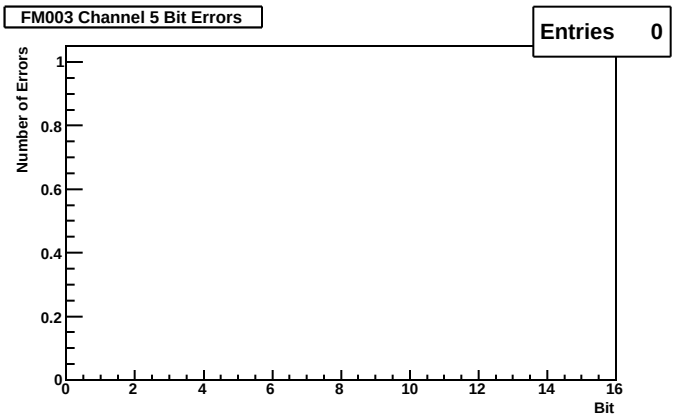

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

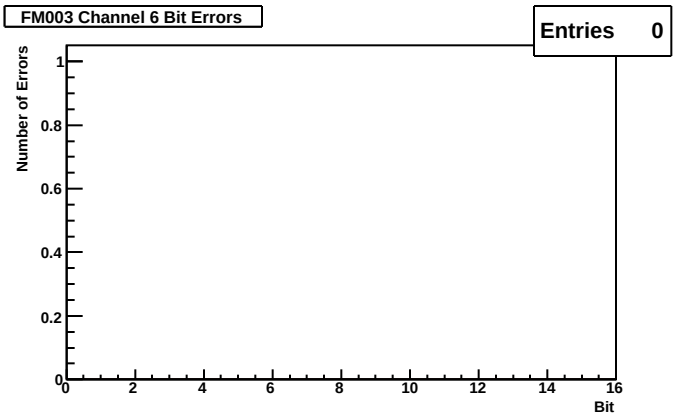

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

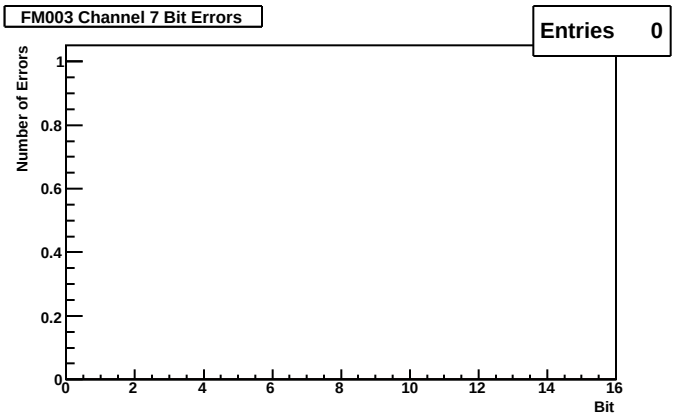

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

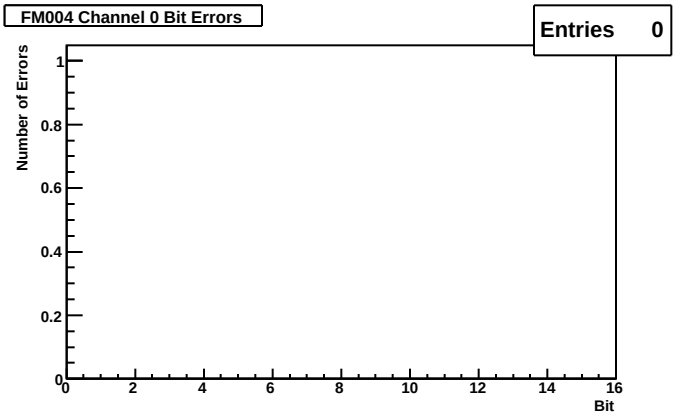

FM003 Channel 3 Bit Errors
Entries 53
Number of Errors
Bit

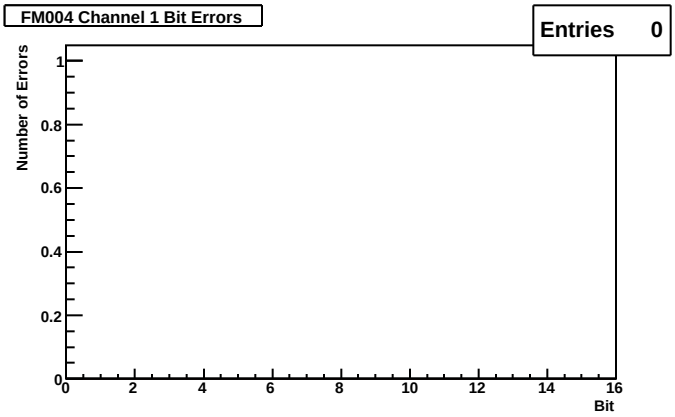

FM003 Channel 4 Bit Errors
Entries 2683
Number of Errors
Bit

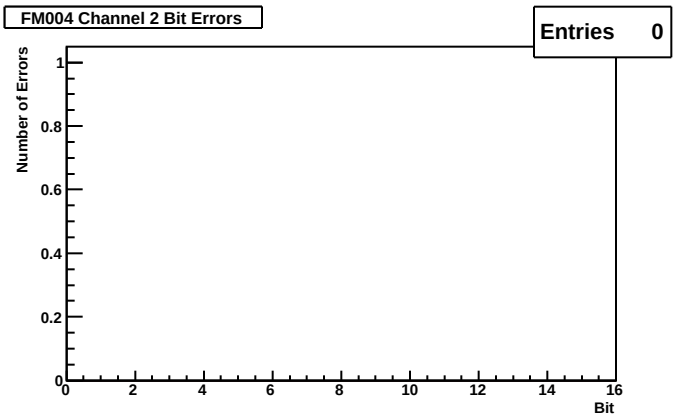

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit


FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit


FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit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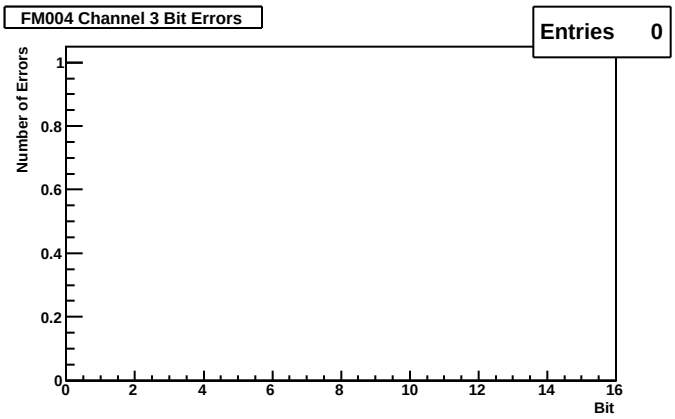
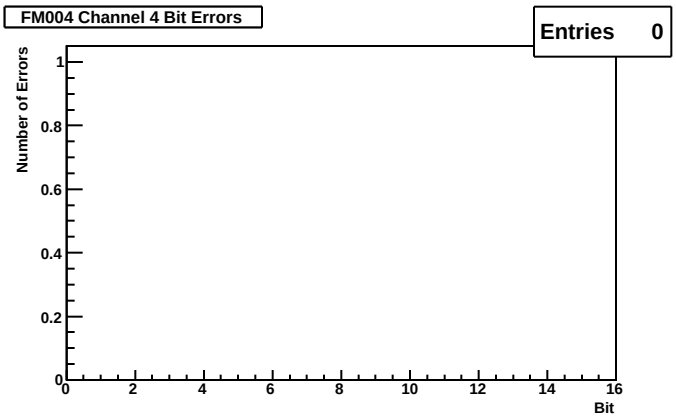
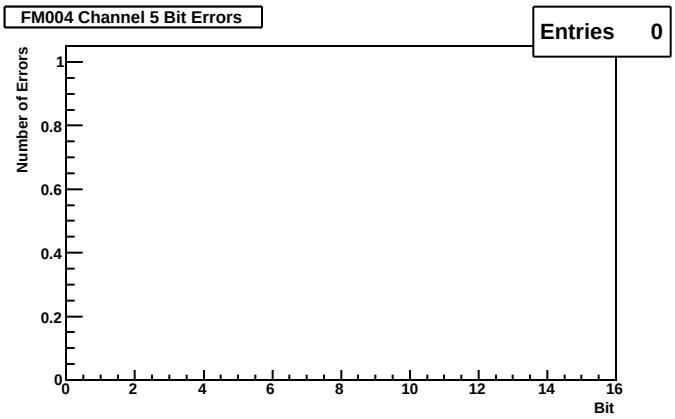
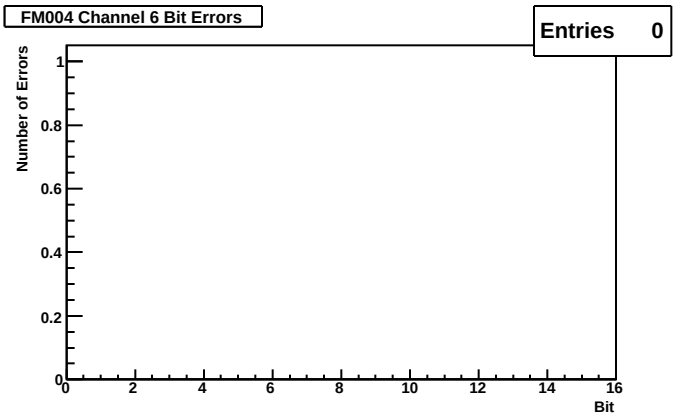
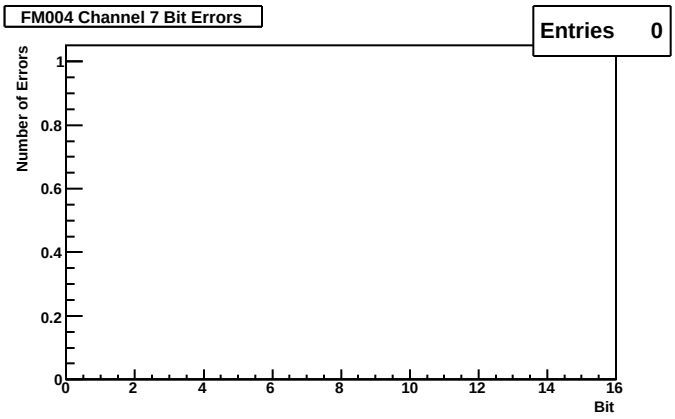
Entries 0
Number of Errors
Bit

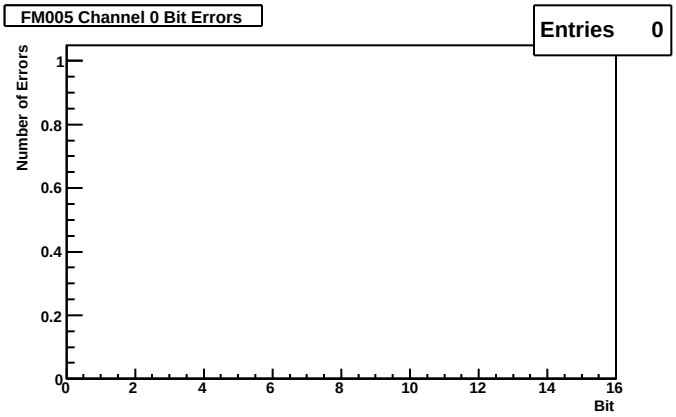

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

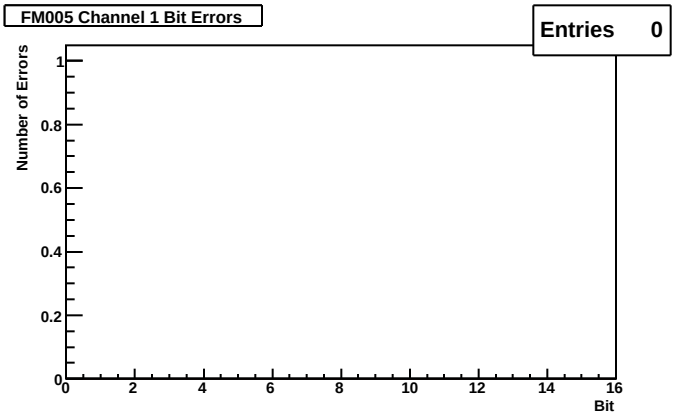

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

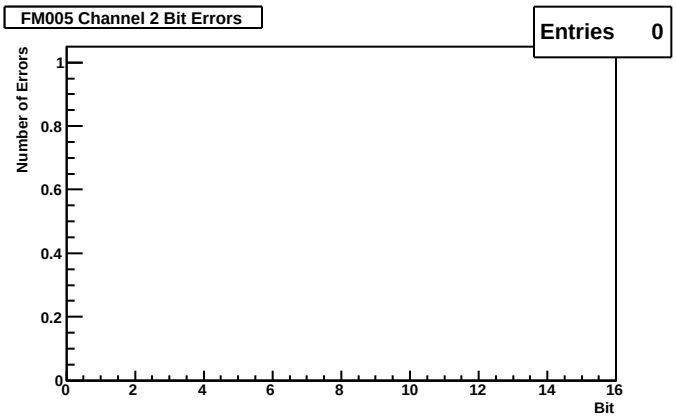

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

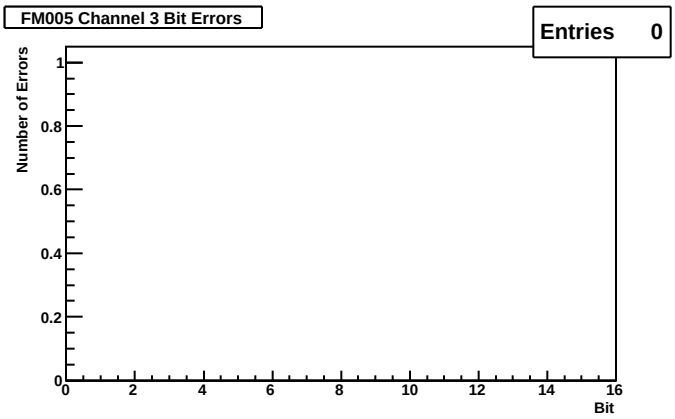

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

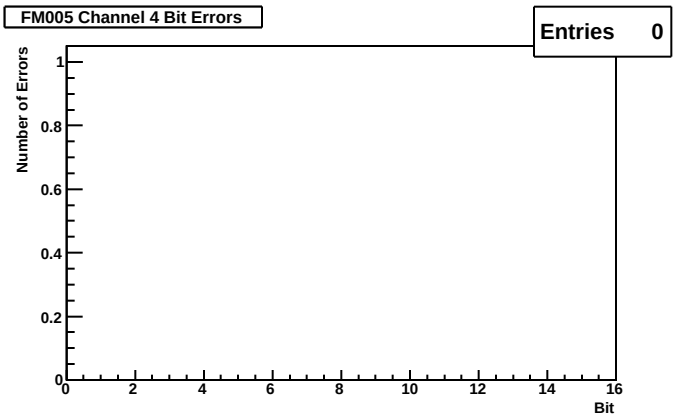

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

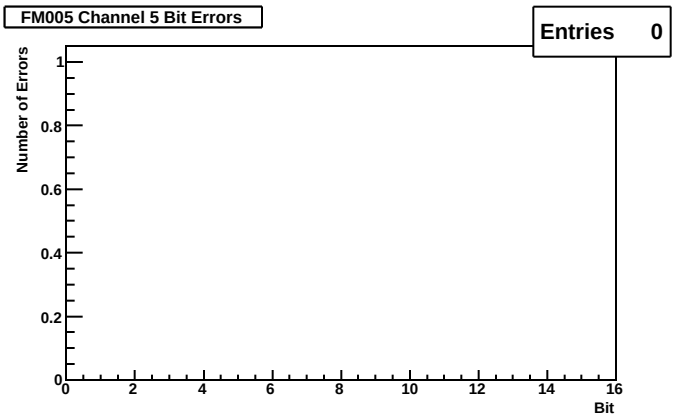

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

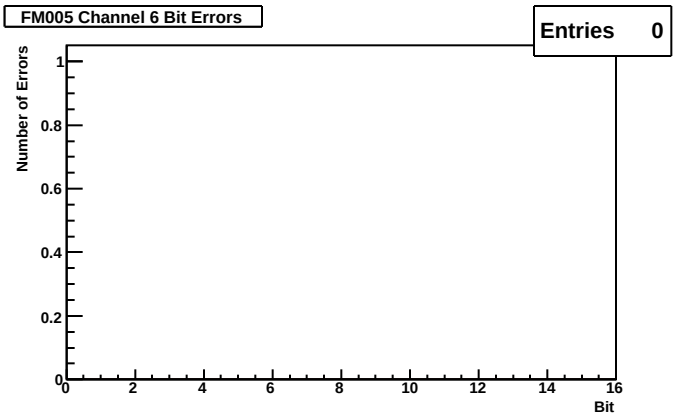

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

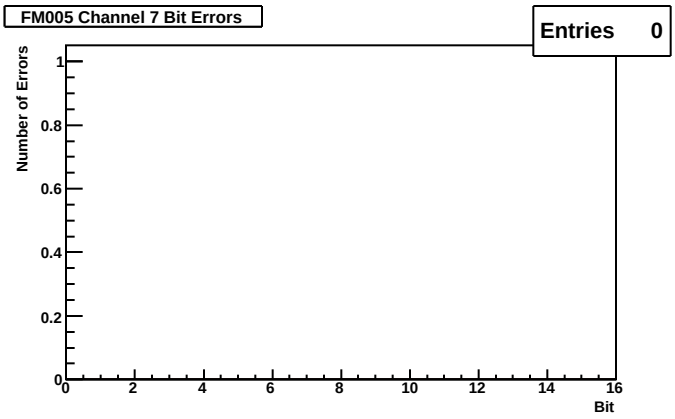

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

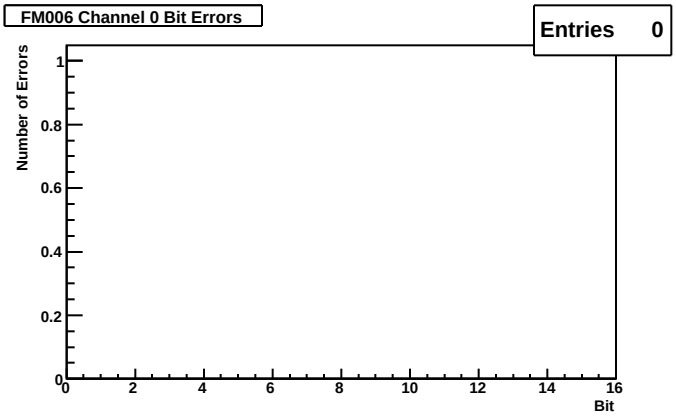

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

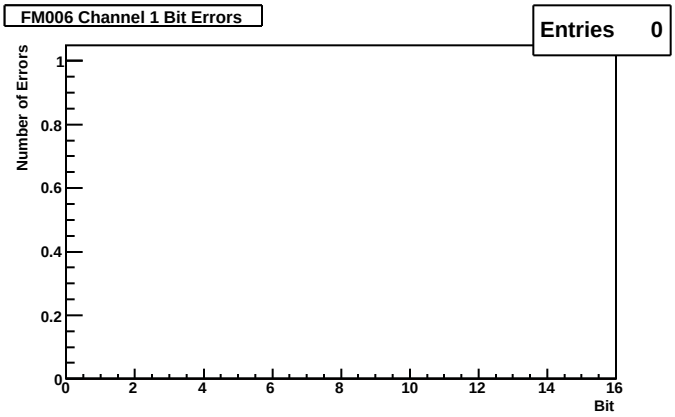

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

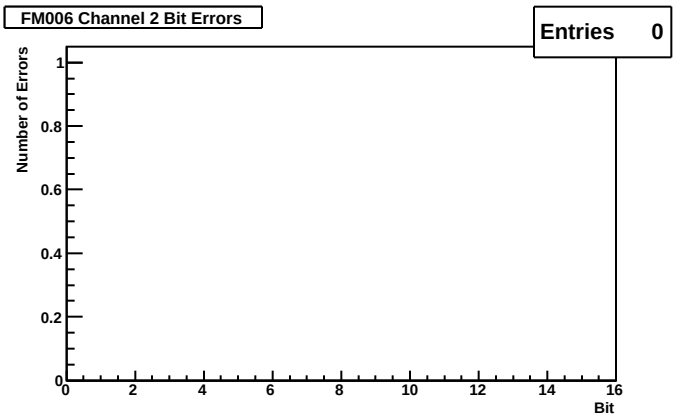

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

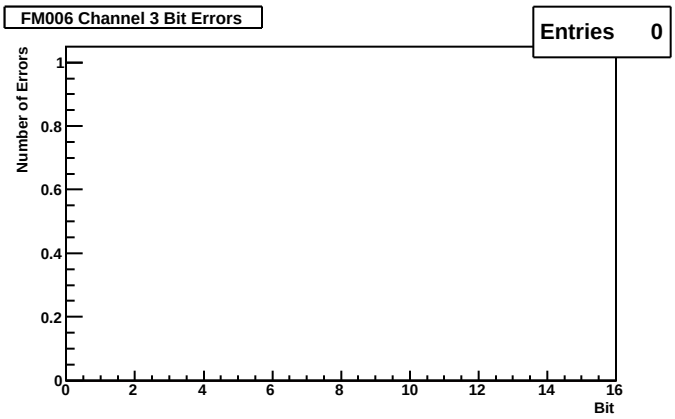

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

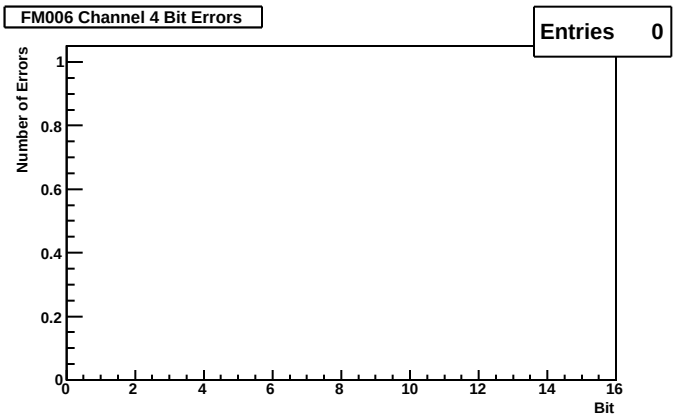

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

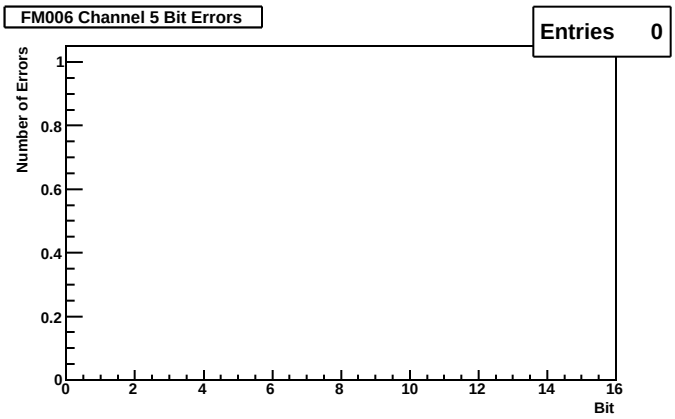

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

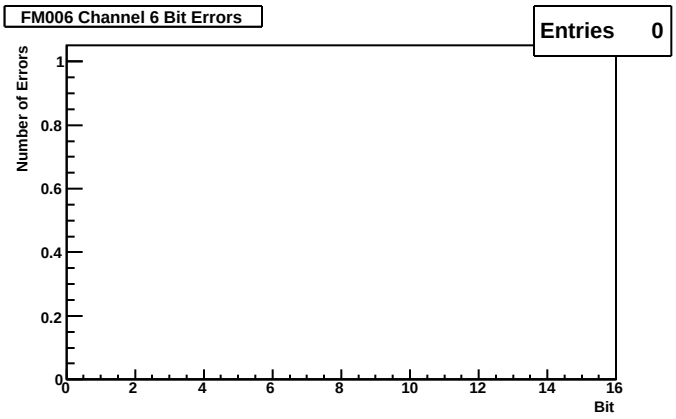

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

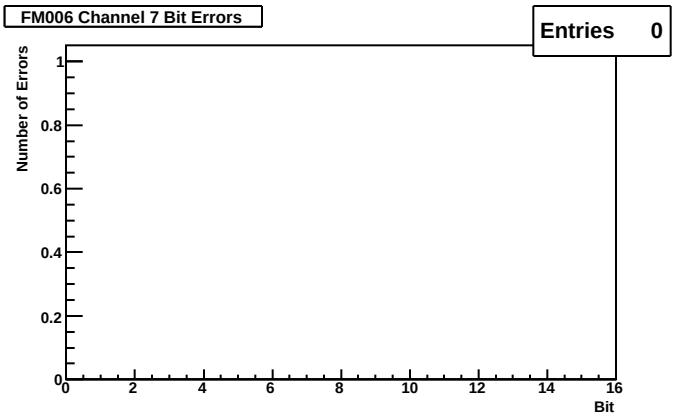

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

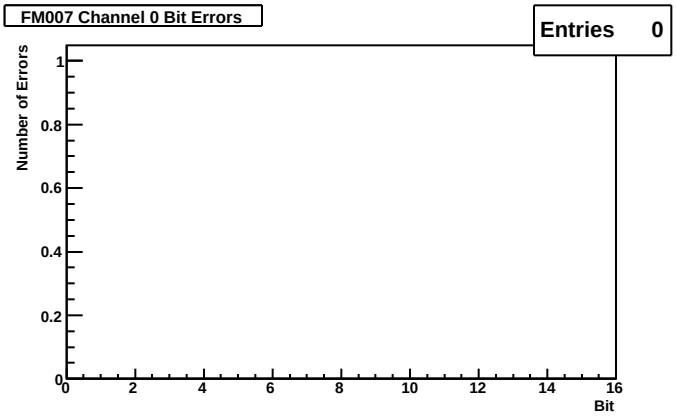

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

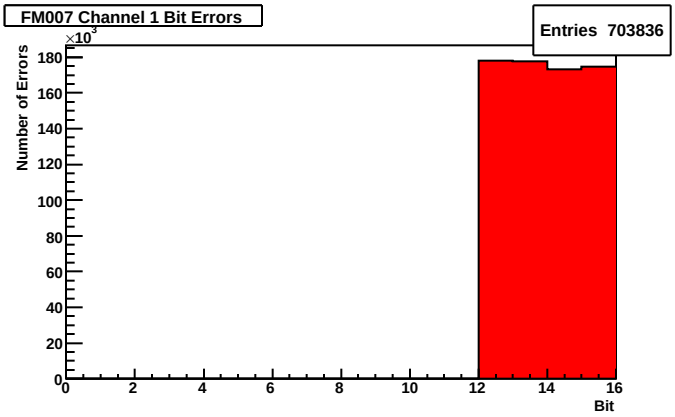

FM006 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

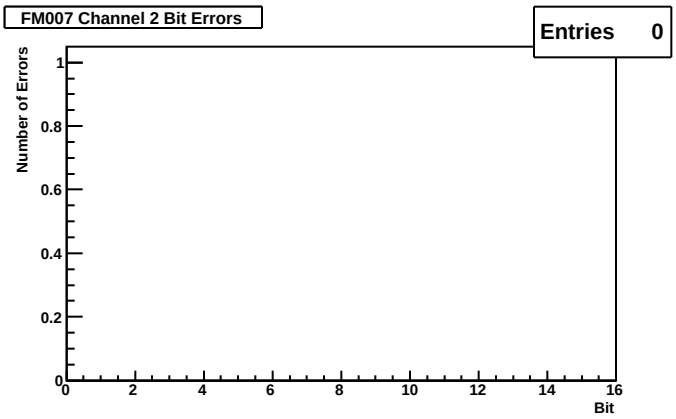

FM006 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

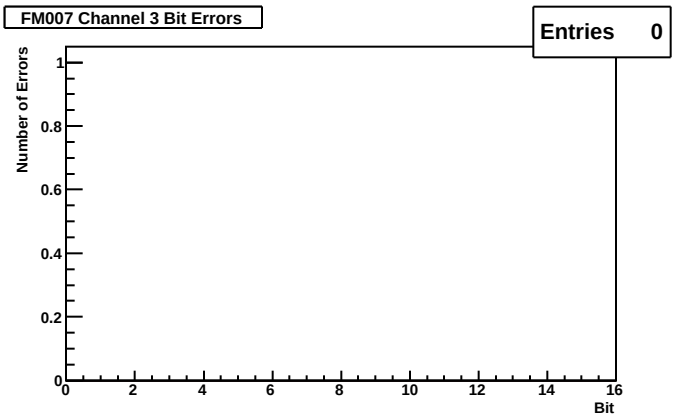

FM006 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

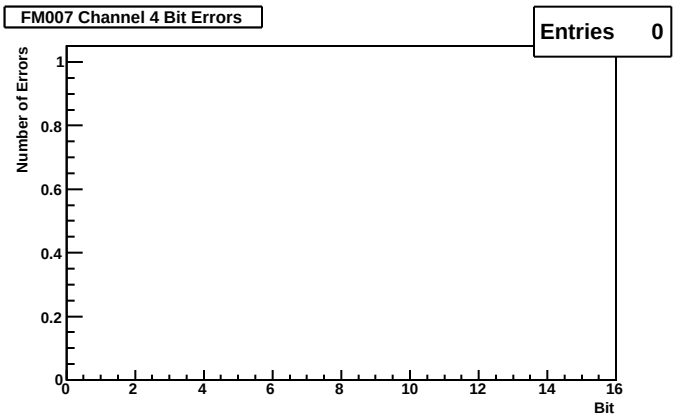

FM006 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

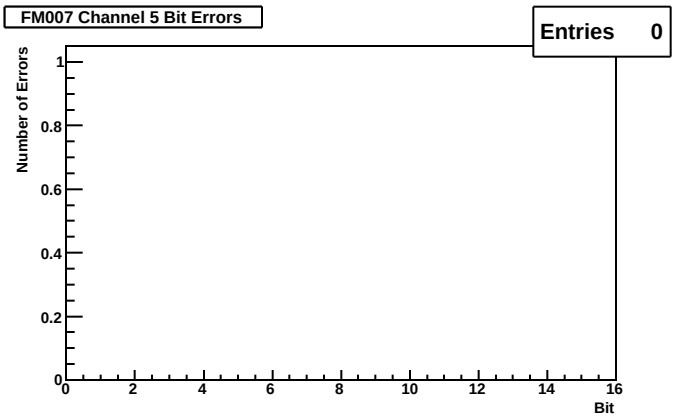

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

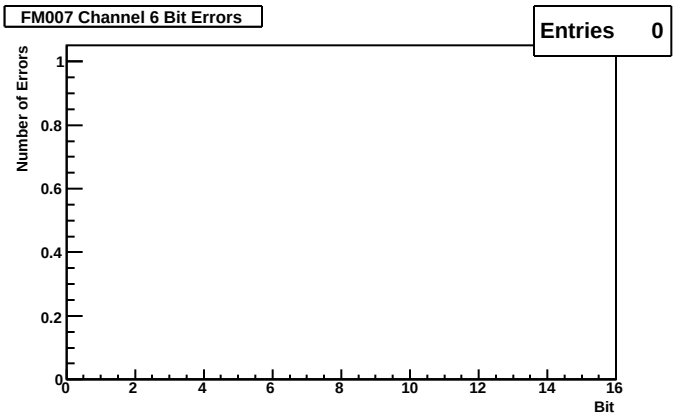

FM007 Channel 1 Bit Errors
Entries 703836
×10³
Number of Errors
Bit

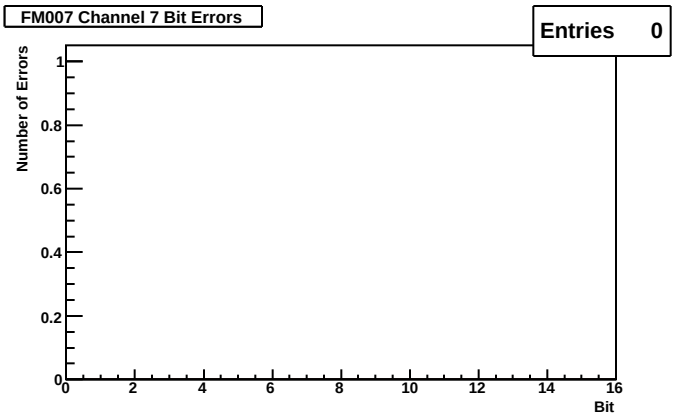

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

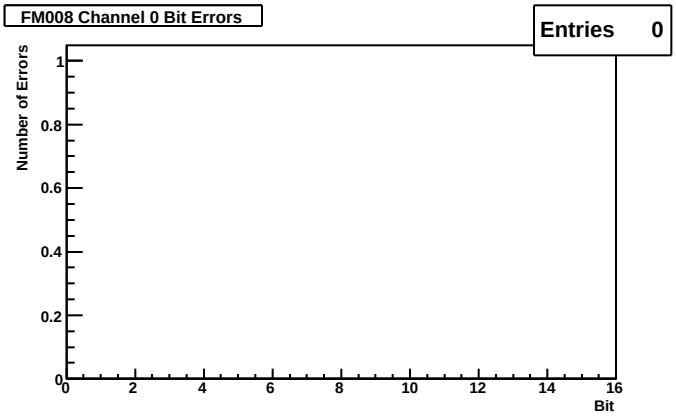

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

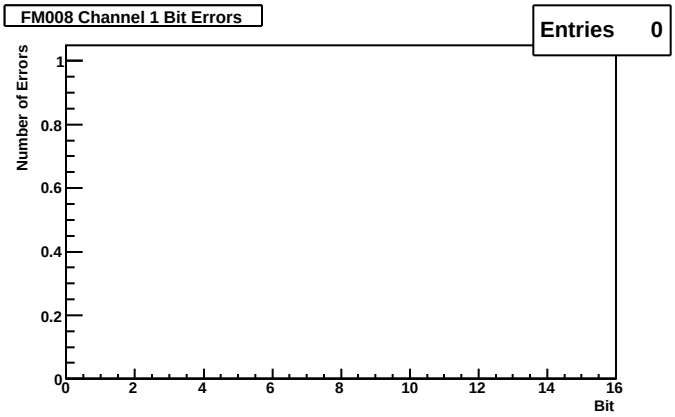

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

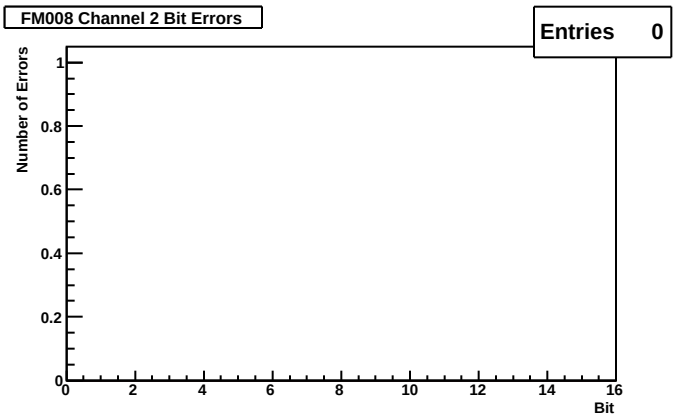

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

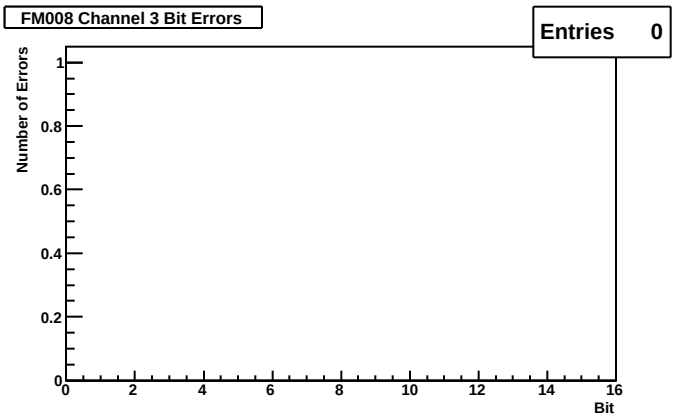

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

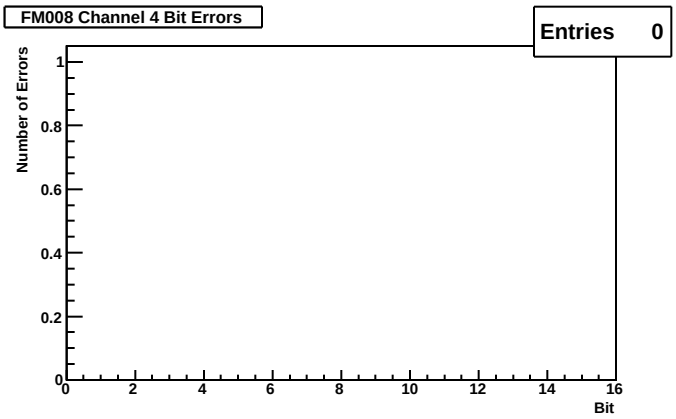

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

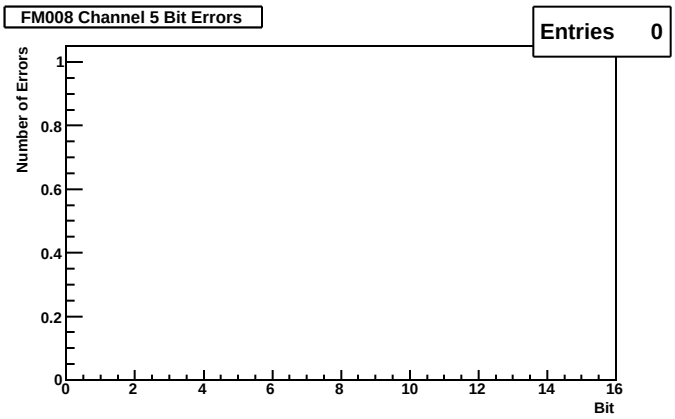

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

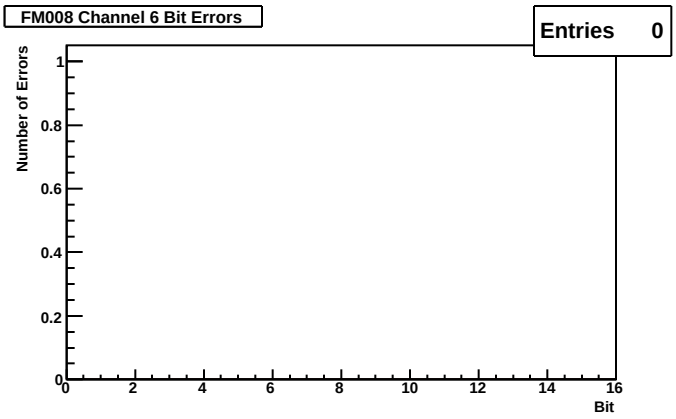

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

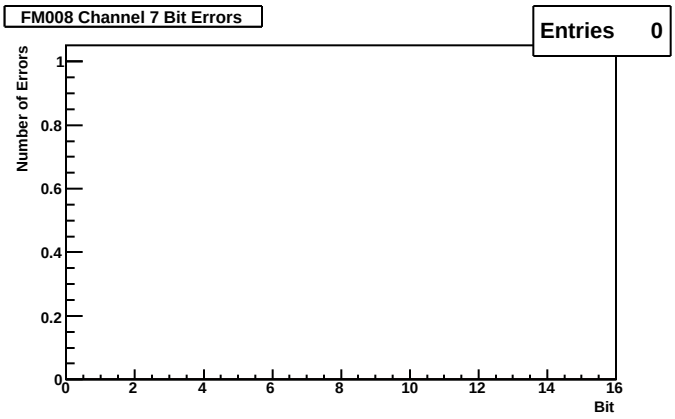

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

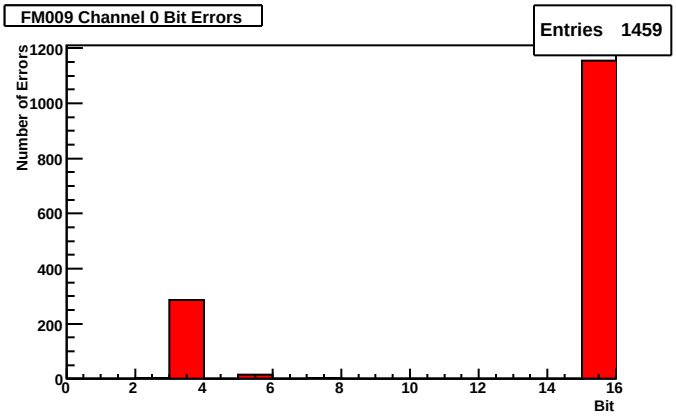

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

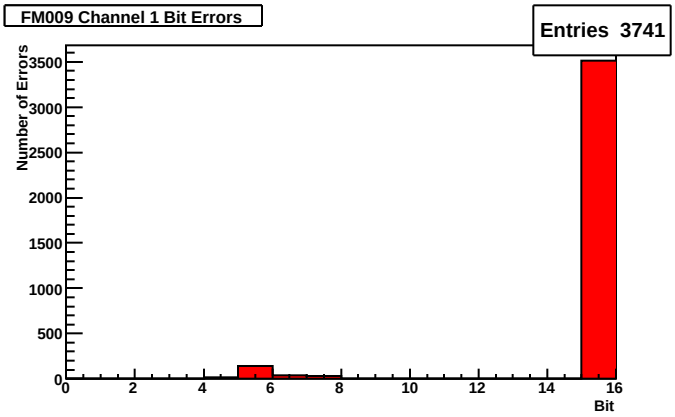

FM008 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

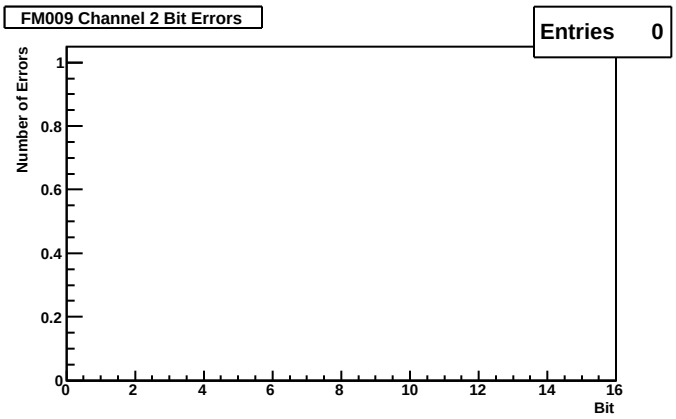

FM008 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

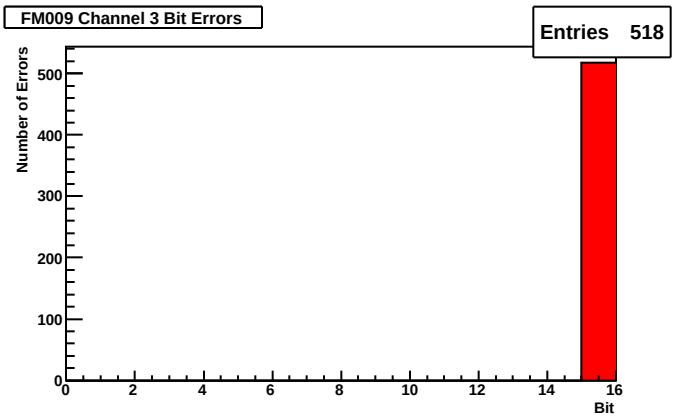

FM008 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

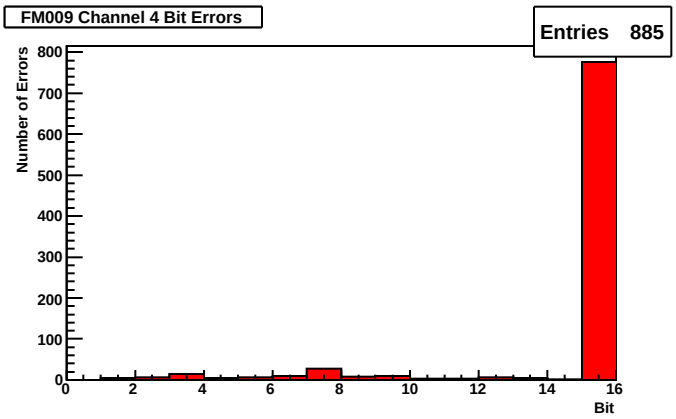

FM008 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

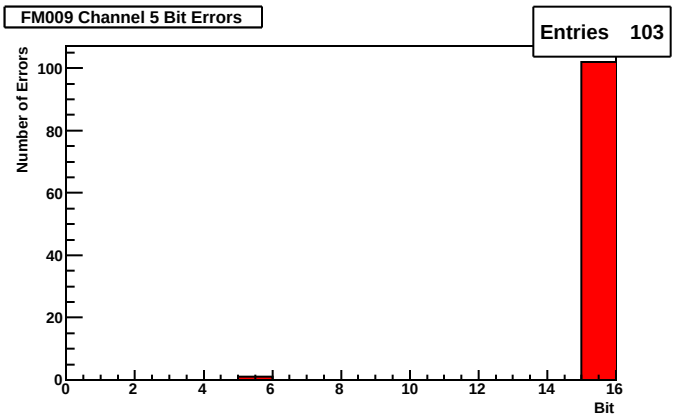

FM009 Channel 0 Bit Errors
Entries 1459
Number of Errors
Bit

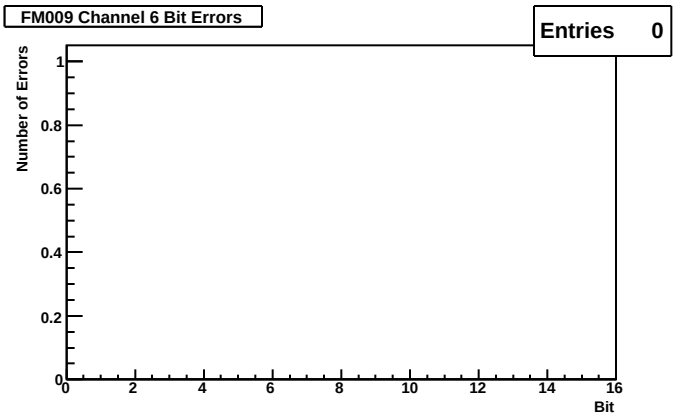

FM009 Channel 1 Bit Errors
Entries 3741
Number of Errors
Bit

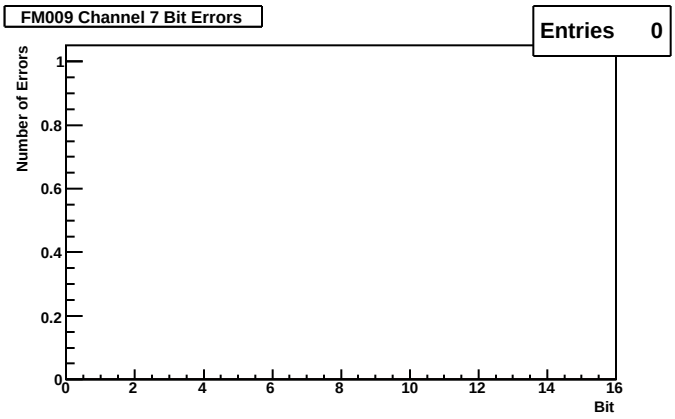

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

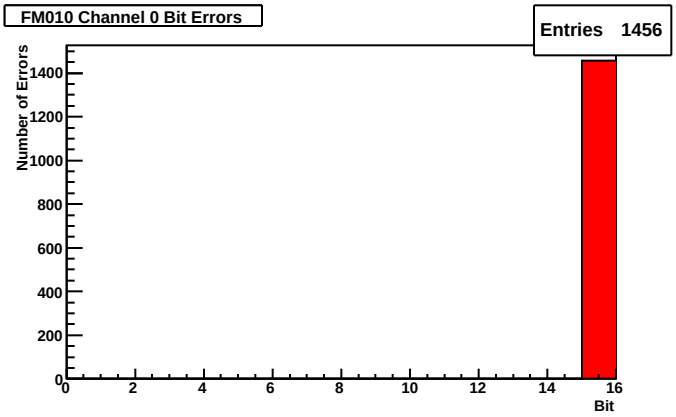

FM009 Channel 3 Bit Errors
Entries 518
Number of Errors
Bit

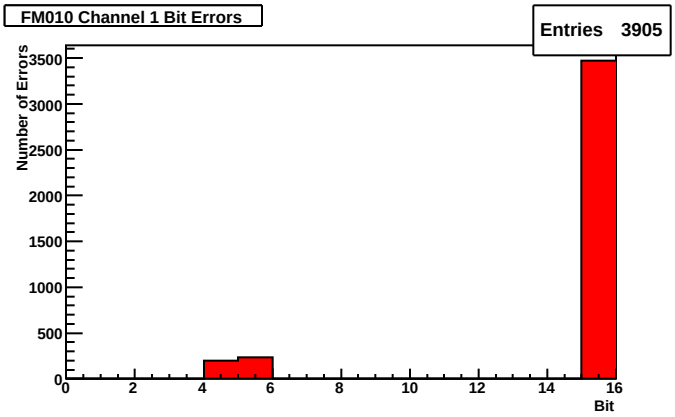

FM009 Channel 4 Bit Errors
Entries 885
Number of Errors
Bit

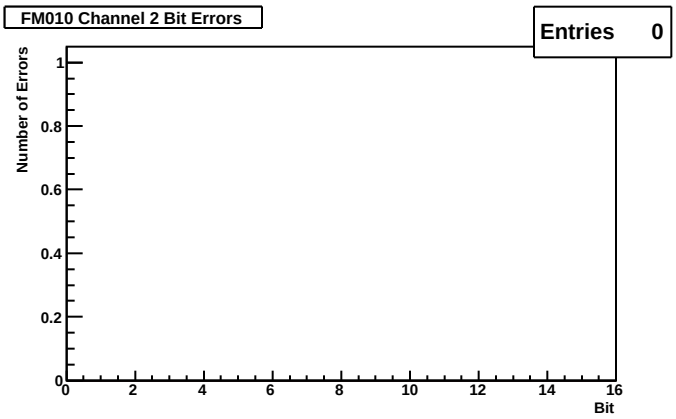

FM009 Channel 5 Bit Errors
Entries 103
Number of Errors
Bit

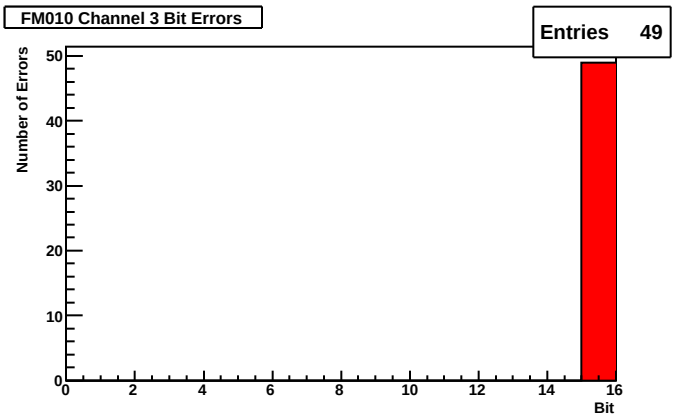

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM009 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

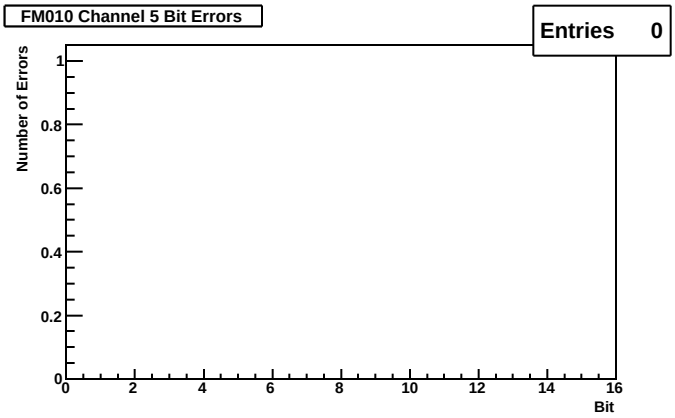

FM010 Channel 0 Bit Errors
Entries 1456
Number of Errors
Bit

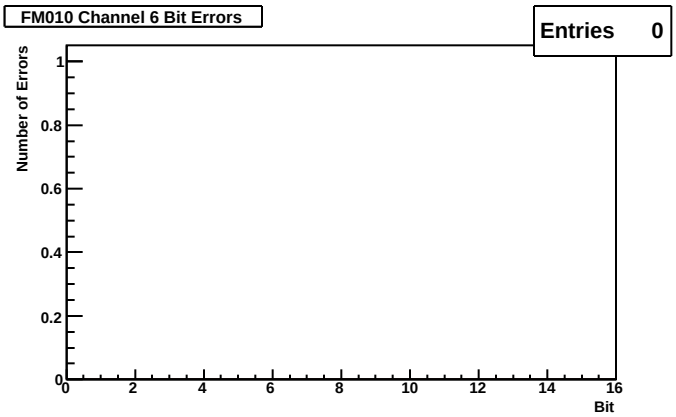

FM010 Channel 1 Bit Errors
Entries 3905
Number of Errors
Bit

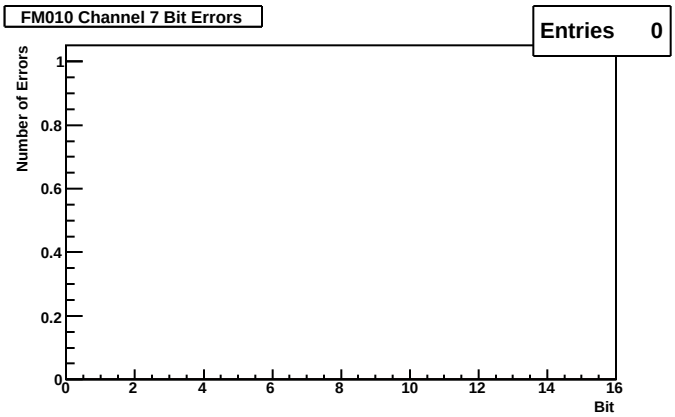

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

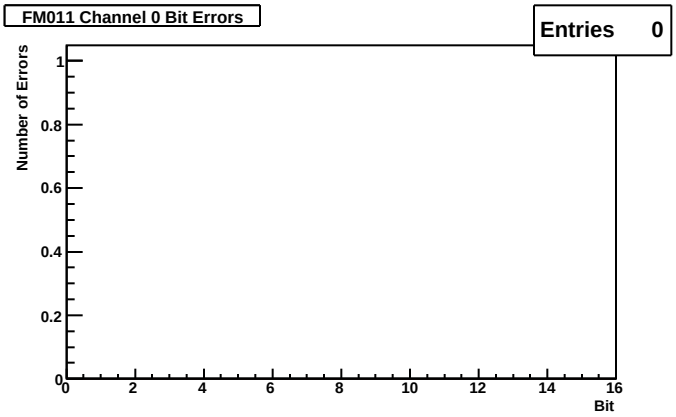

FM010 Channel 3 Bit Errors
Entries 49
Number of Errors
Bit

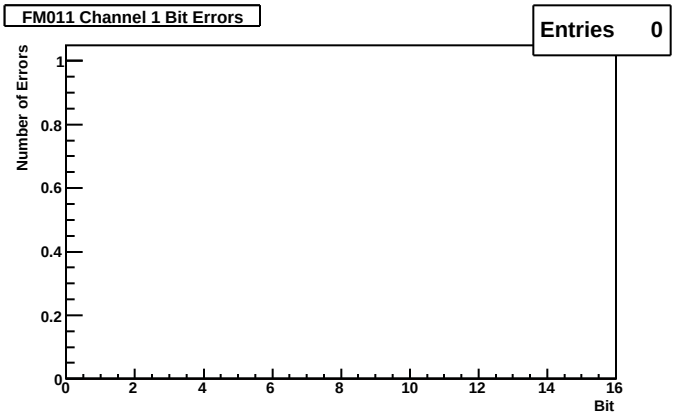
FM010 Channel 4 Bit Errors
Entries 406266
Number of Errors
Bit

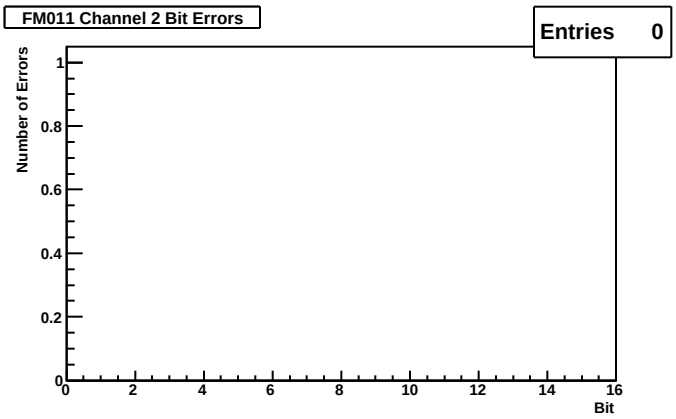

FM010 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

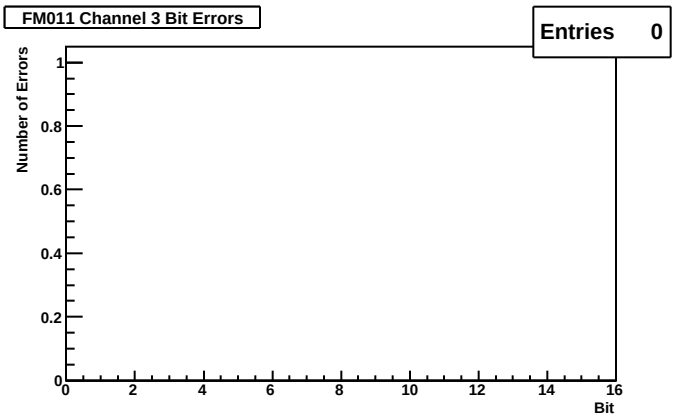

FM010 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

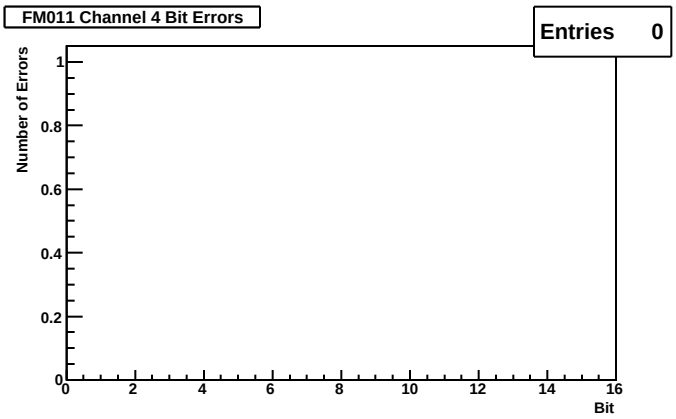

FM010 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

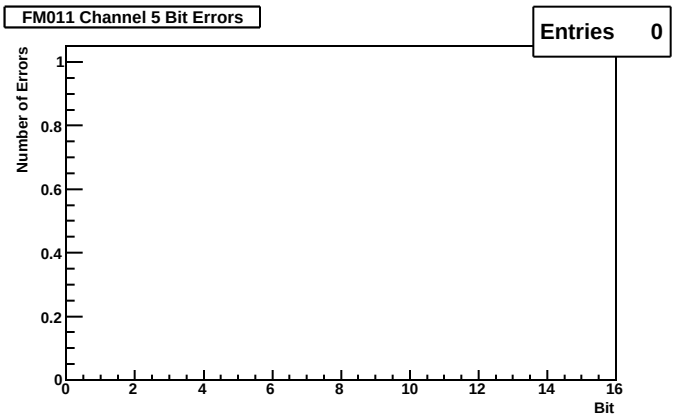

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

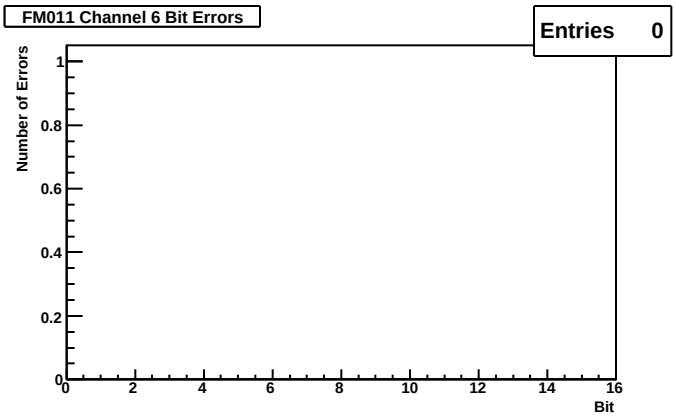

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

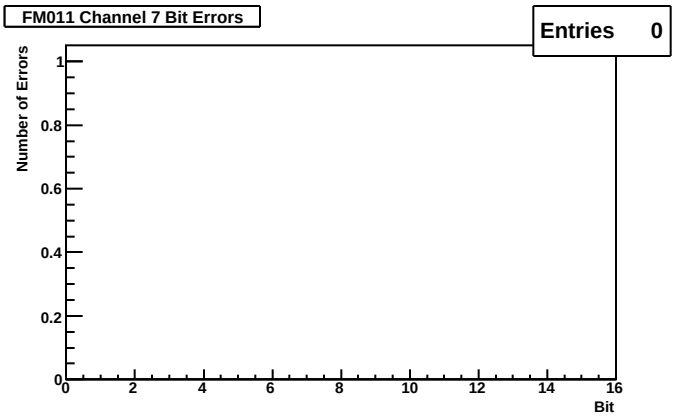

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

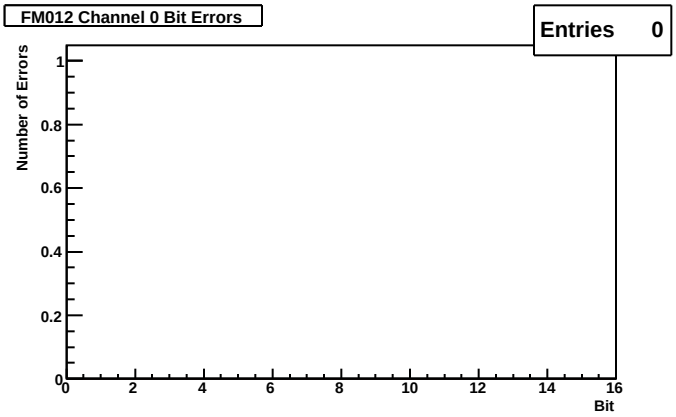

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

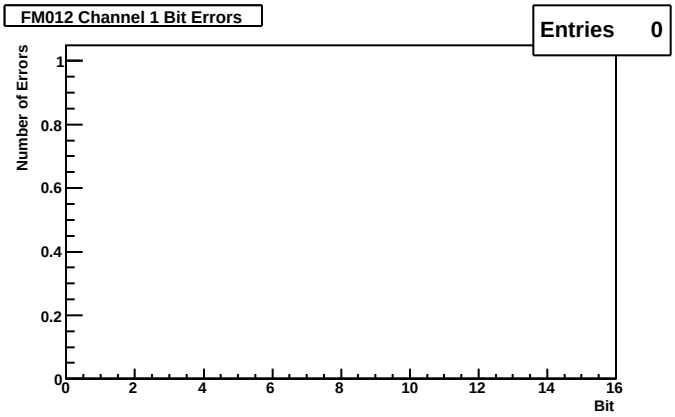

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

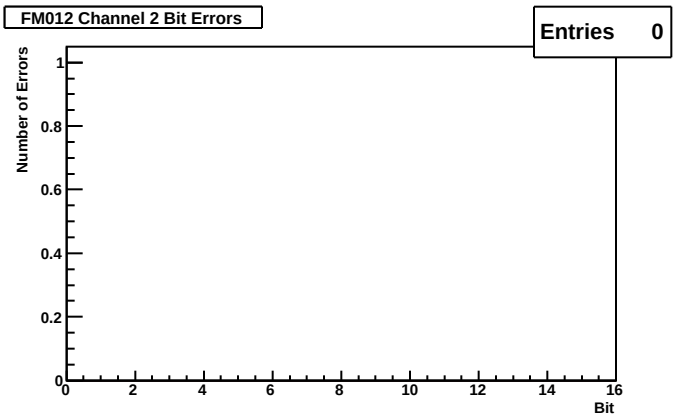

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

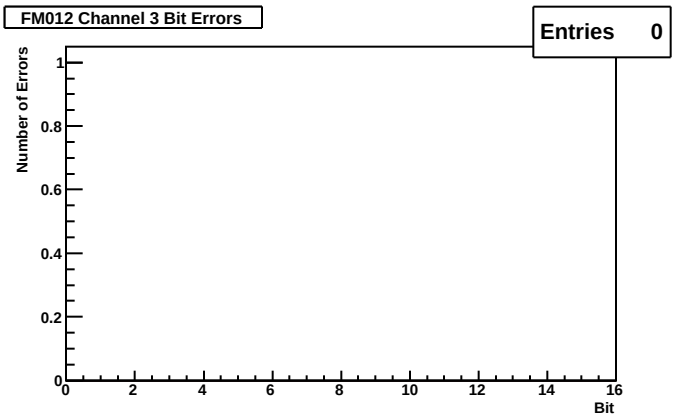

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

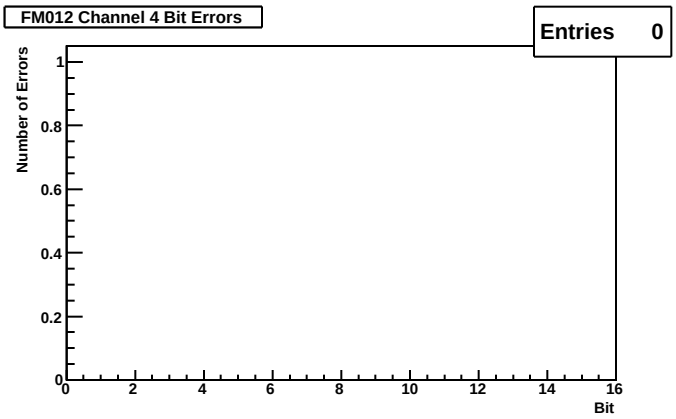

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

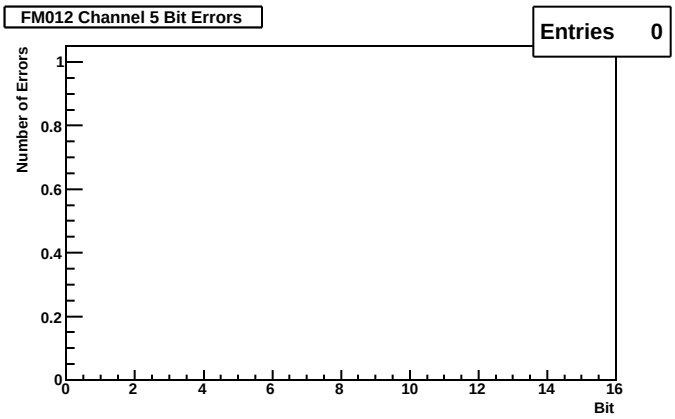

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

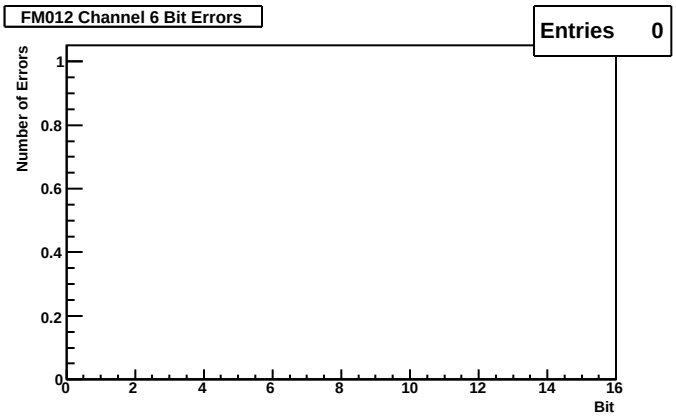

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

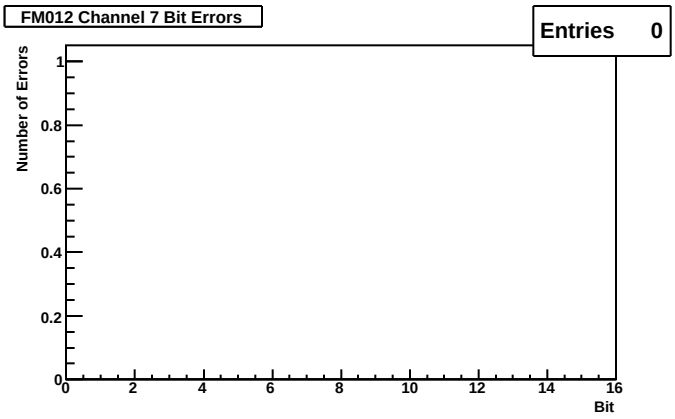

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit