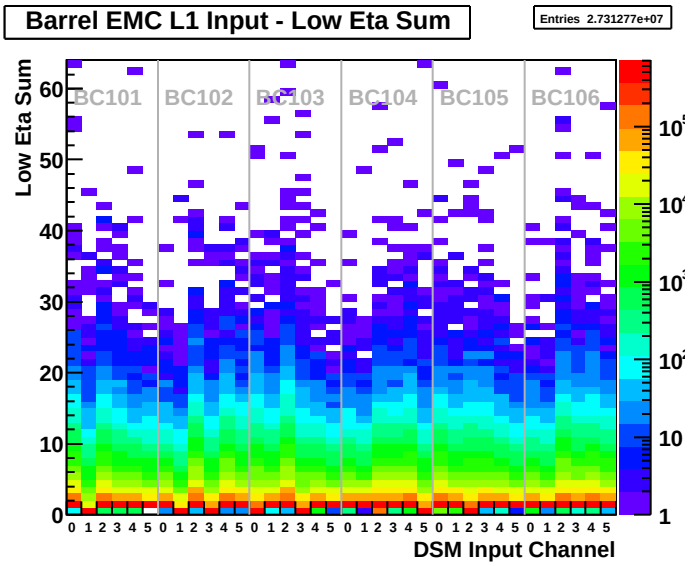
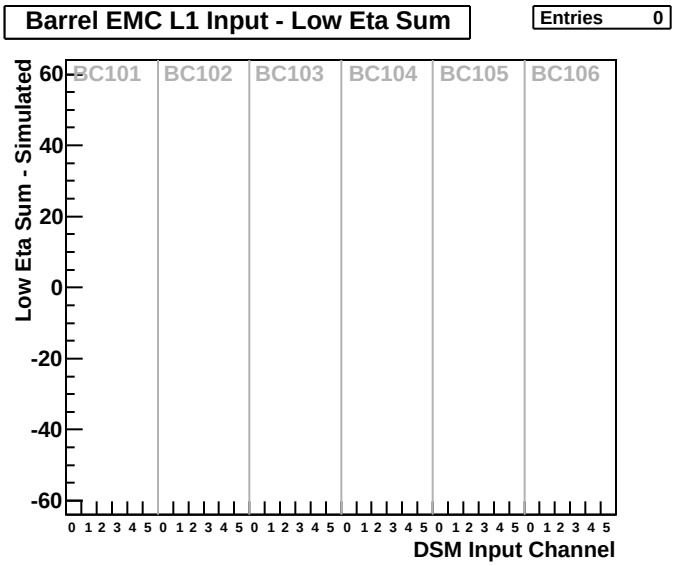
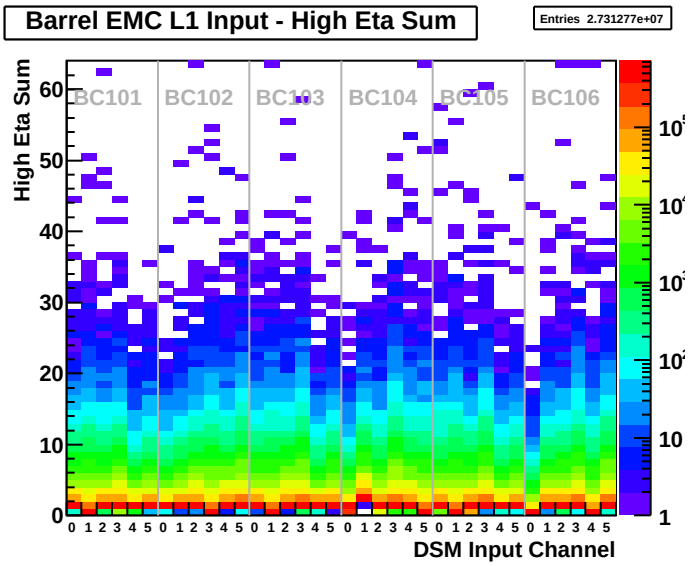
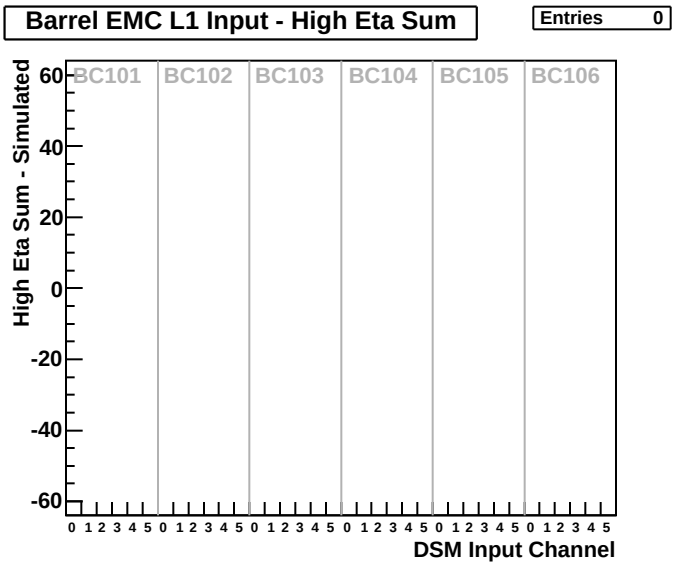
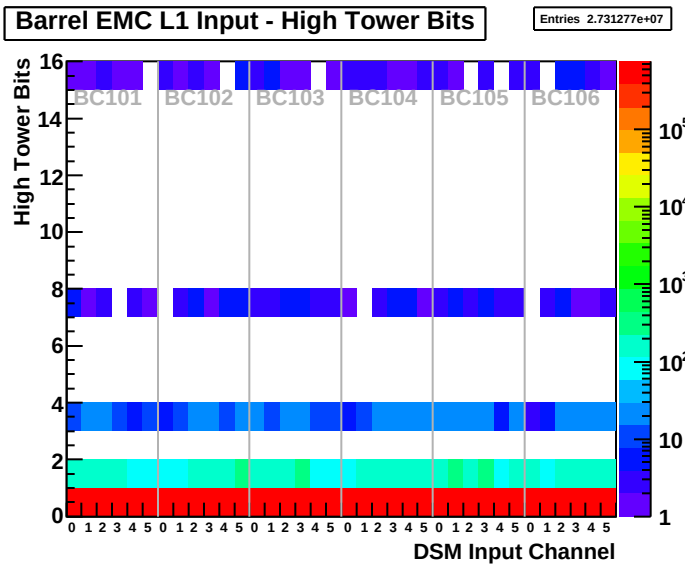
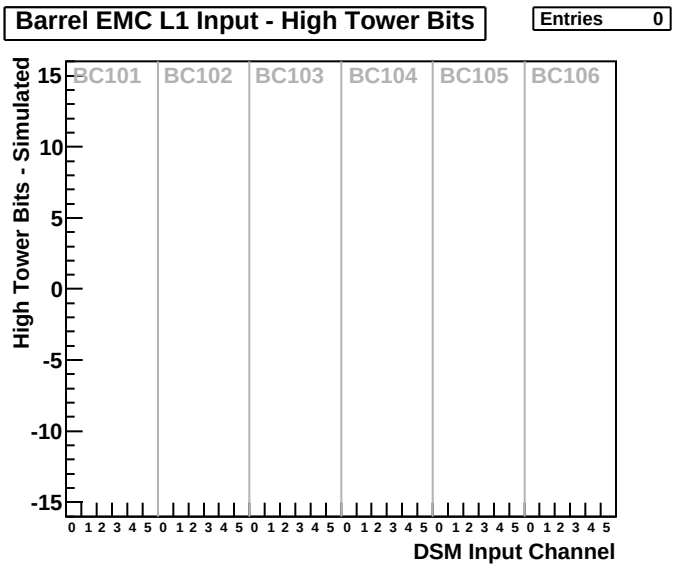

Barrel EMC L1 Input - Low Eta Sum
Entries 2.731277e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

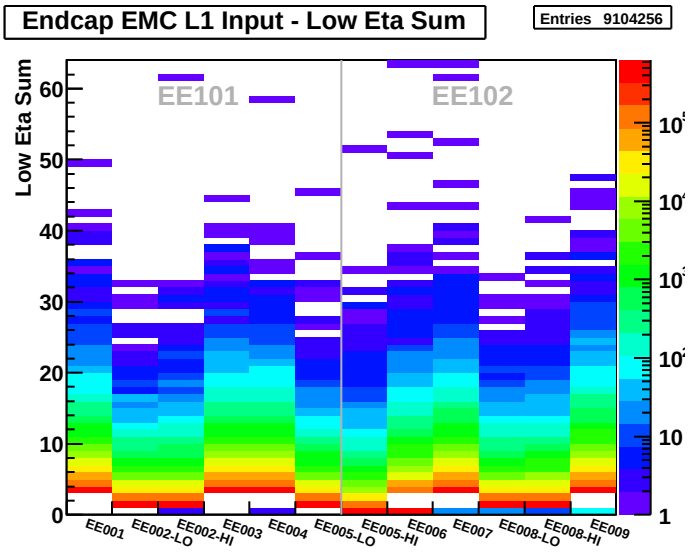

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

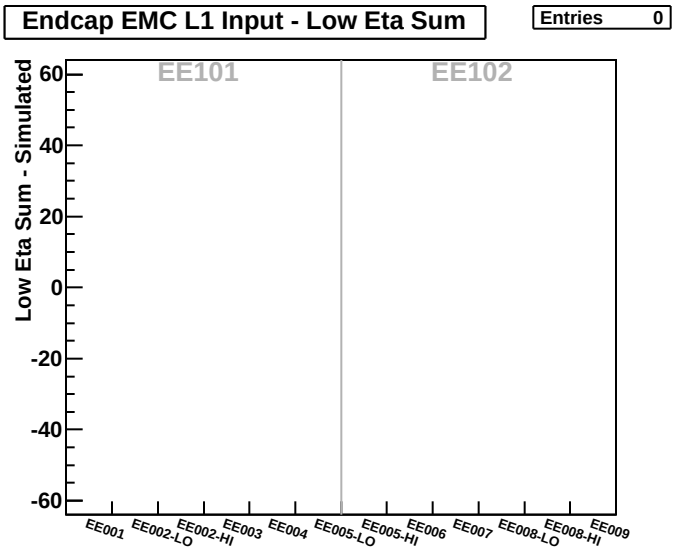

Barrel EMC L1 Input - High Eta Sum
Entries 2.731277e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

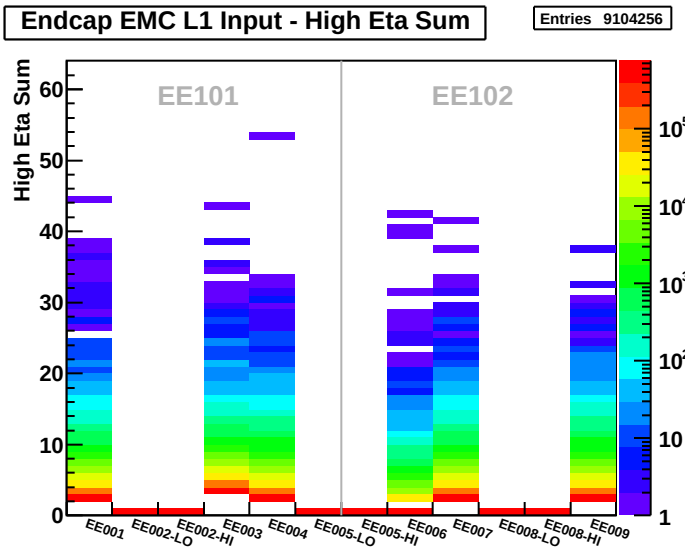

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

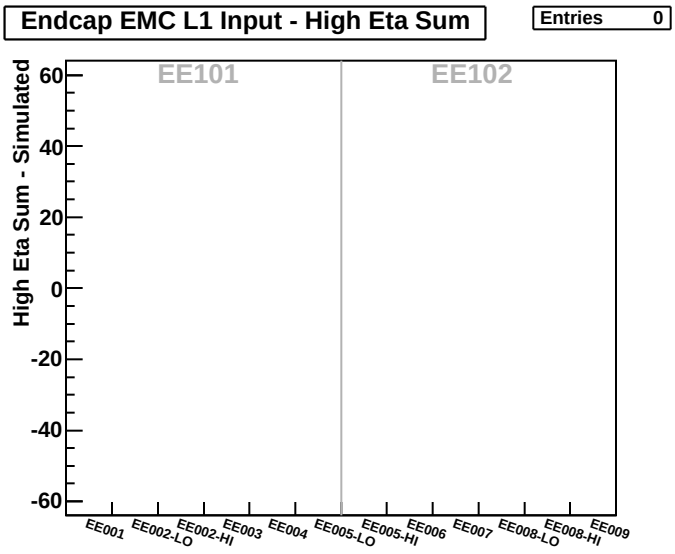

Barrel EMC L1 Input - High Tower Bits
Entries 2.731277e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

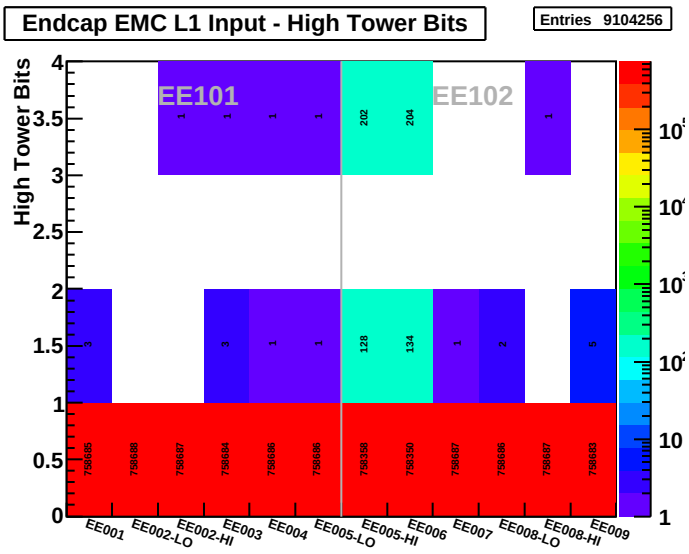

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

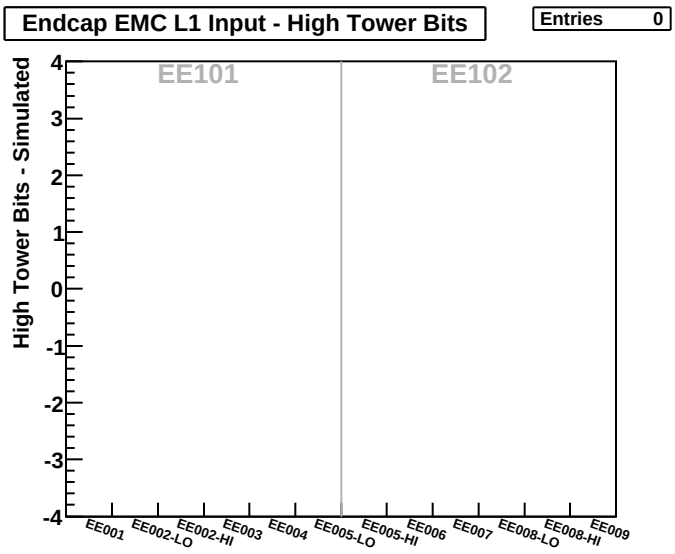

Endcap EMC L1 Input - Low Eta Sum
Entries 9104256
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

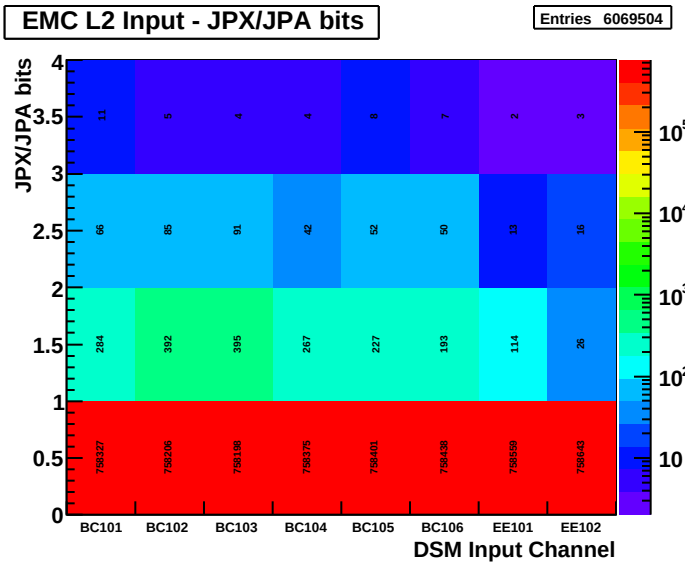

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

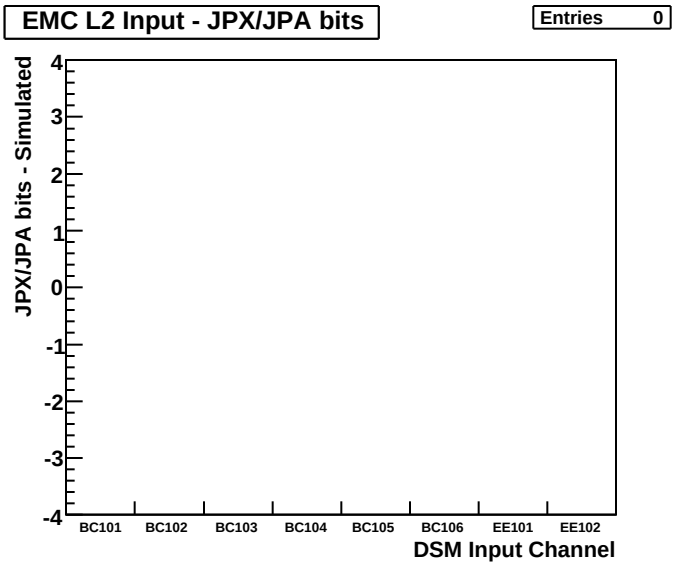

Endcap EMC L1 Input - High Eta Sum
Entries 9104256
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

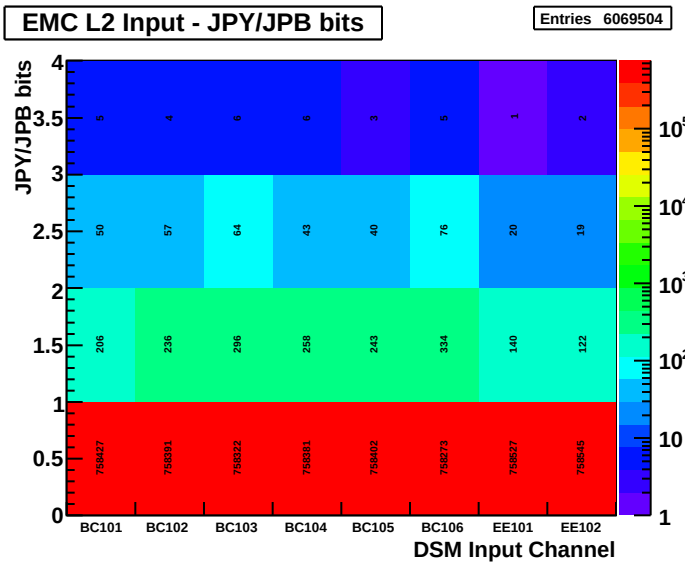

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

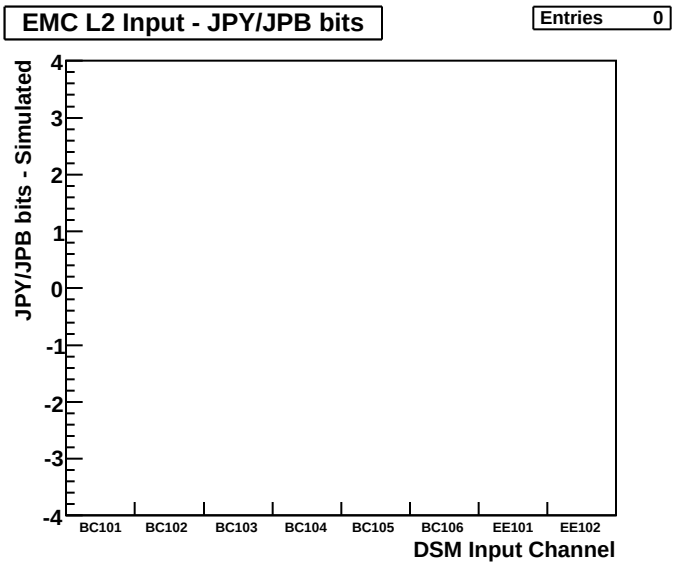

Endcap EMC L1 Input - High Tower Bits
Entries 9104256
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

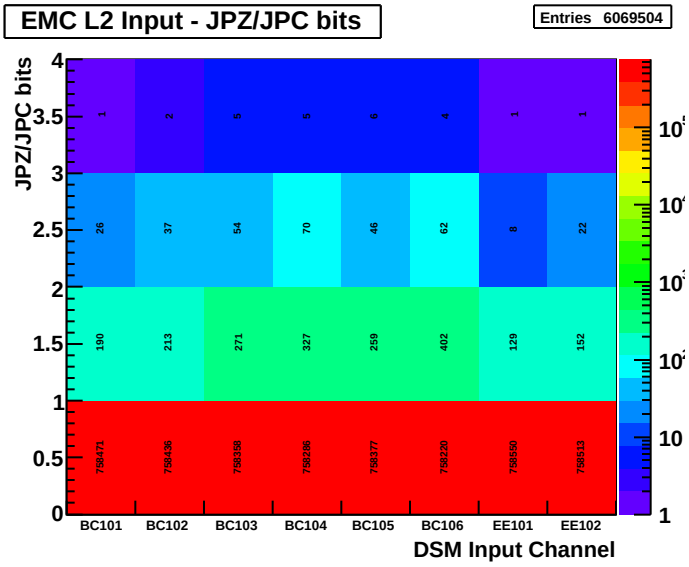

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

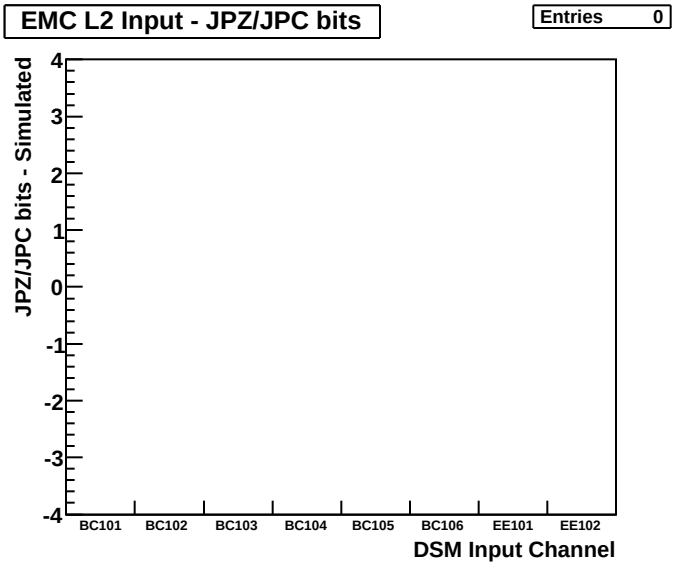

EMC L2 Input - JPX/JPA bits
Entries 6069504
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
11 5 4 4 8 7 2 3
66 85 91 42 52 50 13 16
284 392 395 267 227 193 114 26
758327 758206 758188 758375 758401 758438 758559 758643

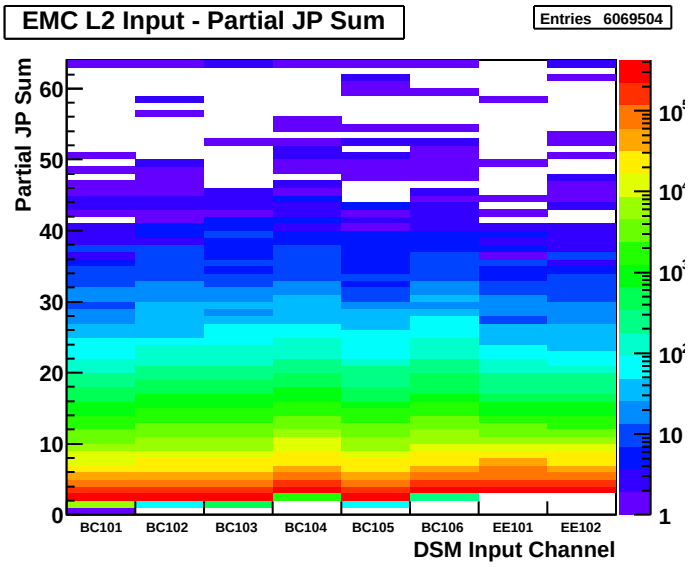

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

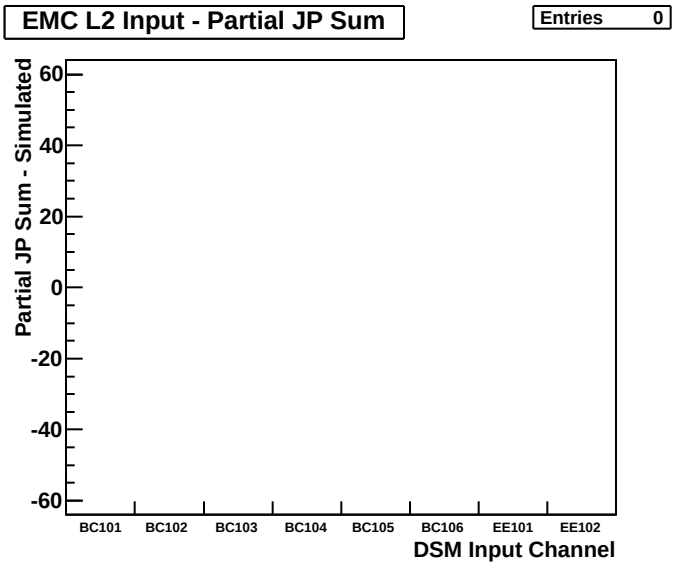

EMC L2 Input - JPY/JPB bits
Entries 6069504
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5 4 6 6 3 5 1 2
50 57 64 43 40 76 20 19
206 236 296 258 243 334 140 122
758427 758391 758322 758381 758402 758273 758527 758545

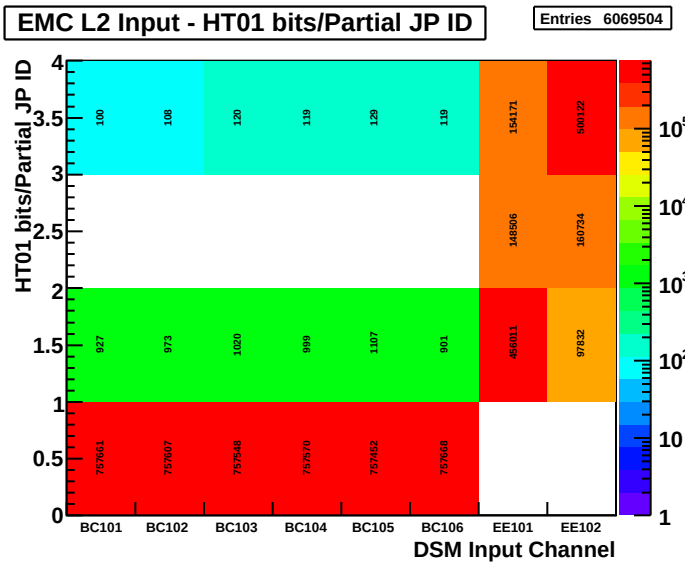

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

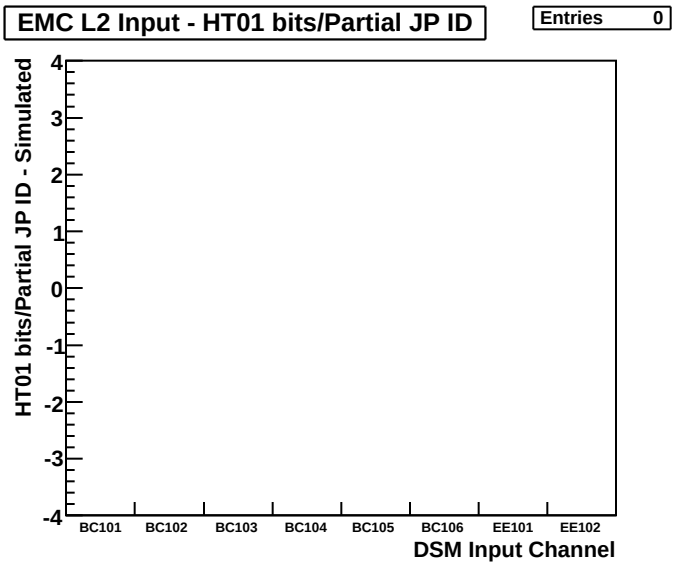

EMC L2 Input - JPZ/JPC bits
Entries 6069504
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 2 5 5 6 4 1 1
26 37 54 70 46 62 8 22
190 213 271 327 259 402 129 152
758471 758436 758359 758286 758377 758220 758550 758513

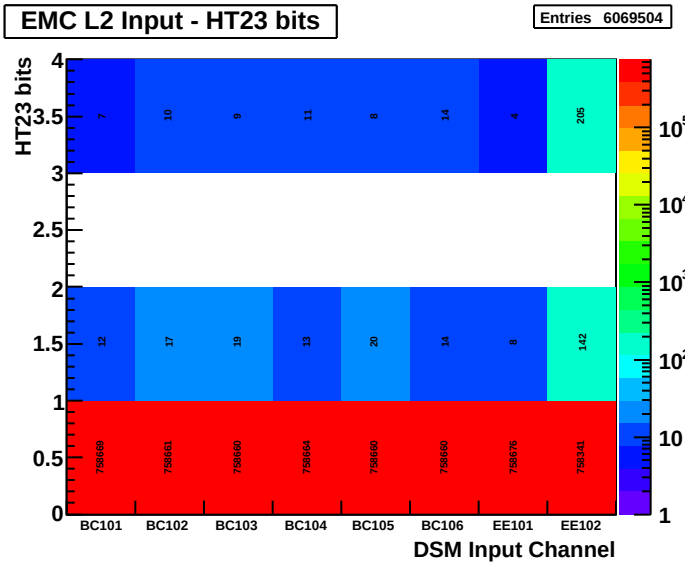

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

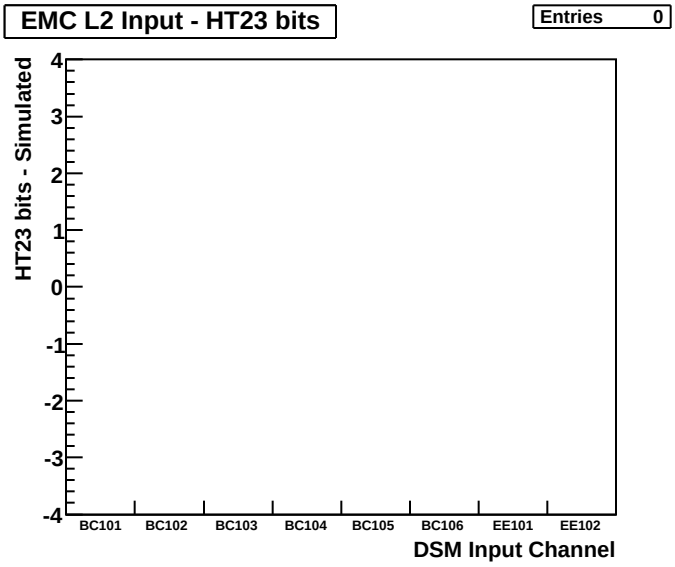

EMC L2 Input - Partial JP Sum
Entries 6069504
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

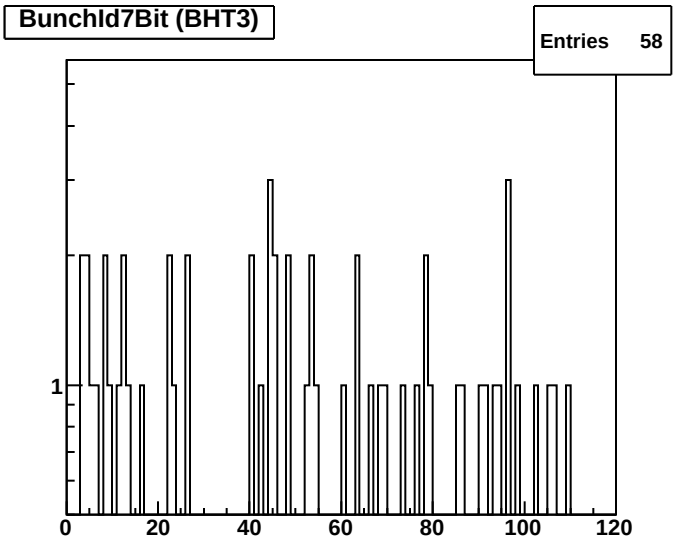

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

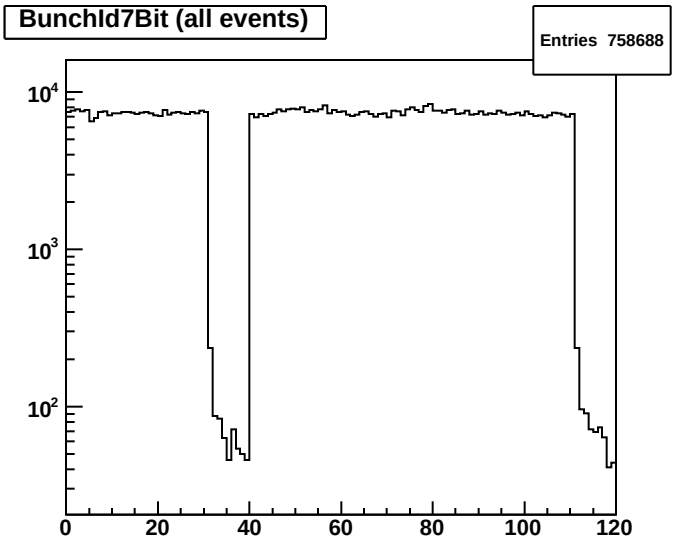

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6069504
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

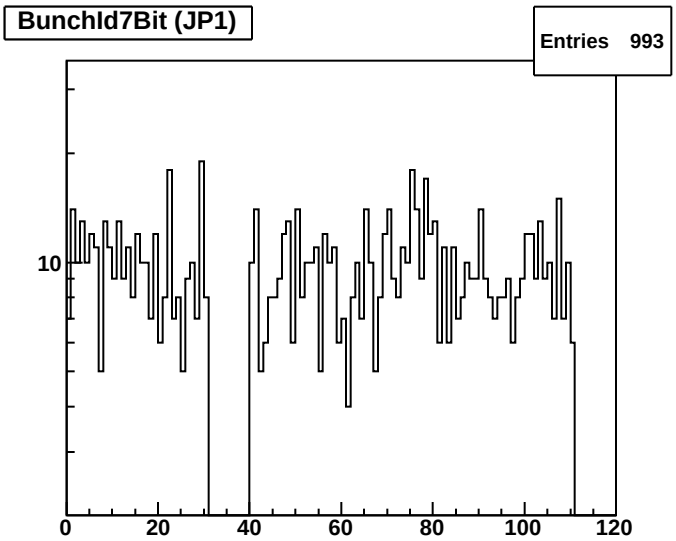

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

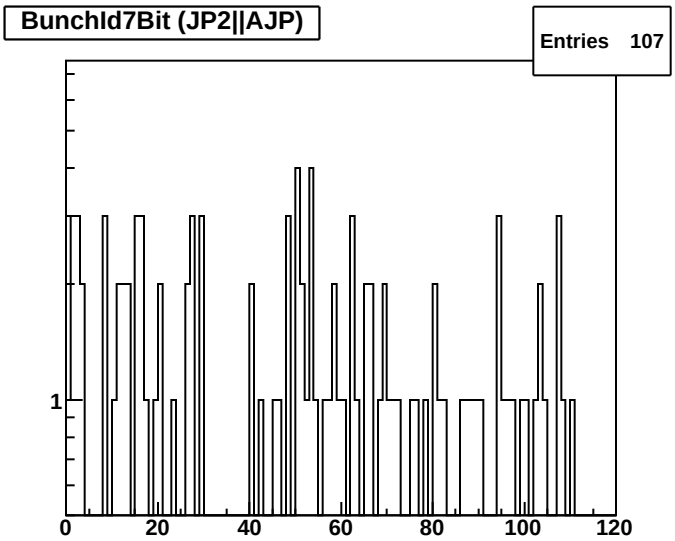

EMC L2 Input - HT23 bits
Entries 6069504
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

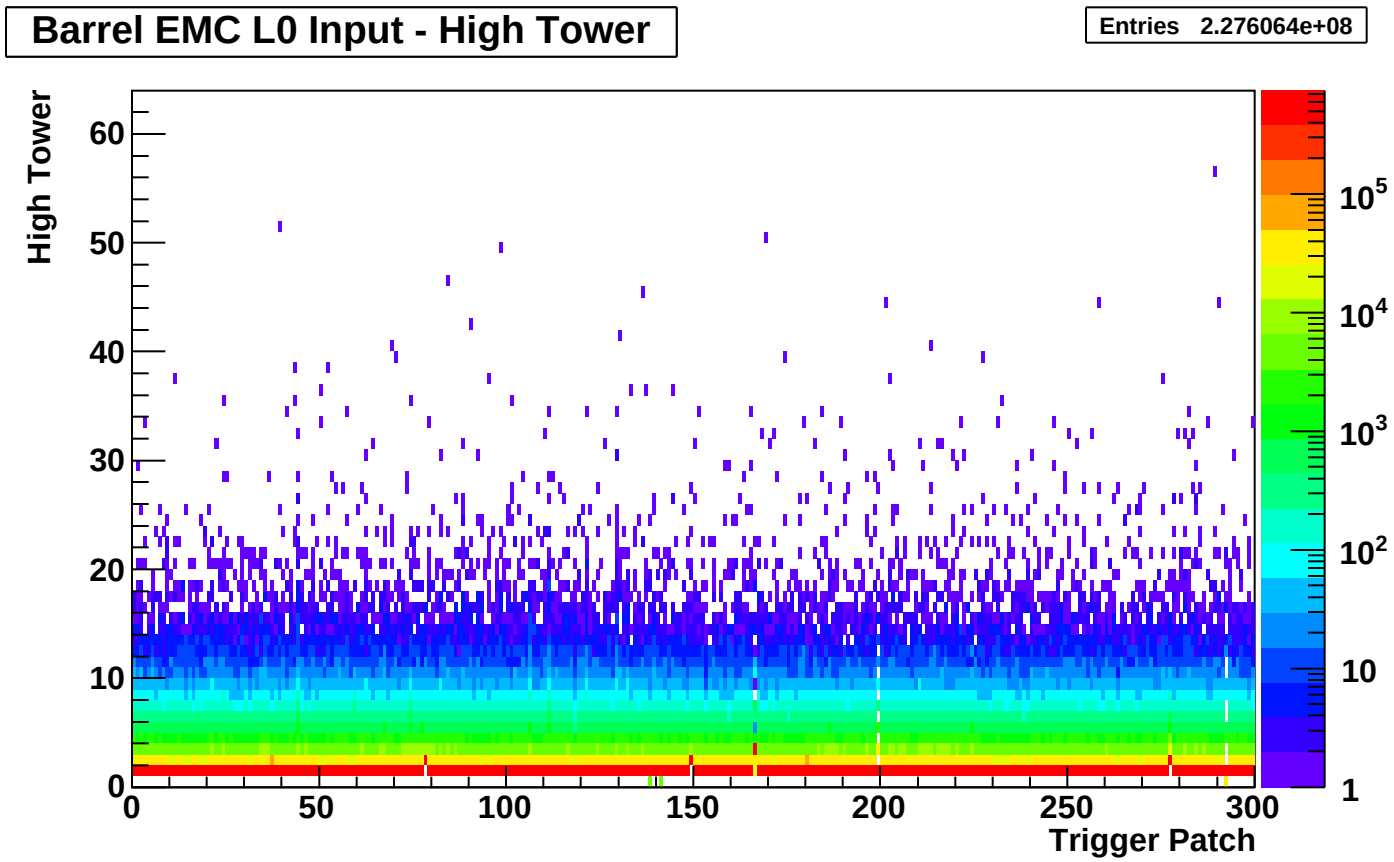

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

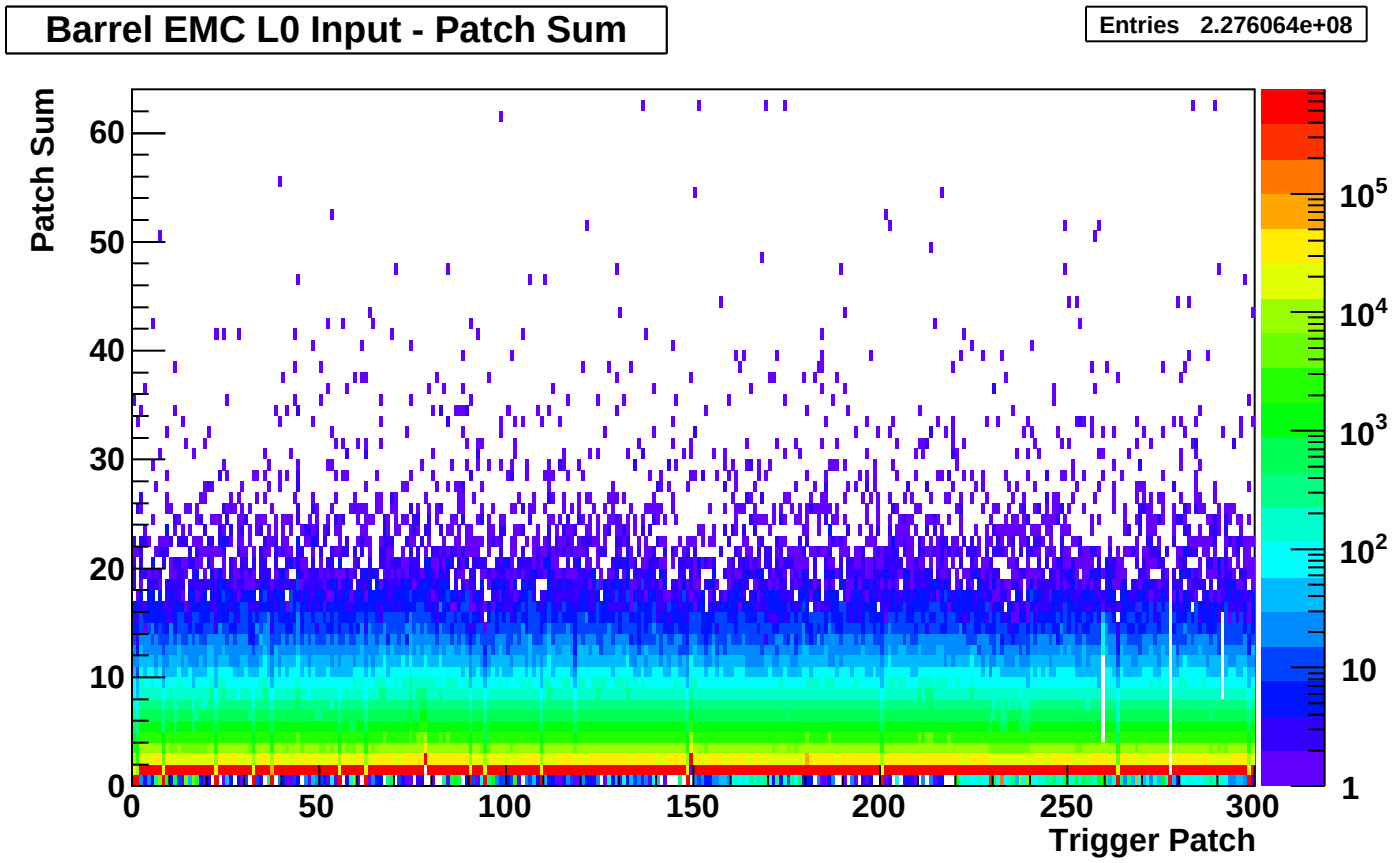

BunchId7Bit (BHT3)
Entries 58
1
0
20
40
60
80
100
120

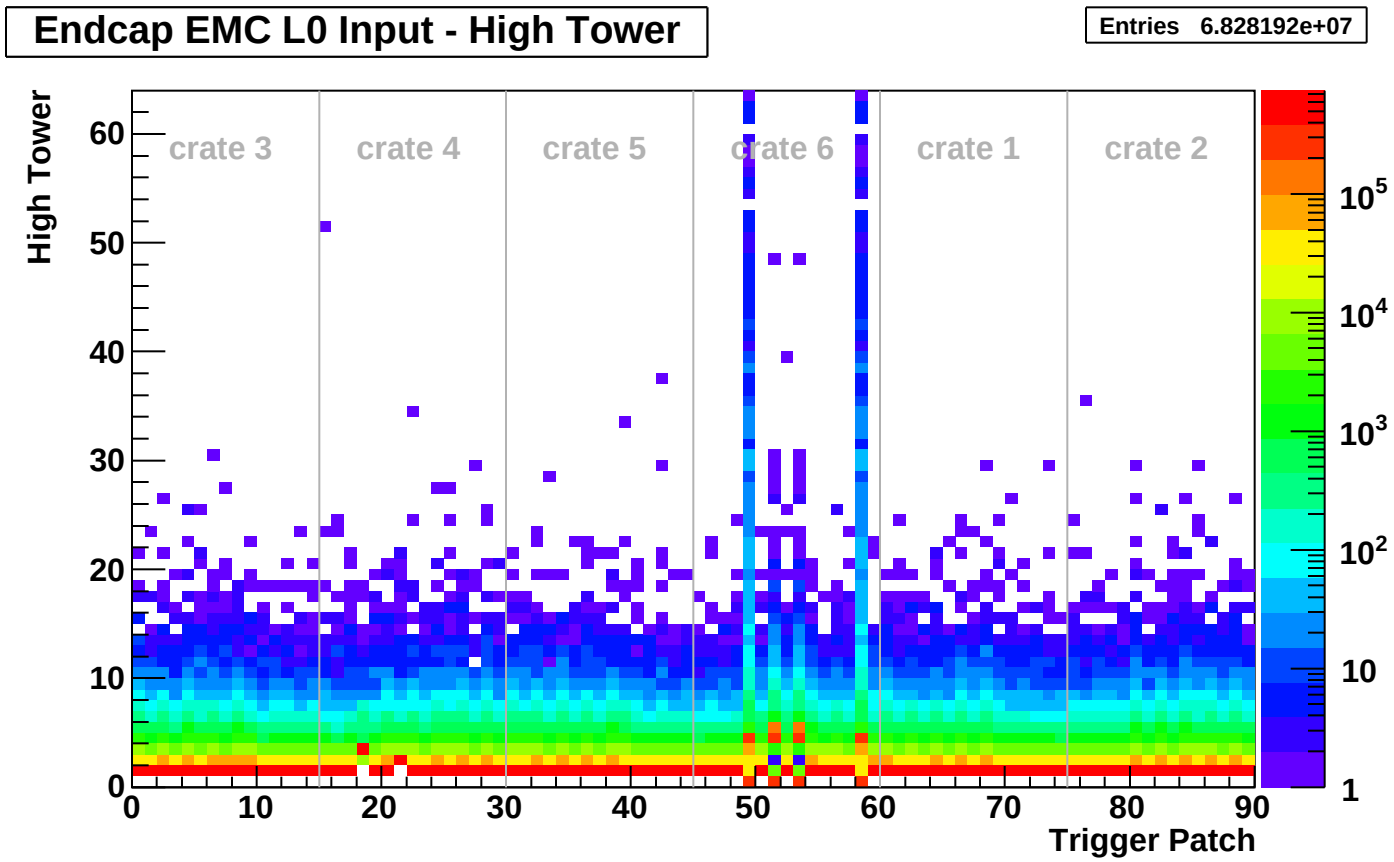

BunchId7Bit (all events)
Entries 758688
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

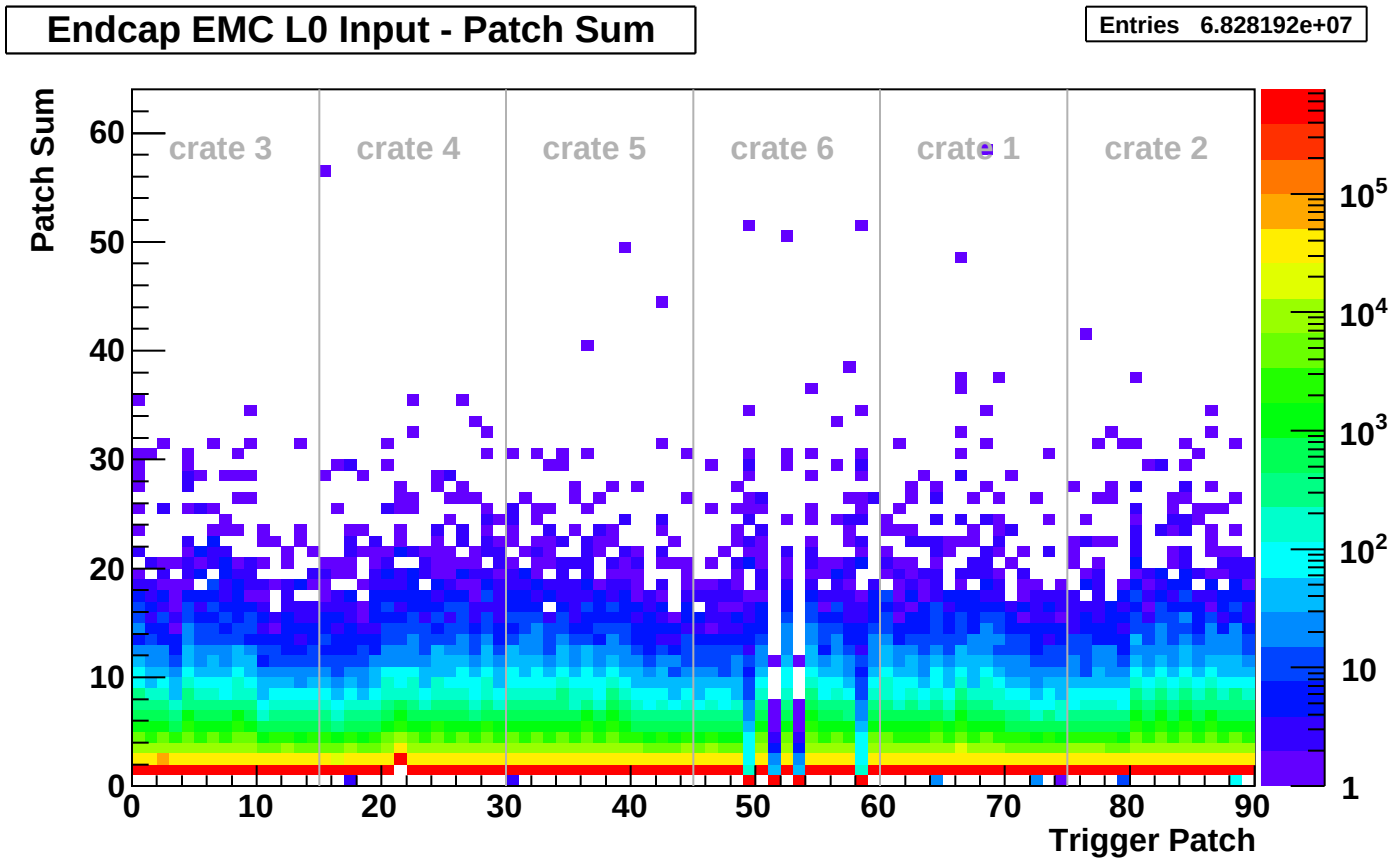

BunchId7Bit (JP1)
Entries 993
10
0
20
40
60
80
100
120

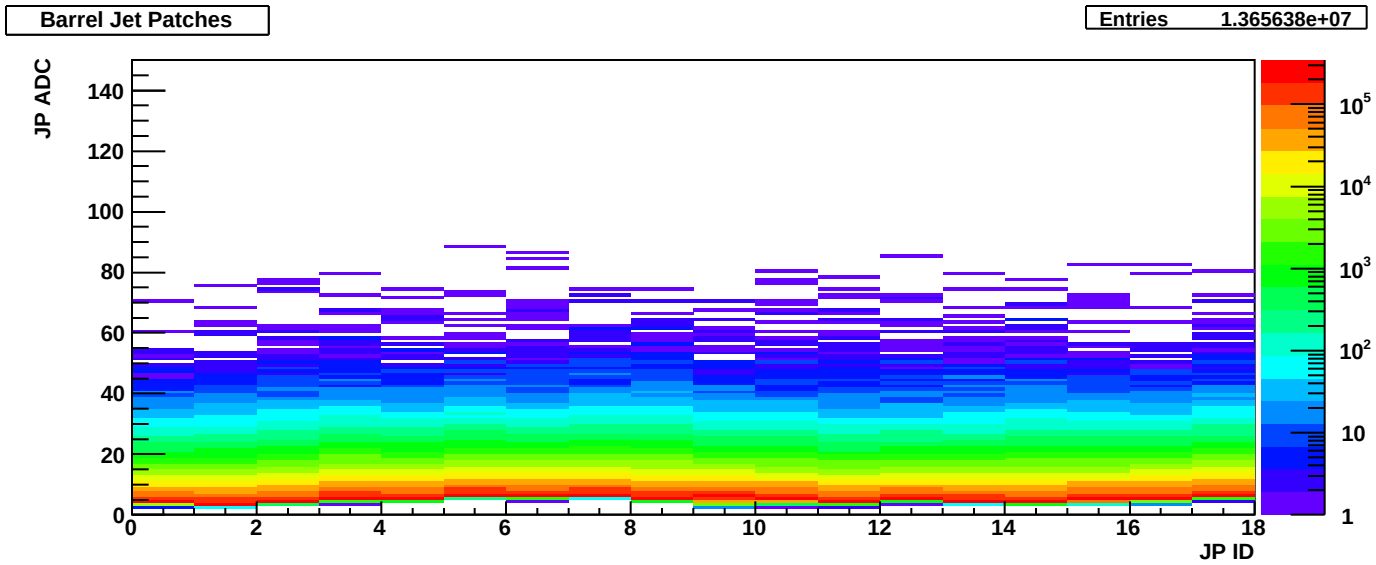

BunchId7Bit (JP2||AJP)
Entries 107
1
0
20
40
60
80
100
120

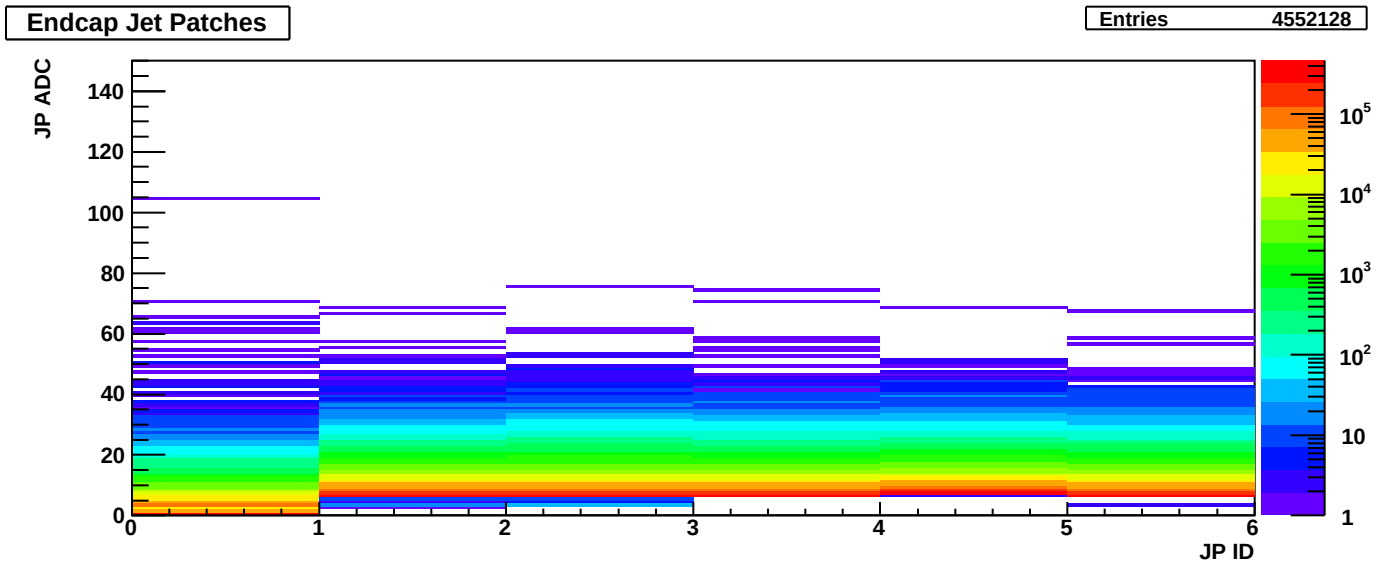

Barrel EMC L0 Input - High Tower
Entries 2.276064e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

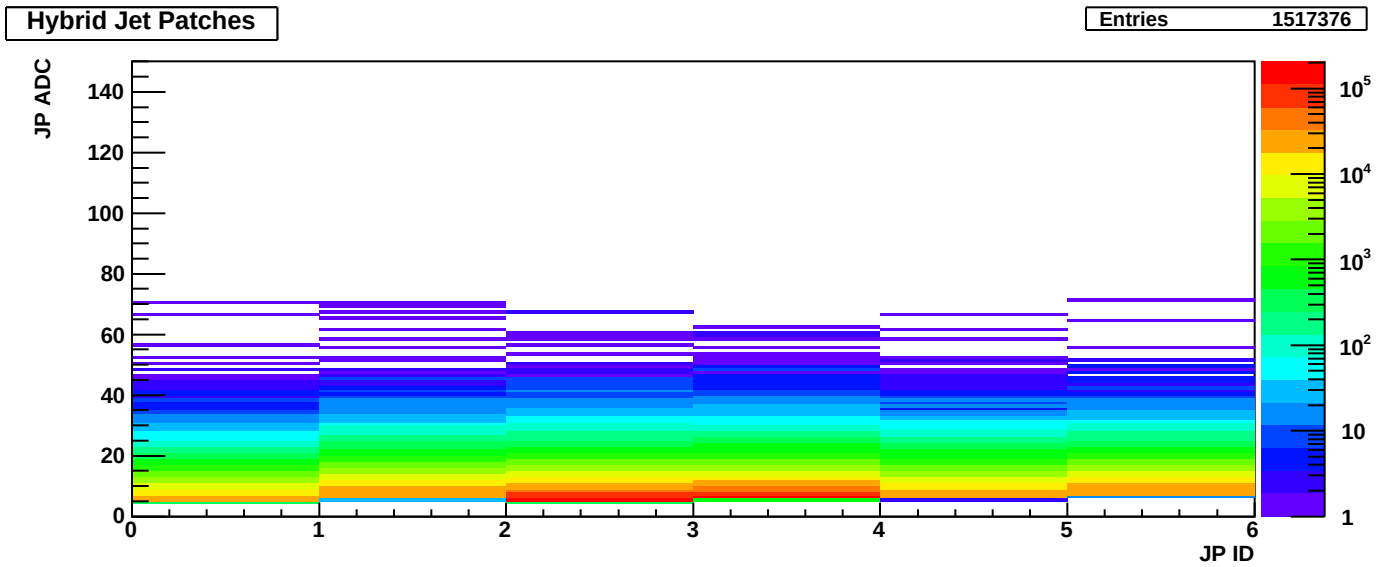

Barrel EMC L0 Input - Patch Sum
Entries 2.276064e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

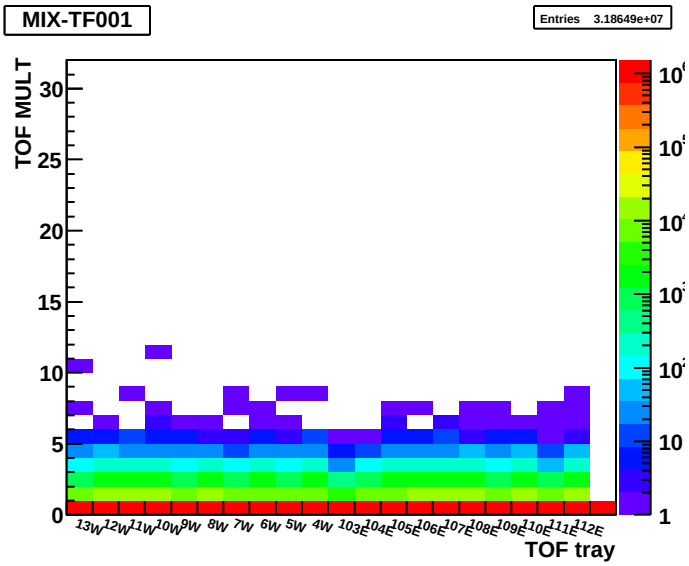

Endcap EMC L0 Input - High Tower
Entries 6.828192e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

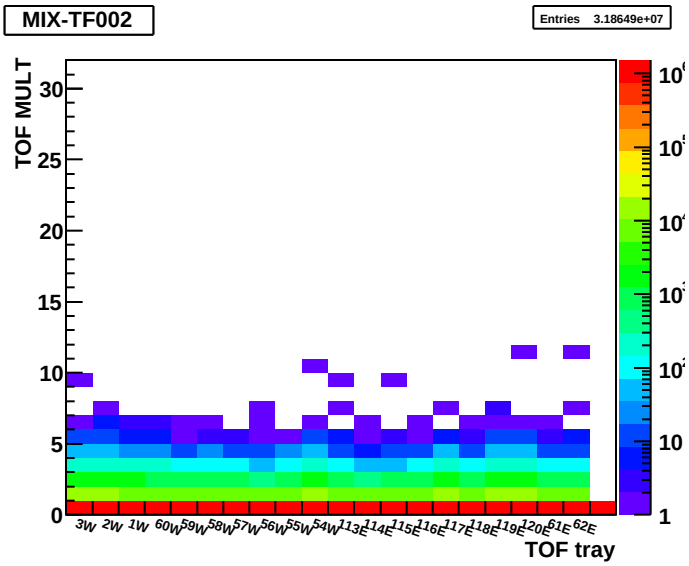

Endcap EMC L0 Input - Patch Sum
Entries 6.828192e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

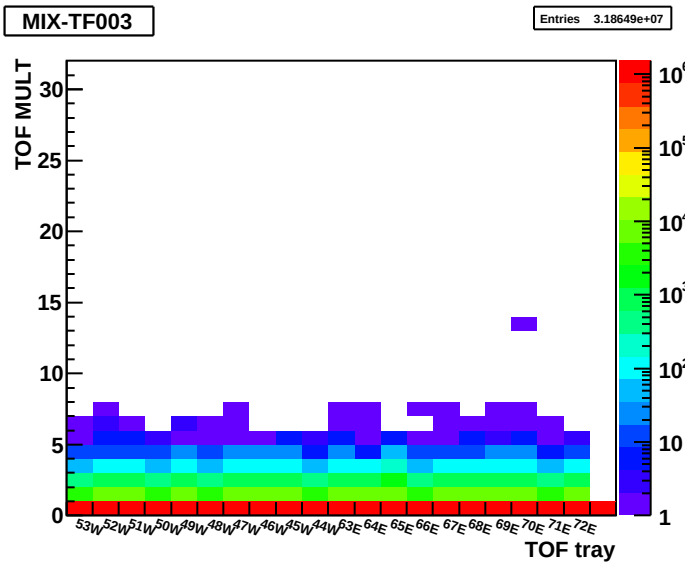

Barrel Jet Patches
Entries 1.365638e+07
JP ADC
JP ID

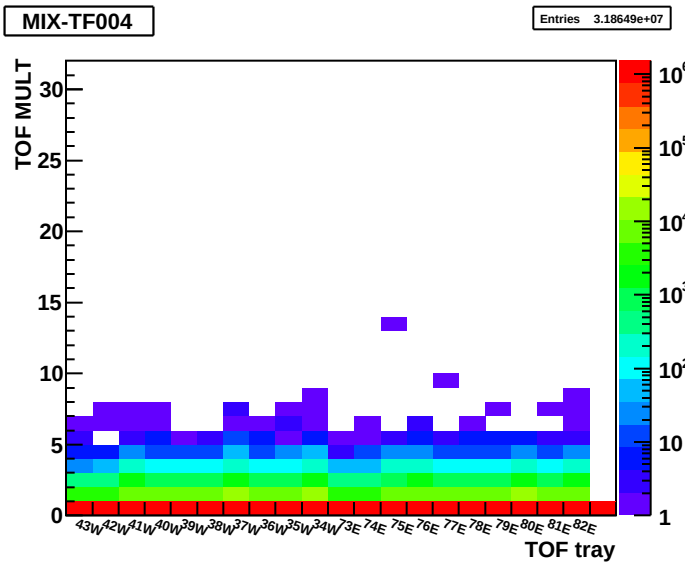

Endcap Jet Patches
Entries 4552128
JP ADC
JP ID

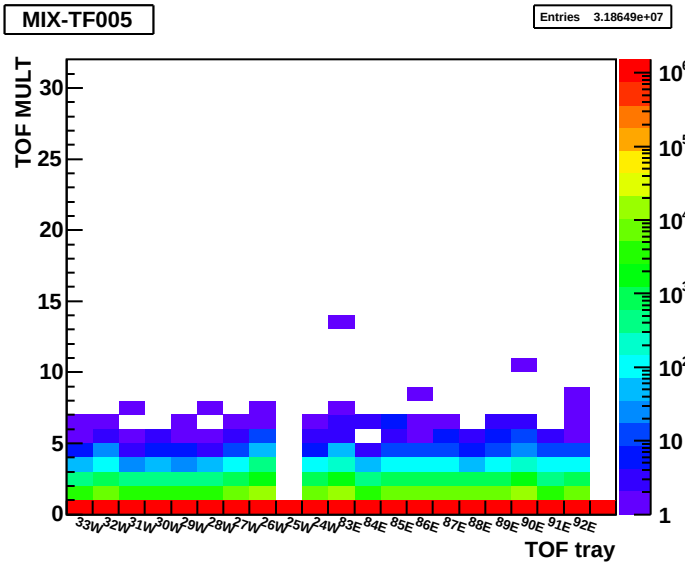

Hybrid Jet Patches
Entries 1517376
JP ADC
JP ID

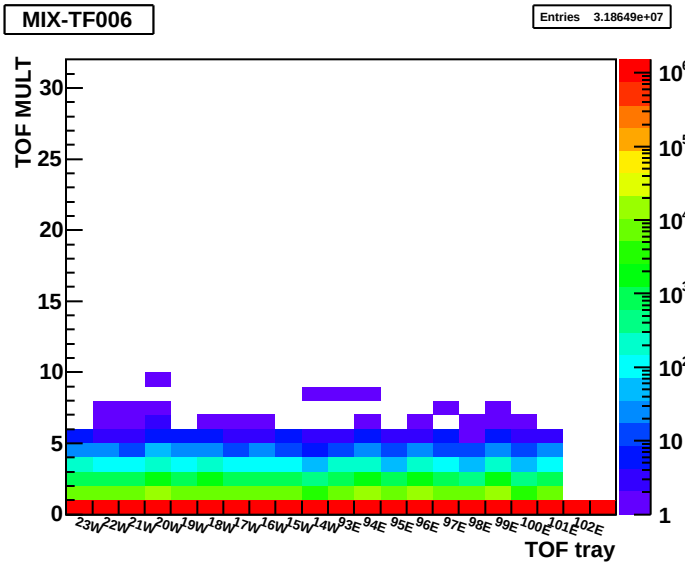

MIX-TF001
Entries 3.18649e+07
TOF MULT
TOF tray

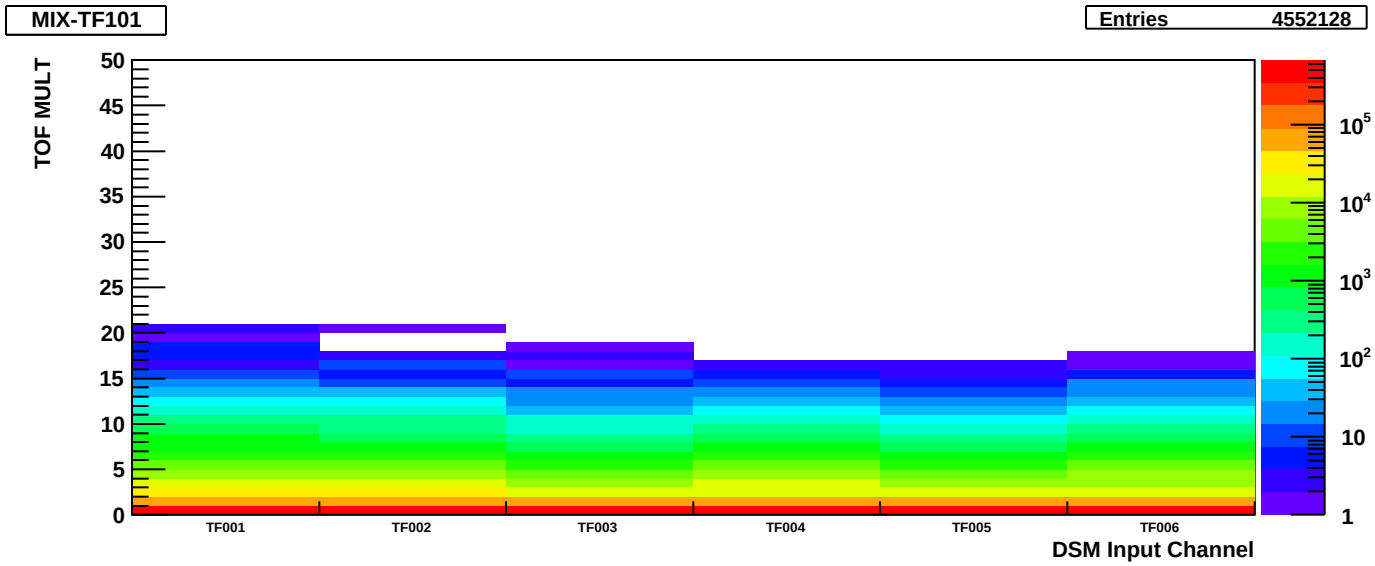

MIX-TF002
Entries 3.18649e+07
TOF MULT
TOF tray

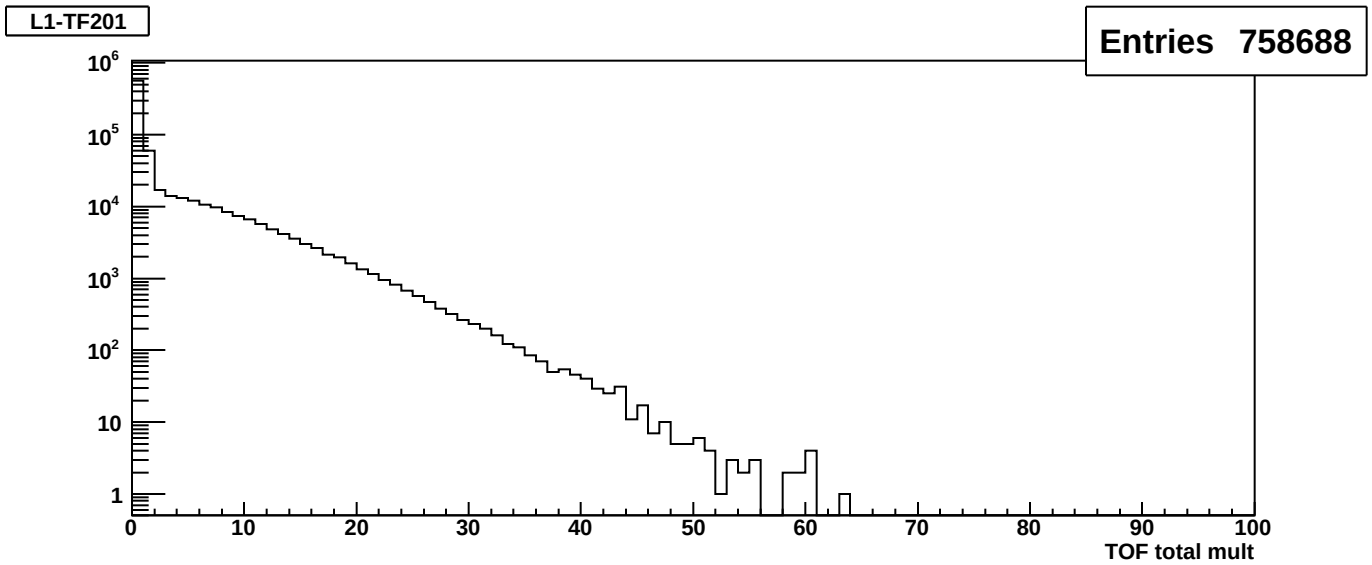

MIX-TF003
Entries 3.18649e+07
TOF MULT
TOF tray

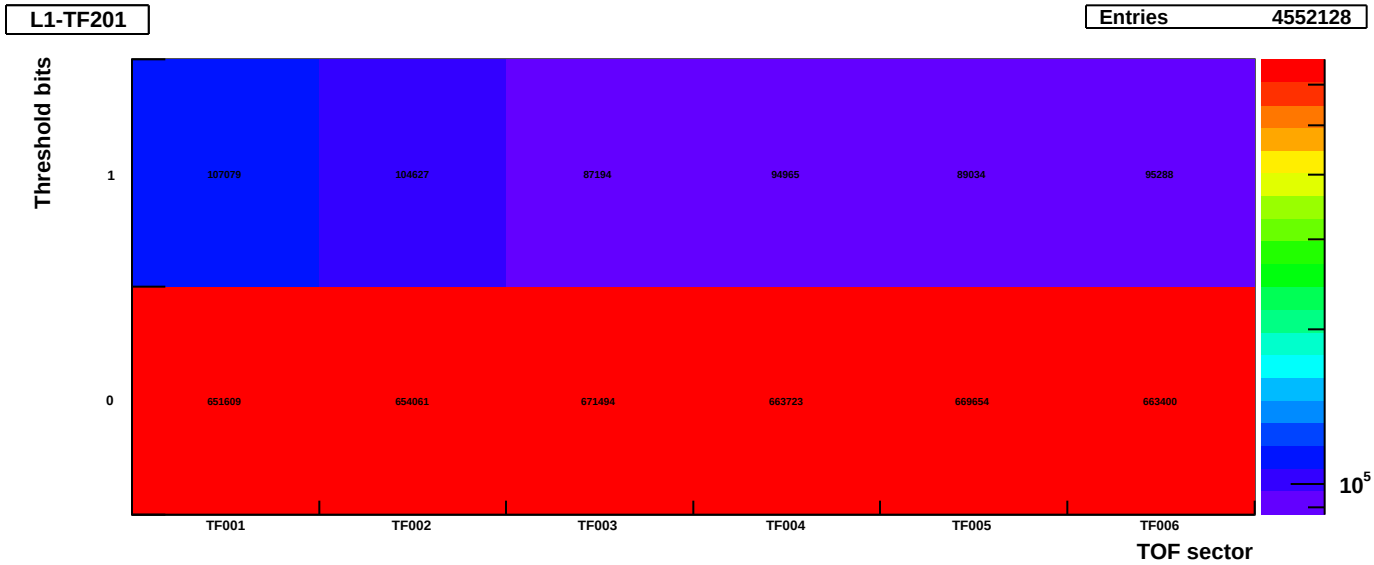

MIX-TF004
Entries 3.18649e+07
TOF MULT
TOF tray

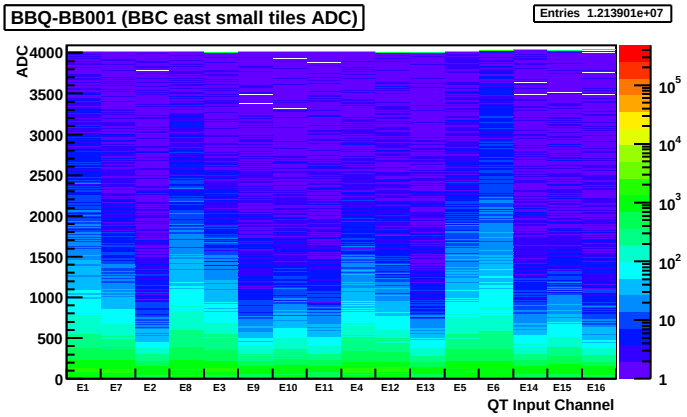

MIX-TF005
Entries 3.18649e+07
TOF MULT
TOF tray

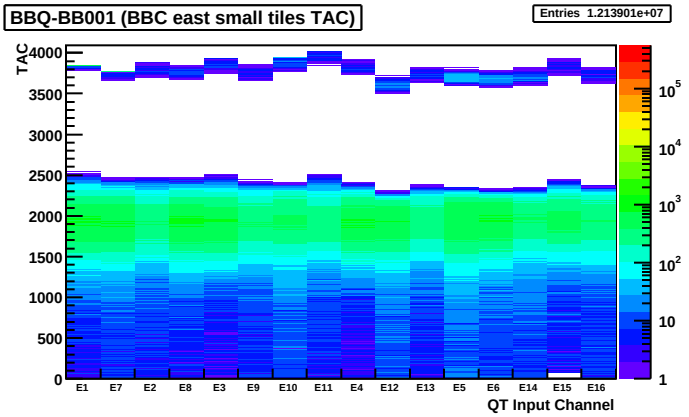

MIX-TF006
Entries 3.18649e+07
TOF MULT
TOF tray

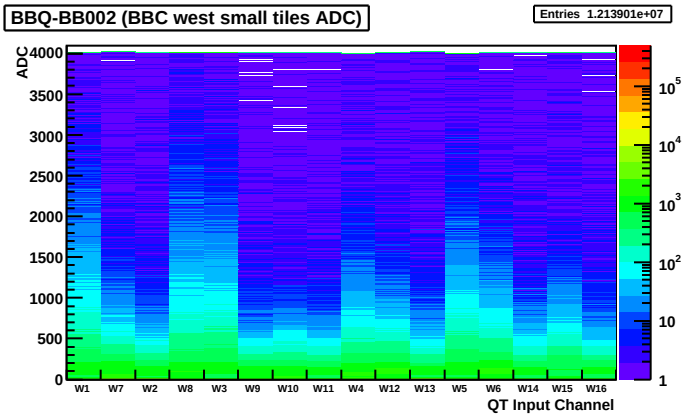

MIX-TF101
Entries 4552128
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^5$
$10^4$
$10^3$
$10^2$
10
1

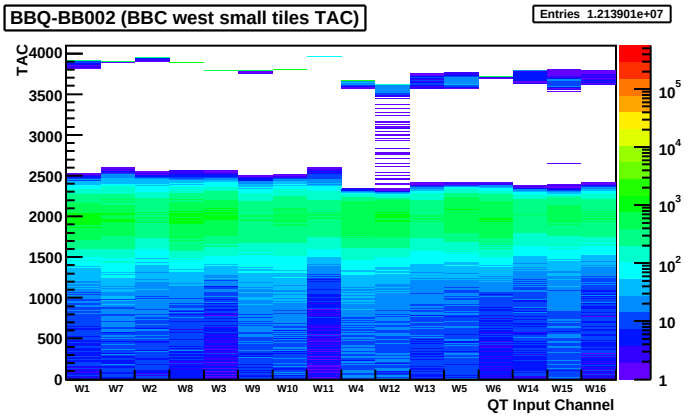

L1-TF201
Entries 758688
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

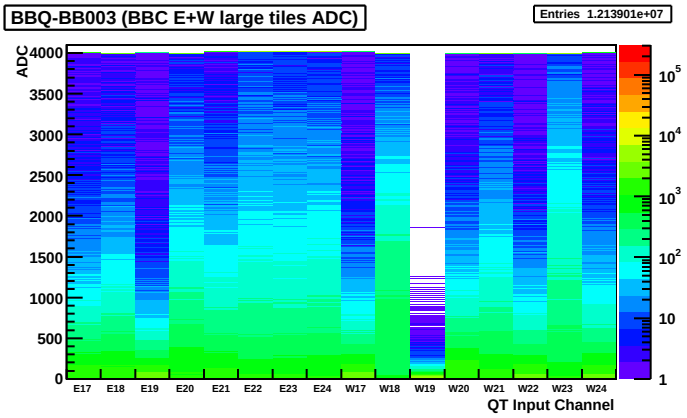

L1-TF201
Entries 4552128
Threshold bits
1
0
107079
104627
87194
94965
89034
95288
651609
654061
671494
663723
669654
663400
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^5$

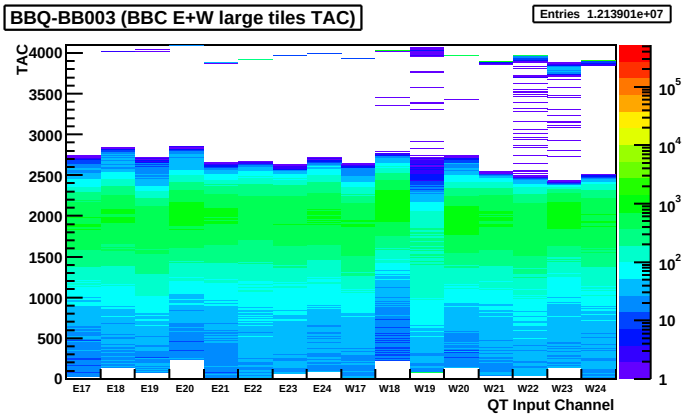

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.213901e+07
ADC
QT Input Channel

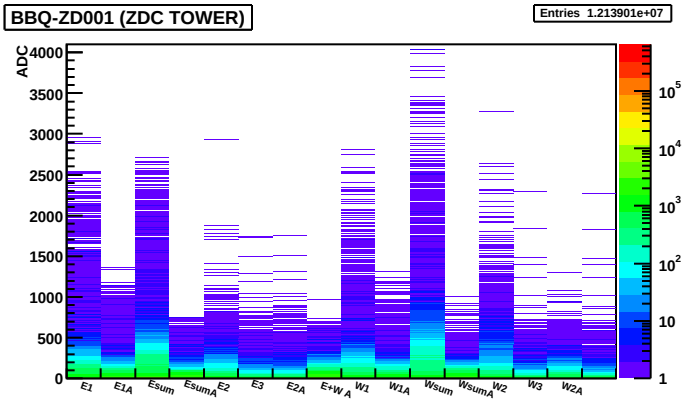

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.213901e+07
TAC
QT Input Channel

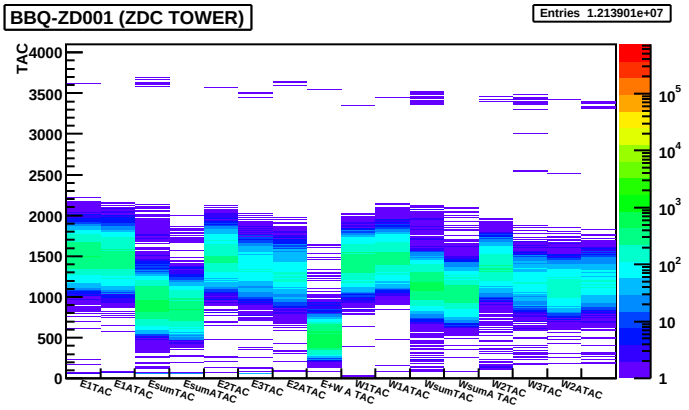

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.213901e+07
ADC
QT Input Channel

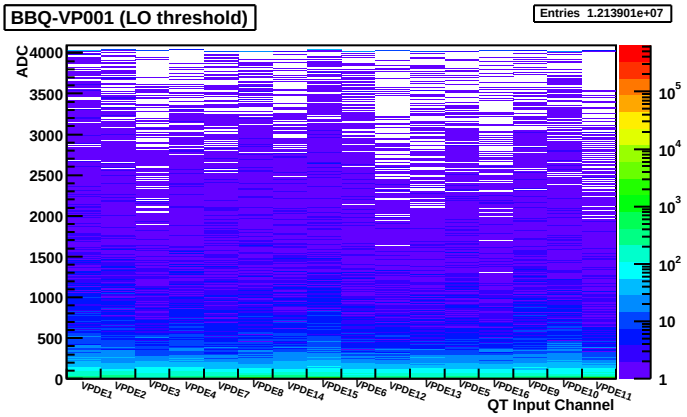

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.213901e+07
TAC
QT Input Channel

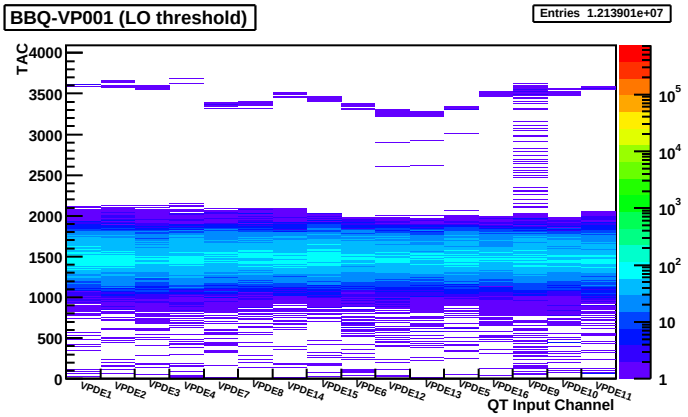

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.213901e+07
ADC
QT Input Channel

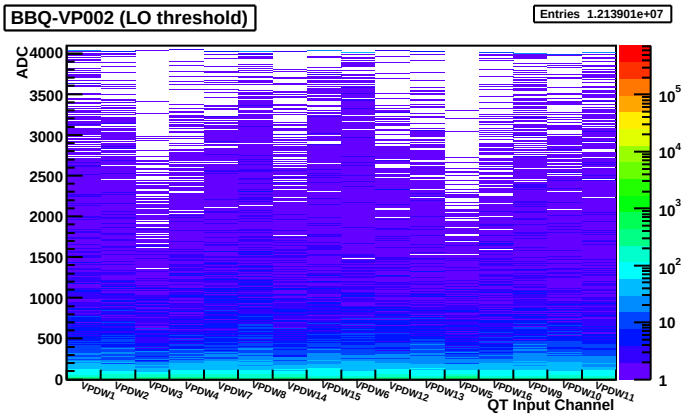

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.213901e+07
TAC
QT Input Channel

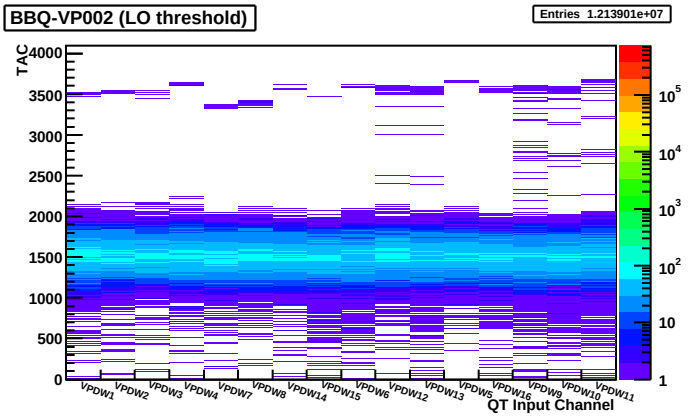

BBQ-ZD001 (ZDC TOWER)
Entries 1.213901e+07
ADC

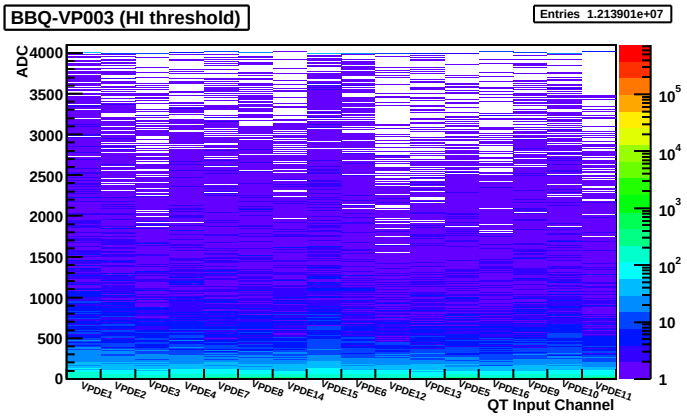

BBQ-ZD001 (ZDC TOWER)
Entries 1.213901e+07
TAC

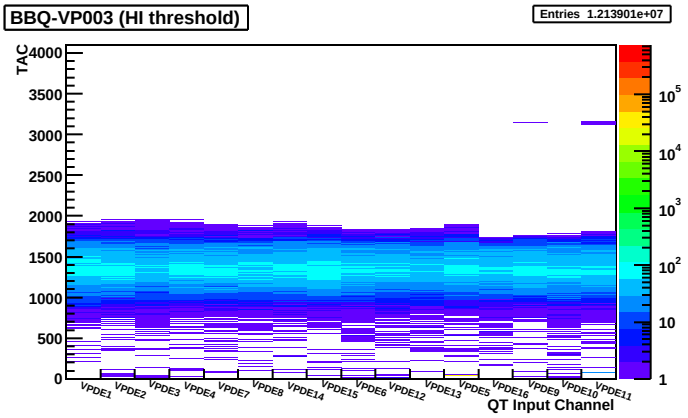

BBQ-VP001 (LO threshold)
Entries 1.213901e+07
ADC
QT Input Channel

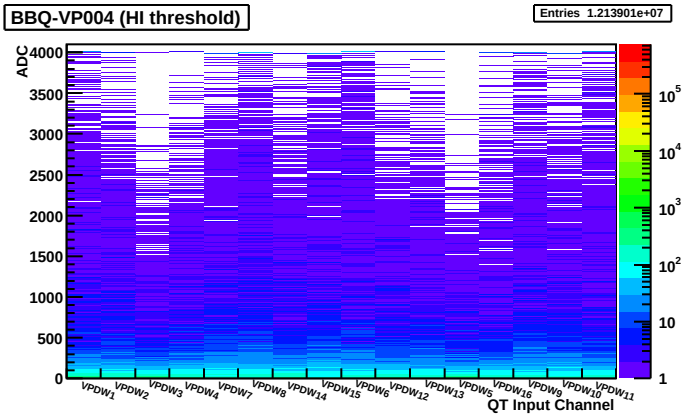

BBQ-VP001 (LO threshold)
Entries 1.213901e+07
TAC
QT Input Channel

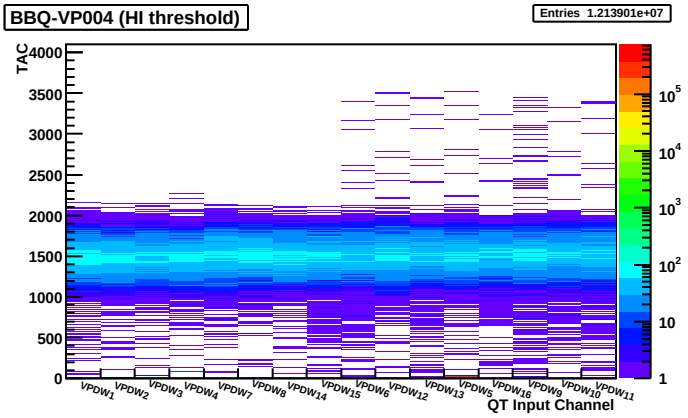

BBQ-VP002 (LO threshold)
Entries 1.213901e+07
ADC
QT Input Channel

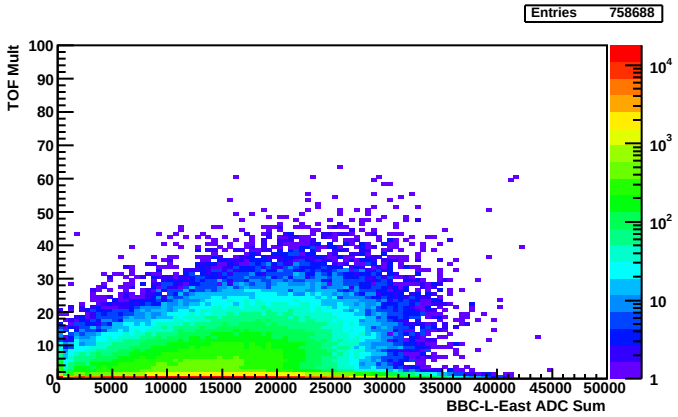

BBQ-VP002 (LO threshold)
Entries 1.213901e+07
TAC
QT Input Channel

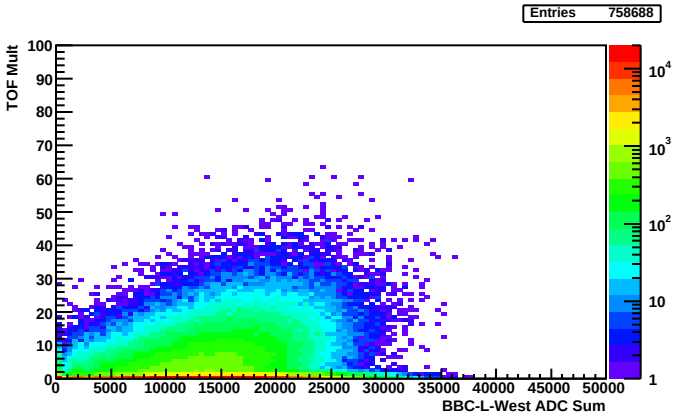

BBQ-VP003 (HI threshold)
Entries 1.213901e+07
ADC
QT Input Channel

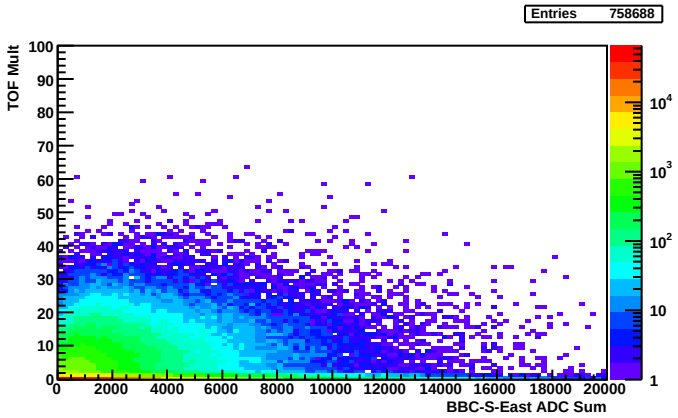

BBQ-VP003 (HI threshold)
Entries 1.213901e+07
TAC
QT Input Channel

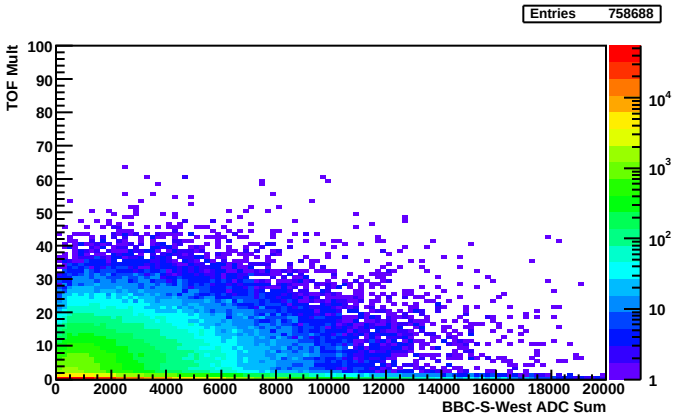

BBQ-VP004 (HI threshold)
Entries 1.213901e+07
ADC
QT Input Channel

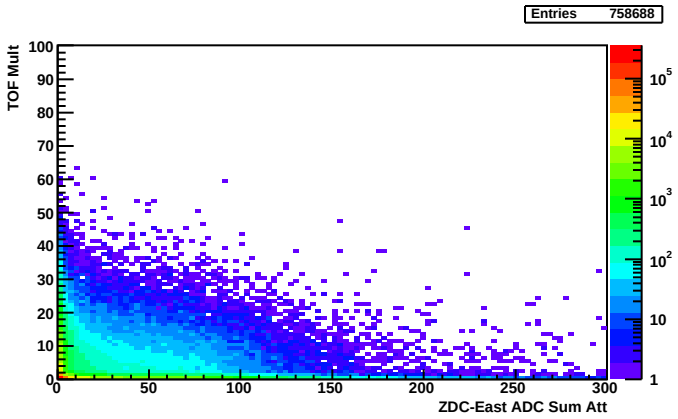

BBQ-VP004 (HI threshold)
Entries 1.213901e+07
TAC
QT Input Channel

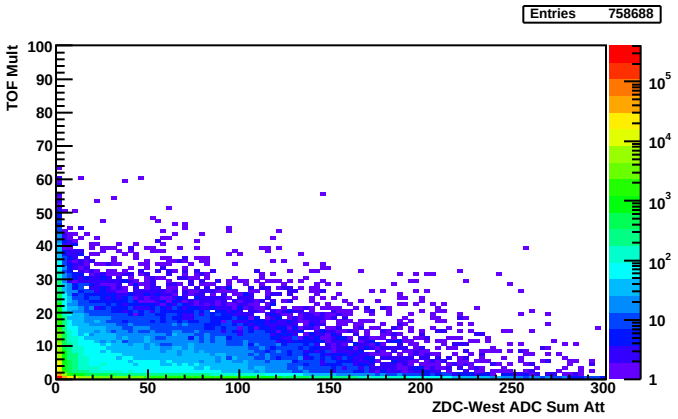

Entries 758688
TOF Mult
BBC-L-East ADC Sum

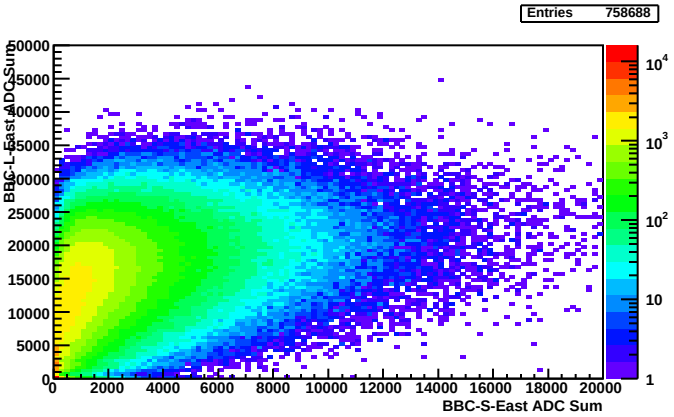

Entries 758688
TOF Mult
BBC-L-West ADC Sum

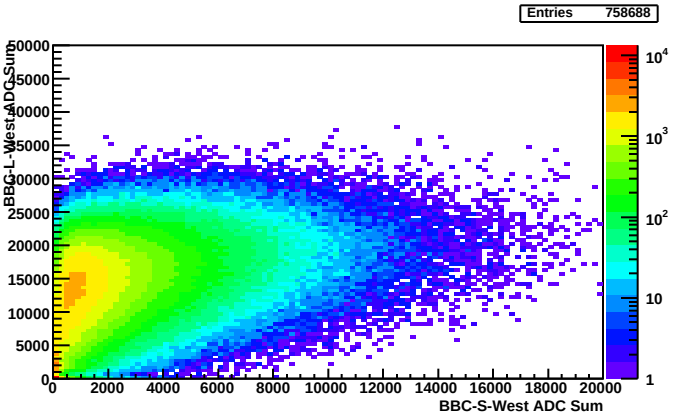

Entries 758688
TOF Mult
BBC-S-East ADC Sum

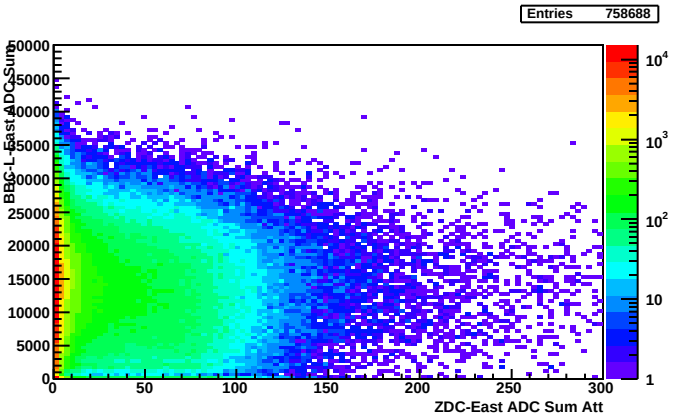

Entries 758688
TOF Mult
BBC-S-West ADC Sum

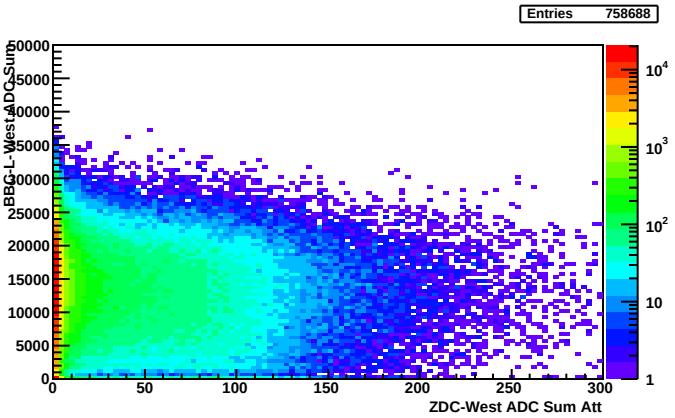

Entries 758688
TOF Mult
ZDC-East ADC Sum Att

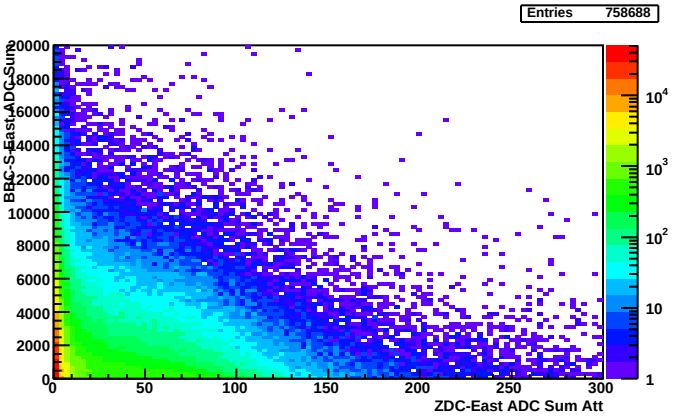

Entries 758688
TOF Mult
ZDC-West ADC Sum Att

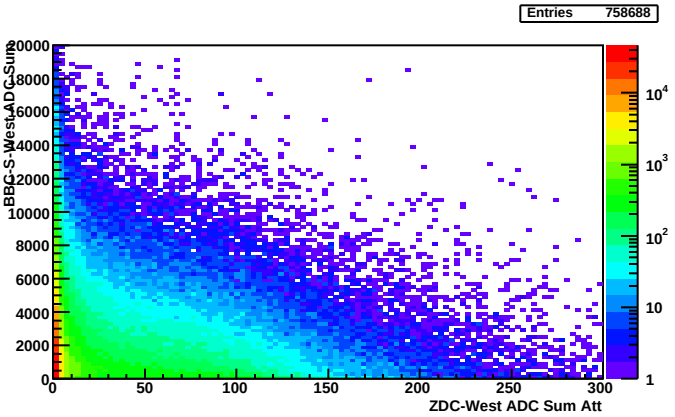

Entries 758688
BBC-L-East ADC Sum
BBC-S-East ADC Sum

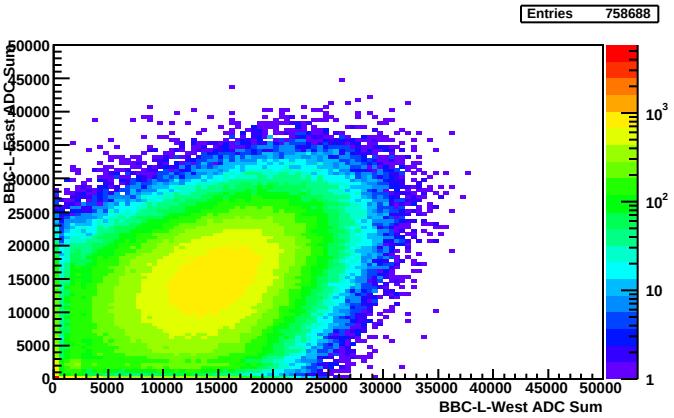

Entries 758688
BBC-L-West ADC Sum
BBC-S-West ADC Sum

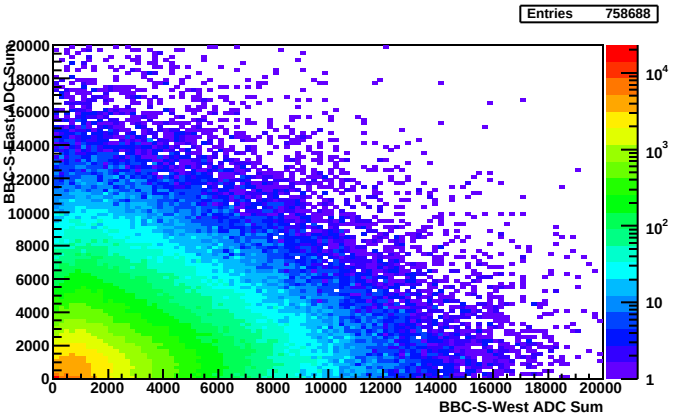

Entries 758688
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

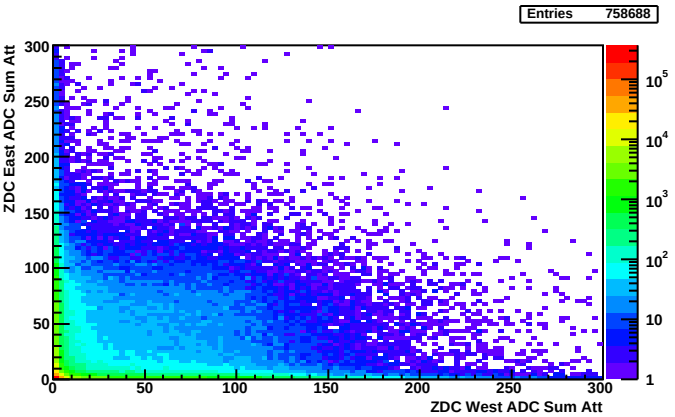

Entries 758688
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

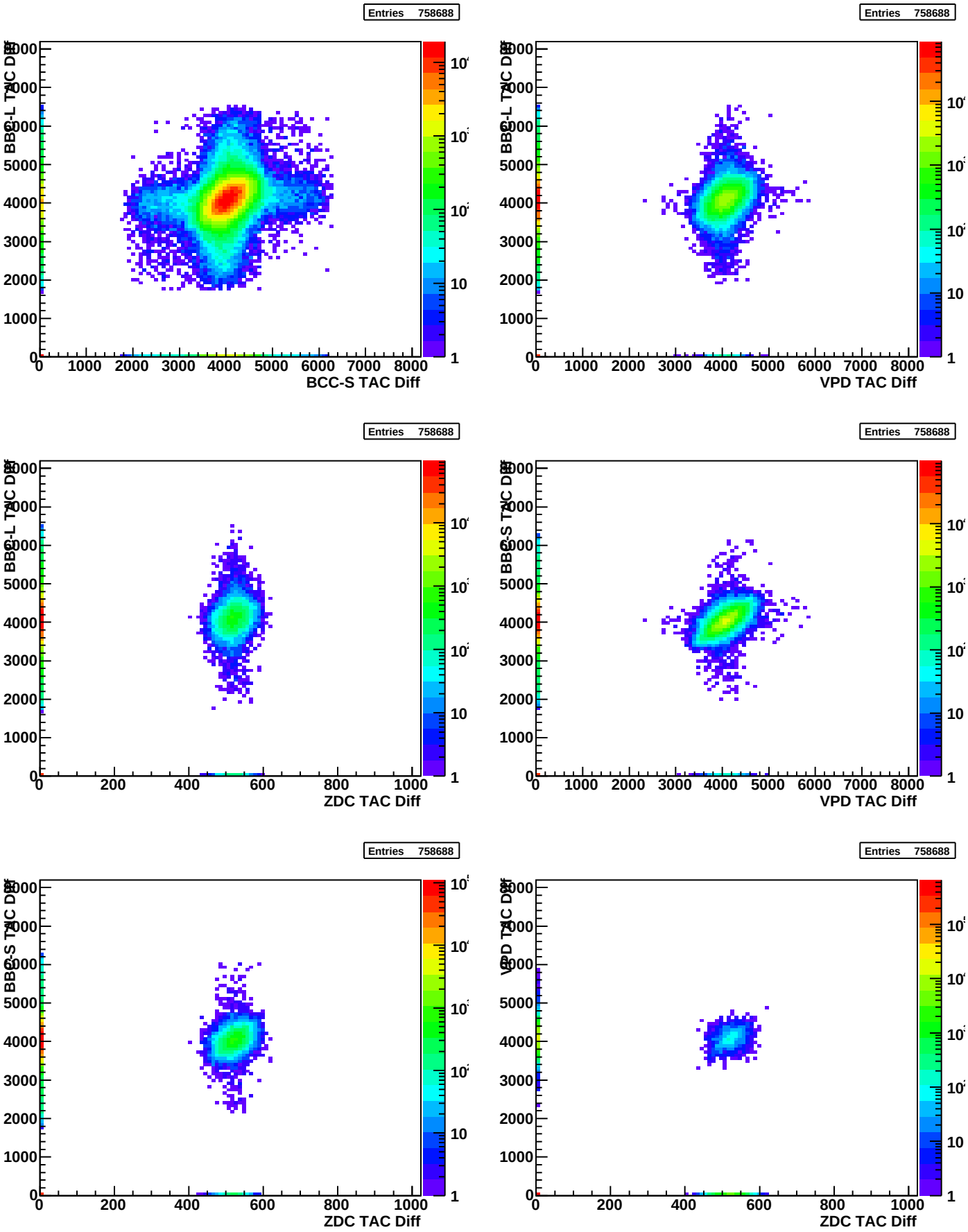

Entries 758688
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

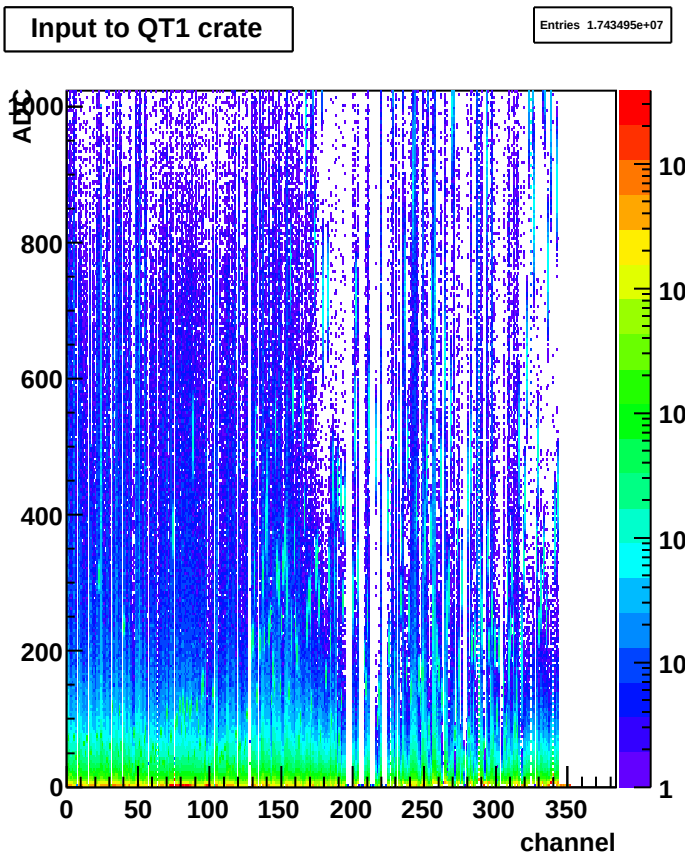

Entries 758688
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

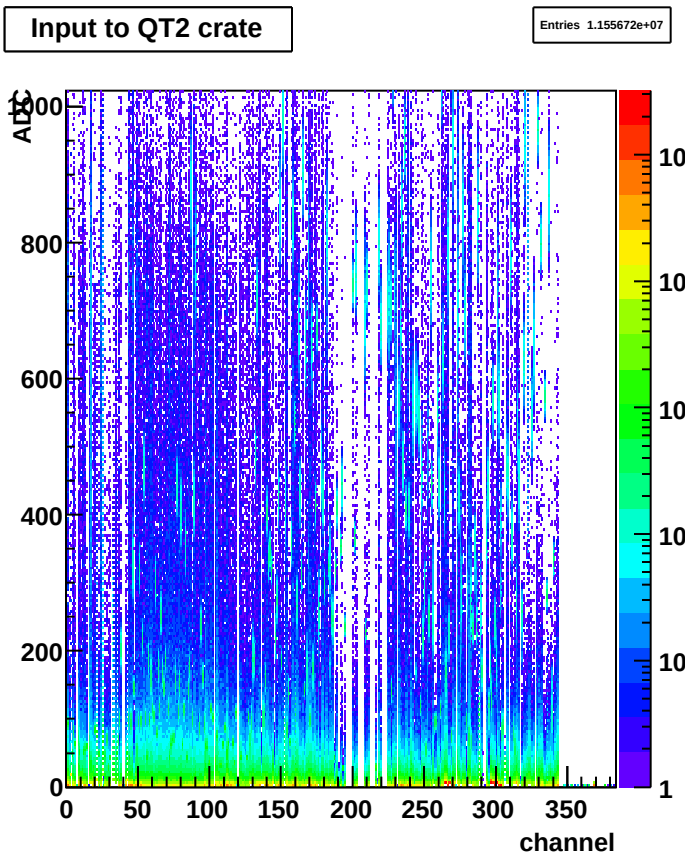

Entries 758688
BBC-L-East ADC Sum
BBC-L-West ADC Sum

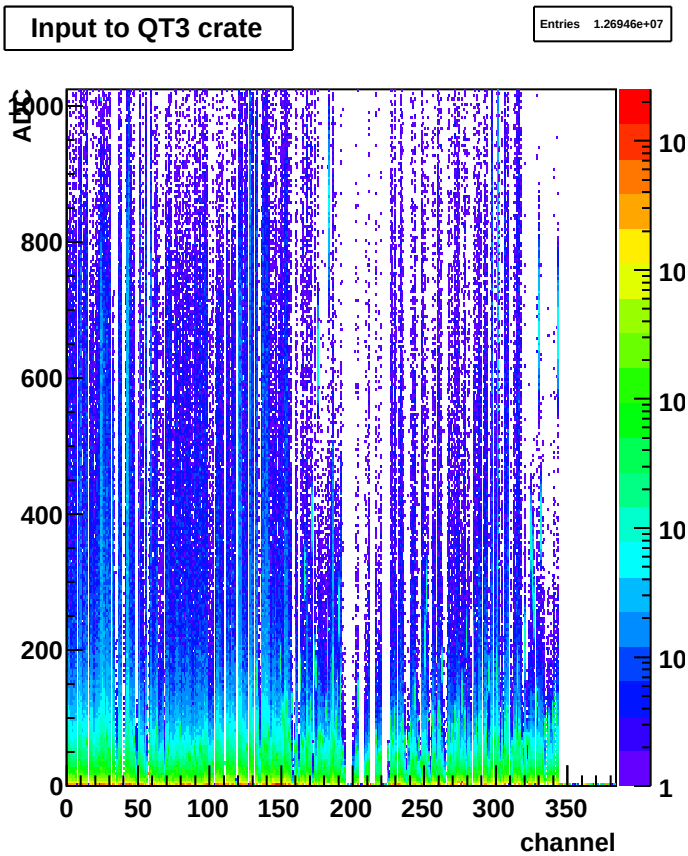

Entries 758688
BBC-S-East ADC Sum
BBC-S-West ADC Sum

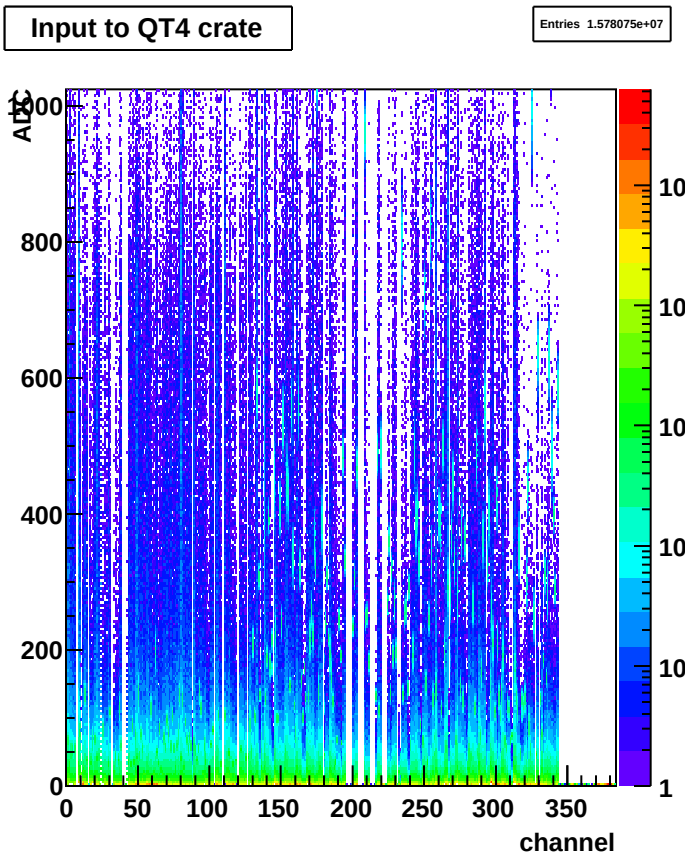

Entries 758688
ZDC East ADC Sum Att
ZDC West ADC Sum Att

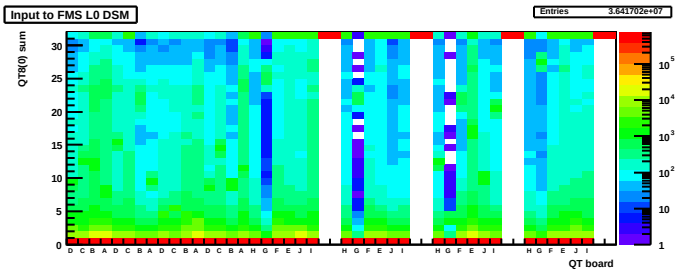

Entries 758688
BBC-L TAC Diff
BCC-S TAC Diff
Entries 758688
BBC-L TAC Diff
VPD TAC Diff
Entries 758688
BBC-L TAC Diff
ZDC TAC Diff
Entries 758688
BBC-S TAC Diff
VPD TAC Diff
Entries 758688
BBC-S TAC Diff
ZDC TAC Diff
Entries 758688
VPD TAC Diff
ZDC TAC Diff

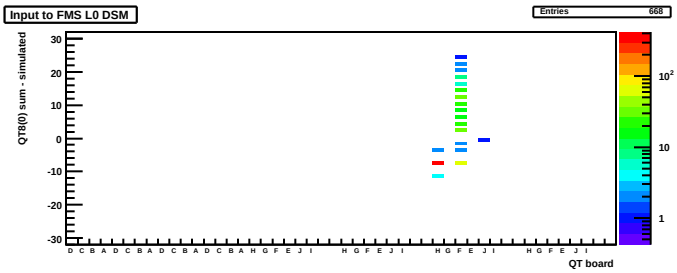

Input to QT1 crate
Entries 1.743495e+07
ADC
channel

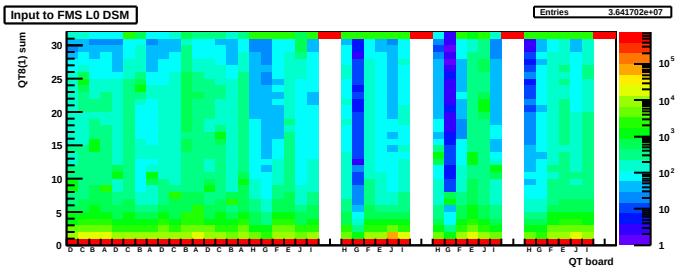

Input to QT2 crate
Entries 1.155672e+07
ADC
channel

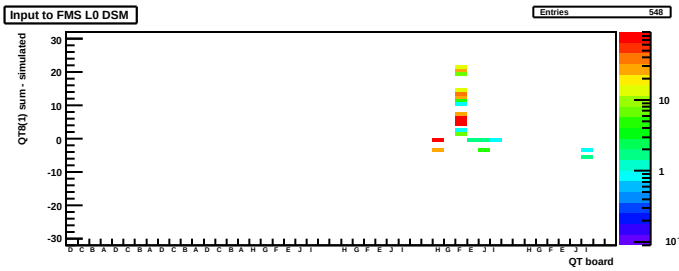

Input to QT3 crate
Entries 1.26946e+07
ADC
channel

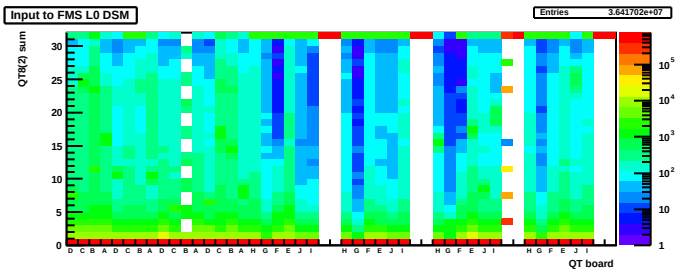

Input to QT4 crate
Entries 1.578075e+07
ADC
channel

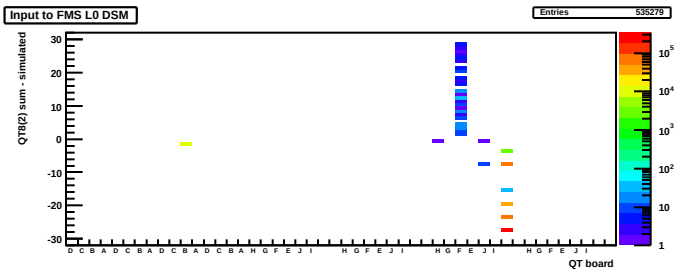

Input to FMS L0 DSM
Entries 3.641702e+07
QT8(0) sum
QT board

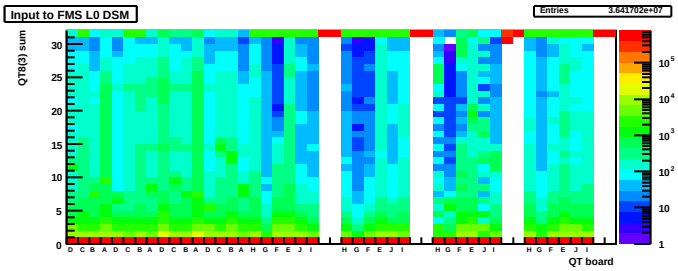

Input to FMS L0 DSM
Entries 668
QT8(0) sum - simulated
QT board

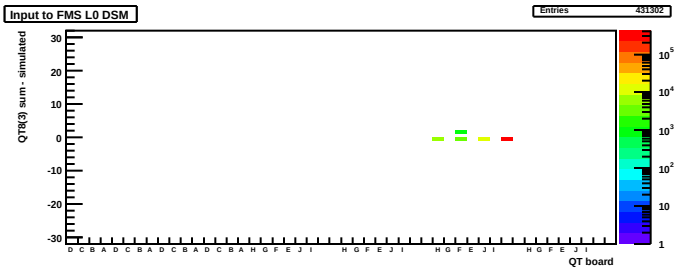

Input to FMS L0 DSM
Entries 3.641702e+07
QT8(1) sum
QT board

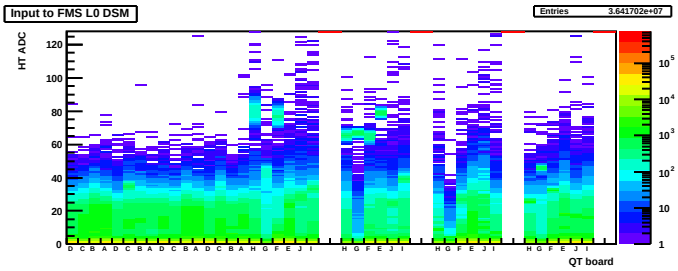

Input to FMS L0 DSM
Entries 548
QT8(1) sum - simulated
QT board

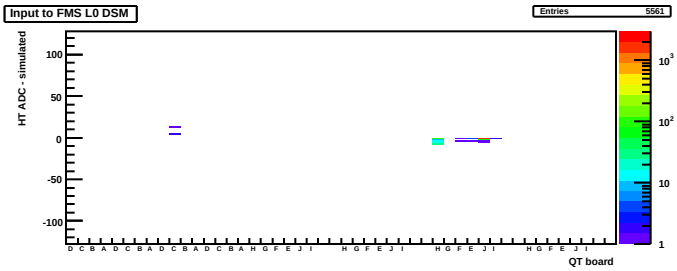

Input to FMS L0 DSM
Entries 3.641702e+07
QT8(2) sum
QT board

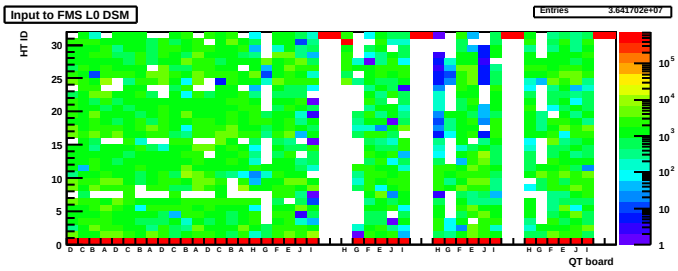

Input to FMS L0 DSM
Entries 535279
QT8(2) sum - simulated
QT board

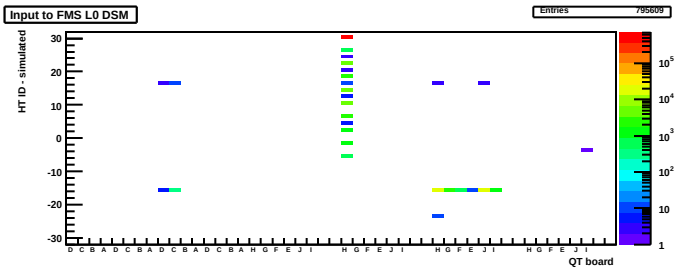

Input to FMS L0 DSM
Entries 3.641702e+07
QT8(3) sum
QT board

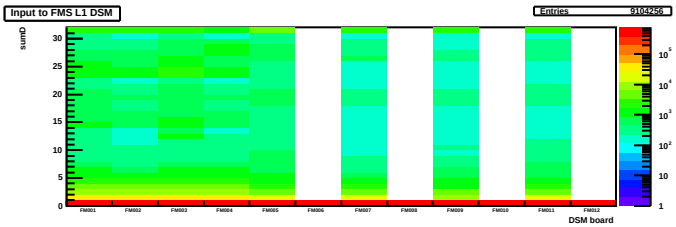

Input to FMS L0 DSM
Entries 431302
QT8(3) sum - simulated
QT board

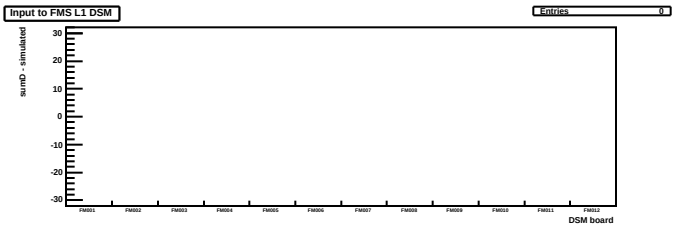

Input to FMS L0 DSM
Entries 3.641702e+07
HT ADC
QT board

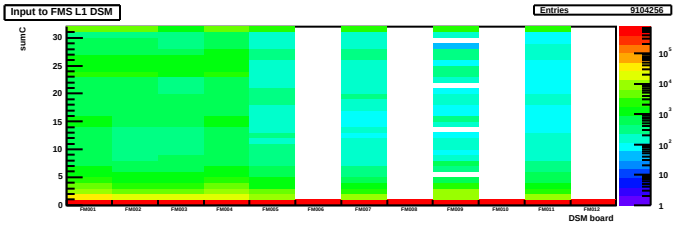

Input to FMS L0 DSM
Entries 5561
HT ADC - simulated
QT board

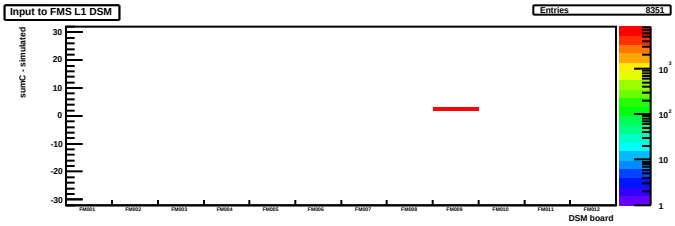

Input to FMS L0 DSM
Entries 3.641702e+07
HT ID
QT board

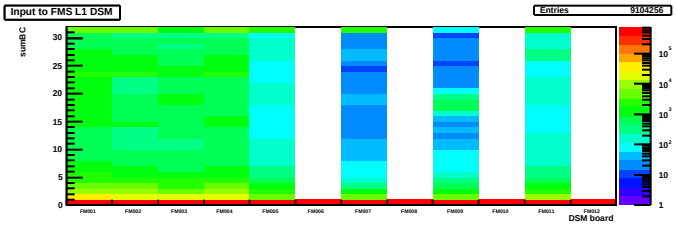

Input to FMS L0 DSM
Entries 795609
HT ID - simulated
QT board

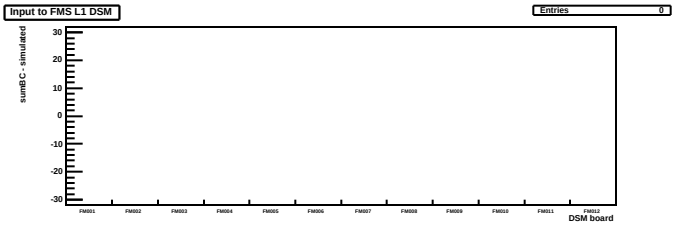

Input to FMS L1 DSM
Entries 9104256
sumD
DSM board

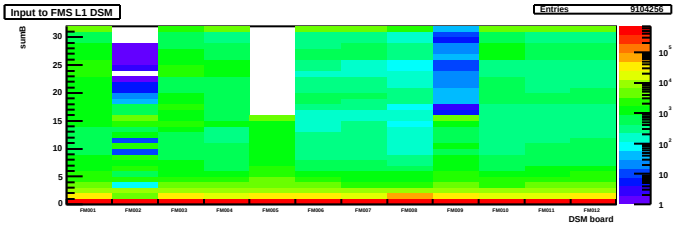

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

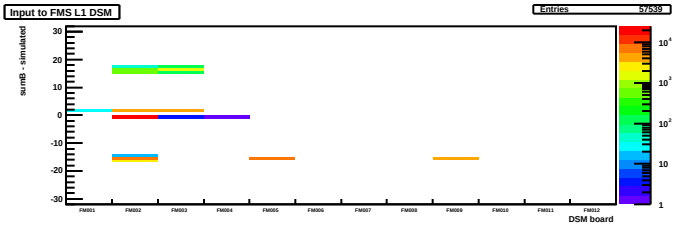

Input to FMS L1 DSM
Entries 9104256
sumC
DSM board

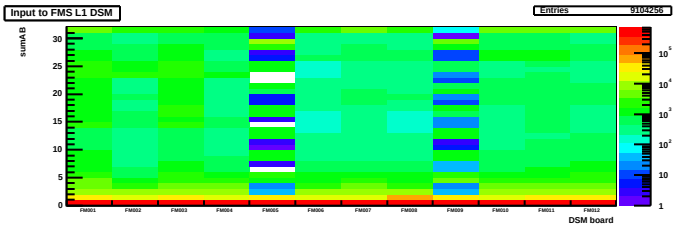

Input to FMS L1 DSM
Entries 8351
sumC - simulated
DSM board

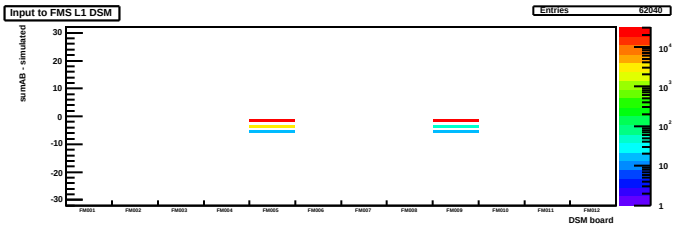

Input to FMS L1 DSM
Entries 9104256
sumBC
DSM board

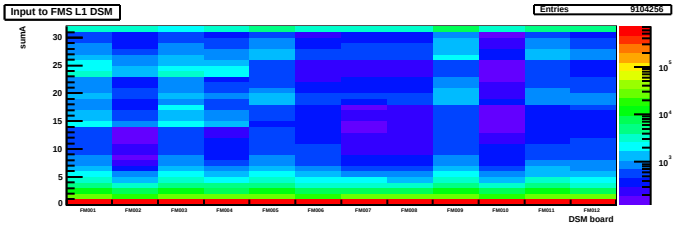

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

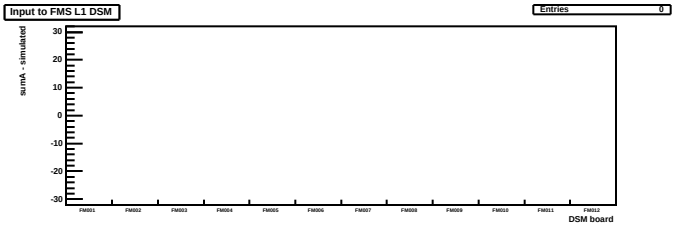

Input to FMS L1 DSM
Entries 9104256
sumB
DSM board

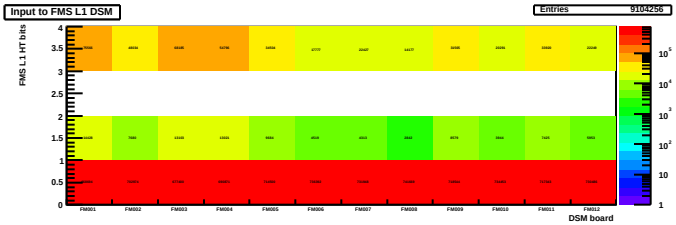

Input to FMS L1 DSM
Entries 57539
sumB - simulated
DSM board

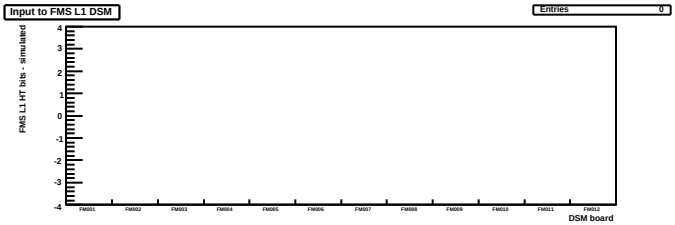

Input to FMS L1 DSM
Entries 9104256
sumAB
DSM board

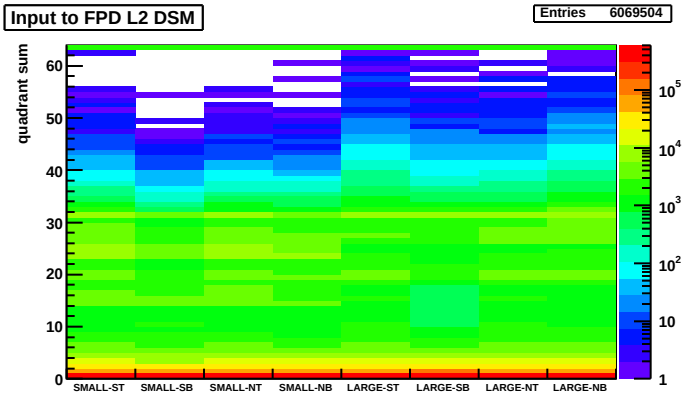

Input to FMS L1 DSM
Entries 62040
sumAB - simulated
DSM board

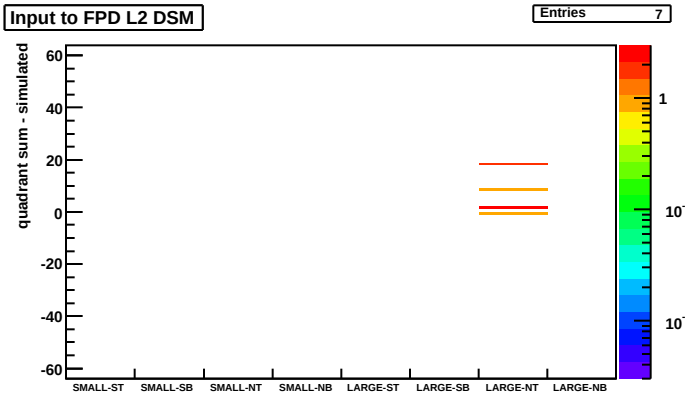

Input to FMS L1 DSM
Entries 9104256
sumA
DSM board

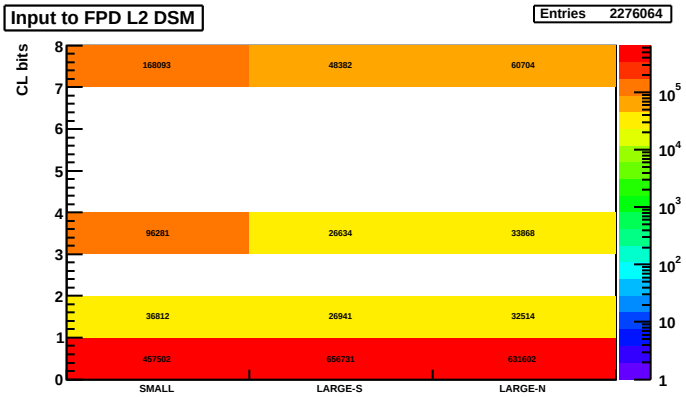

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

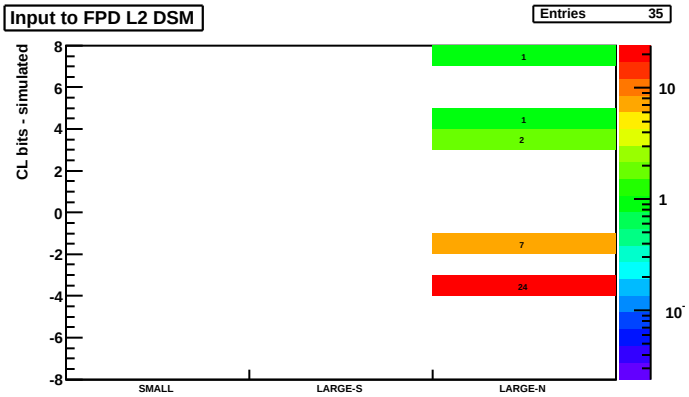

Input to FMS L1 DSM
Entries 9104256
FMS L1 HT bits
DSM board

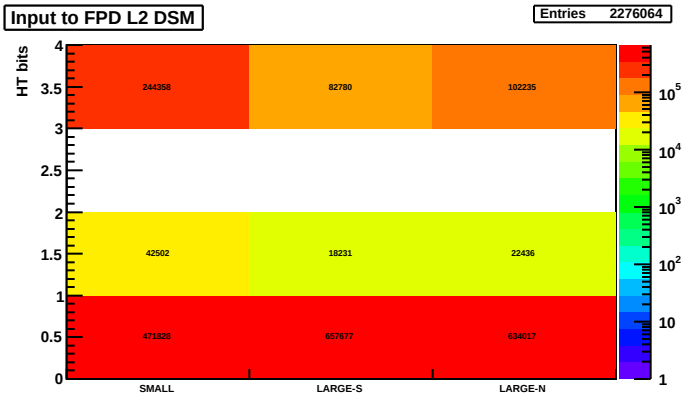

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

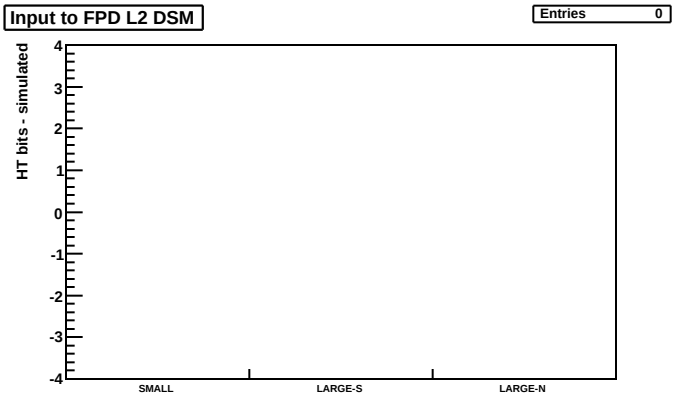

Input to FPD L2 DSM
Entries 6069504
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

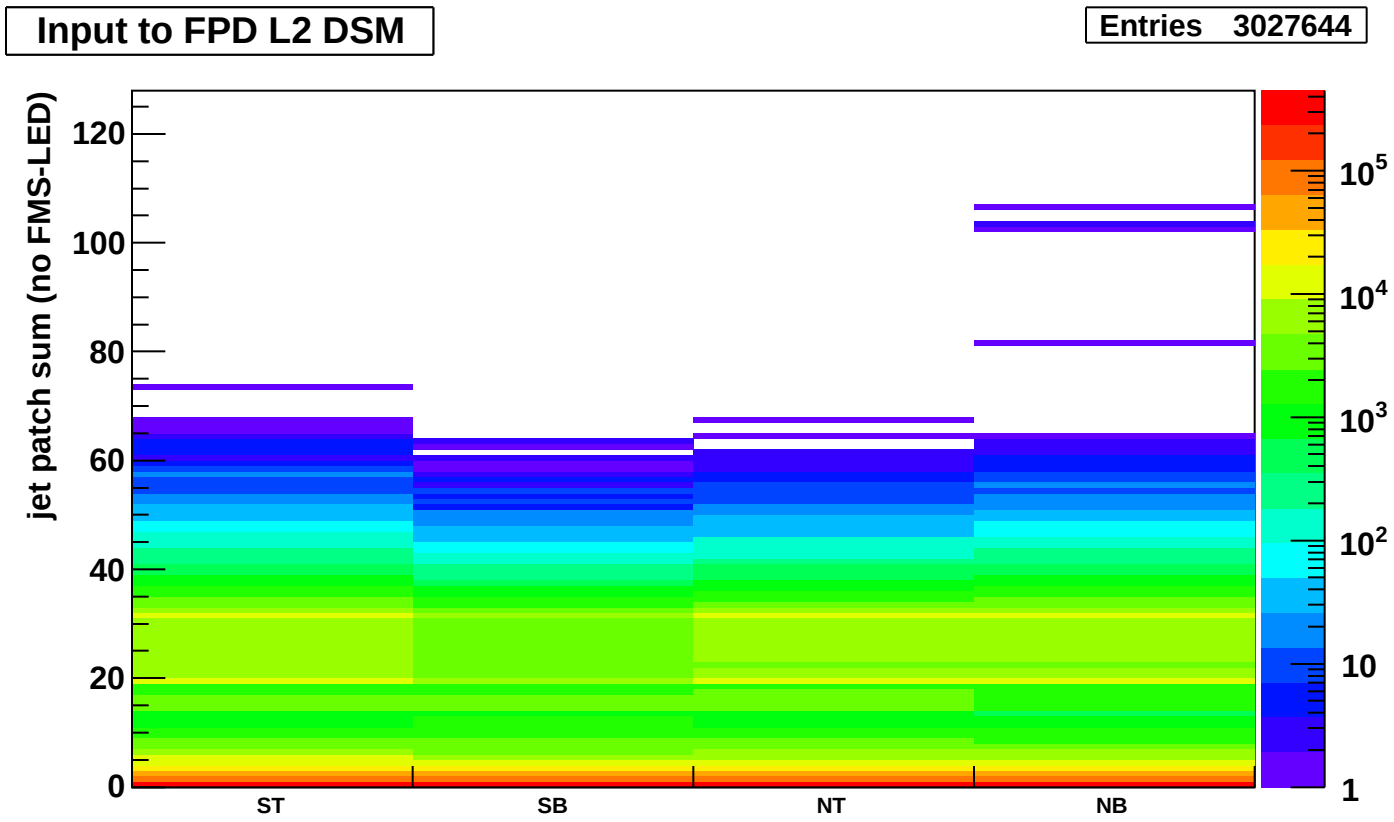

Input to FPD L2 DSM
Entries 7
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

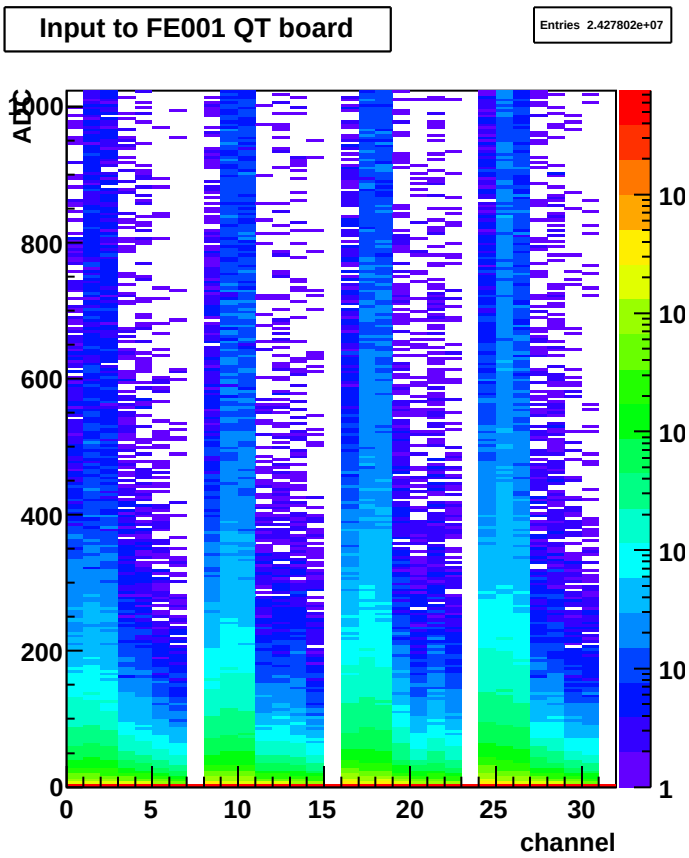

Input to FPD L2 DSM
Entries 2276064
CL bits
168093
48382
60704
96281
26634
33868
36812
26941
32514
457502
656731
631602
SMALL
LARGE-S
LARGE-N

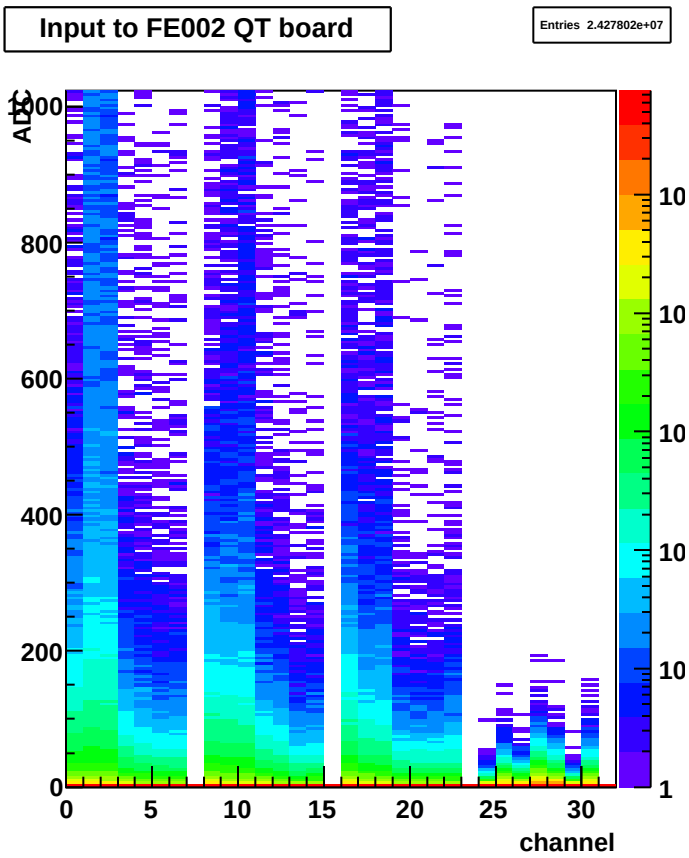

Input to FPD L2 DSM
Entries 35
CL bits - simulated
1
1
2
7
24
SMALL
LARGE-S
LARGE-N

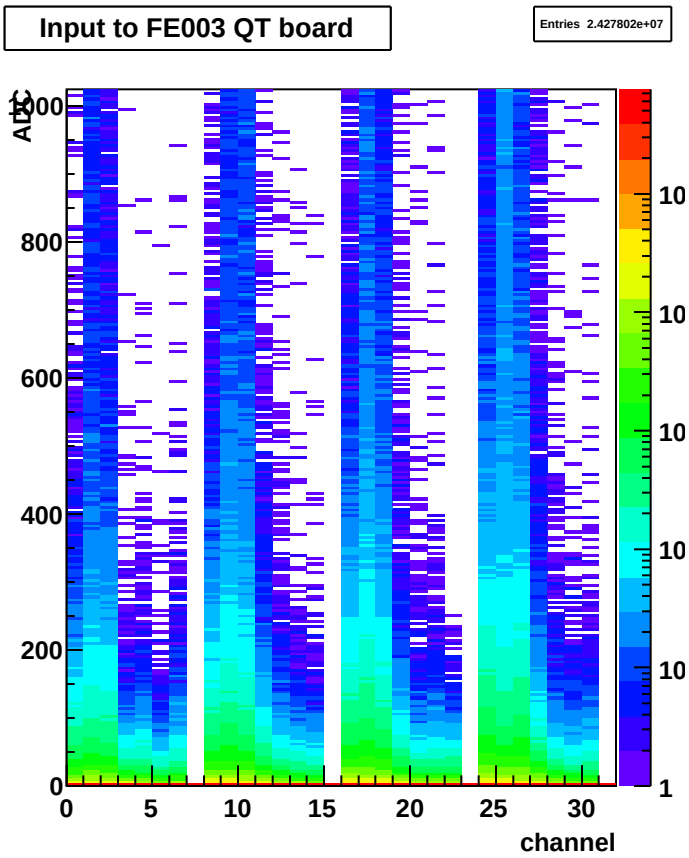

Input to FPD L2 DSM
Entries 2276064
HT bits
244358
82780
102235
42502
18231
22436
471828
657677
634017
SMALL
LARGE-S
LARGE-N

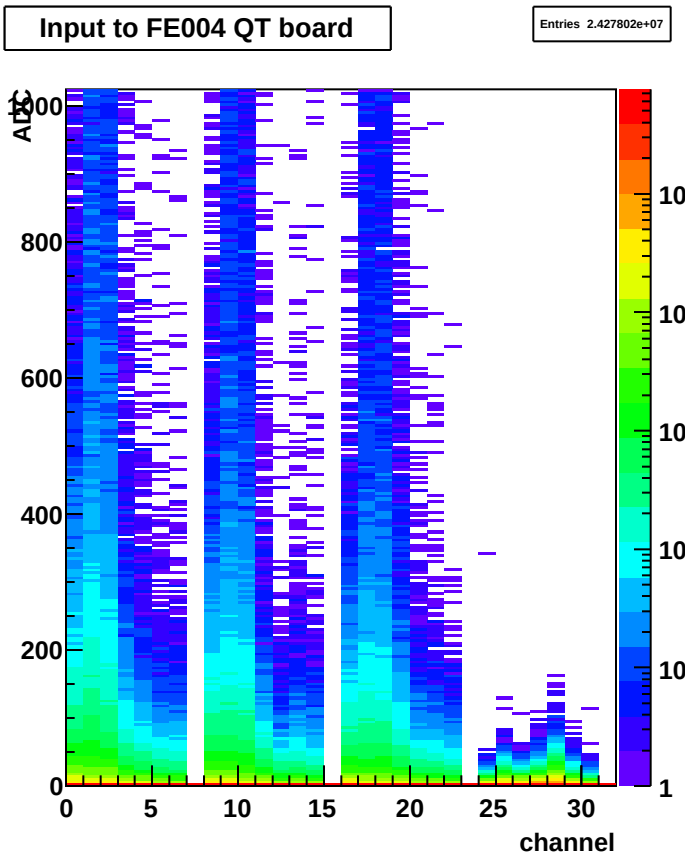

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

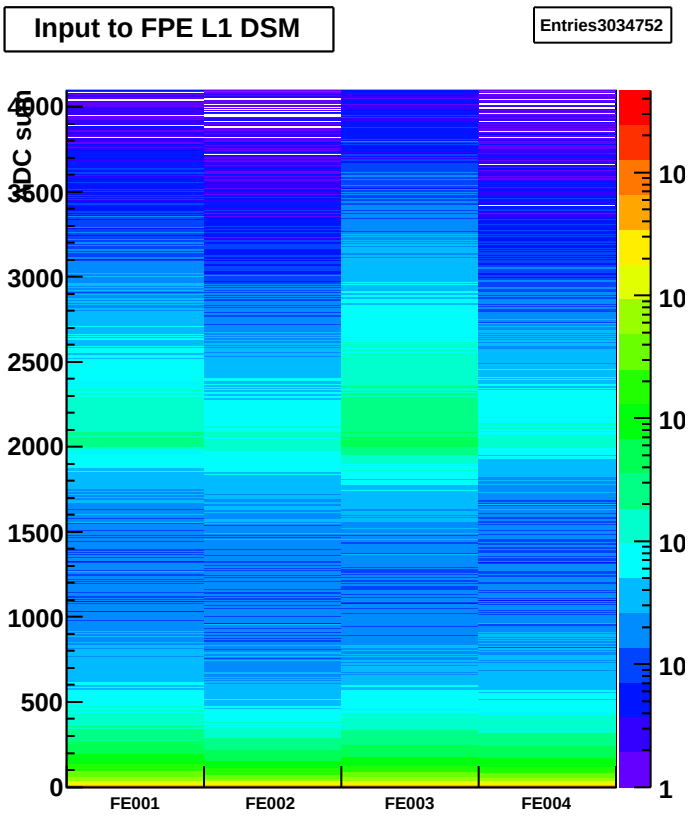

Input to FPD L2 DSM
Entries 3027644
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$
$10^5$

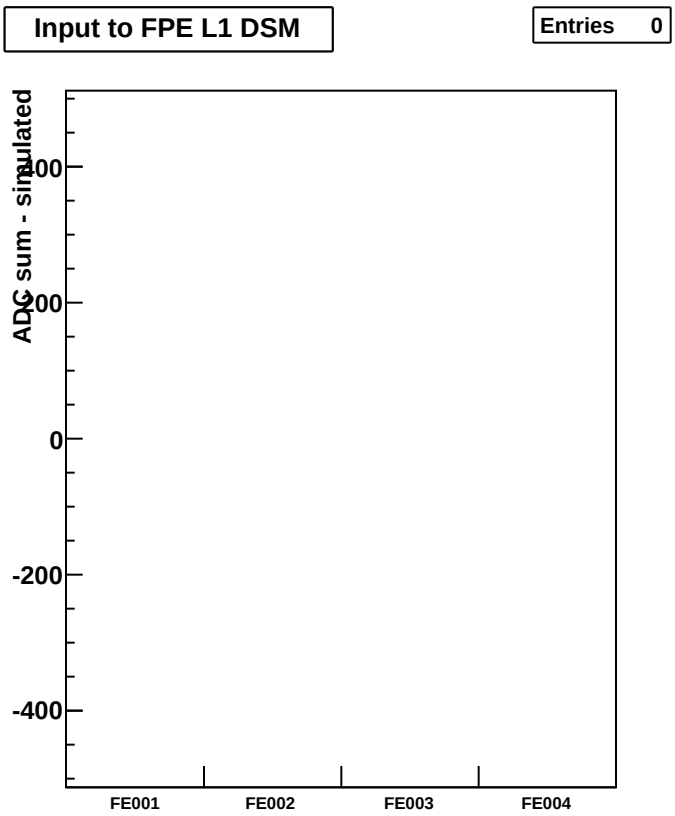

Input to FE001 QT board
Entries 2.427802e+07
ADC
channel


Input to FE002 QT board
Entries 2.427802e+07
ADC
channel

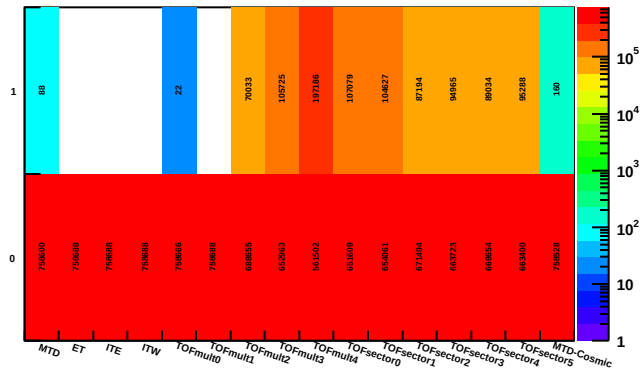

Input to FE003 QT board
Entries 2.427802e+07
ADC
channel


Input to FE004 QT board
Entries 2.427802e+07
ADC
channel

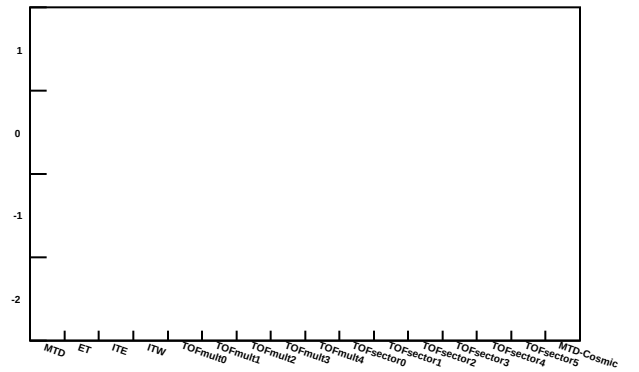

Input to FPE L1 DSM
Entries3034752
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
100
200
0
-200
-400
FE001
FE002
FE003
FE004

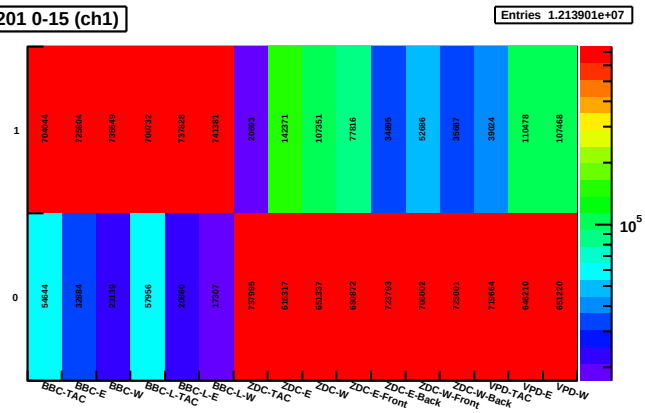
TF201 0-15 (ch0)



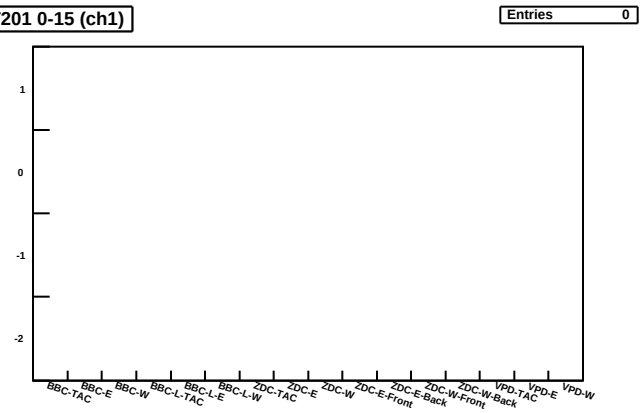

TF201 0-15 (ch0)



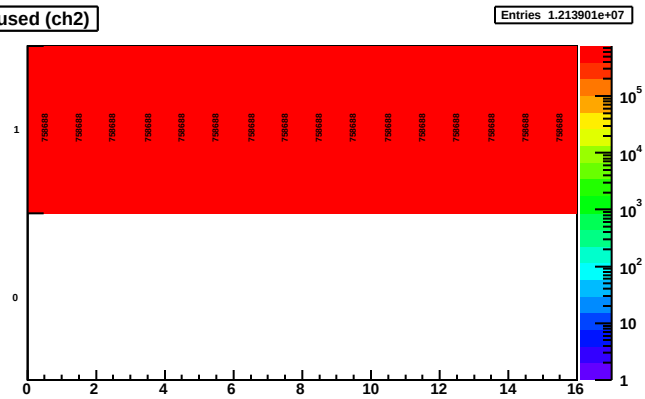

VT201 0-15 (ch1)

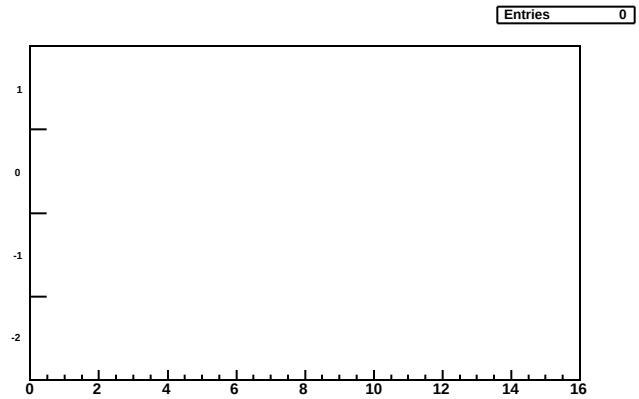



VT201 0-15 (ch1)

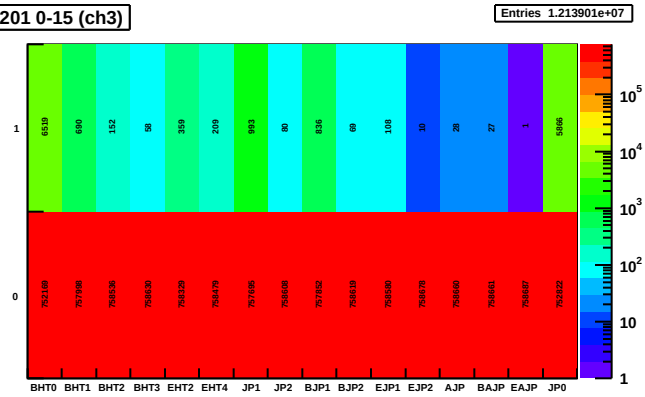



Unused (ch2)

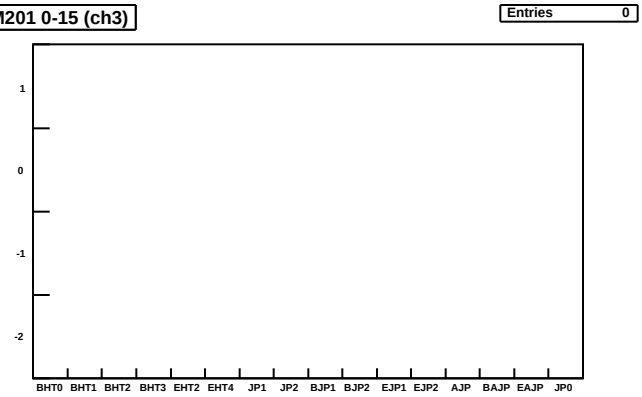


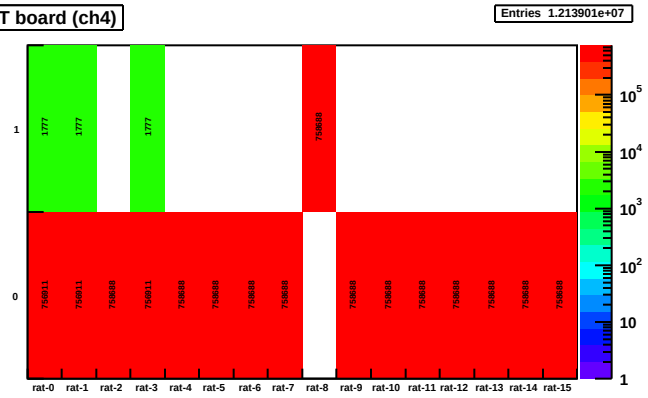

EM201 0-15 (ch3)

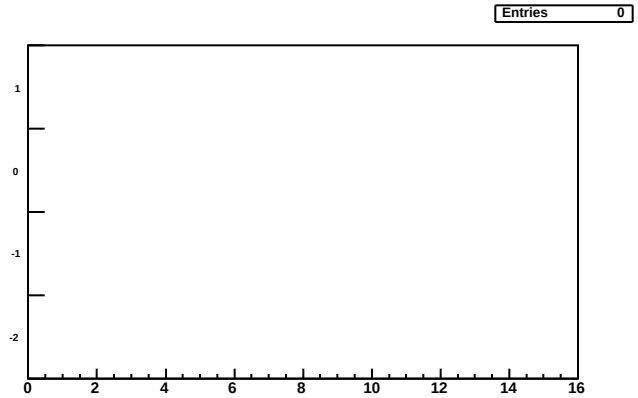



EM201 0-15 (ch3)

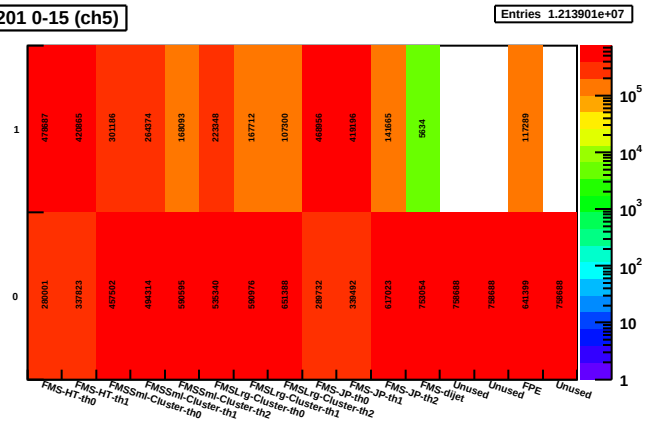


**RAT board (ch4)**

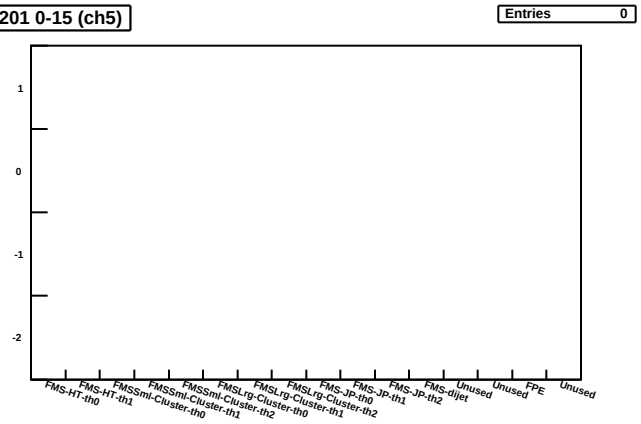


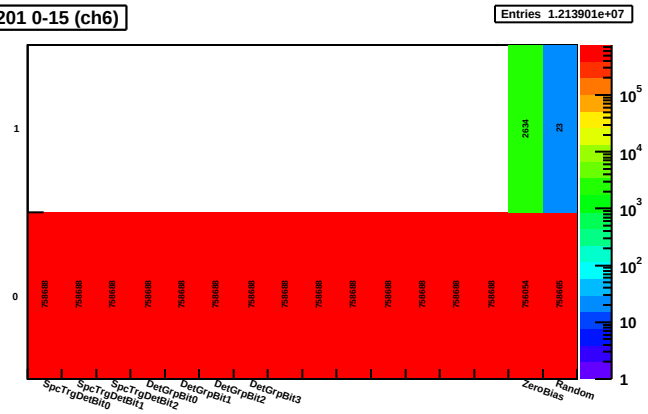

**FP201 0-15 (ch5)**

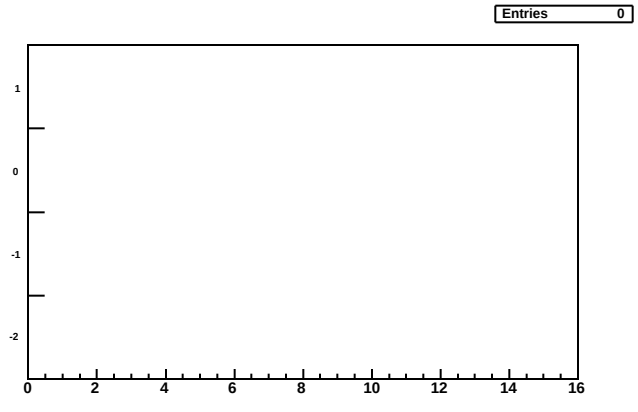



**FP201 0-15 (ch5)**

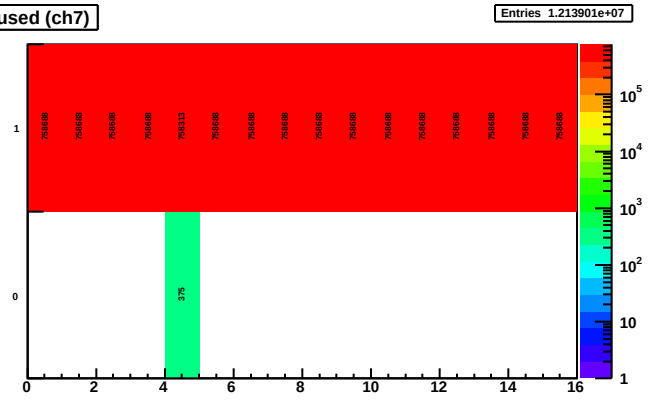


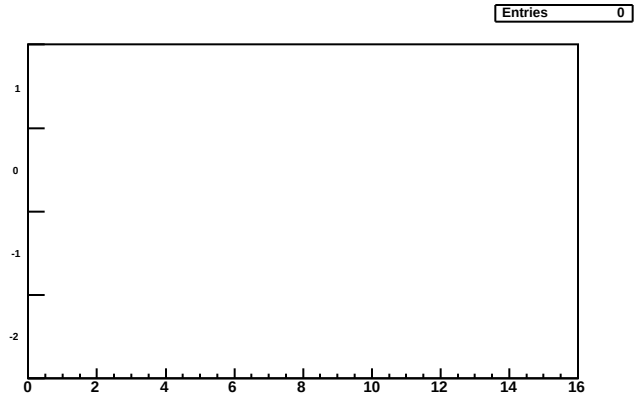

**ST201 0-15 (ch6)**

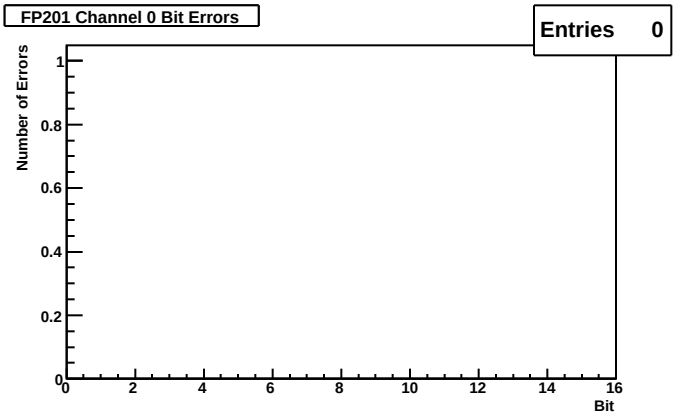


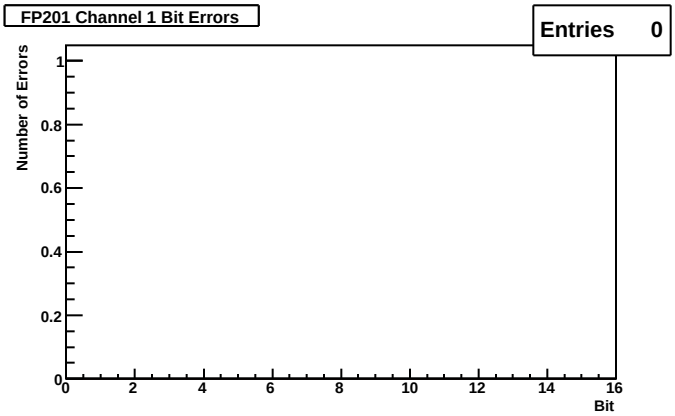



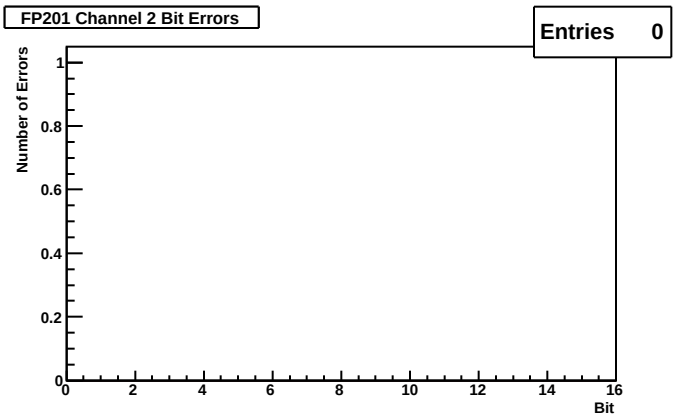

**Unused (ch7)**

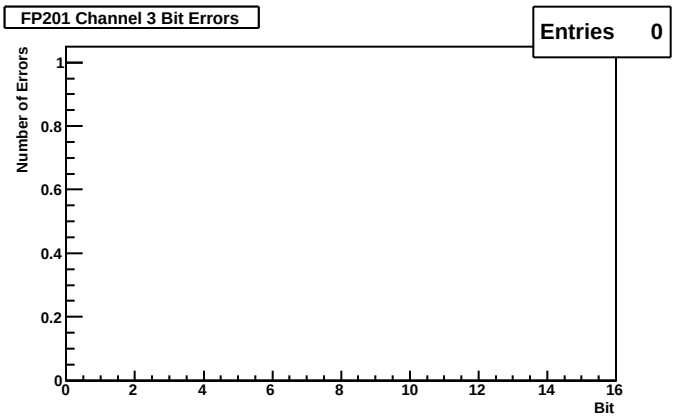


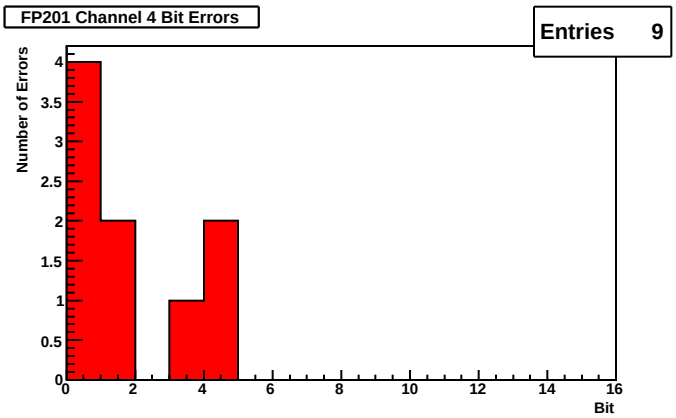



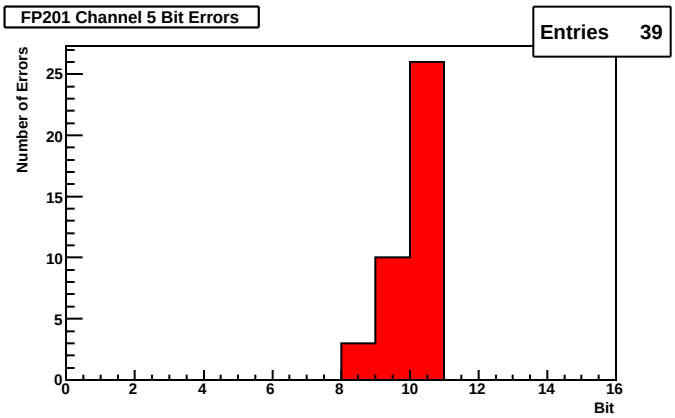

FP201 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

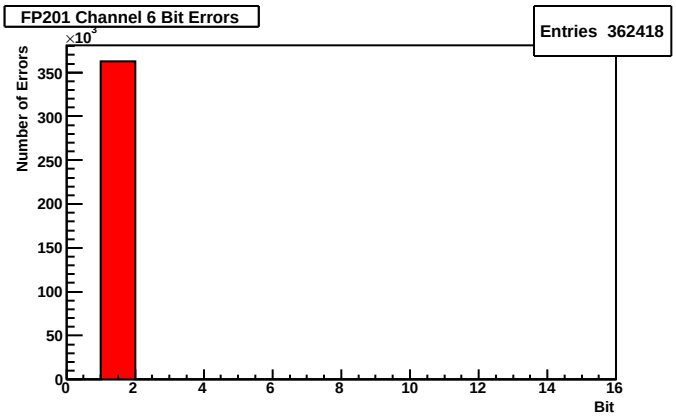

FP201 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

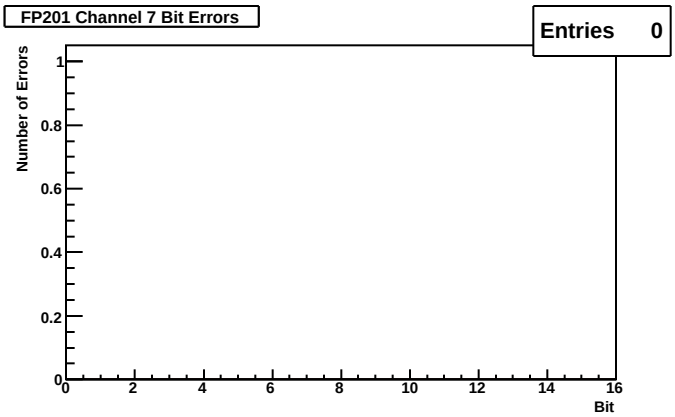

FP201 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

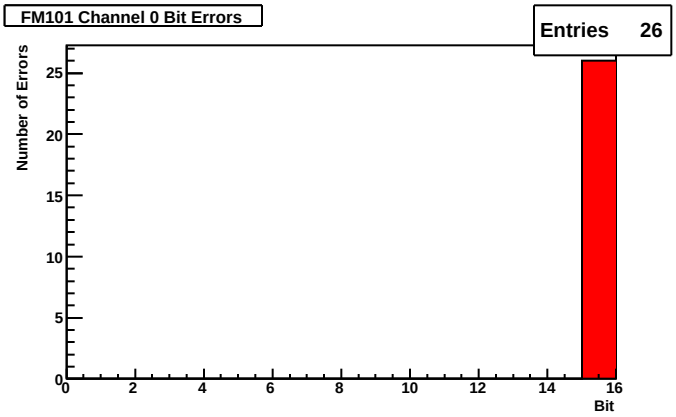

FP201 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

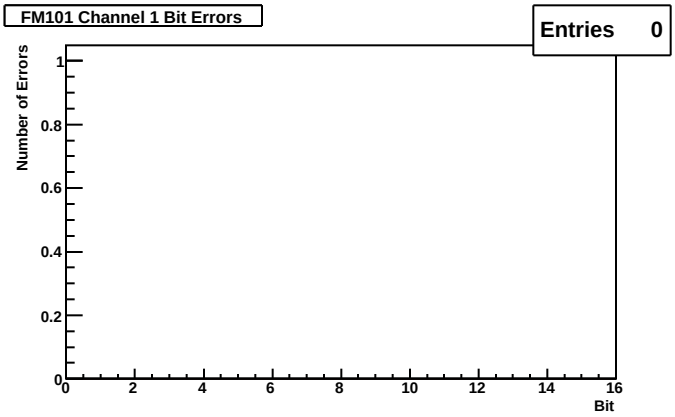

FP201 Channel 4 Bit Errors
Entries 9
Number of Errors
Bit

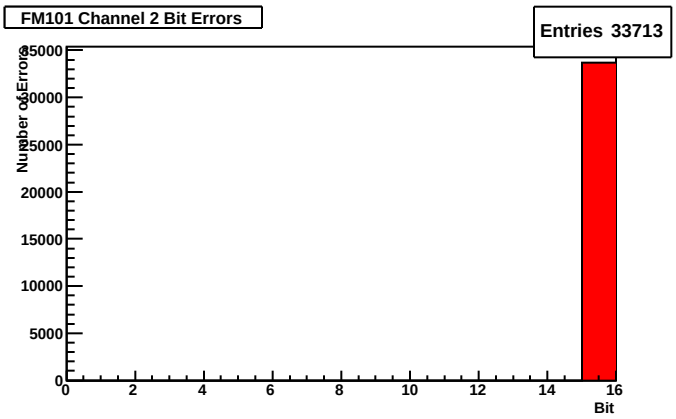

FP201 Channel 5 Bit Errors
Entries 39
Number of Errors
Bit

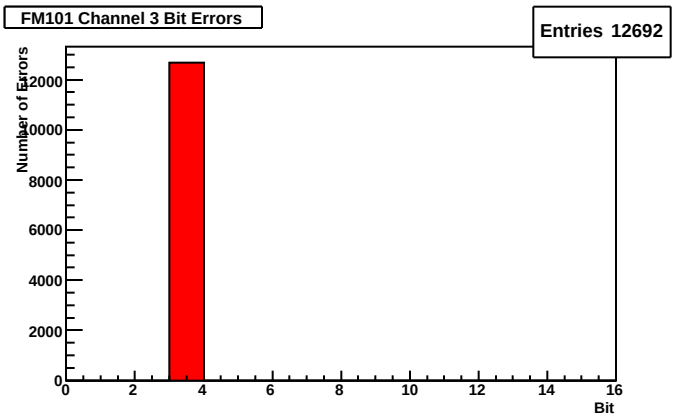

FP201 Channel 6 Bit Errors
Entries 362418
Number of Errors
Bit

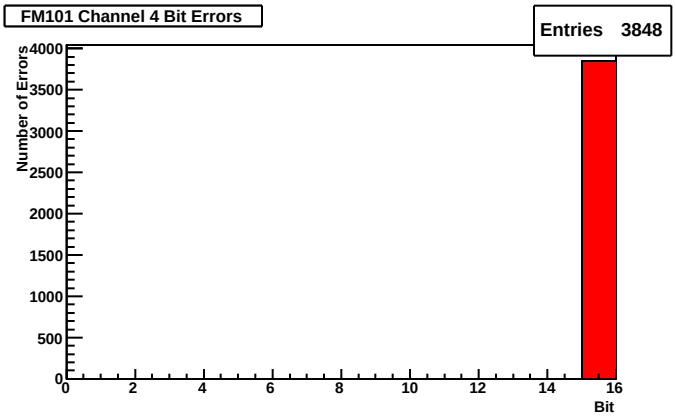

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

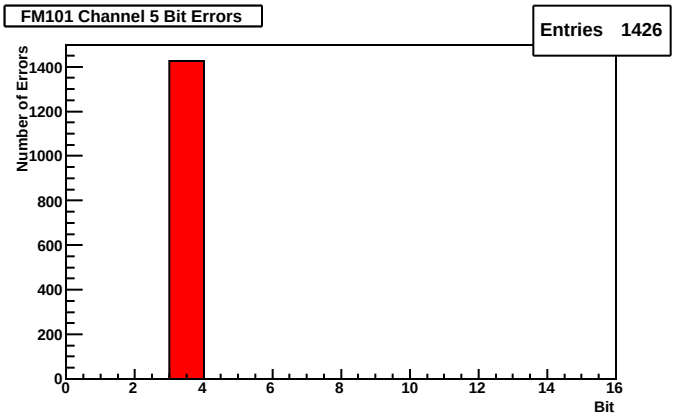

FM101 Channel 0 Bit Errors
Entries 26
Number of Errors
Bit

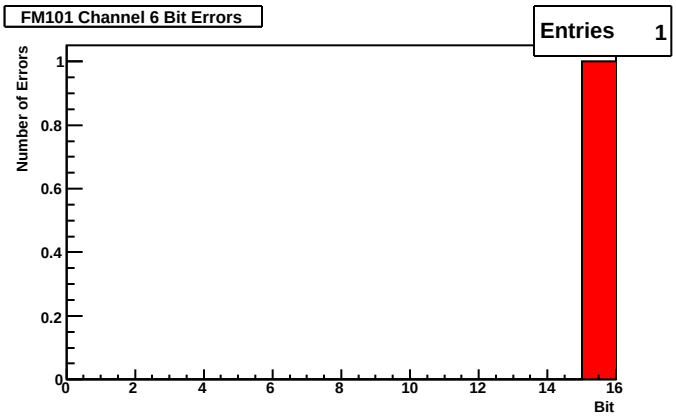

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

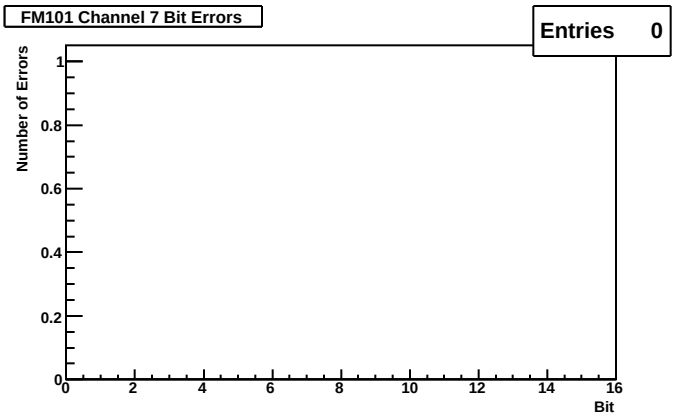

FM101 Channel 2 Bit Errors
Entries 33713
Number of Errors
Bit

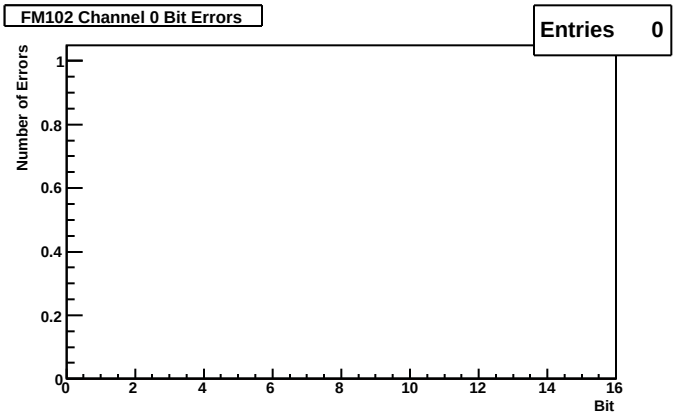

FM101 Channel 3 Bit Errors
Entries 12692
Number of Errors
Bit

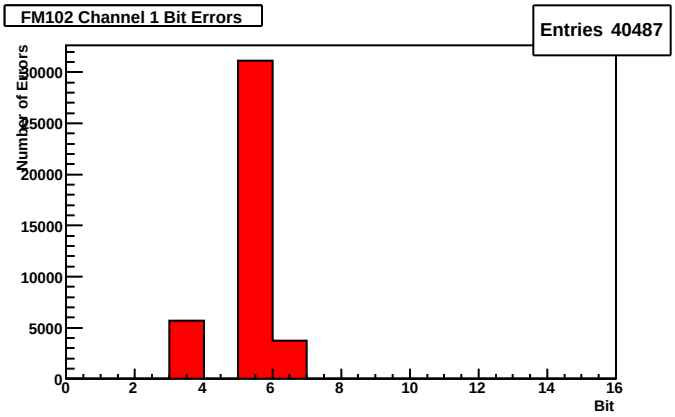

FM101 Channel 4 Bit Errors
Entries 3848
Number of Errors
Bit

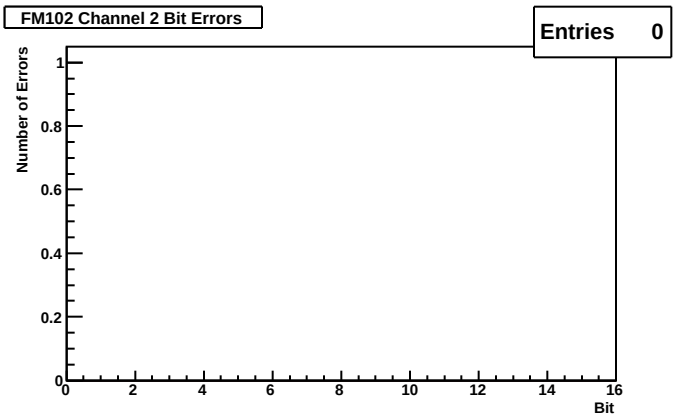

FM101 Channel 5 Bit Errors
Entries 1426
Number of Errors
Bit

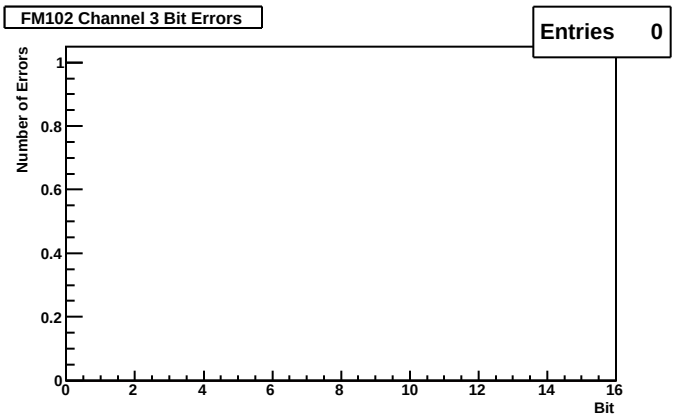

FM101 Channel 6 Bit Errors
Entries 1
Number of Errors
Bit

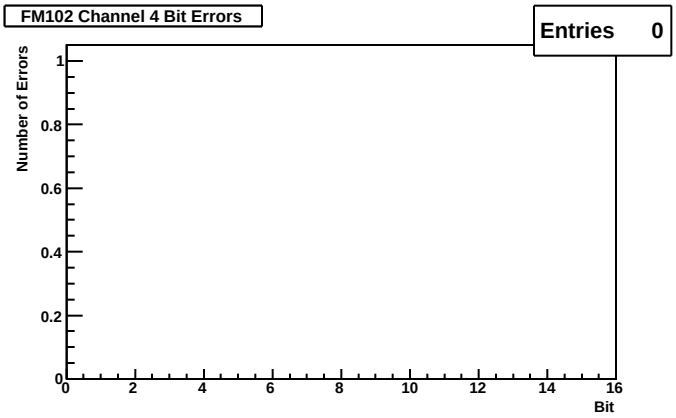

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

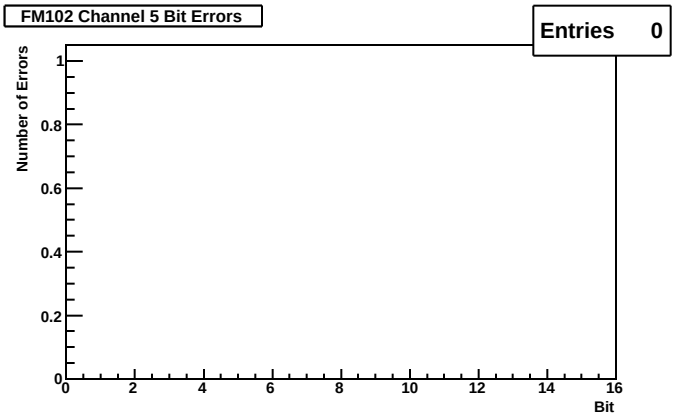

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

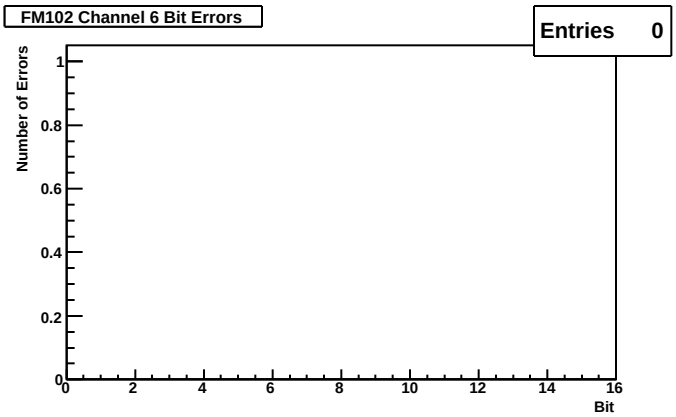

FM102 Channel 1 Bit Errors
Entries 40487
Number of Errors
Bit

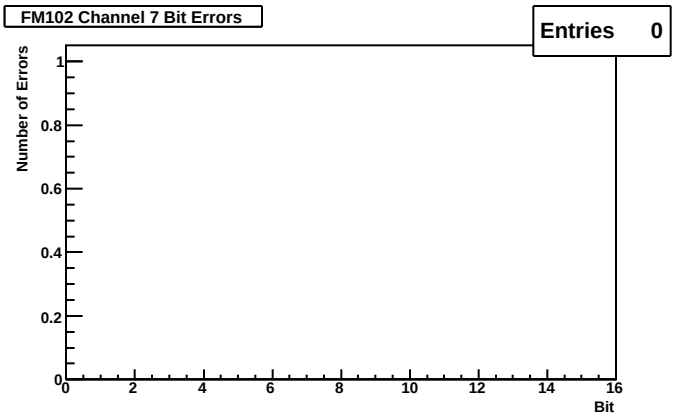

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

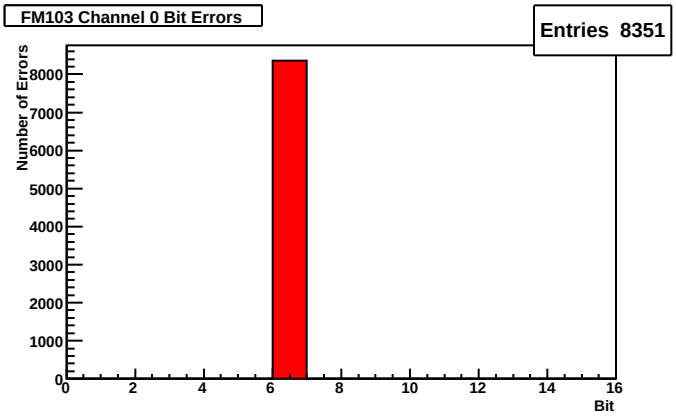

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

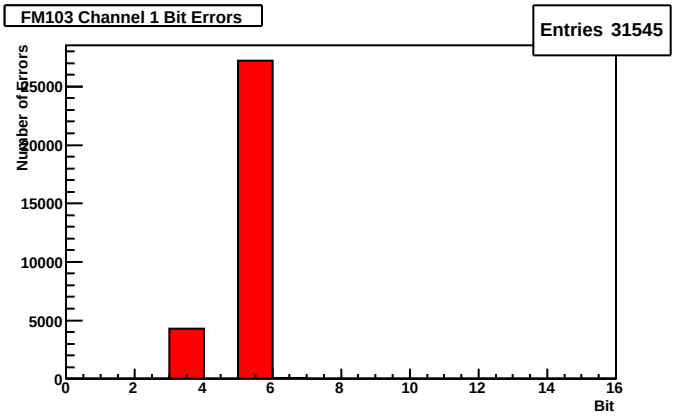

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

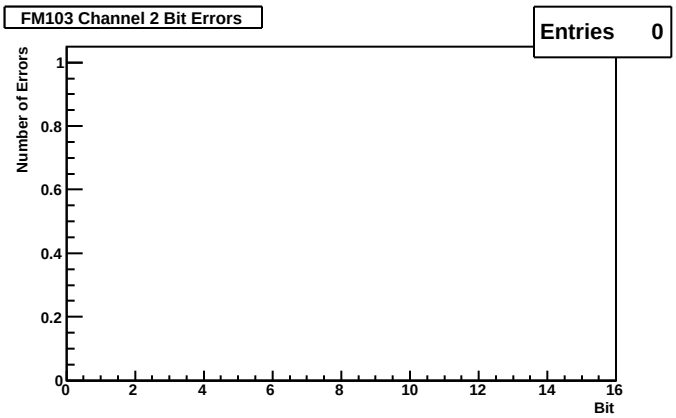

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

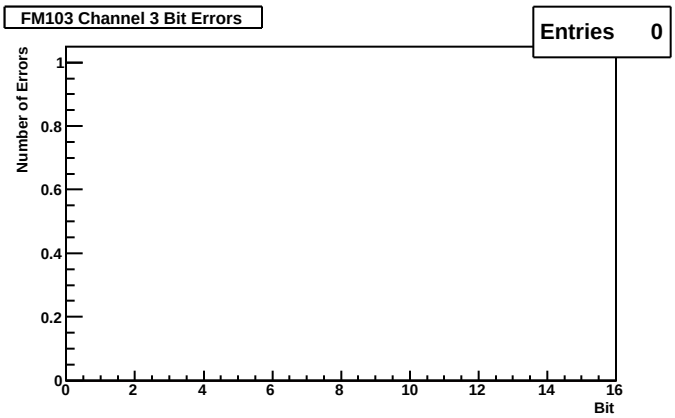

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

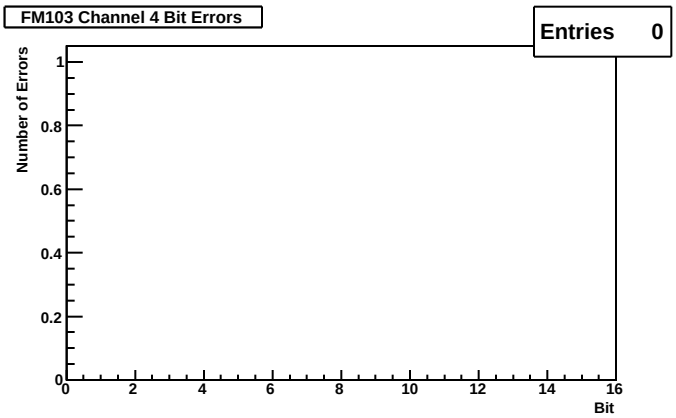

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

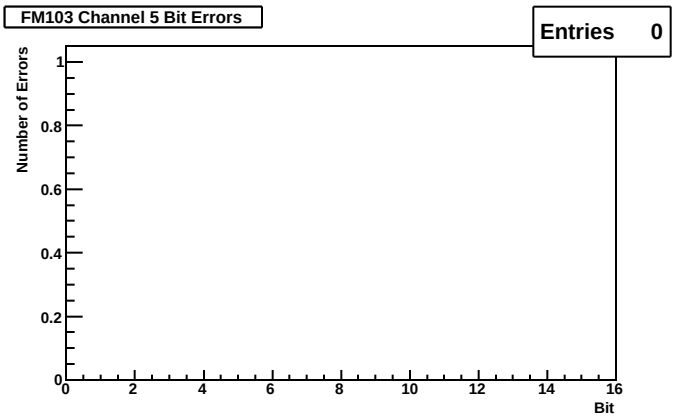

FM103 Channel 0 Bit Errors
Entries 8351
Number of Errors
Bit

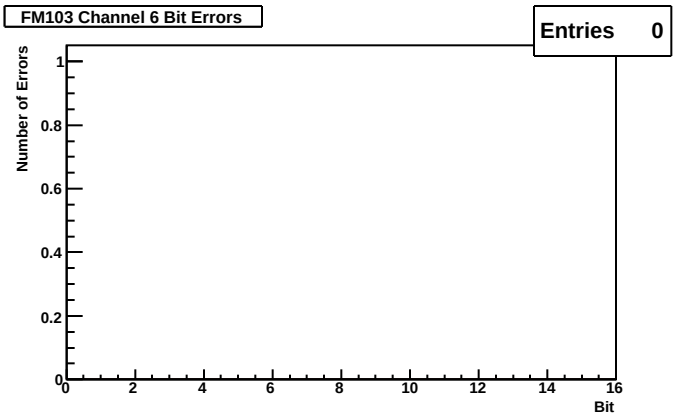

FM103 Channel 1 Bit Errors
Entries 31545
Number of Errors
Bit

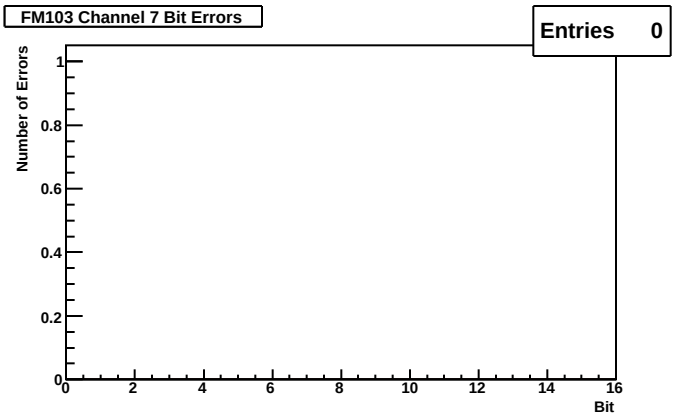

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

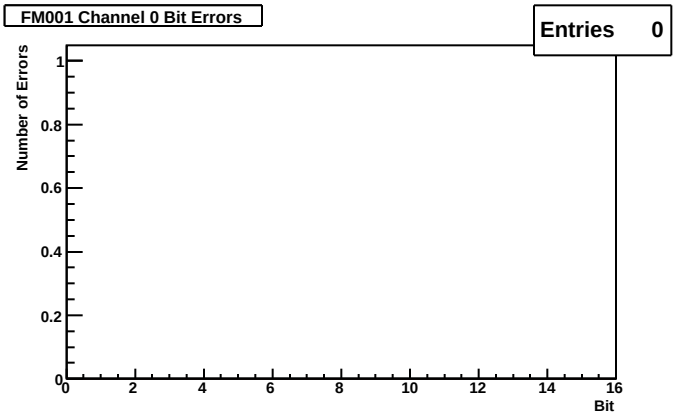

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

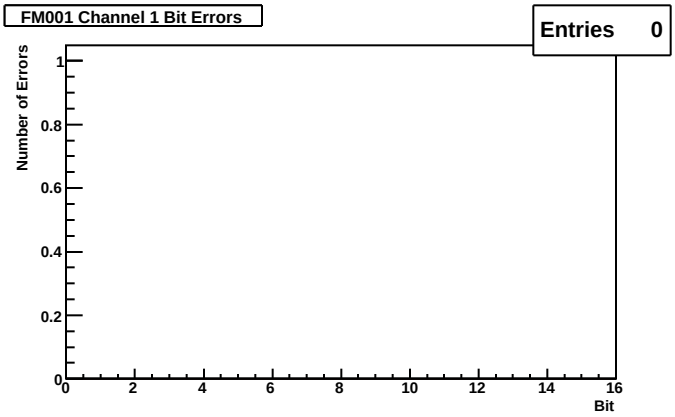

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

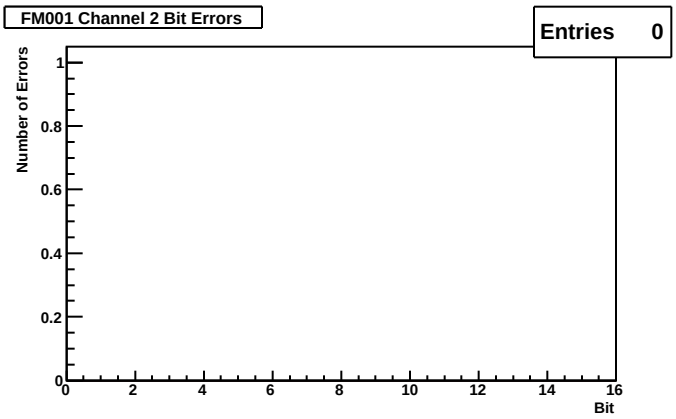

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

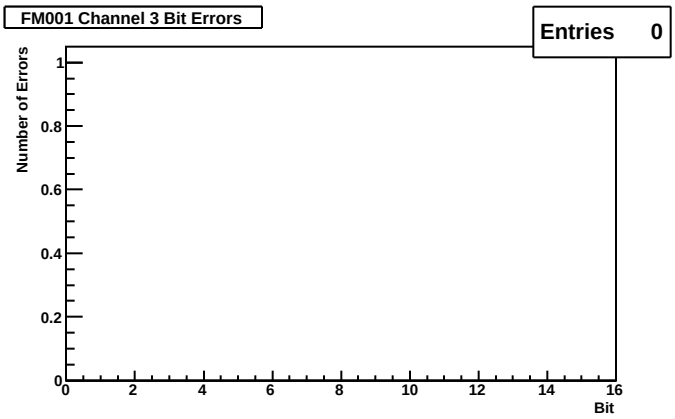

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

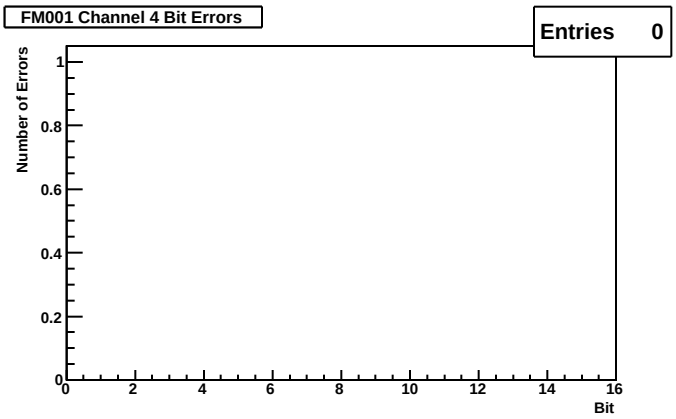

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

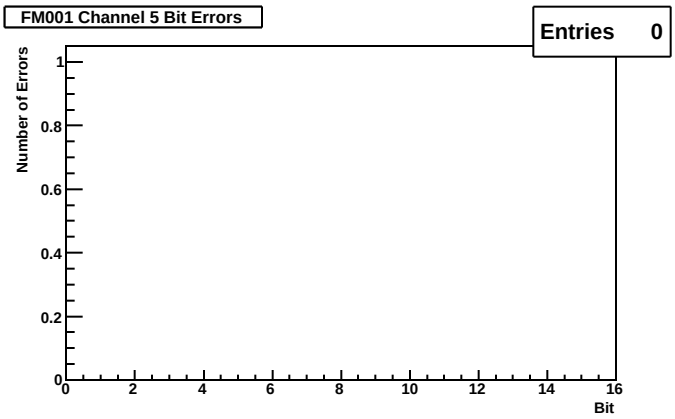

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

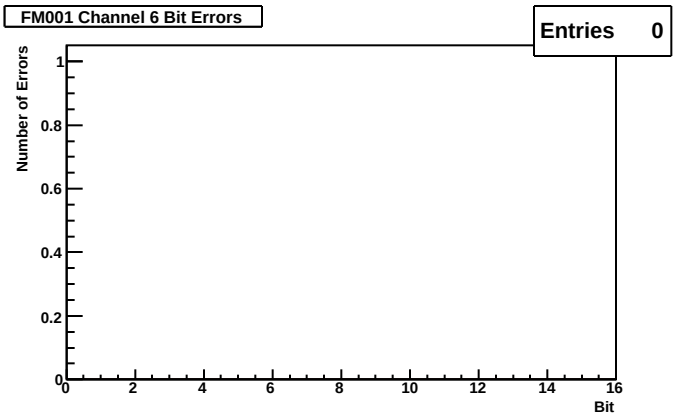

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

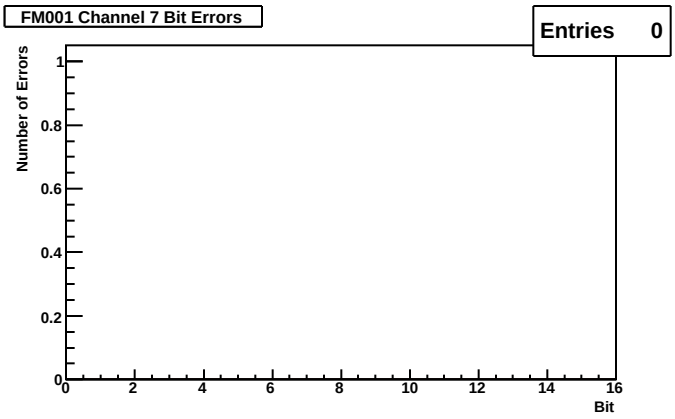

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

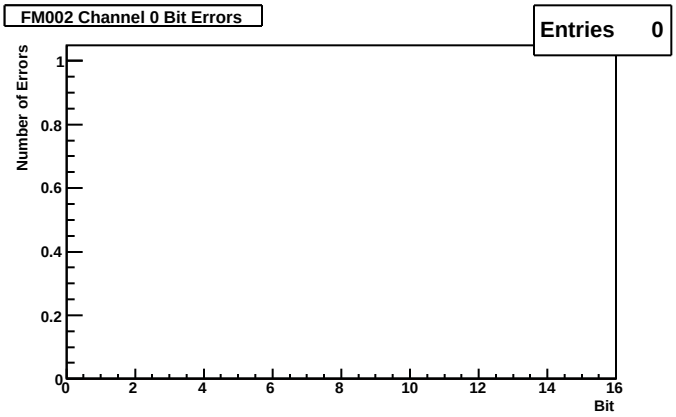

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

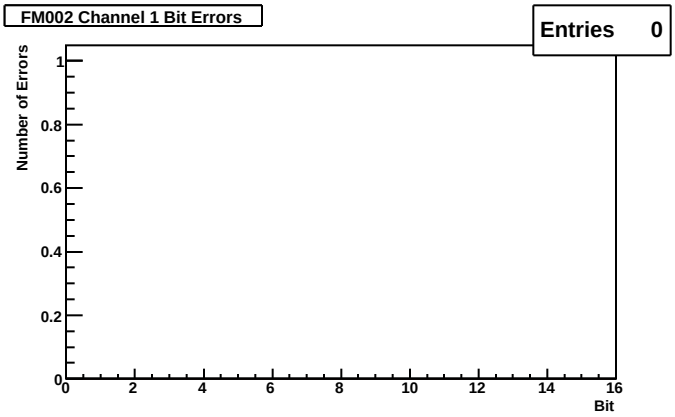

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

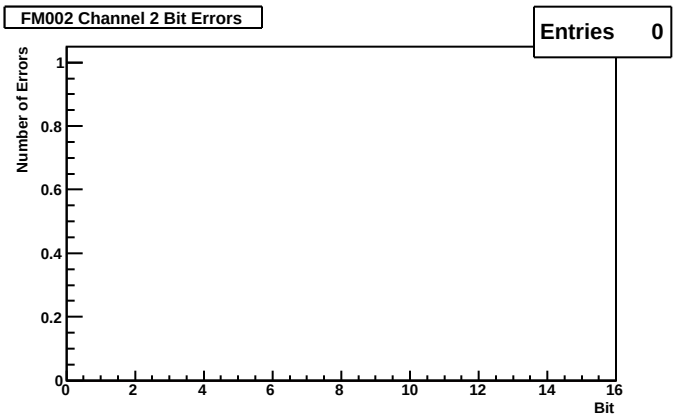

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

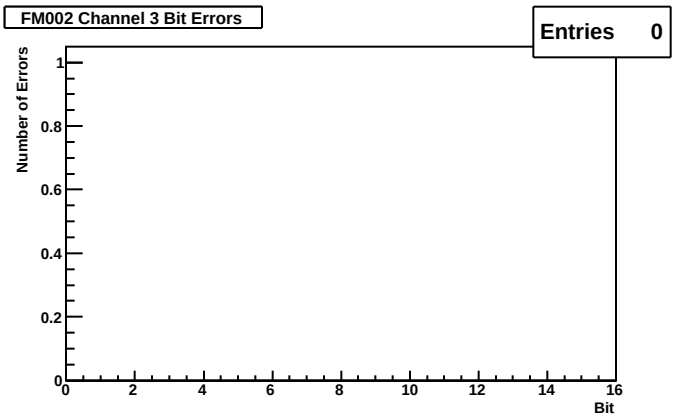

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

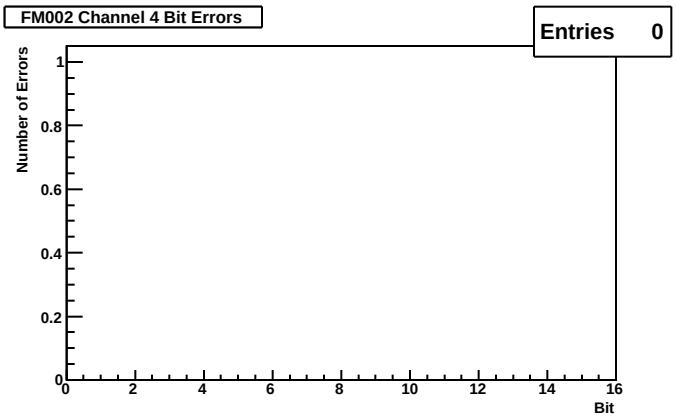

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

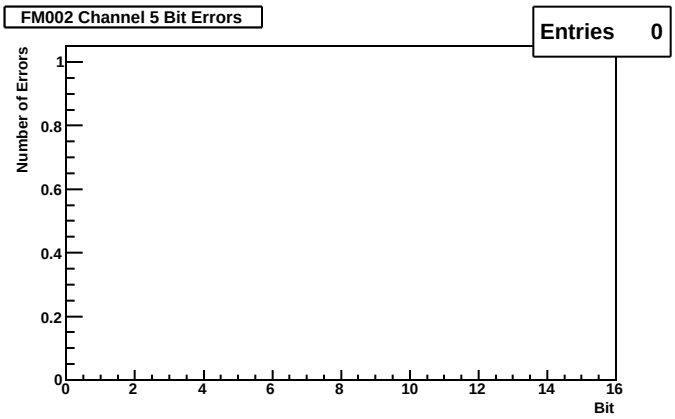

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

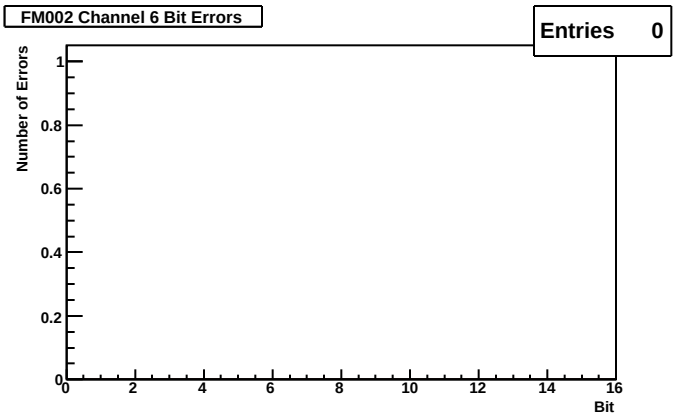

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

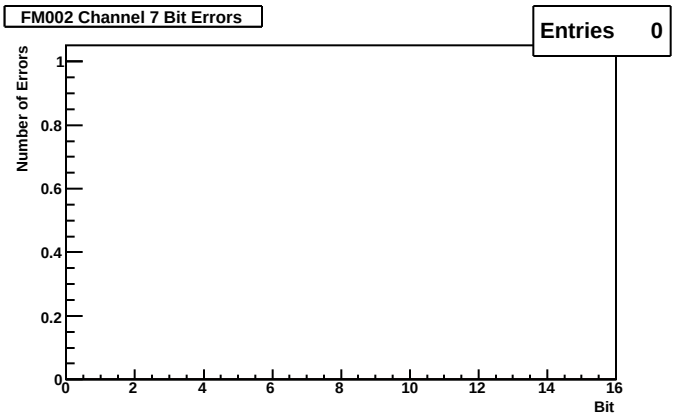

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

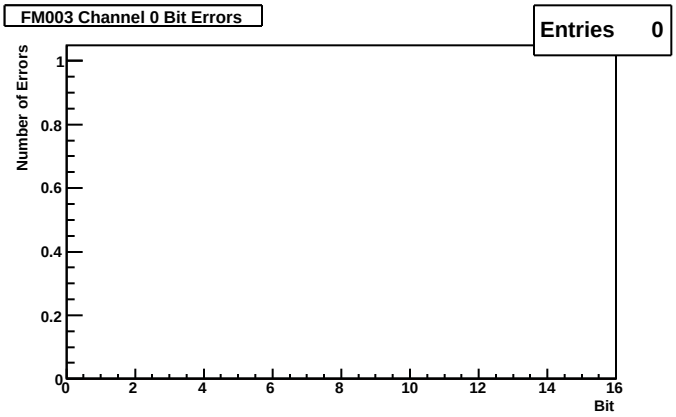

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

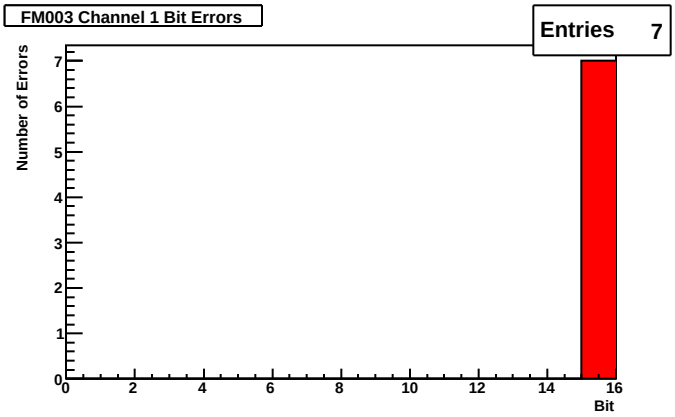

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

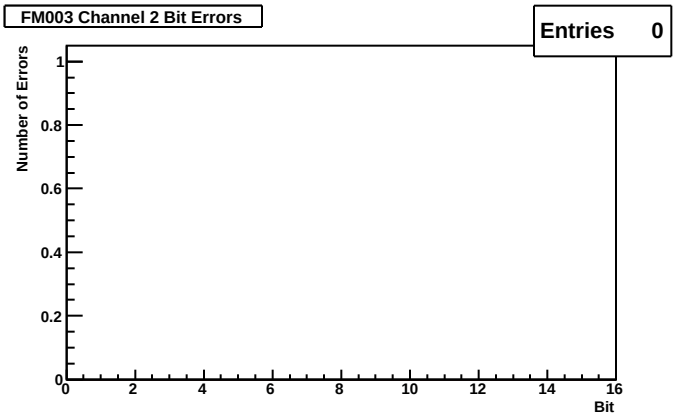

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

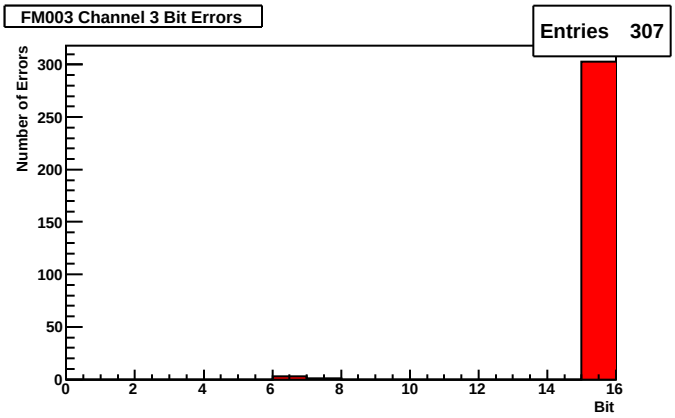

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

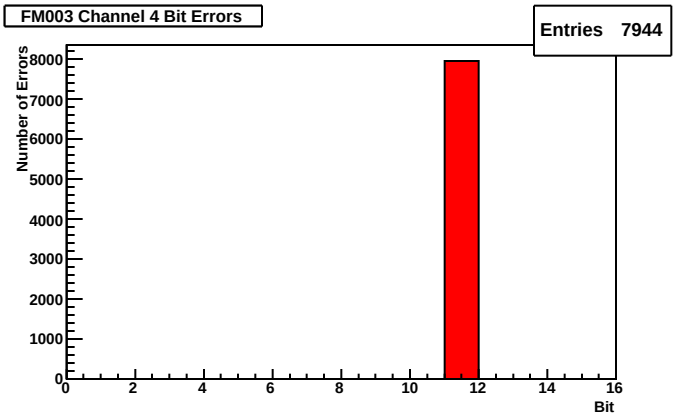

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

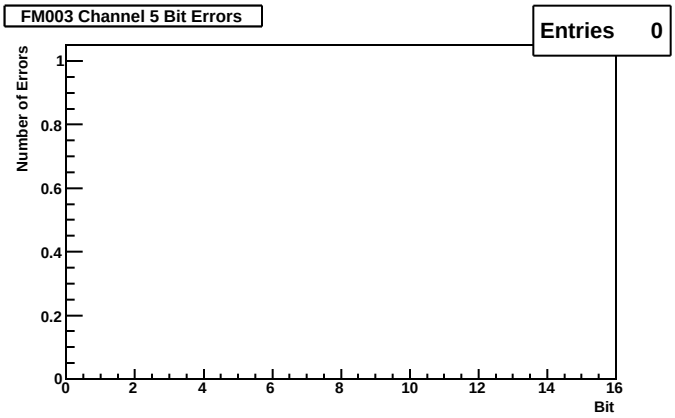

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

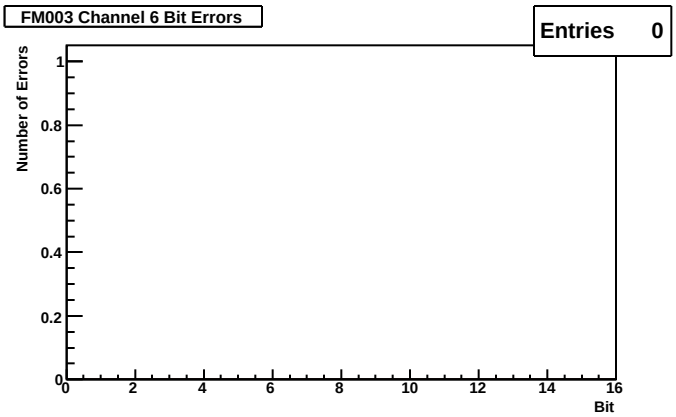

FM003 Channel 1 Bit Errors
Entries 7
Number of Errors
Bit

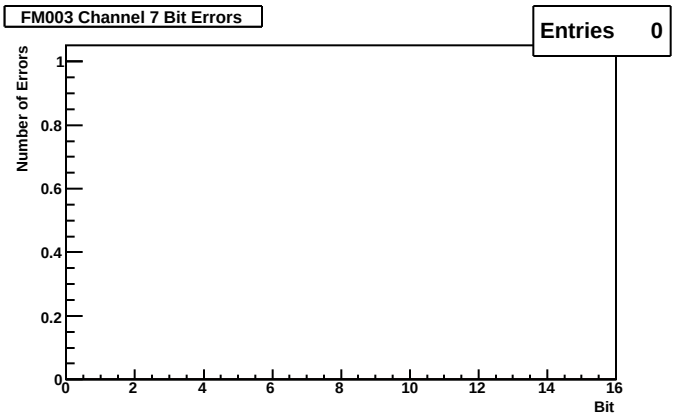

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

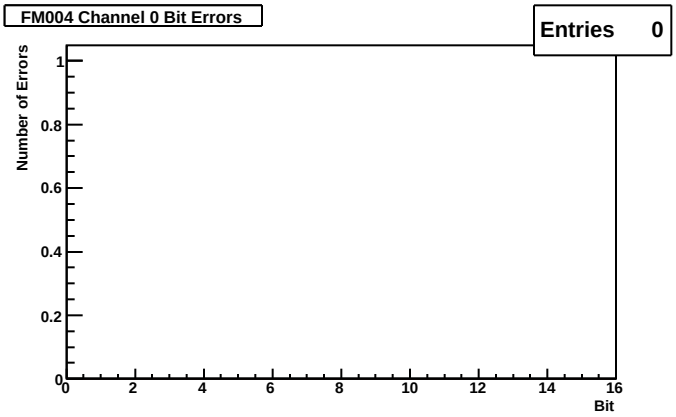

FM003 Channel 3 Bit Errors
Entries 307
Number of Errors
Bit

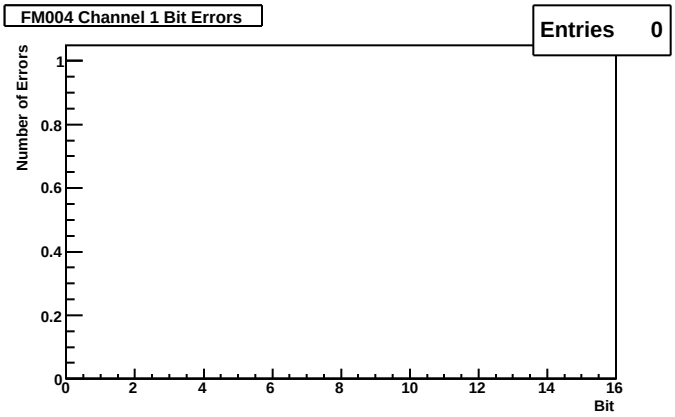

FM003 Channel 4 Bit Errors
Entries 7944
Number of Errors
Bit

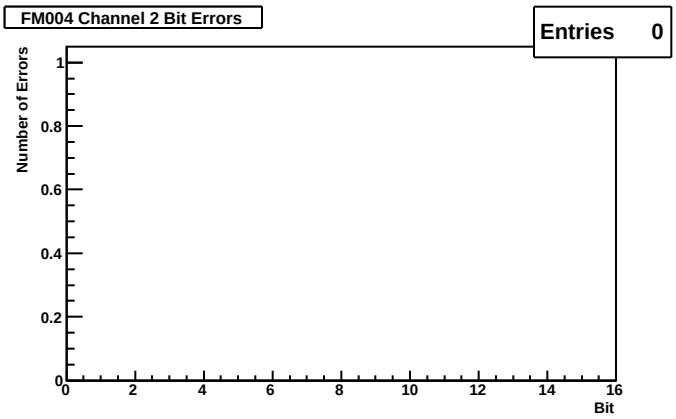

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

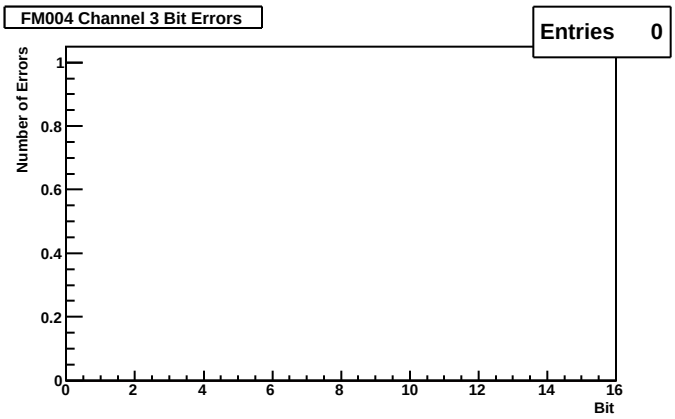

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

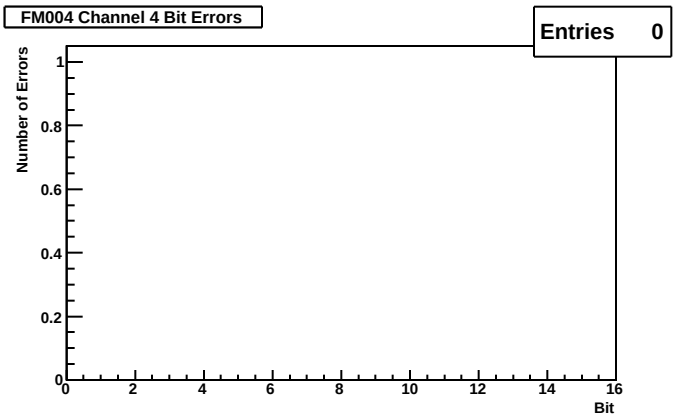

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

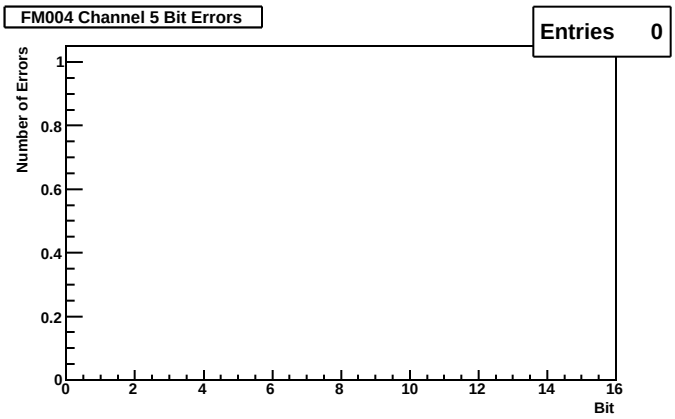

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

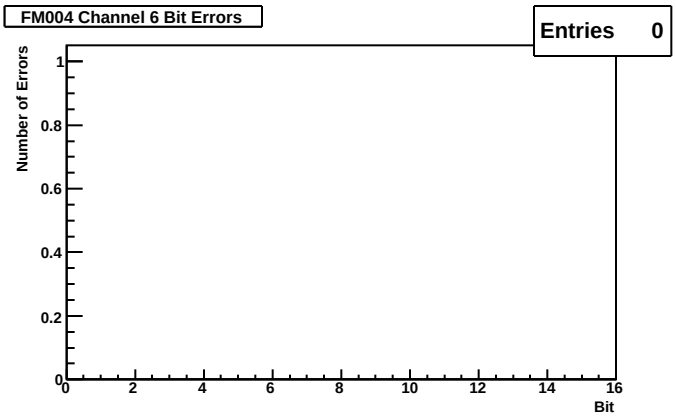

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

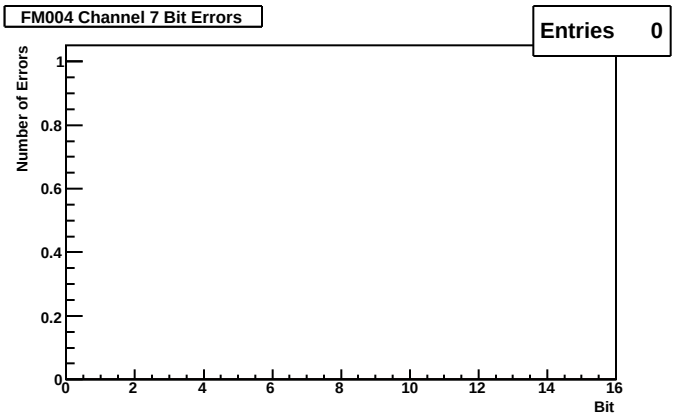

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

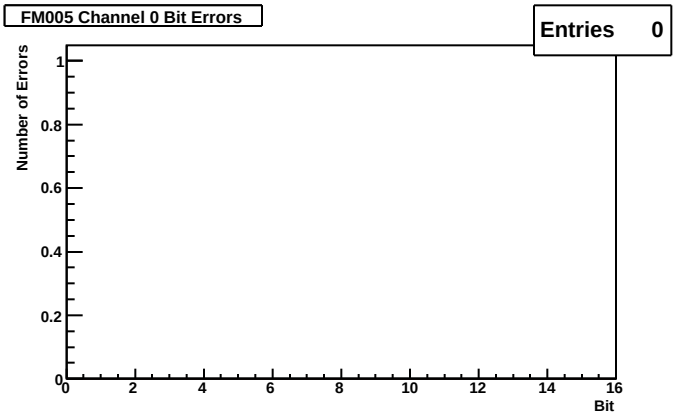

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

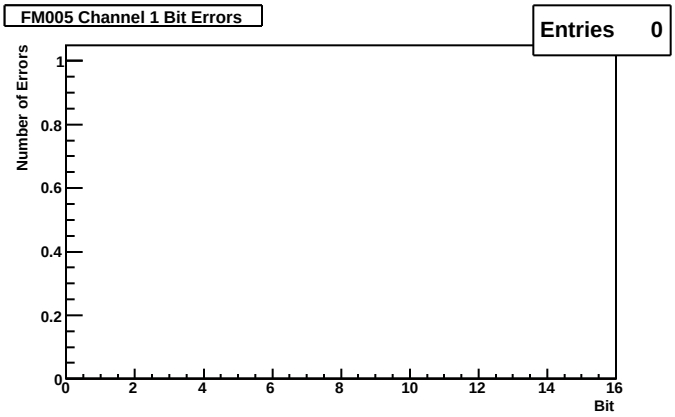

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

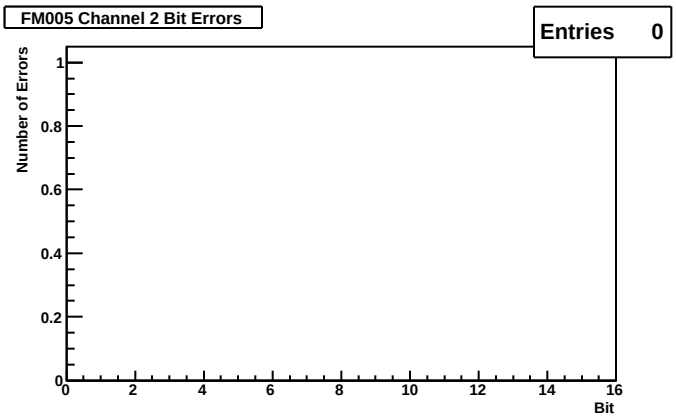

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

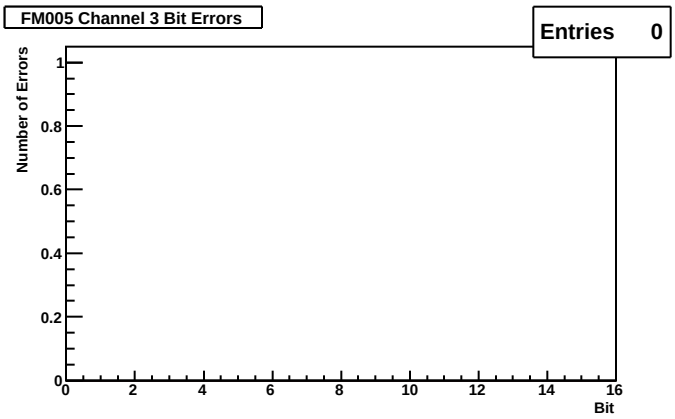

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

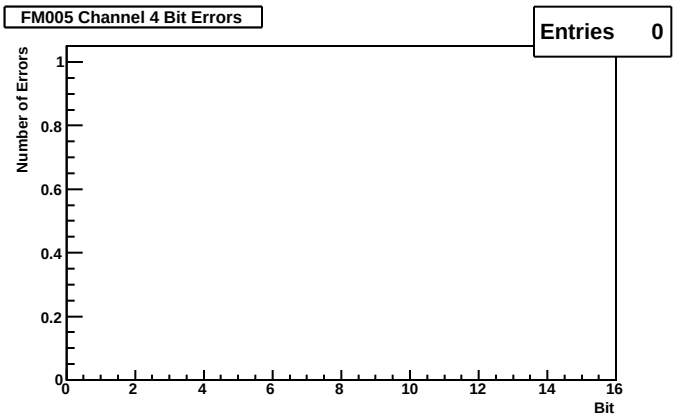

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

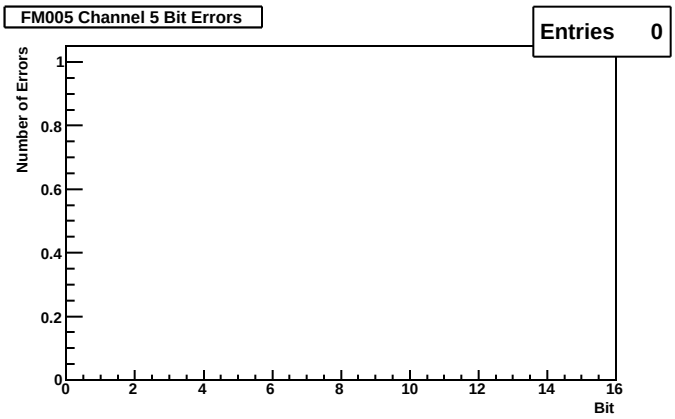

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

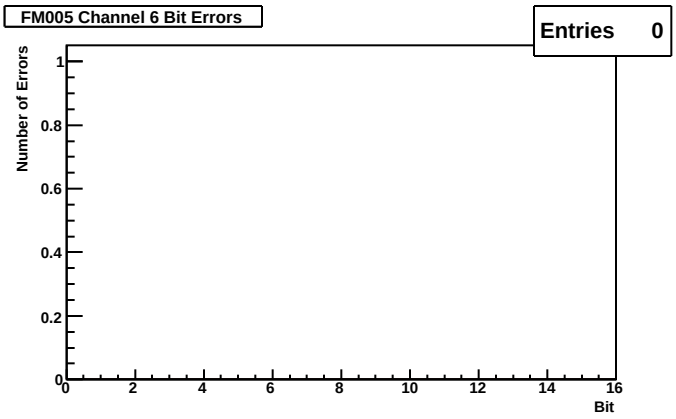

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

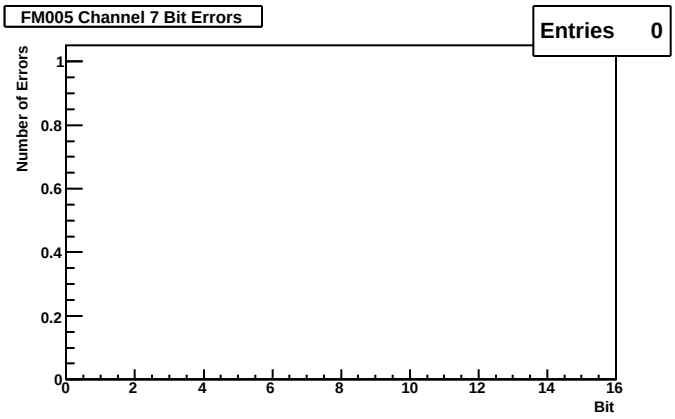

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

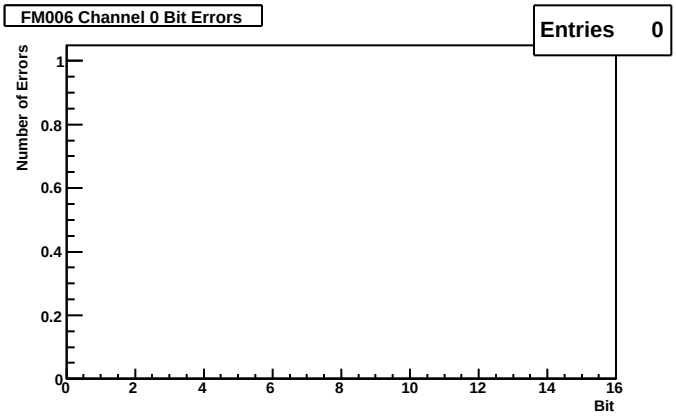

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

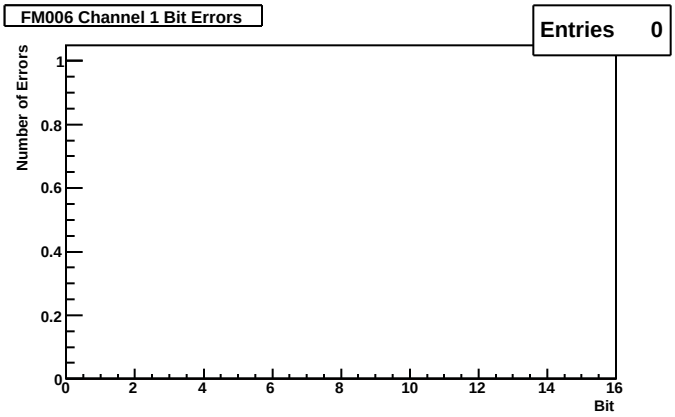

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

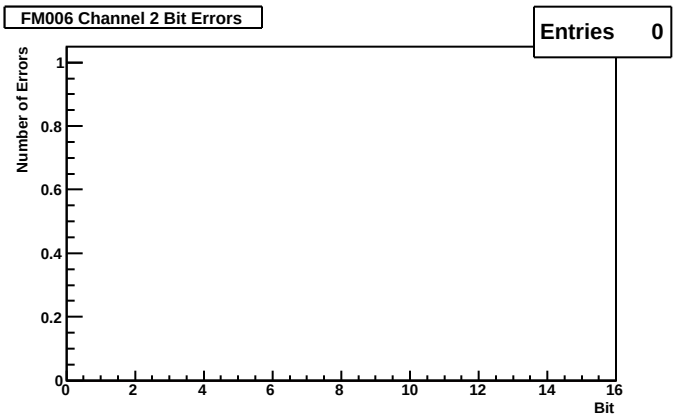

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

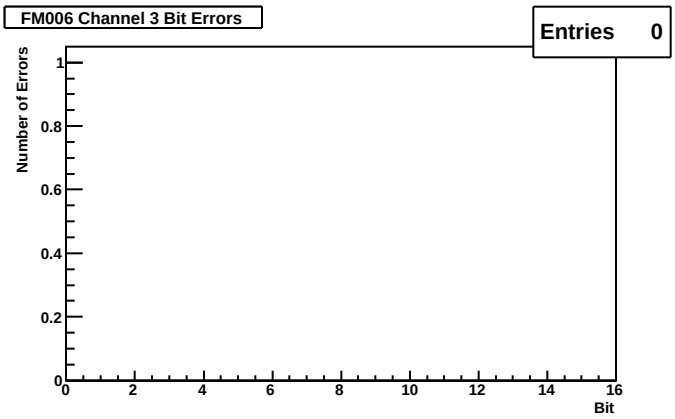

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

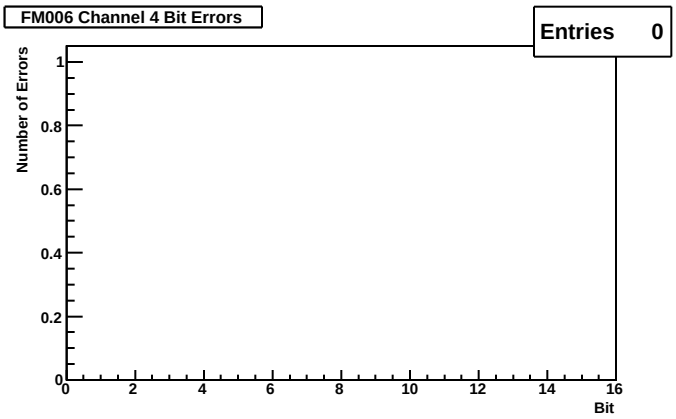

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

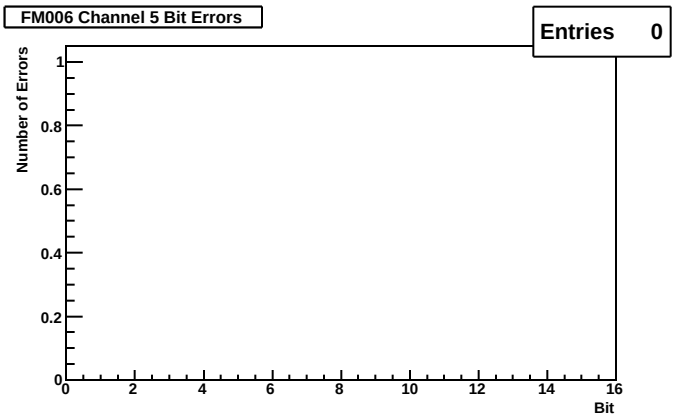

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

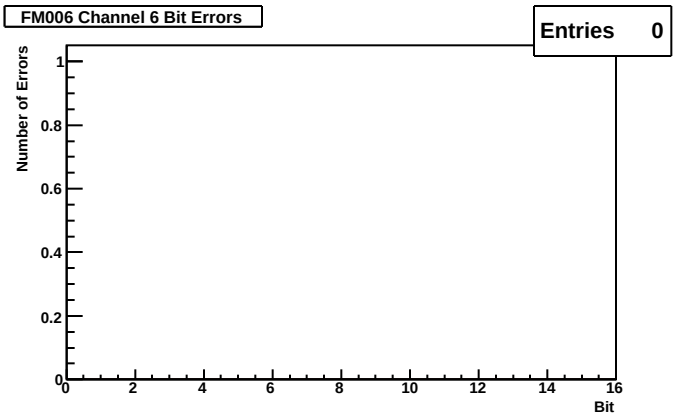

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

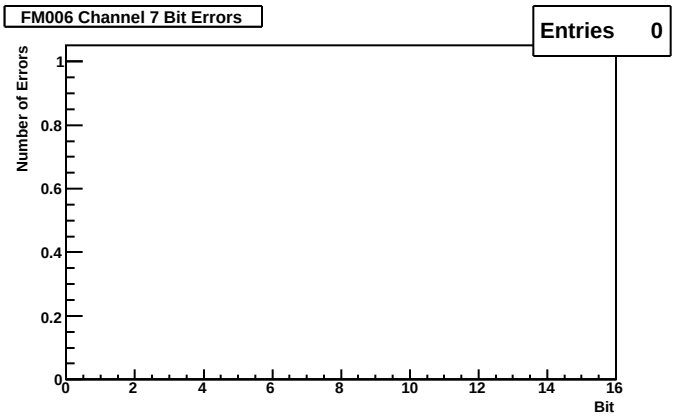

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

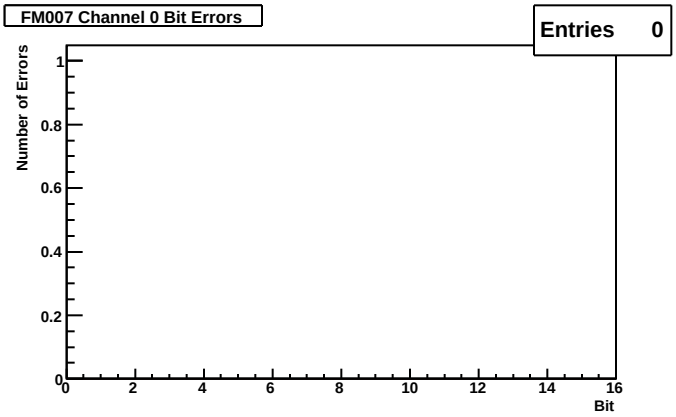

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

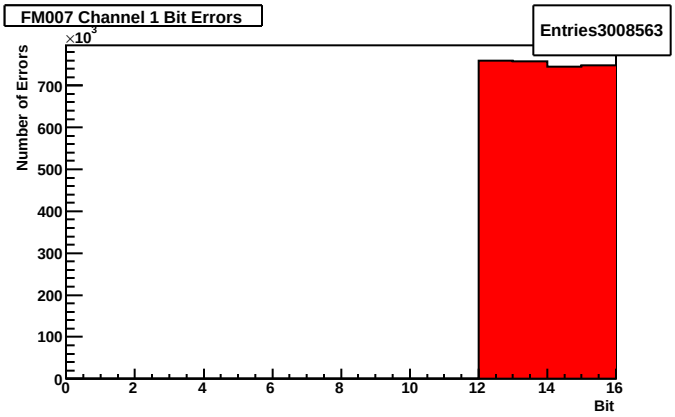

FM006 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

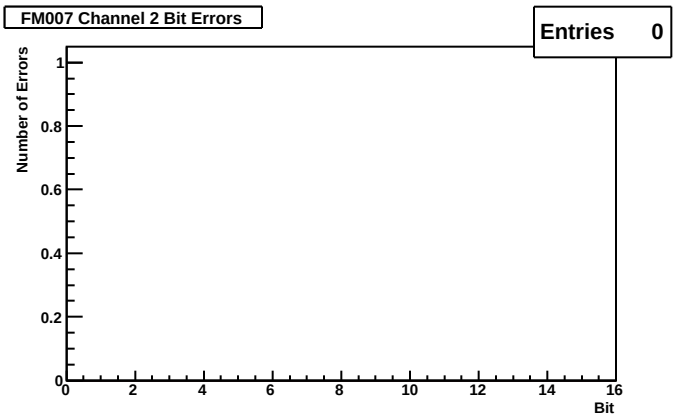

FM006 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

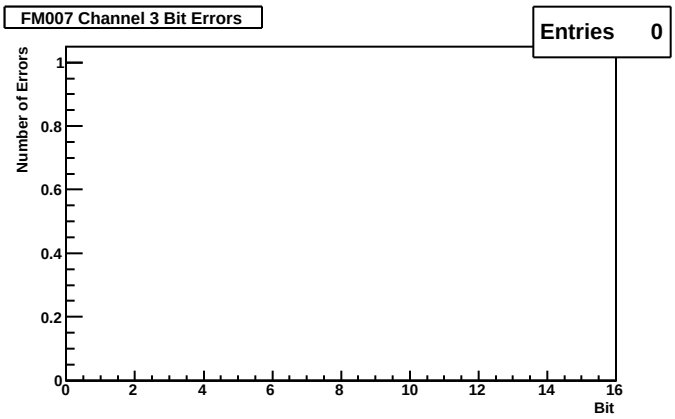

FM006 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

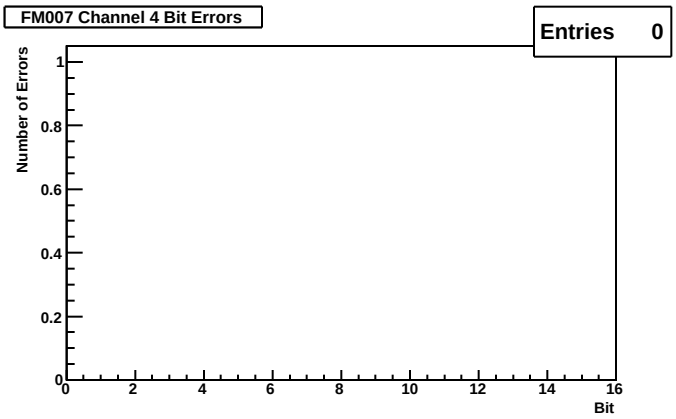

FM006 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

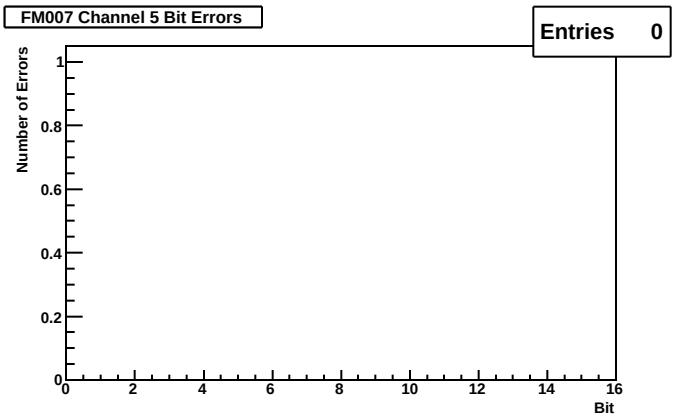

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

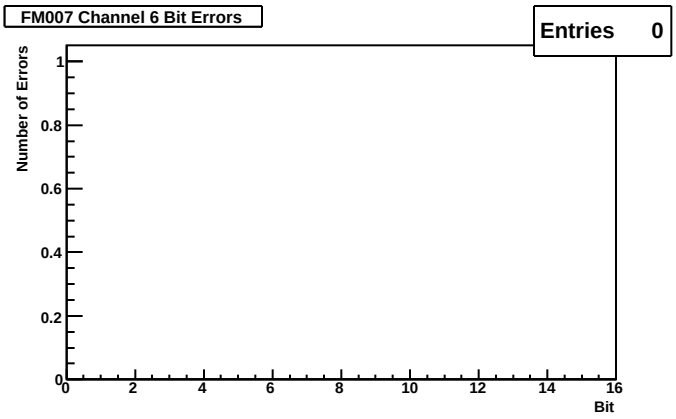

FM007 Channel 1 Bit Errors
Entries3008563
×10³
Number of Errors
Bit

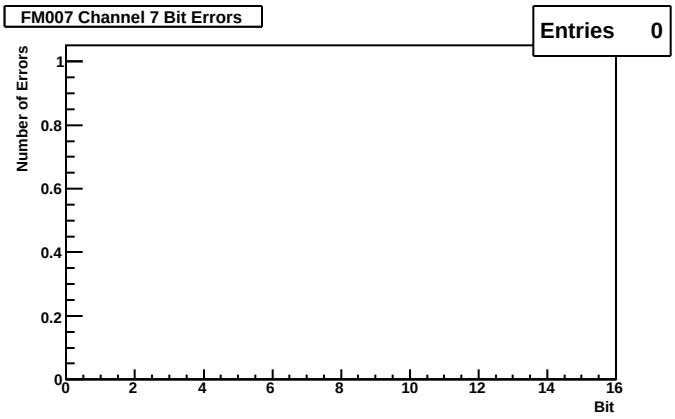

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

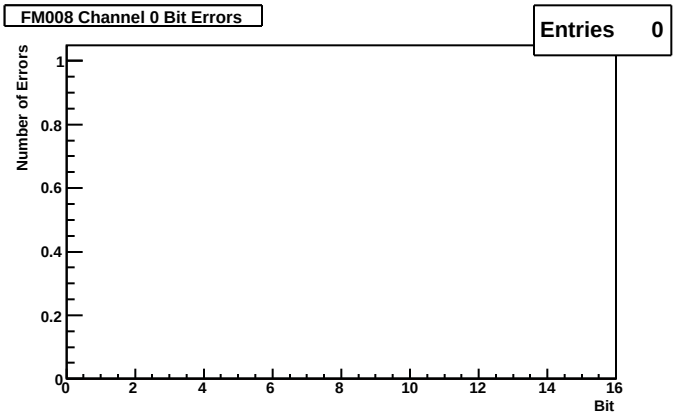

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

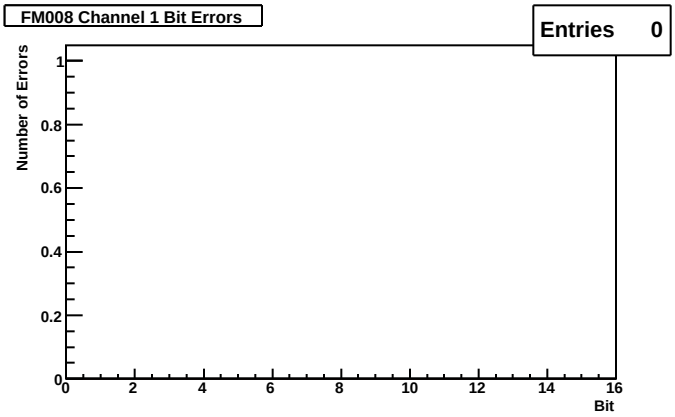

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

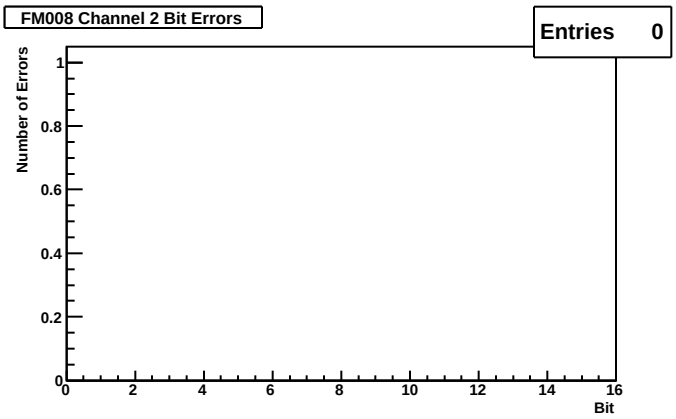

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

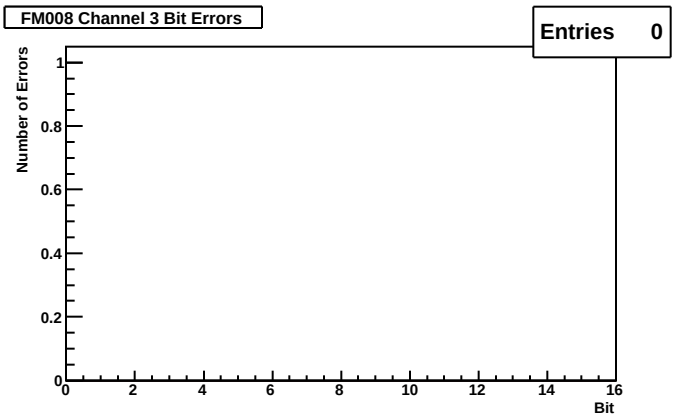

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

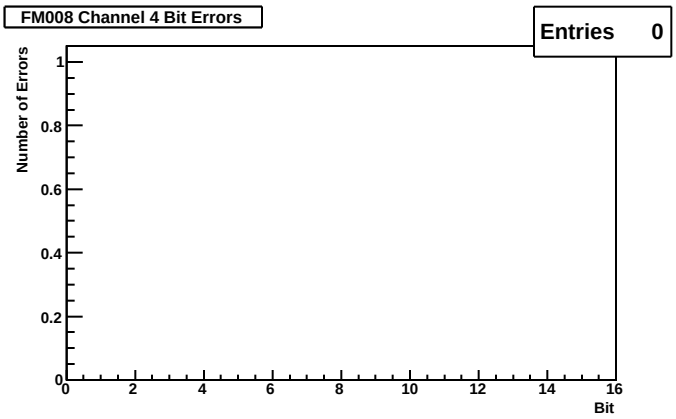

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

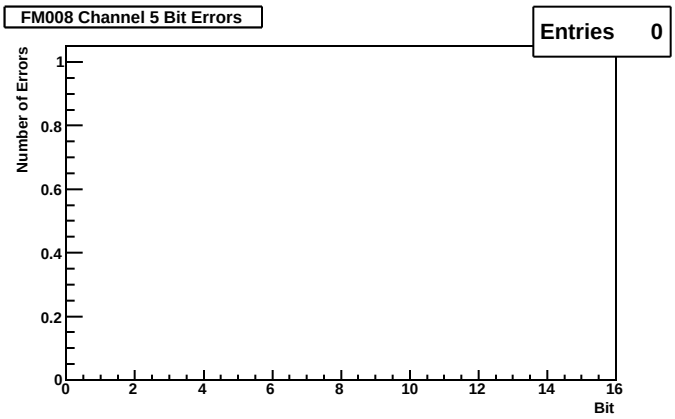

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

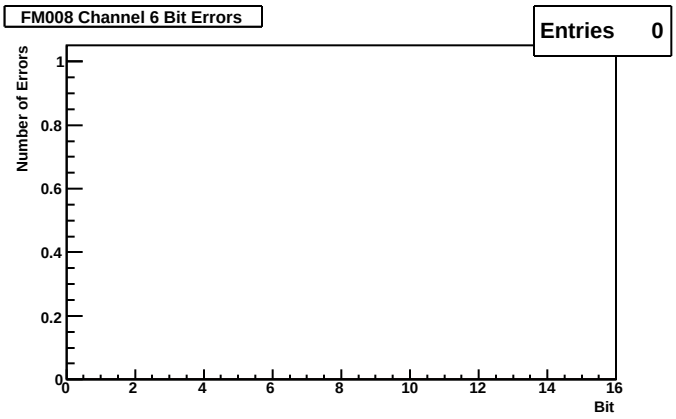

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

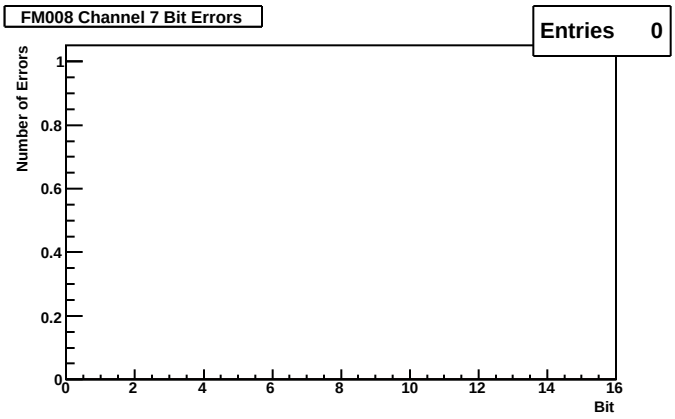

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

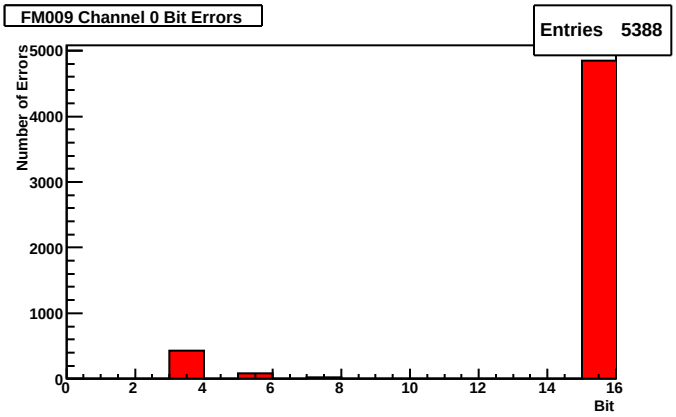

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

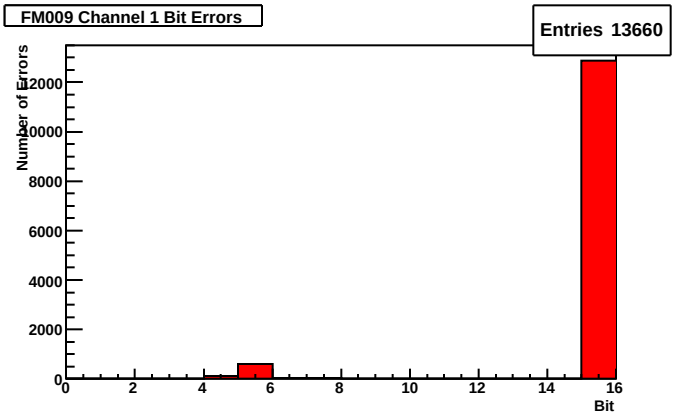

FM008 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

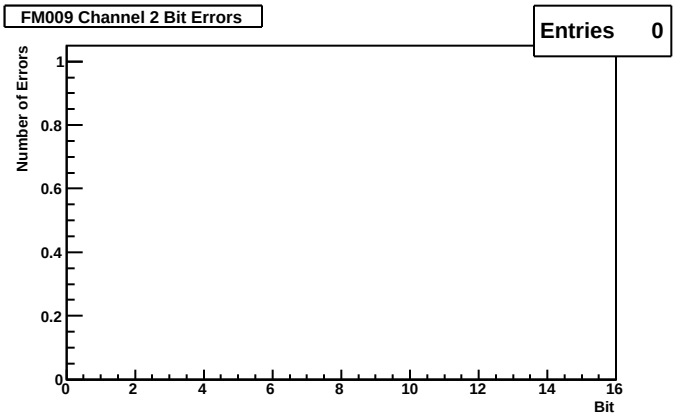

FM008 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

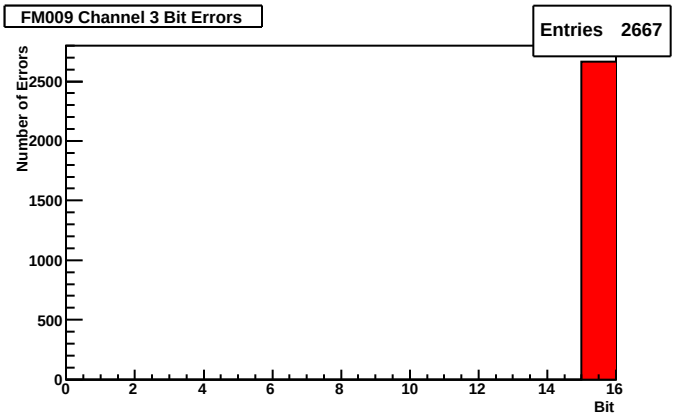

FM008 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

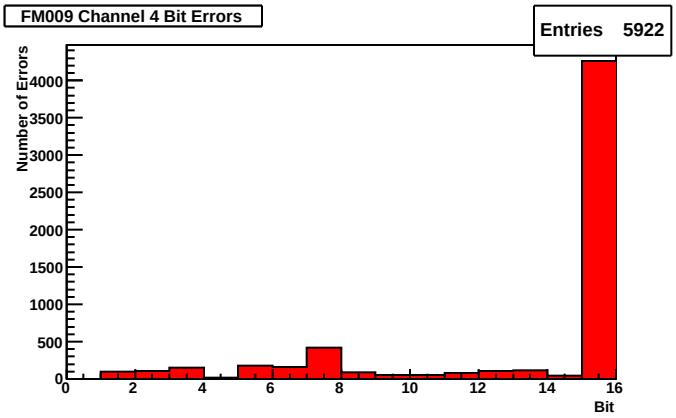

FM008 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

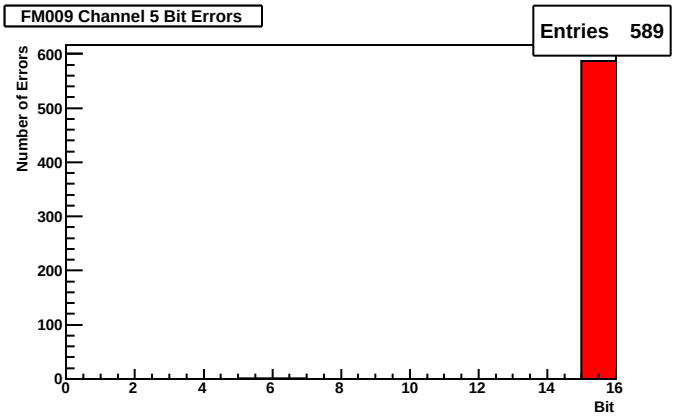

FM009 Channel 0 Bit Errors
Entries 5388
Number of Errors
Bit

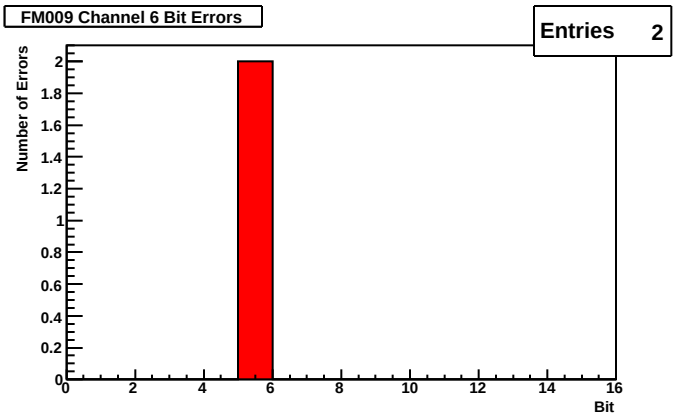

FM009 Channel 1 Bit Errors
Entries 13660
Number of Errors
Bit

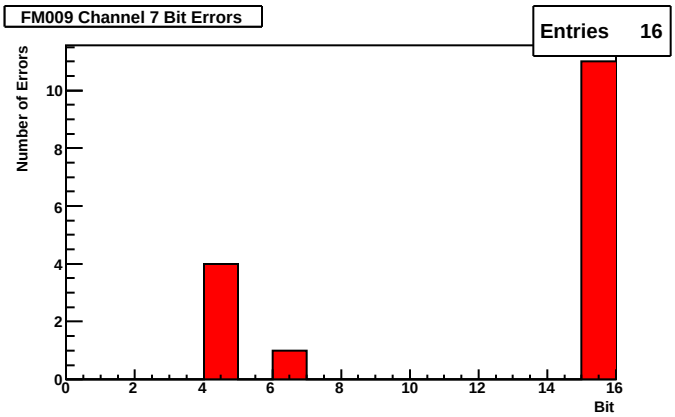

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

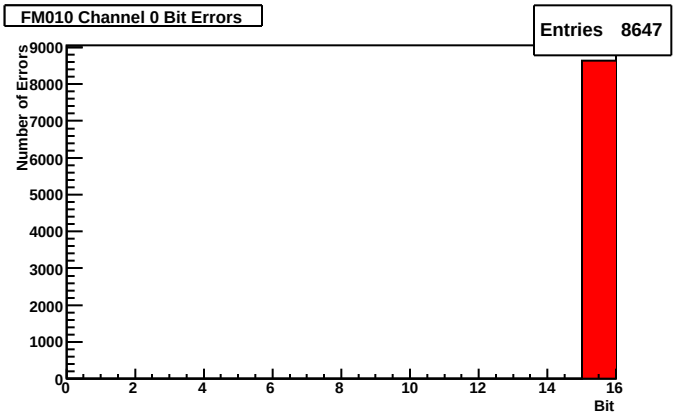

FM009 Channel 3 Bit Errors
Entries 2667
Number of Errors
Bit

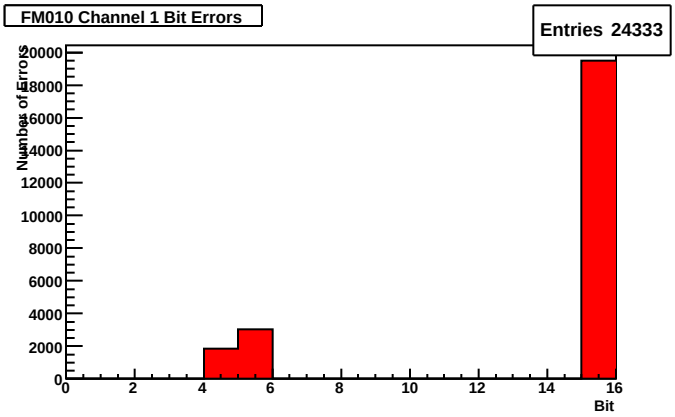

FM009 Channel 4 Bit Errors
Entries 5922
Number of Errors
Bit


FM009 Channel 5 Bit Errors
Entries 589
Number of Errors
Bit

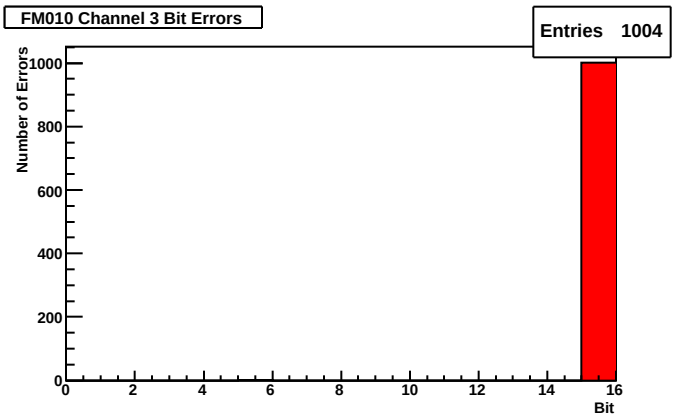

FM009 Channel 6 Bit Errors
Entries 2
Number of Errors
Bit


FM009 Channel 7 Bit Errors
Entries 16
Number of Errors
Bit

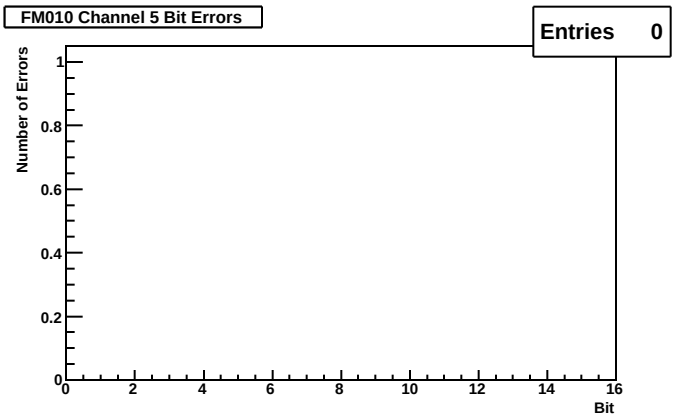

FM010 Channel 0 Bit Errors
Entries 8647
Number of Errors
Bit

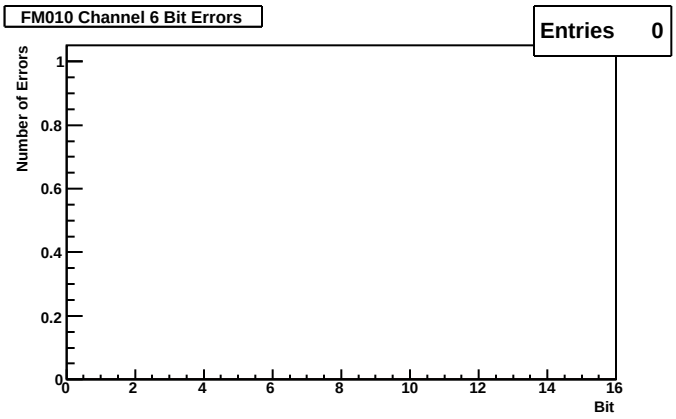

FM010 Channel 1 Bit Errors
Entries 24333
Number of Errors
Bit

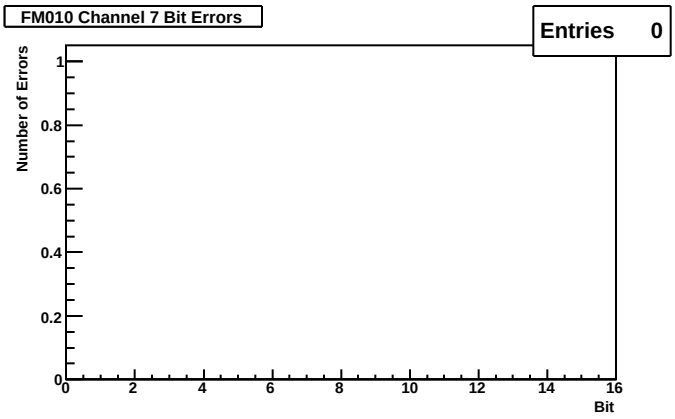
FM010 Channel 2 Bit Errors
Entries 1
Number of Errors
Bit

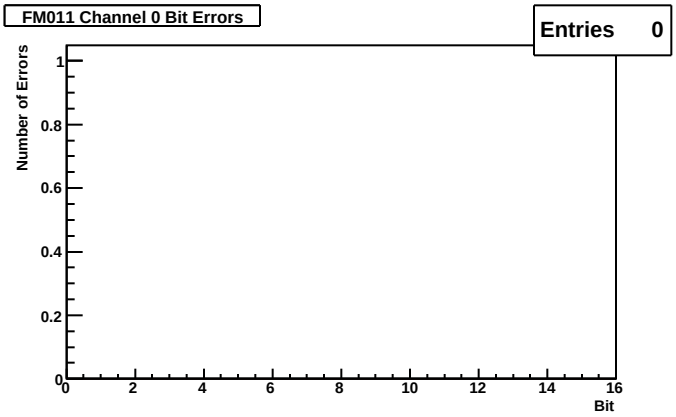

FM010 Channel 3 Bit Errors
Entries 1004
Number of Errors
Bit

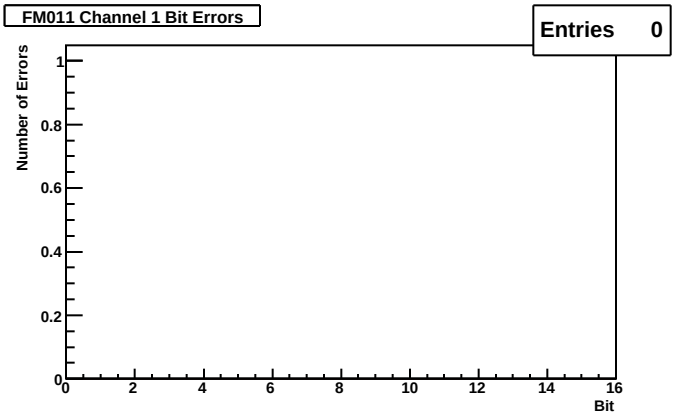
FM010 Channel 4 Bit Errors
Entries1730980
Number of Errors
Bit

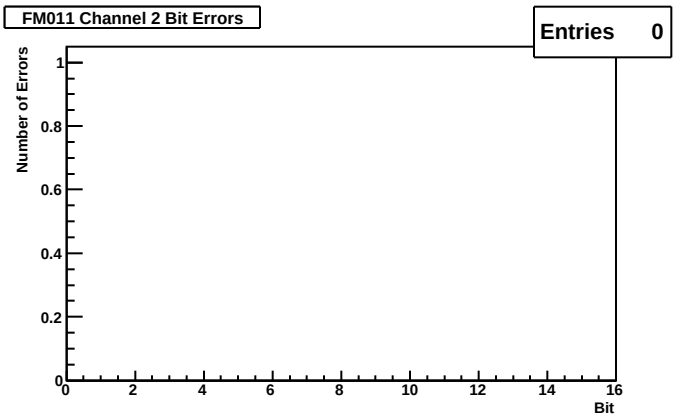

FM010 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

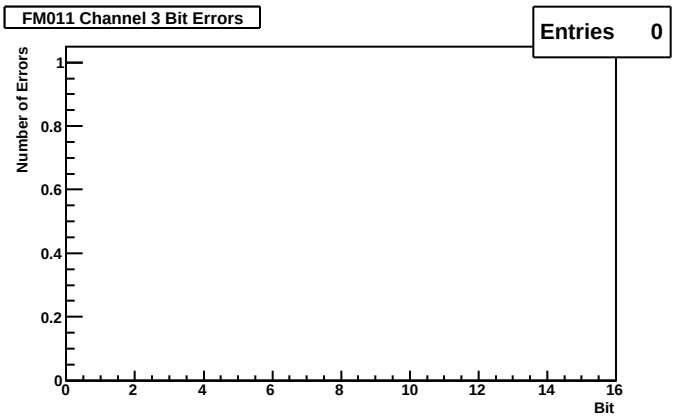

FM010 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

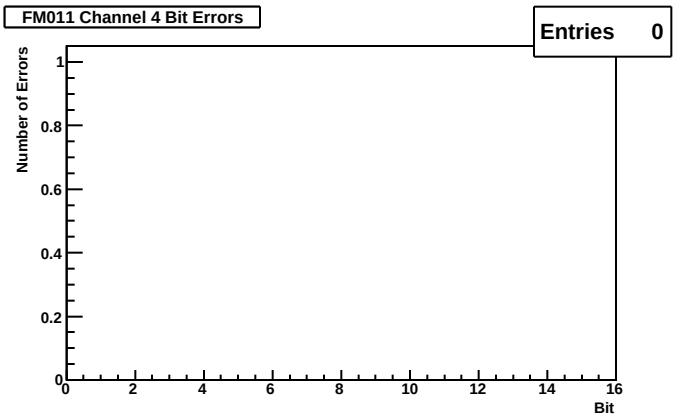

FM010 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

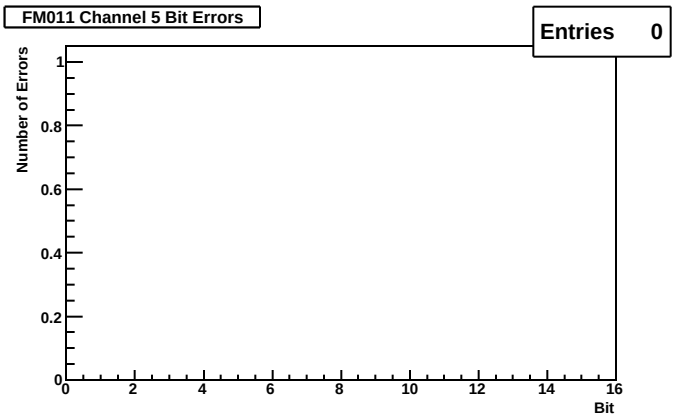

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

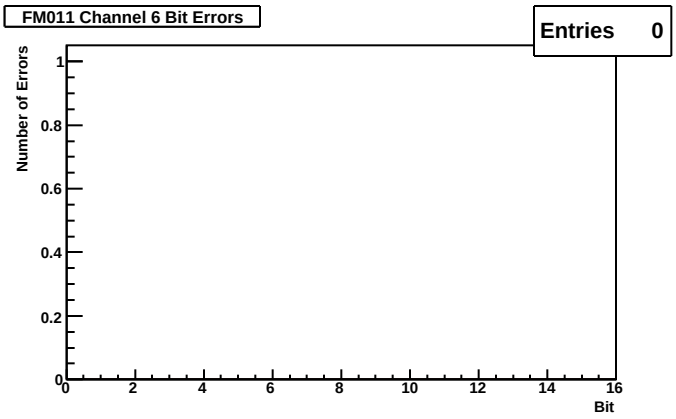

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

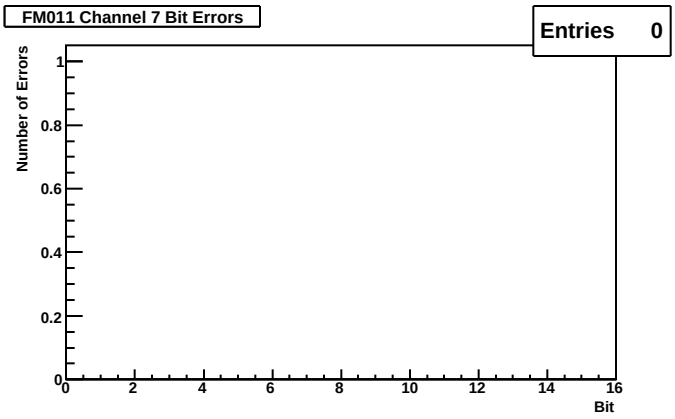

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

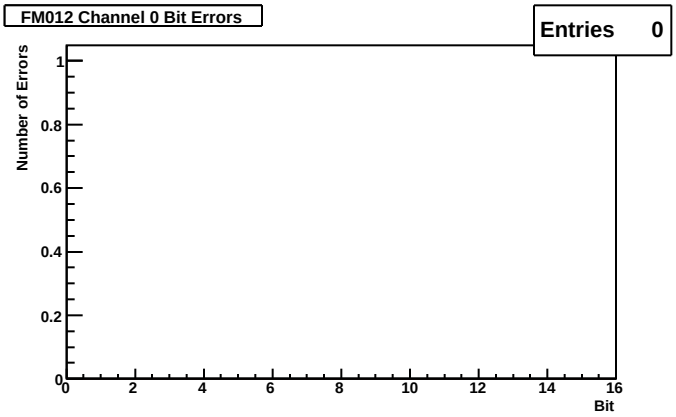

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

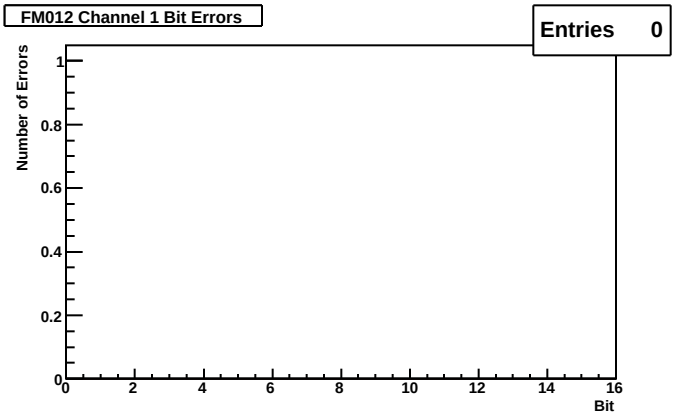

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

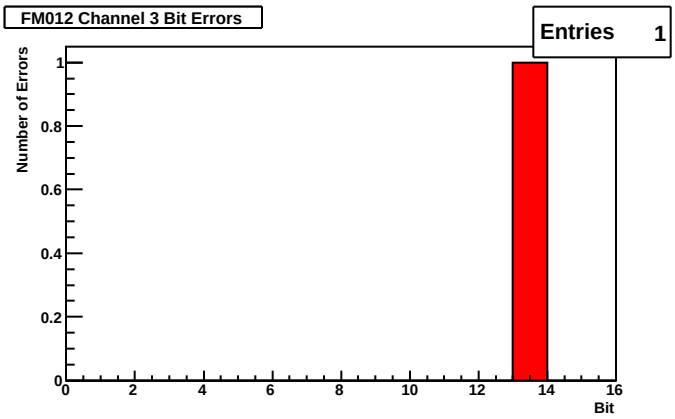

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

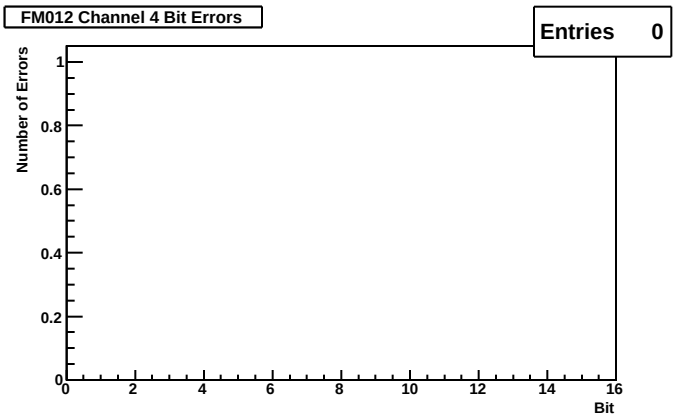

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

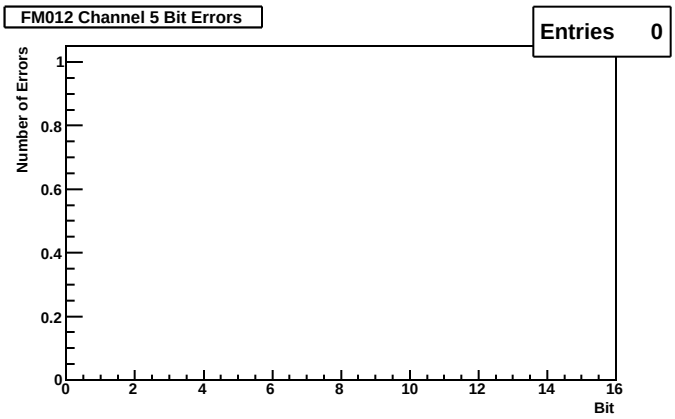

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

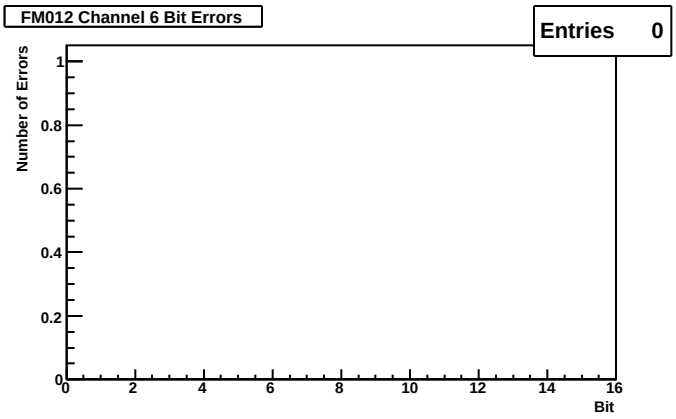

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

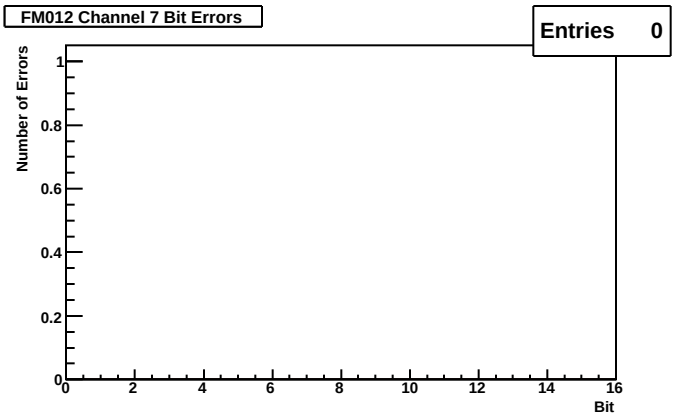
FM012 Channel 2 Bit Errors
Entries 5
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 1
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit