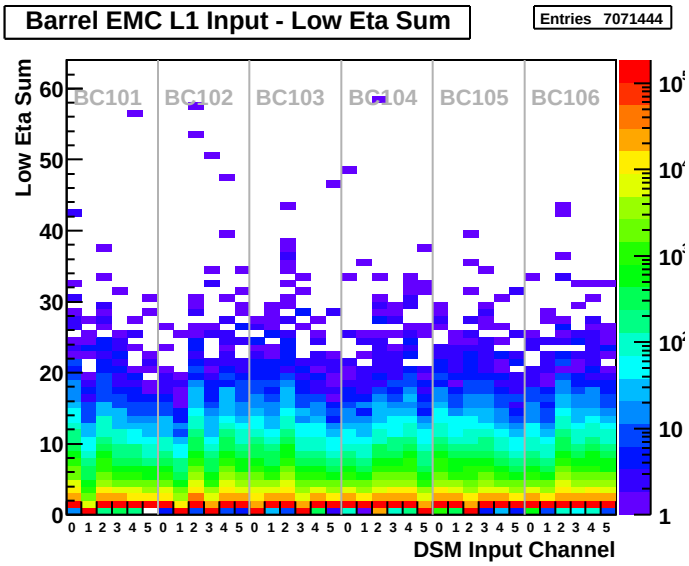
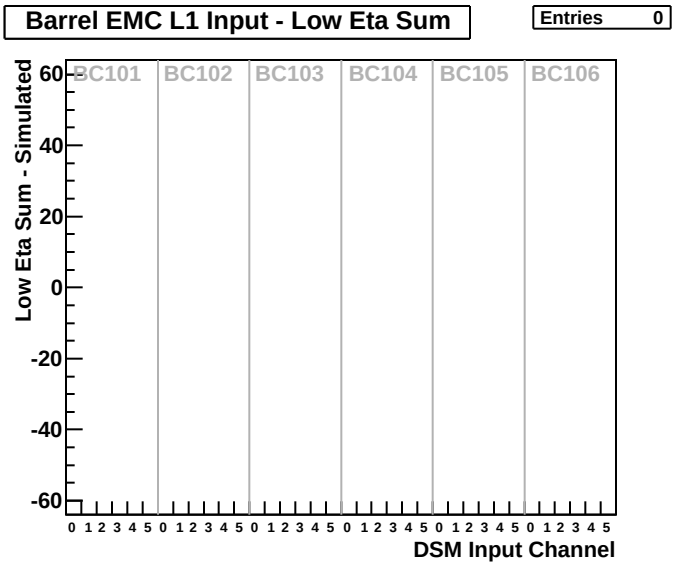
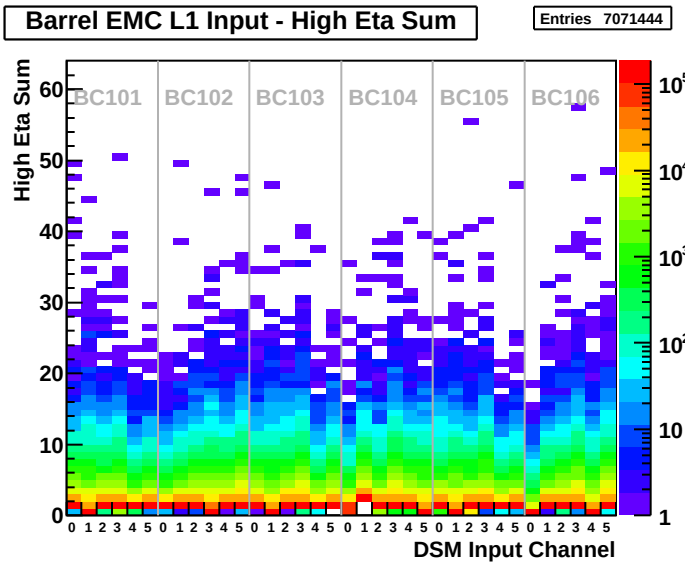
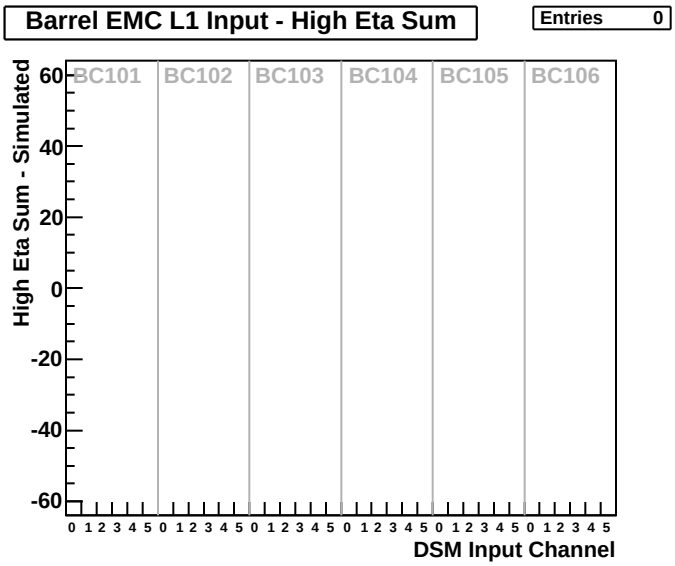
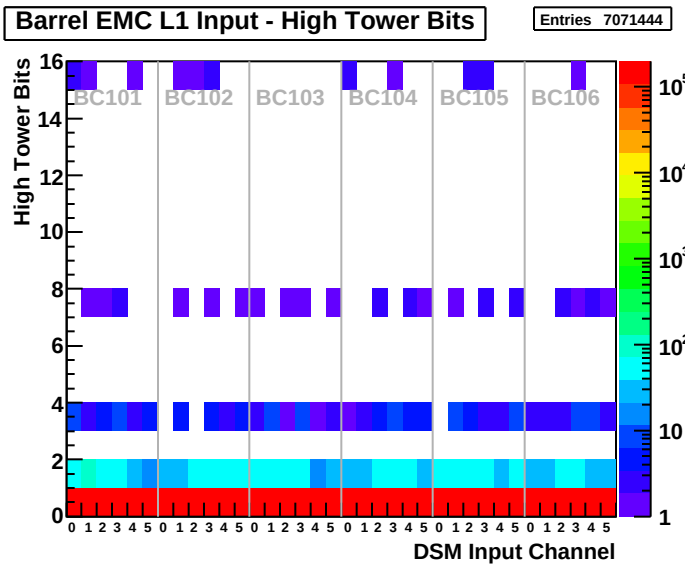
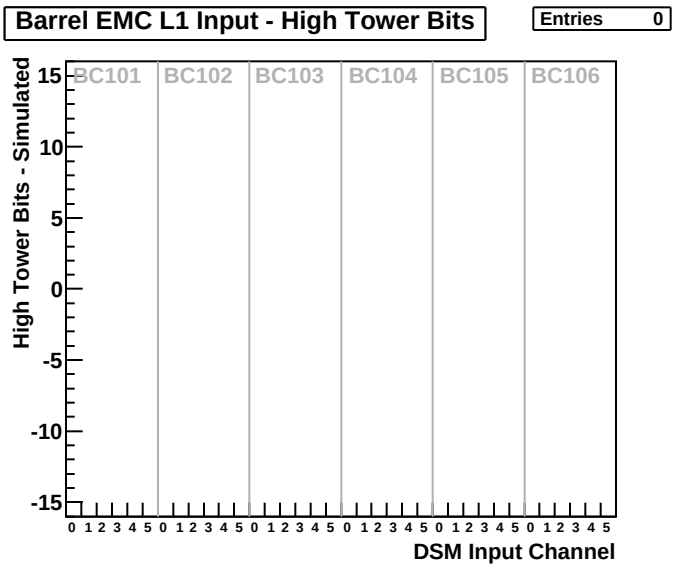

Barrel EMC L1 Input - Low Eta Sum
Entries 7071444
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

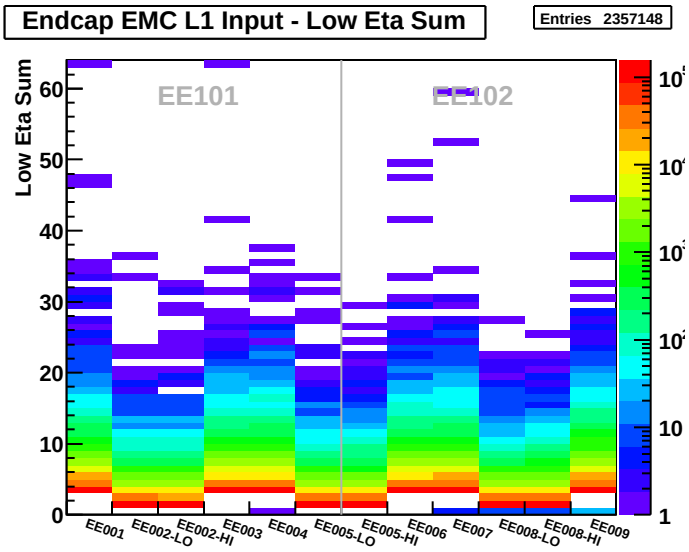

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

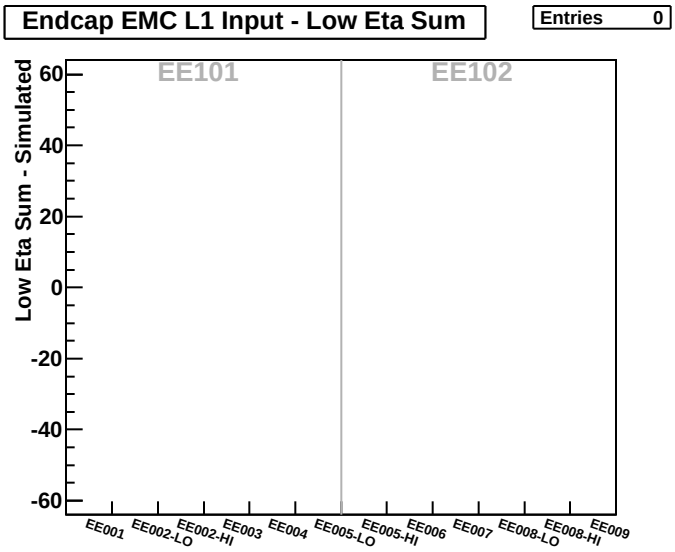

Barrel EMC L1 Input - High Eta Sum
Entries 7071444
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

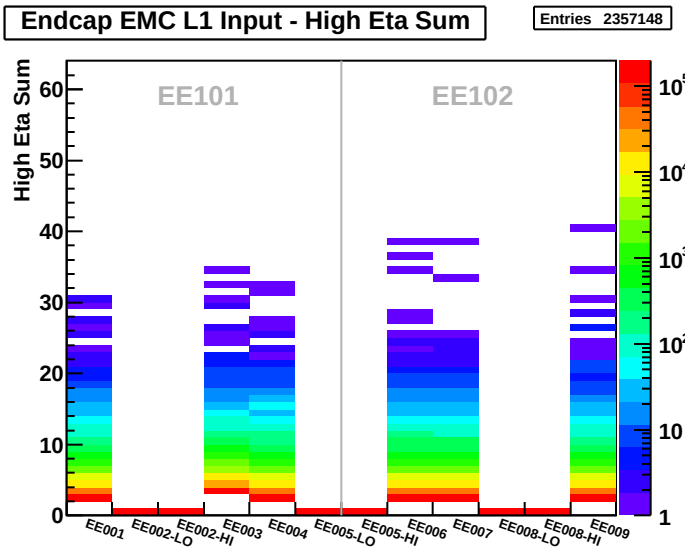

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

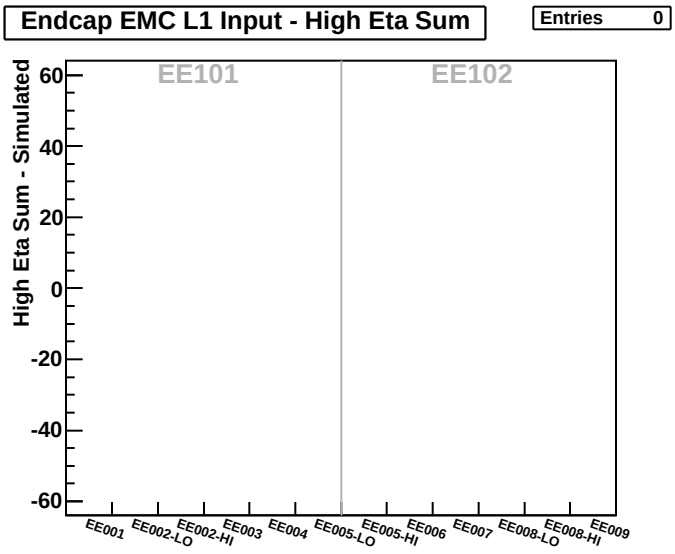

Barrel EMC L1 Input - High Tower Bits
Entries 7071444
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

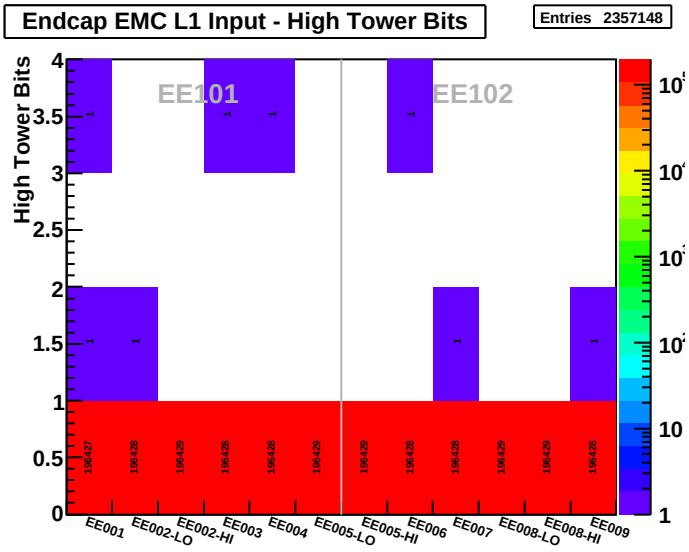

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

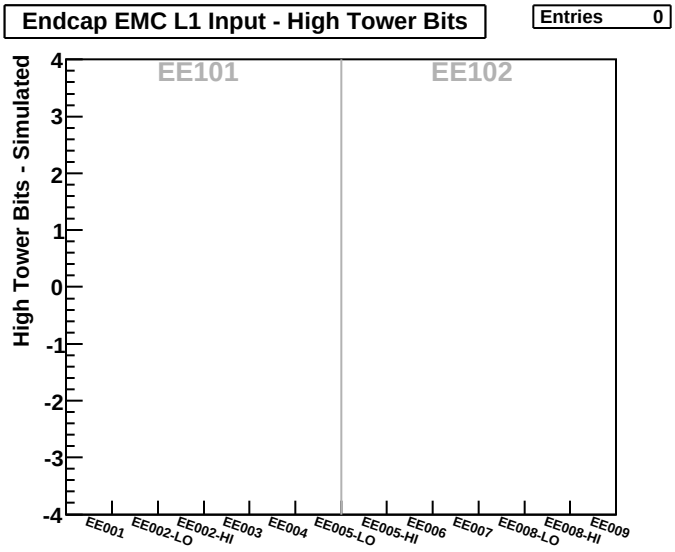

Endcap EMC L1 Input - Low Eta Sum
Entries 2357148
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

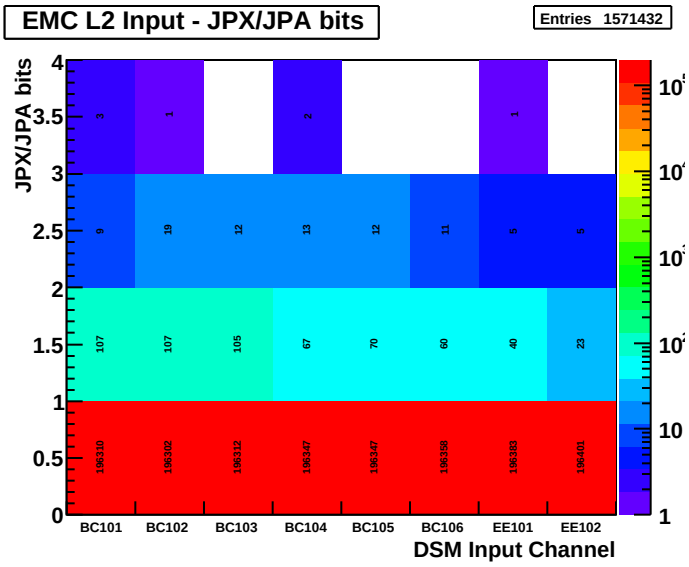

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

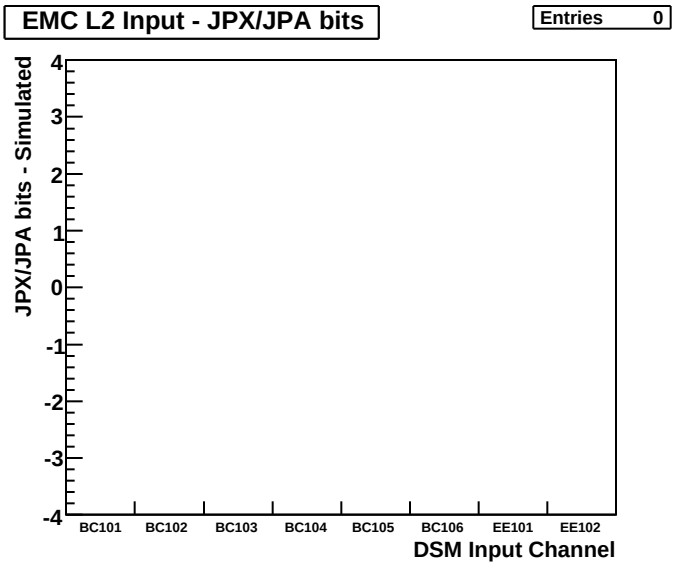

Endcap EMC L1 Input - High Eta Sum
Entries 2357148
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

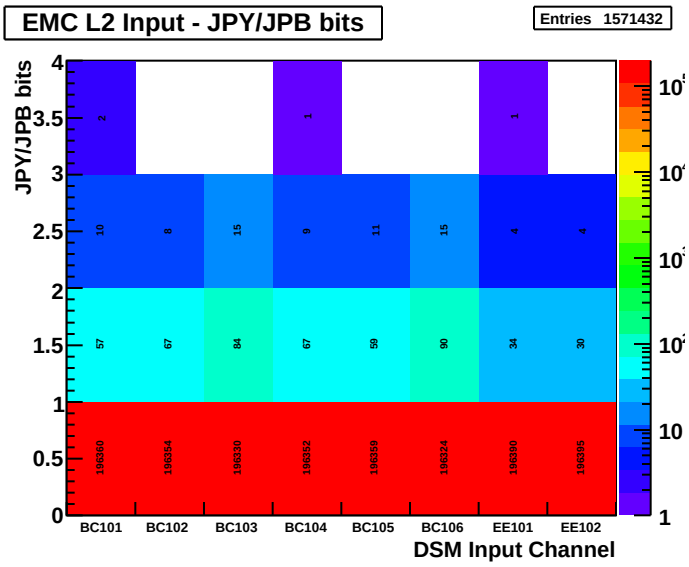

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

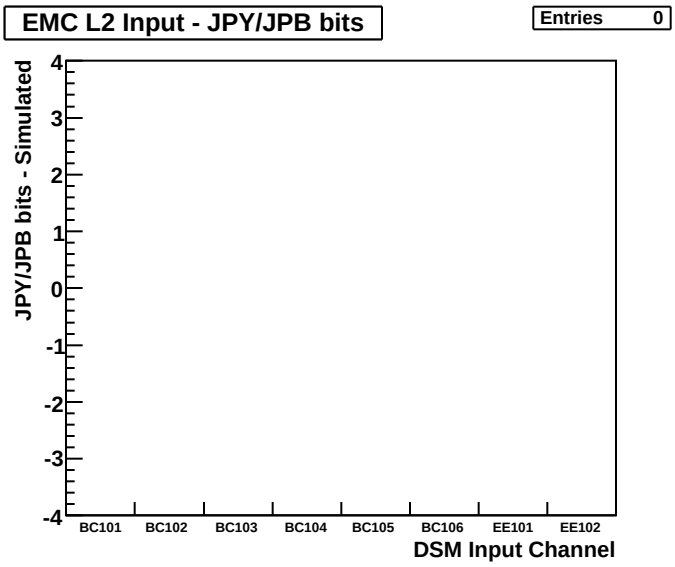

Endcap EMC L1 Input - High Tower Bits
Entries 2357148
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

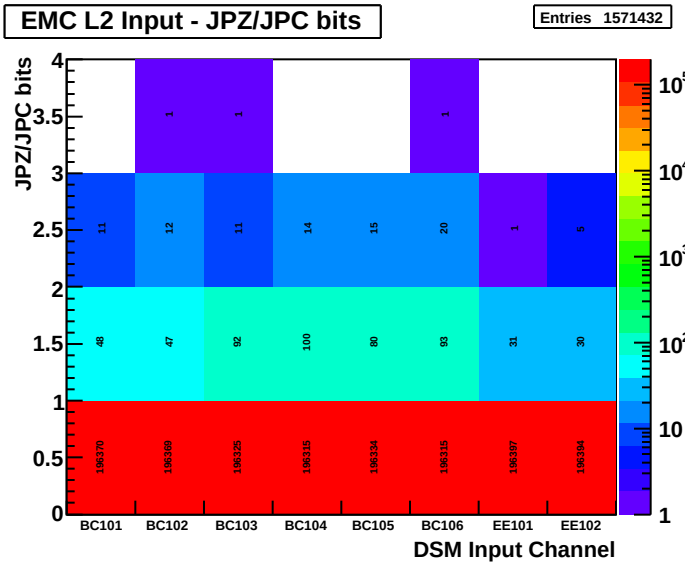

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

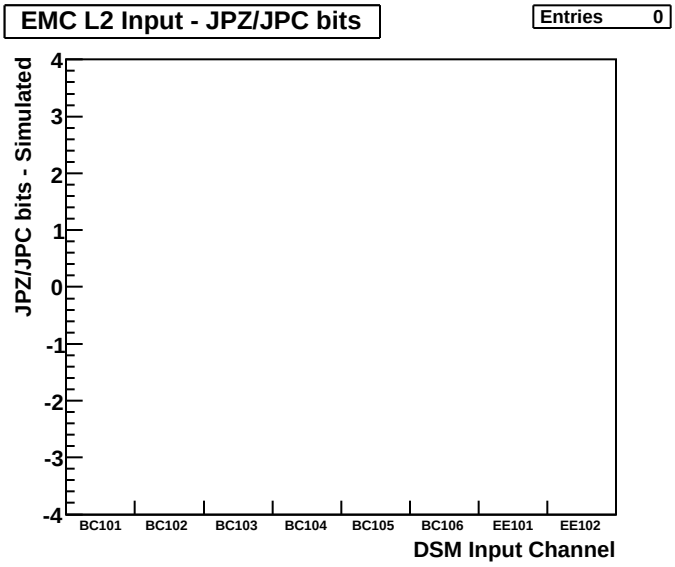

EMC L2 Input - JPX/JPA bits
Entries 1571432
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 1 2 1
9 19 12 13 12 11 5 5
107 107 105 67 70 60 40 23
196310 196302 196312 196347 196347 196358 196383 196401

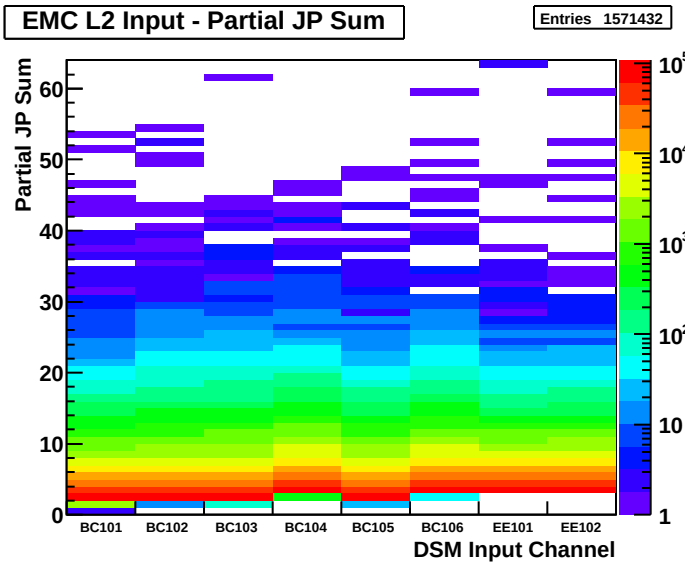

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

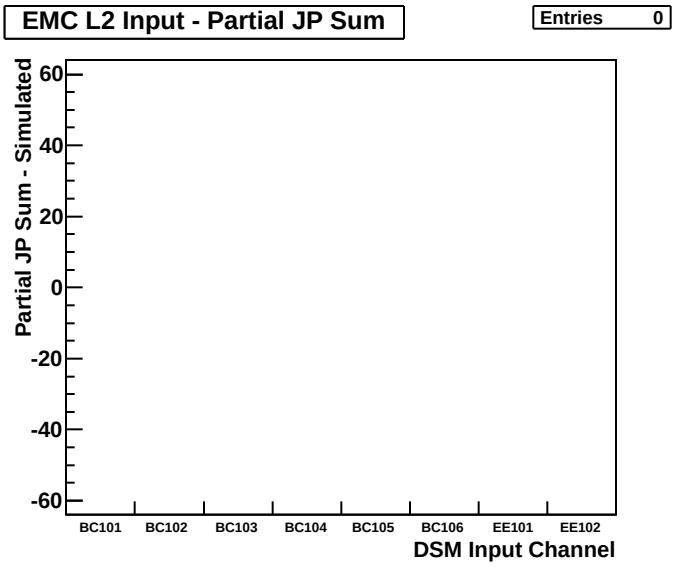

EMC L2 Input - JPY/JPB bits
Entries 1571432
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 1
10 8 15 9 11 15 4 4
57 67 84 67 59 90 34 30
196360 196354 196330 196352 196359 196324 196390 196395

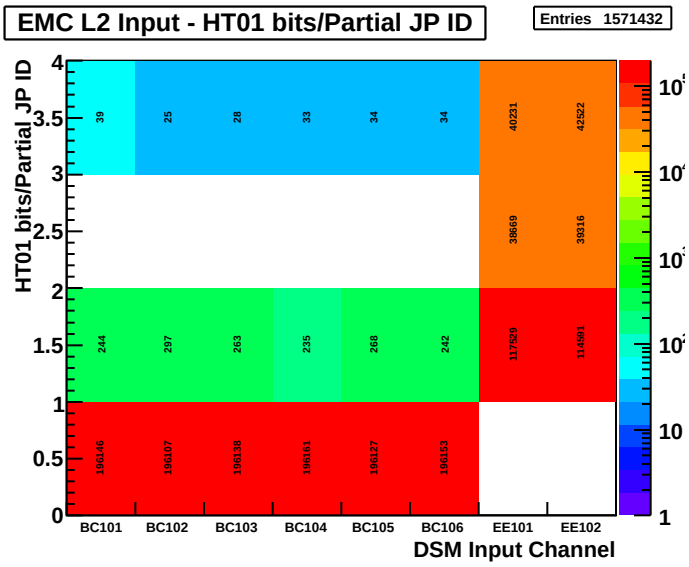

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

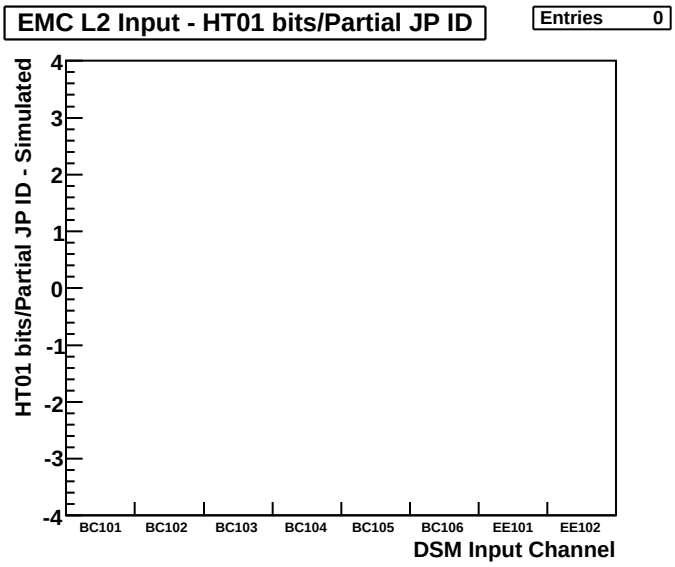

EMC L2 Input - JPZ/JPC bits
Entries 1571432
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
11 12 11 14 15 20 1 5
48 47 92 100 80 93 31 30
196370 196369 196325 196315 196334 196315 196397 196394

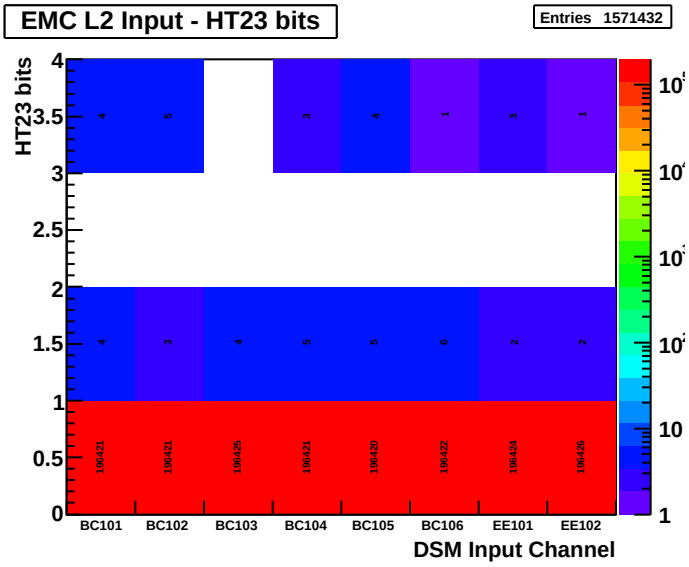

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

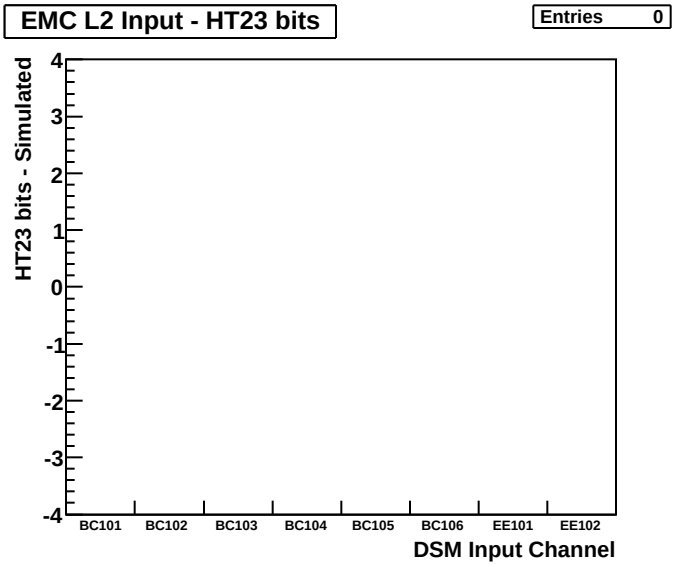

EMC L2 Input - Partial JP Sum
Entries 1571432
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

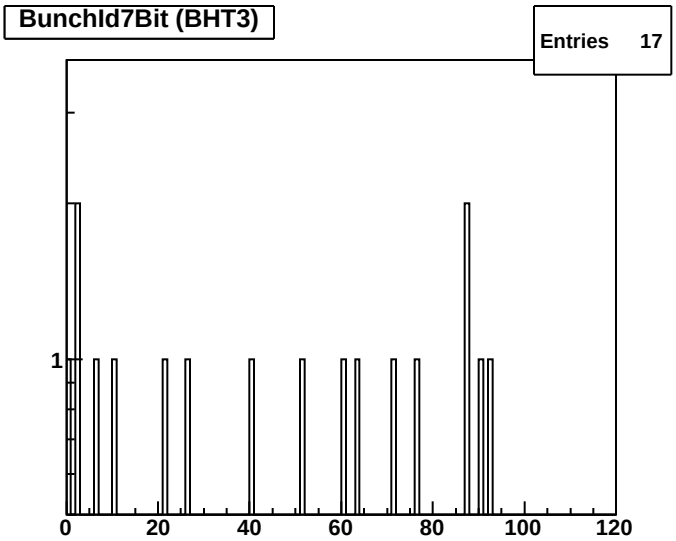

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

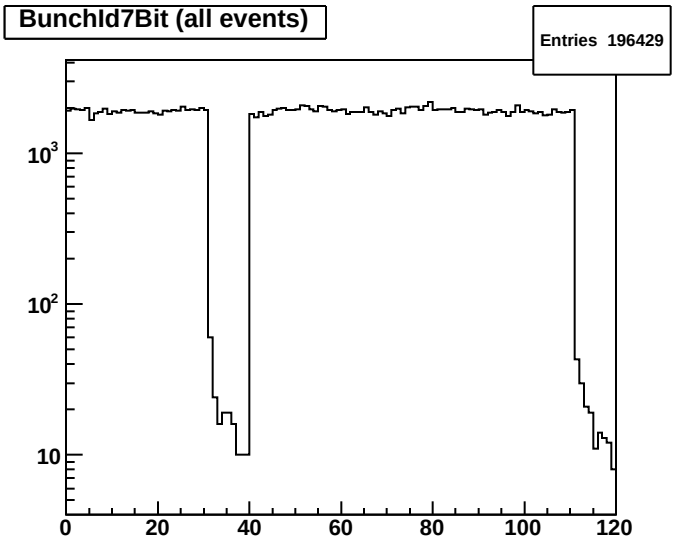

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1571432
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

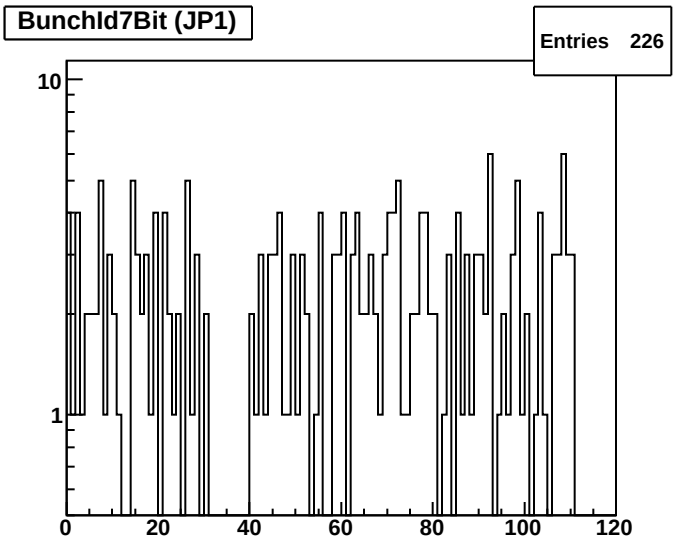

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

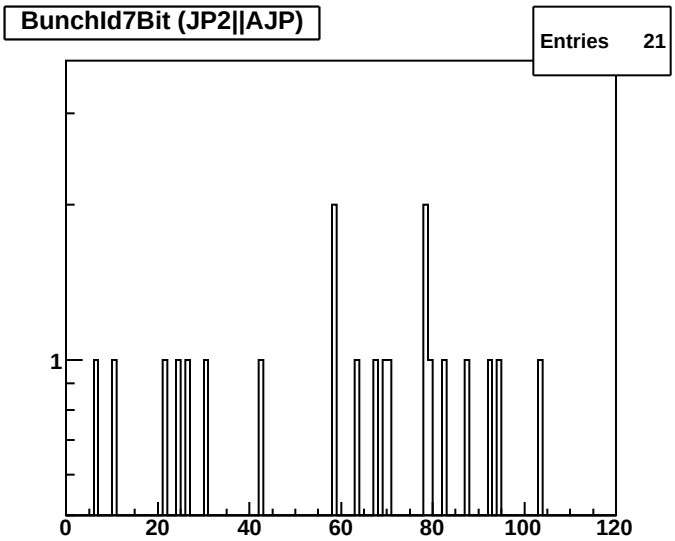

EMC L2 Input - HT23 bits
Entries 1571432
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

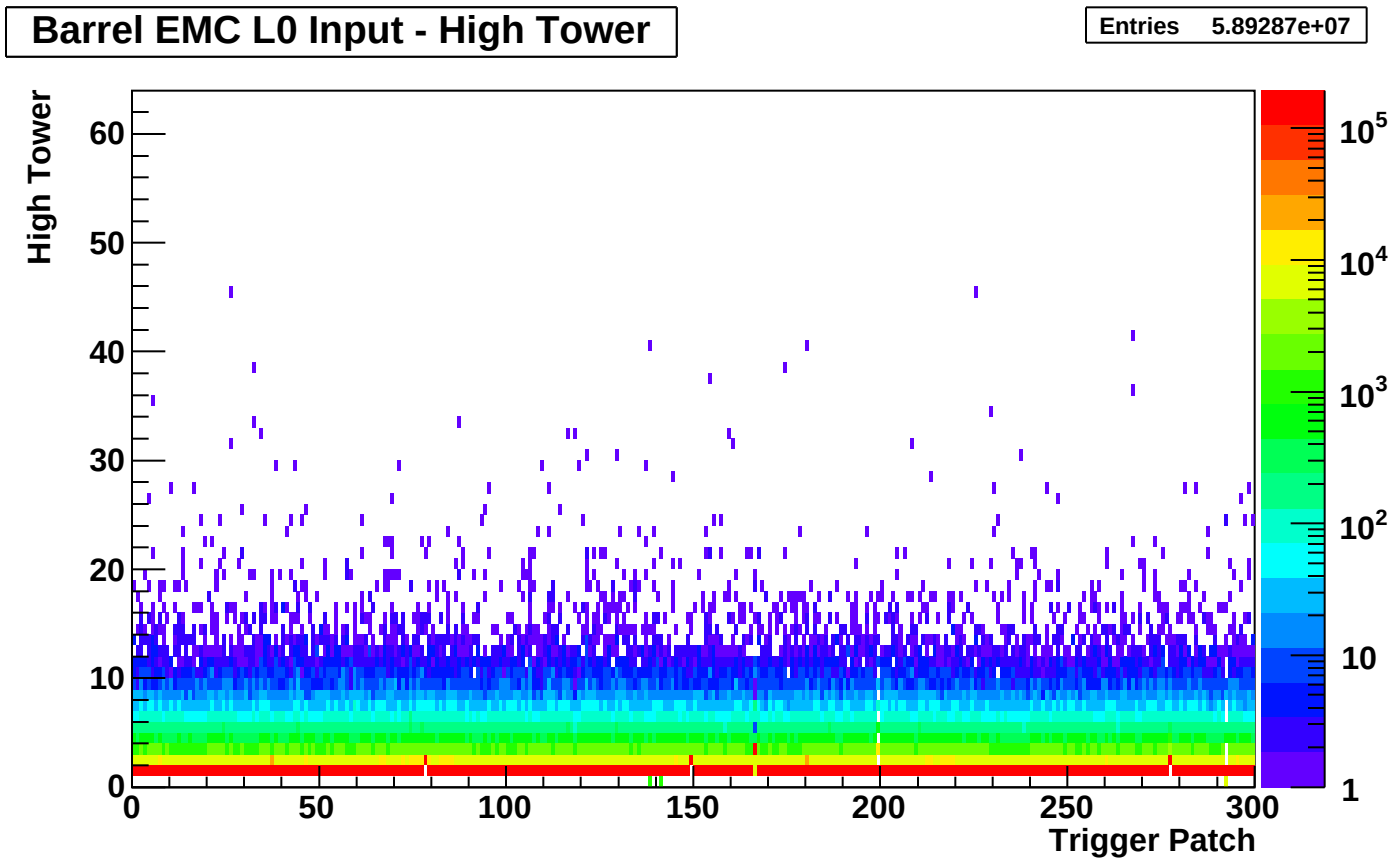

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

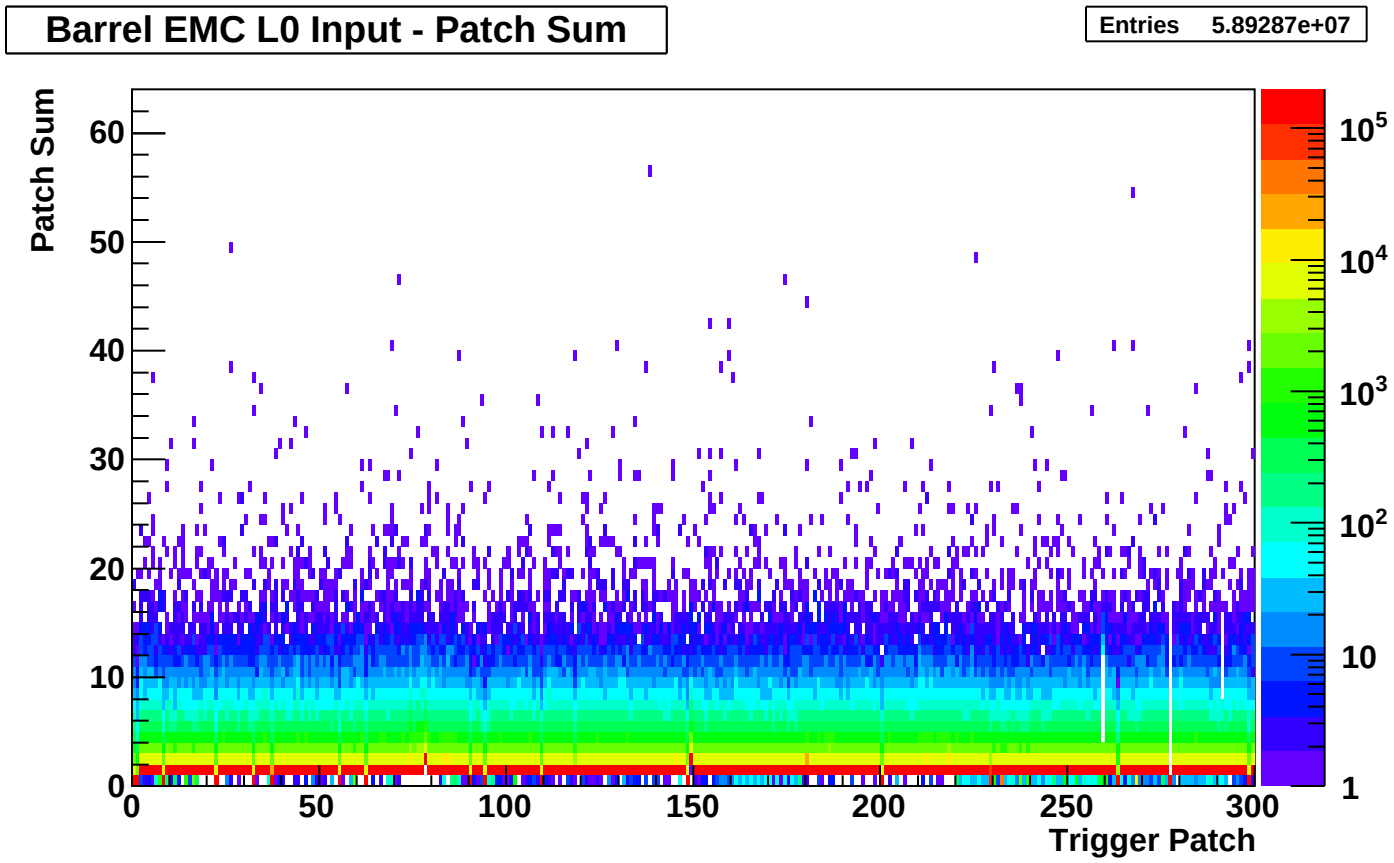

BunchId7Bit (BHT3)
Entries 17
1
0
20
40
60
80
100
120

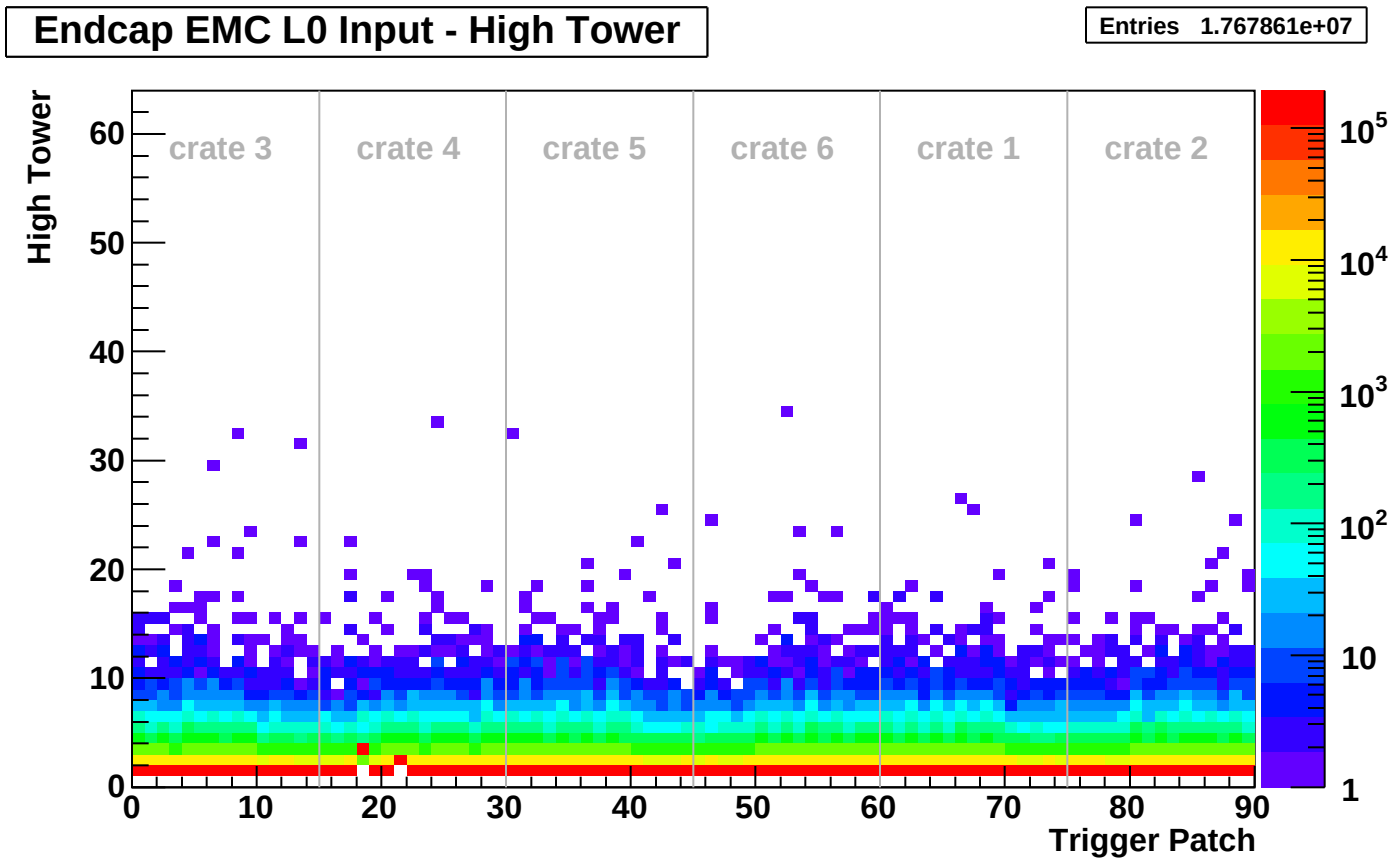

BunchId7Bit (all events)
Entries 196429
10^3
10^2
10
0
20
40
60
80
100
120

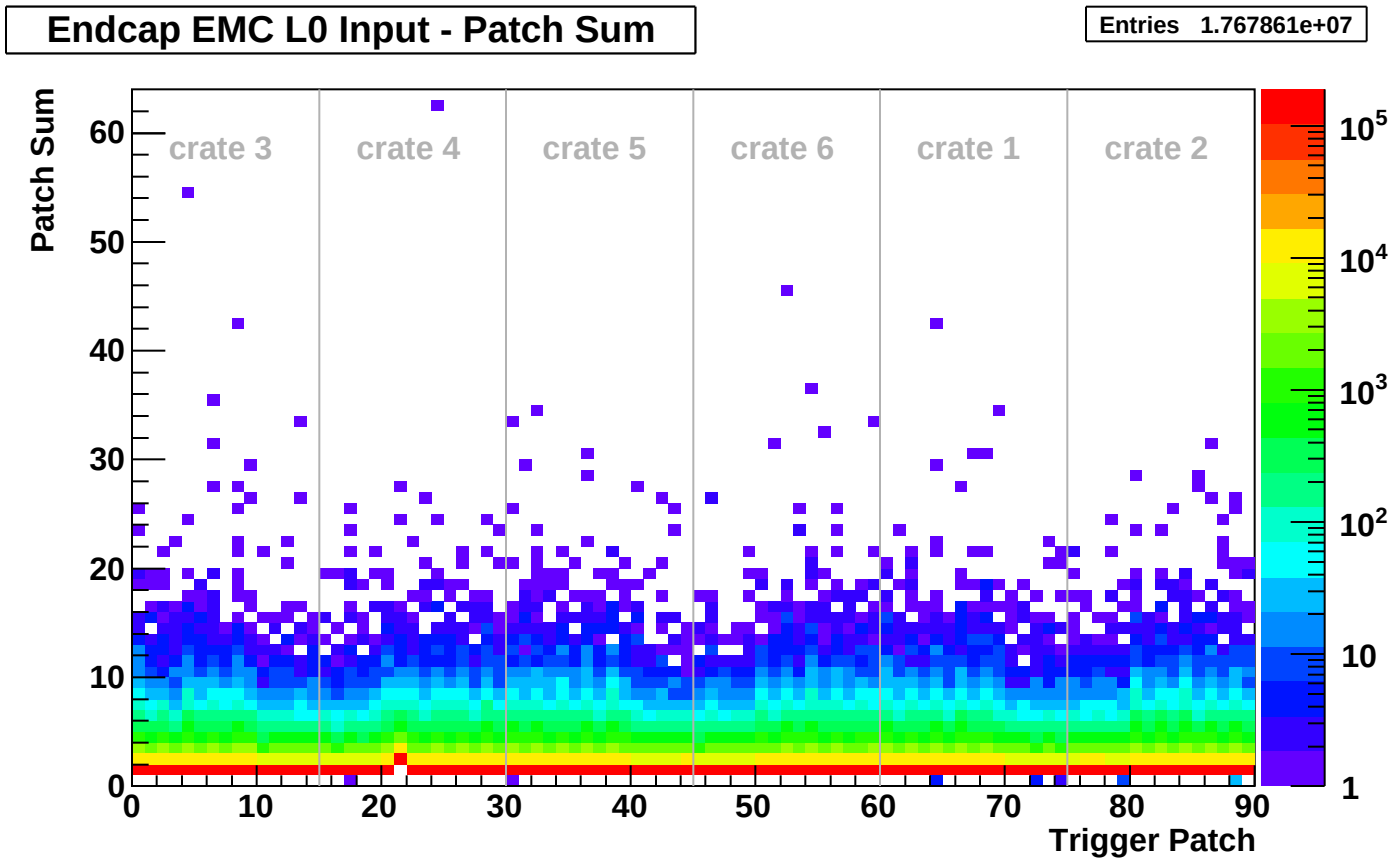

BunchId7Bit (JP1)
Entries 226
10
1
0
20
40
60
80
100
120

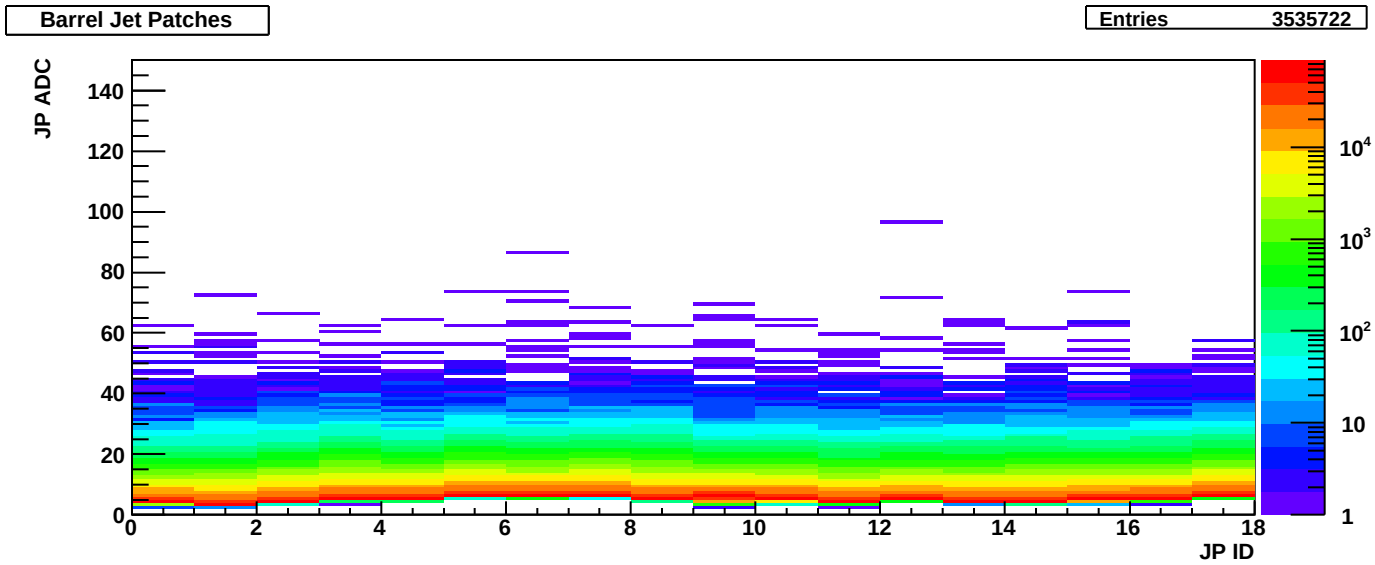

BunchId7Bit (JP2||AJP)
Entries 21
1
0
20
40
60
80
100
120

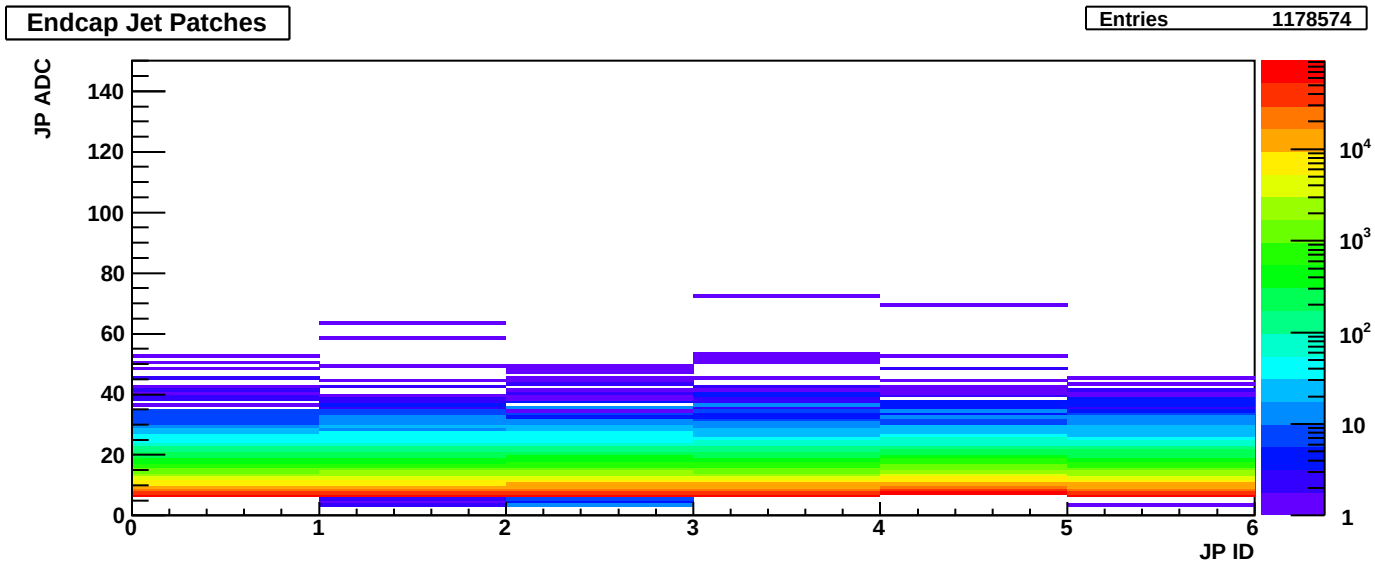

Barrel EMC L0 Input - High Tower
Entries 5.89287e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

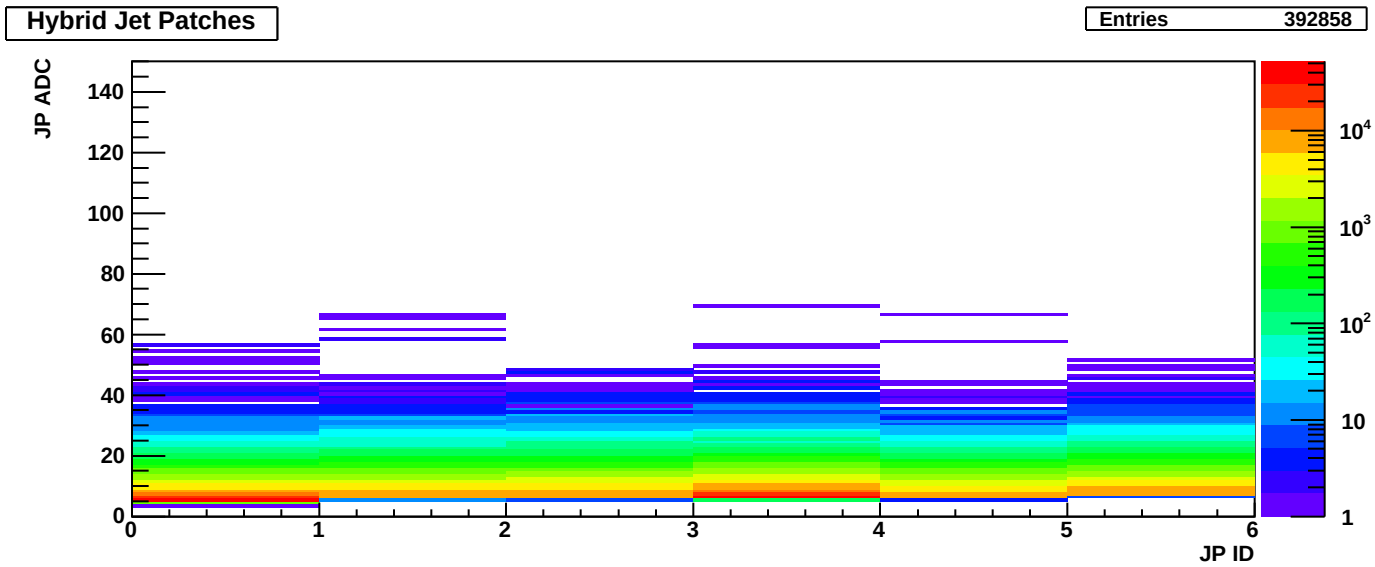

Barrel EMC L0 Input - Patch Sum
Entries 5.89287e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

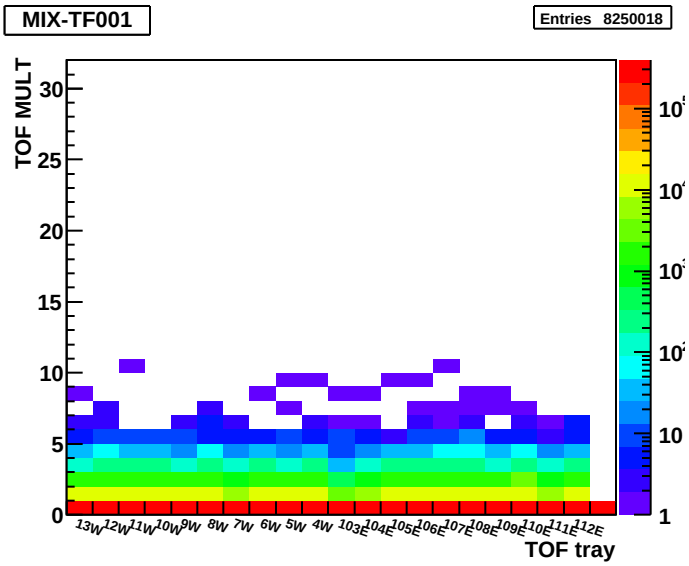

Endcap EMC L0 Input - High Tower
Entries 1.767861e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

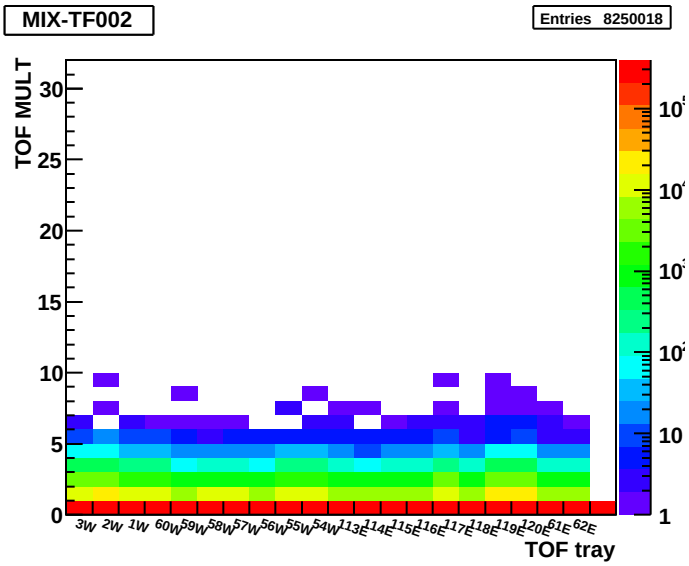

Endcap EMC L0 Input - Patch Sum
Entries 1.767861e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

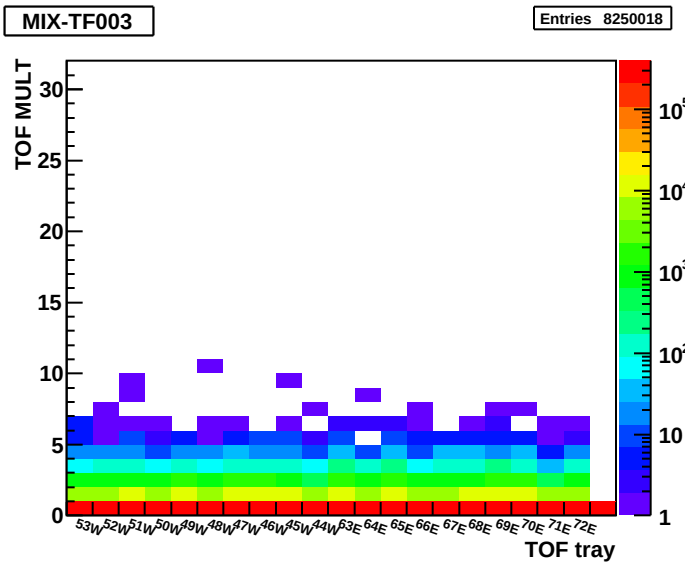

Barrel Jet Patches
Entries 3535722
JP ADC
JP ID

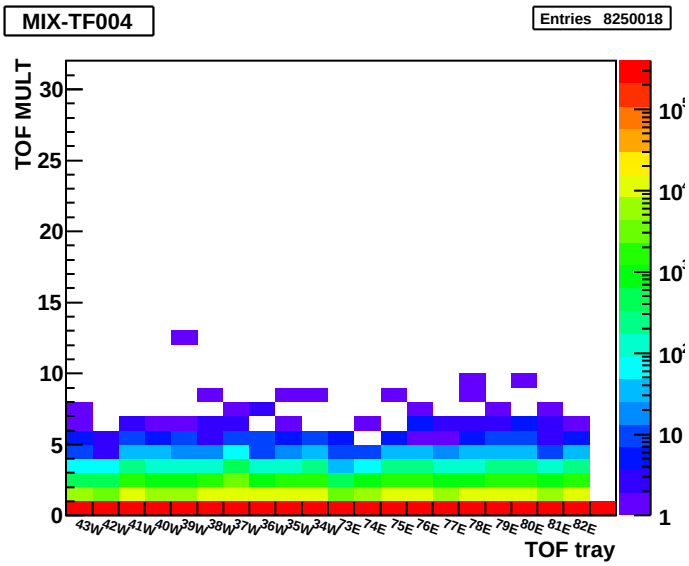

Endcap Jet Patches
Entries 1178574
JP ADC
JP ID

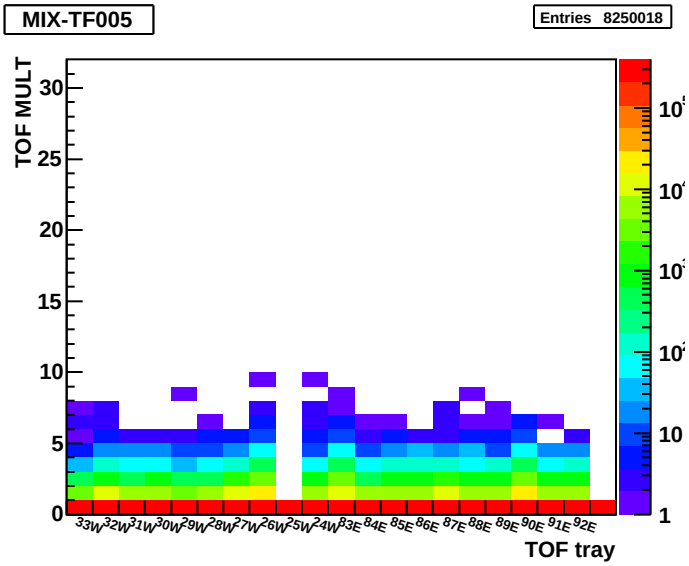

Hybrid Jet Patches
Entries 392858
JP ADC
JP ID

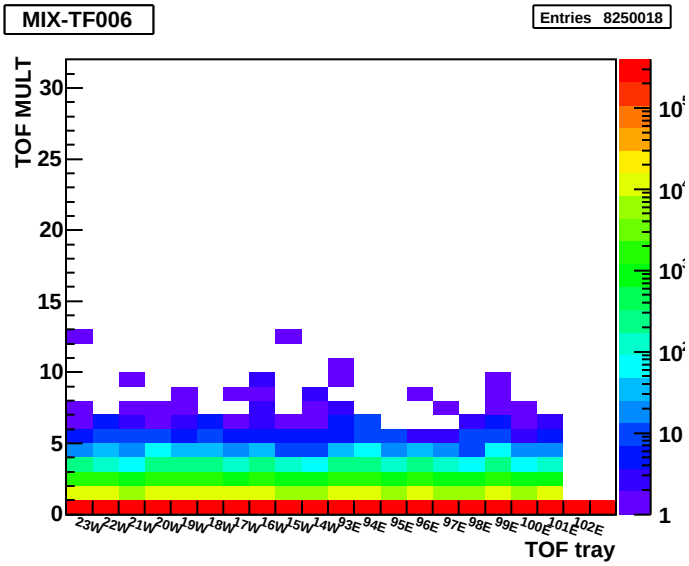

MIX-TF001
Entries 8250018
TOF MULT
TOF tray

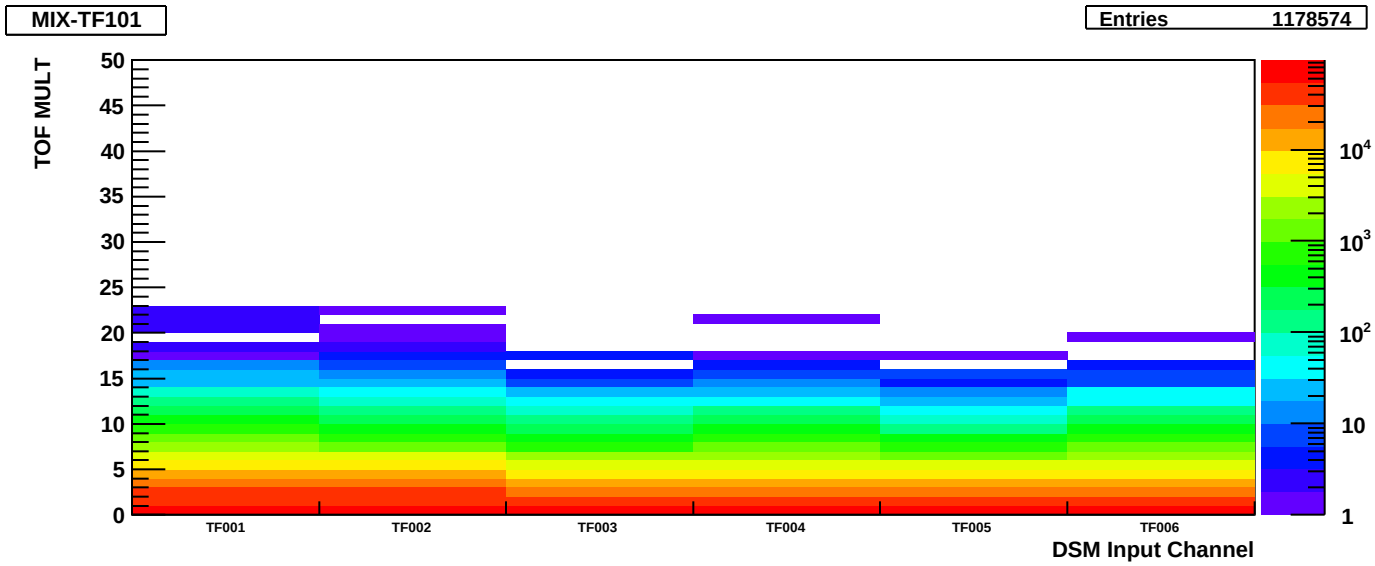

MIX-TF002
Entries 8250018
TOF MULT
TOF tray

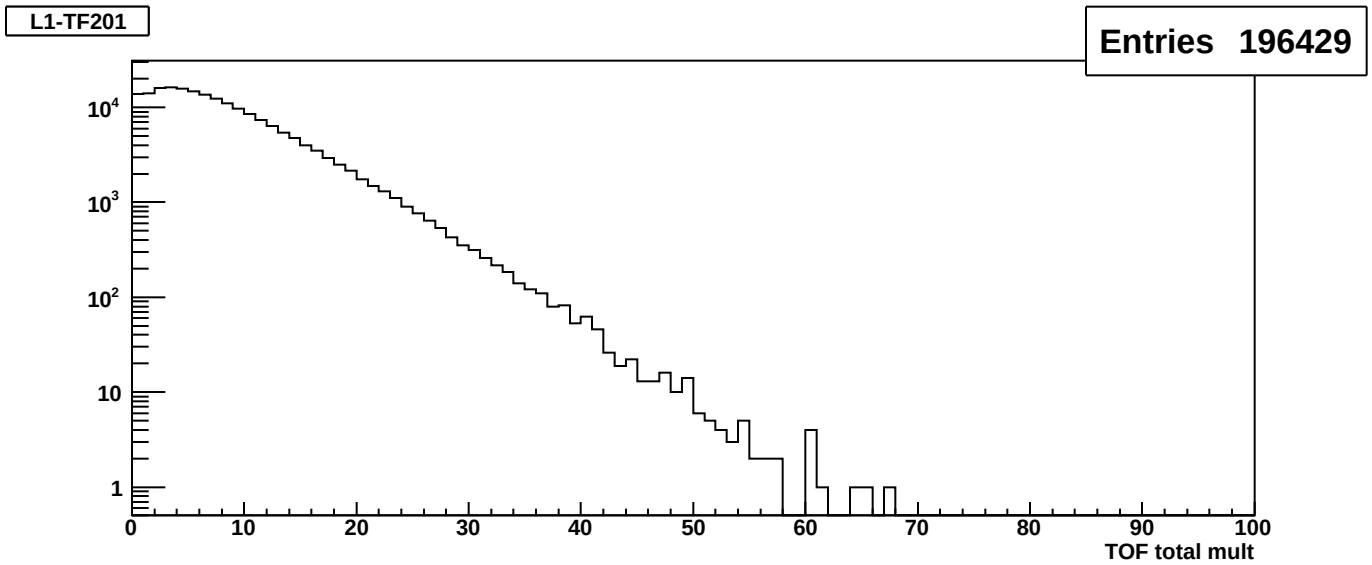

MIX-TF003
Entries 8250018
TOF MULT
TOF tray

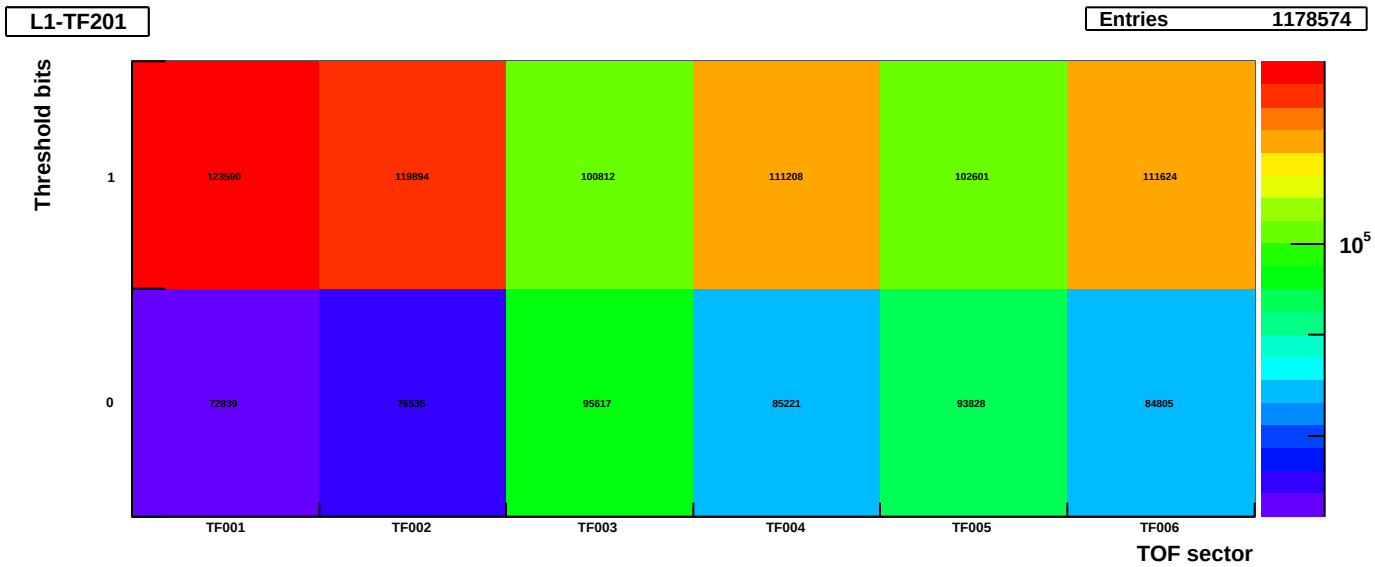

MIX-TF004
Entries 8250018
TOF MULT
TOF tray

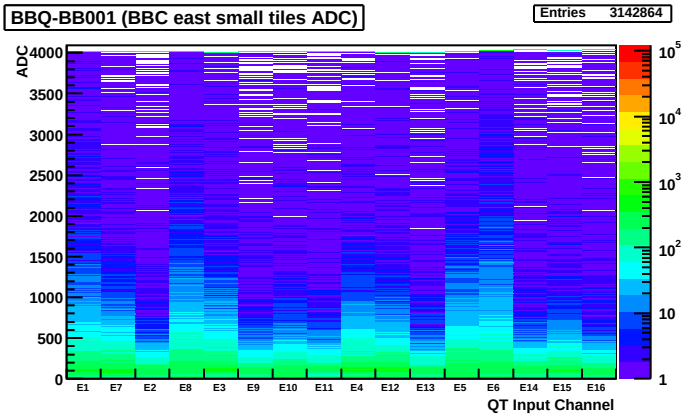

MIX-TF005
Entries 8250018
TOF MULT
TOF tray

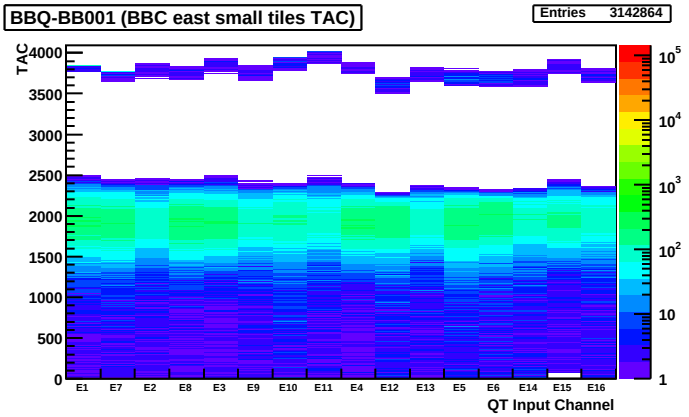

MIX-TF006
Entries 8250018
TOF MULT
TOF tray

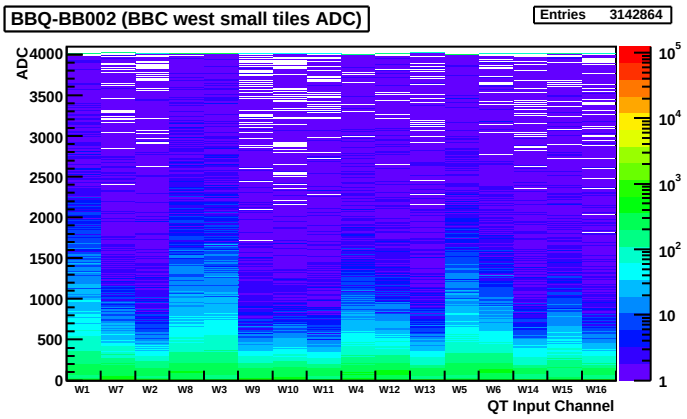

MIX-TF101
Entries 1178574
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

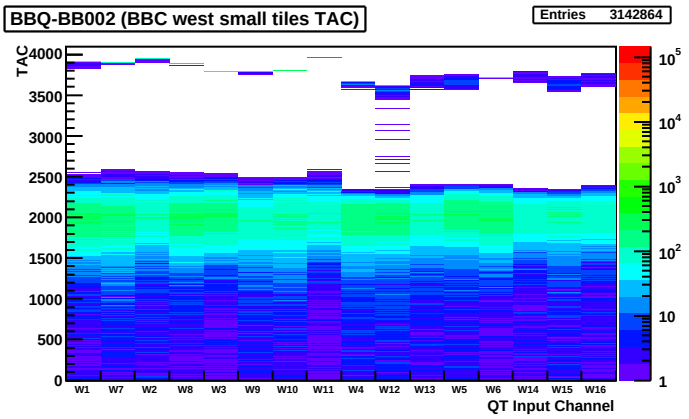

L1-TF201
Entries 196429
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

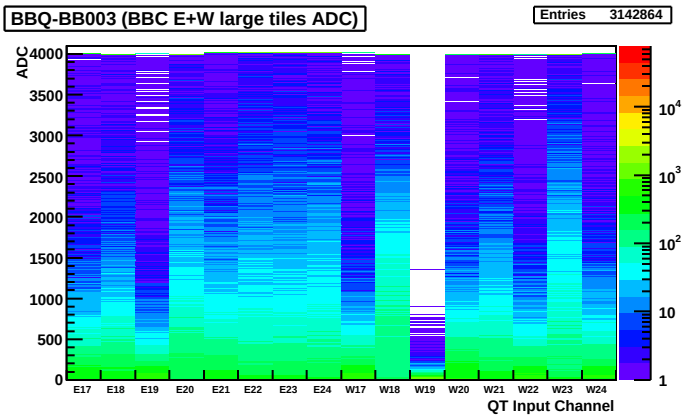

L1-TF201
Entries 1178574
Threshold bits
1
0
123590
119894
100812
111208
102601
111624
72839
76535
95617
85221
93828
84805
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

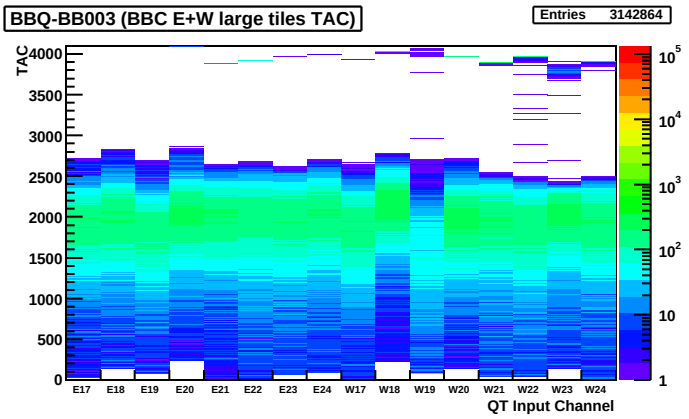

BBQ-BB001 (BBC east small tiles ADC)
Entries 3142864
ADC
QT Input Channel

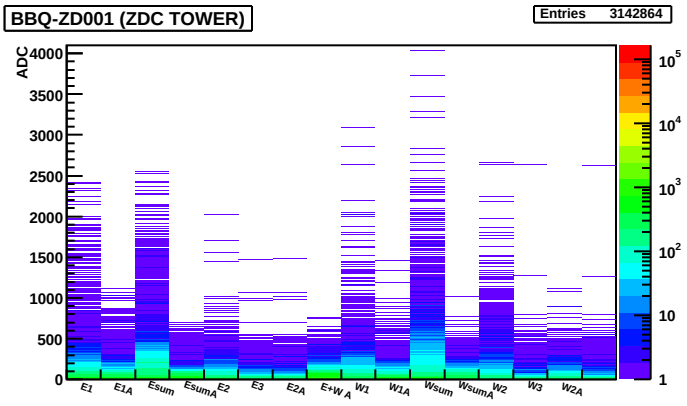

BBQ-BB001 (BBC east small tiles TAC)
Entries 3142864
TAC
QT Input Channel

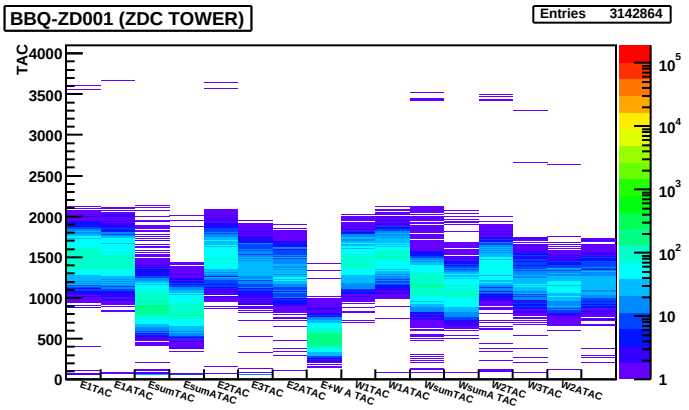

BBQ-BB002 (BBC west small tiles ADC)
Entries 3142864
ADC
QT Input Channel

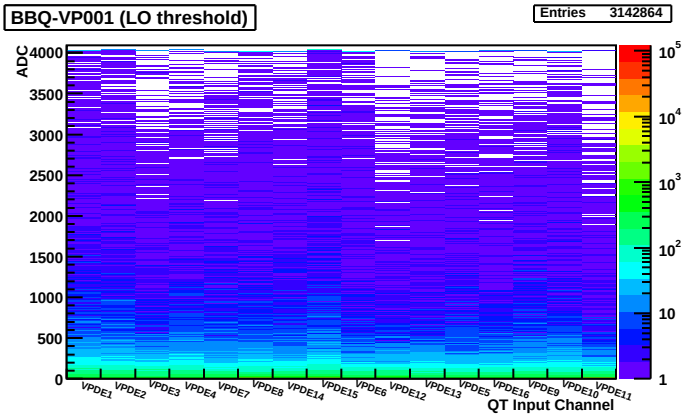

BBQ-BB002 (BBC west small tiles TAC)
Entries 3142864
TAC
QT Input Channel

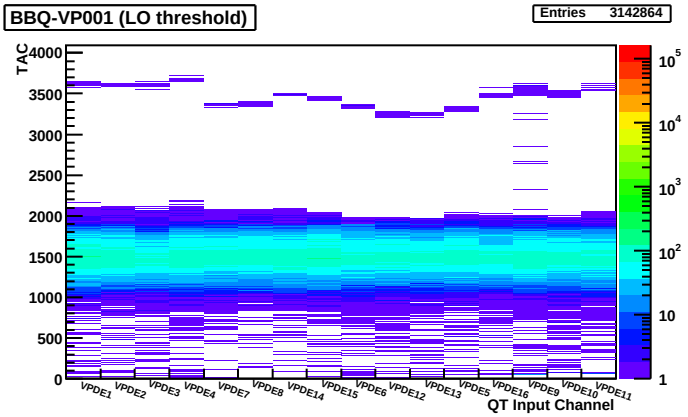

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3142864
ADC
QT Input Channel

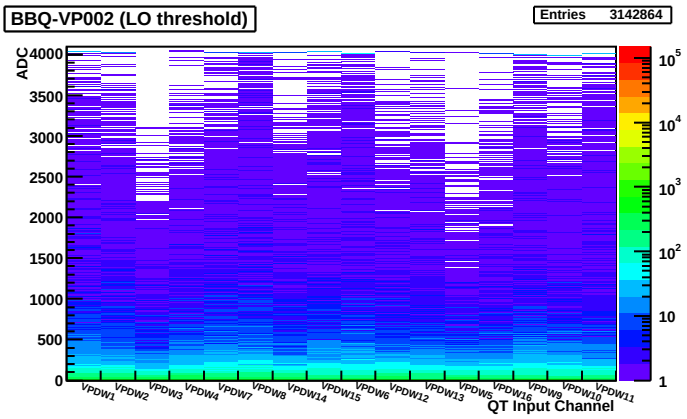

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3142864
TAC
QT Input Channel

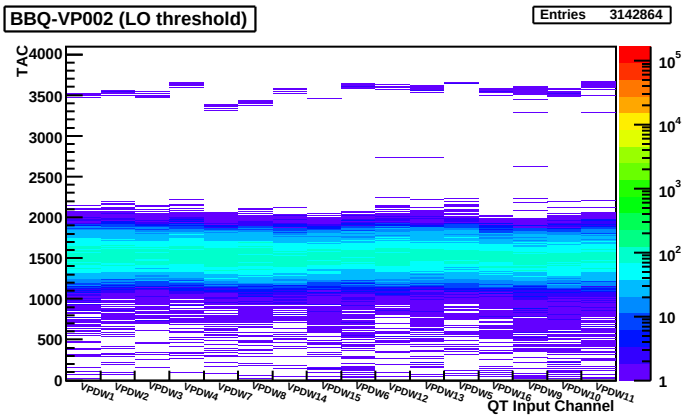

BBQ-ZD001 (ZDC TOWER)
Entries 3142864
ADC

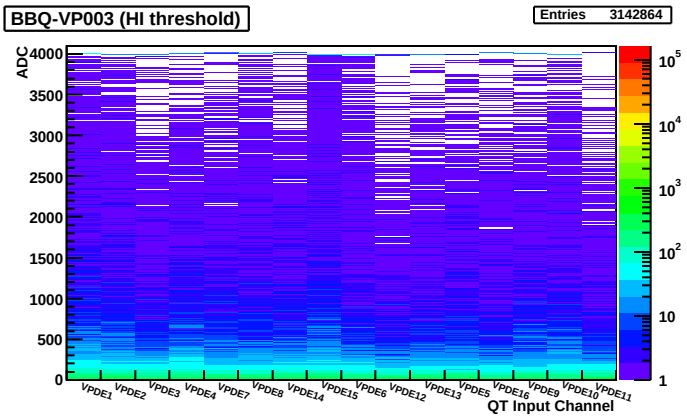

BBQ-ZD001 (ZDC TOWER)
Entries 3142864
TAC

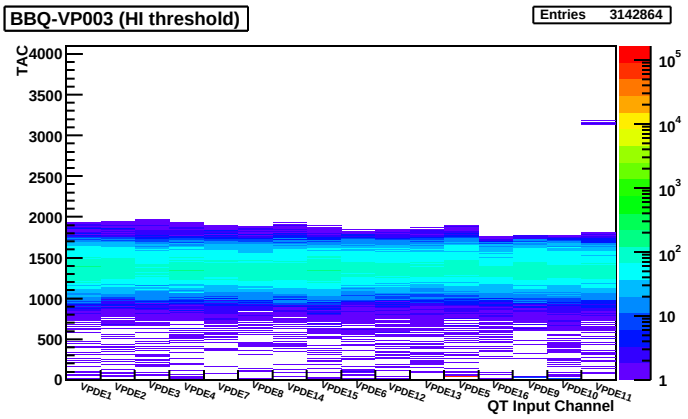

BBQ-VP001 (LO threshold)
Entries 3142864
ADC
QT Input Channel

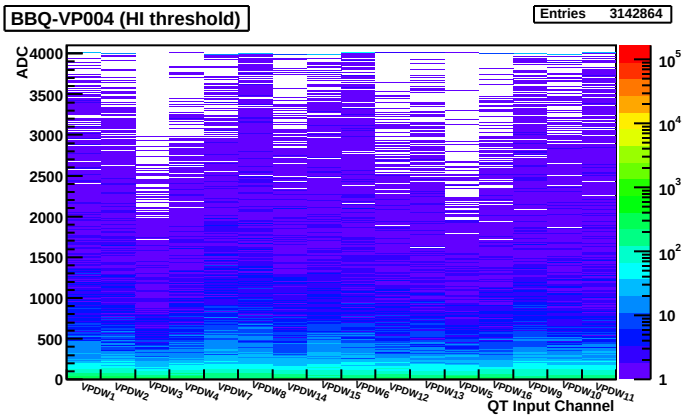

BBQ-VP001 (LO threshold)
Entries 3142864
TAC
QT Input Channel

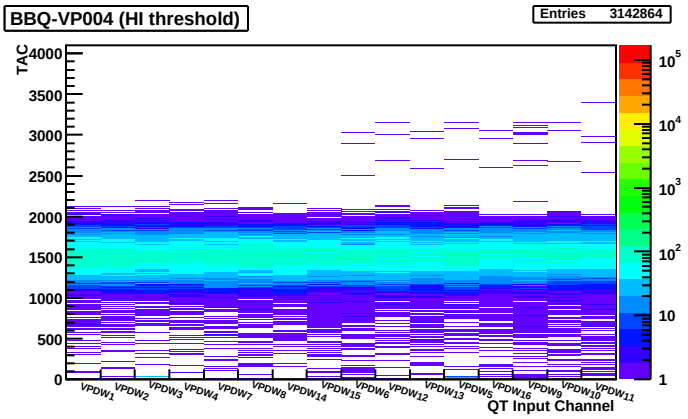

BBQ-VP002 (LO threshold)
Entries 3142864
ADC
QT Input Channel

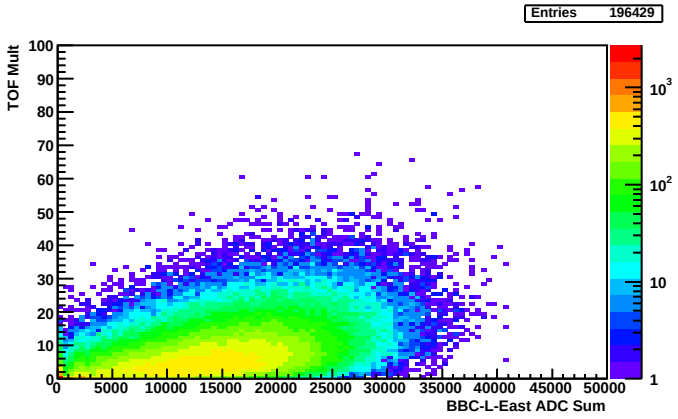

BBQ-VP002 (LO threshold)
Entries 3142864
TAC
QT Input Channel

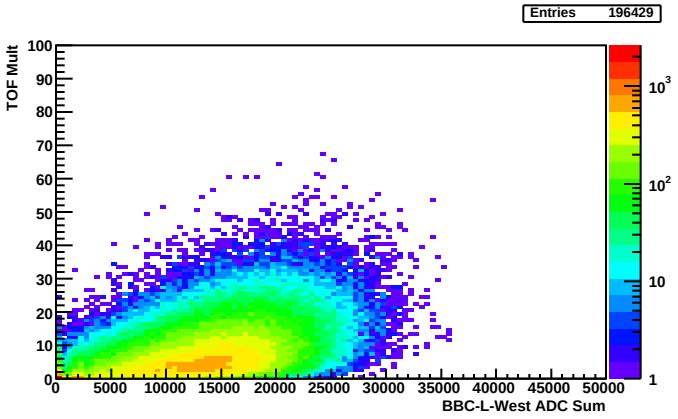

BBQ-VP003 (HI threshold)
Entries 3142864
ADC
QT Input Channel

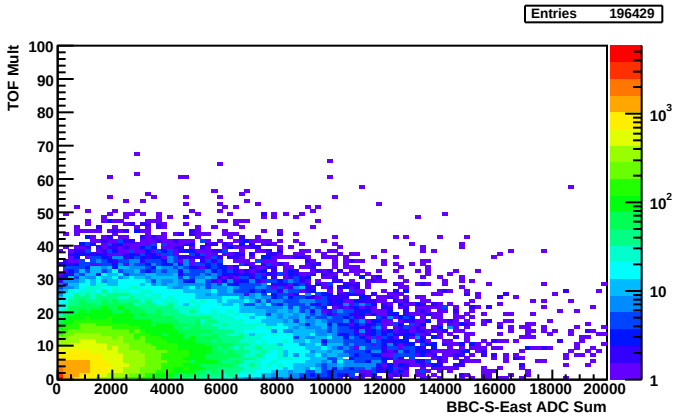

BBQ-VP003 (HI threshold)
Entries 3142864
TAC
QT Input Channel

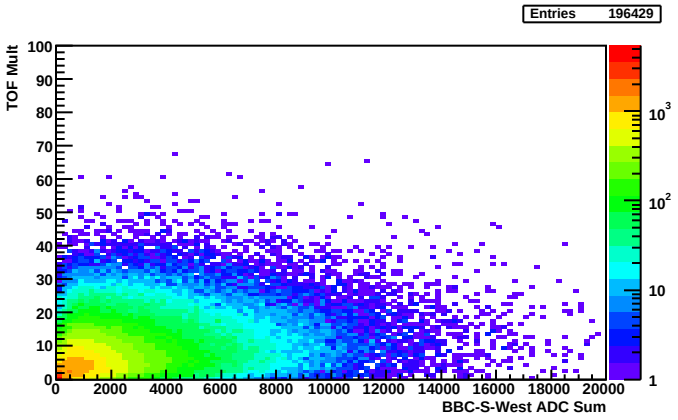

BBQ-VP004 (HI threshold)
Entries 3142864
ADC
QT Input Channel

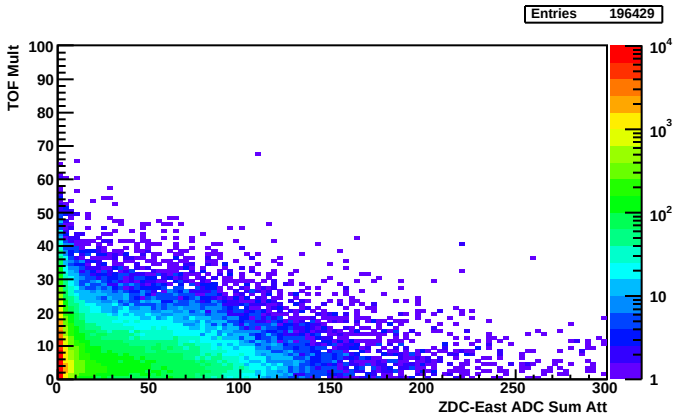

BBQ-VP004 (HI threshold)
Entries 3142864
TAC
QT Input Channel

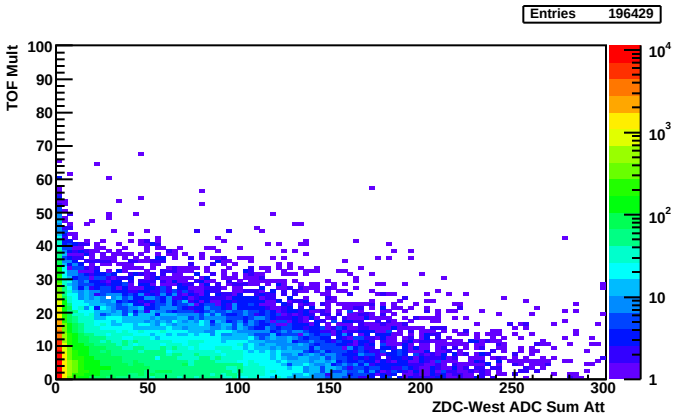

Entries 196429
TOF Mult
BBC-L-East ADC Sum

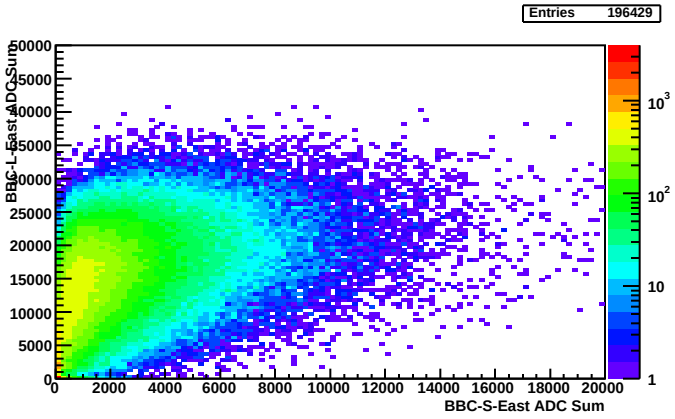

Entries 196429
TOF Mult
BBC-L-West ADC Sum

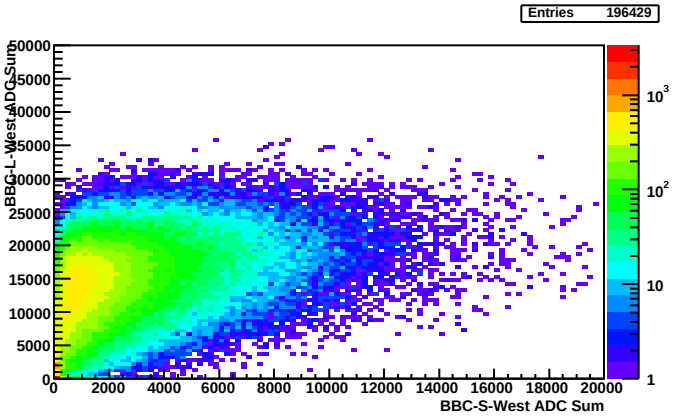

Entries 196429
TOF Mult
BBC-S-East ADC Sum

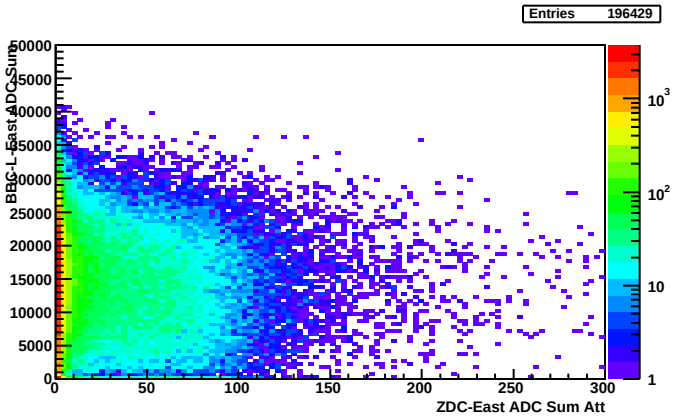

Entries 196429
TOF Mult
BBC-S-West ADC Sum

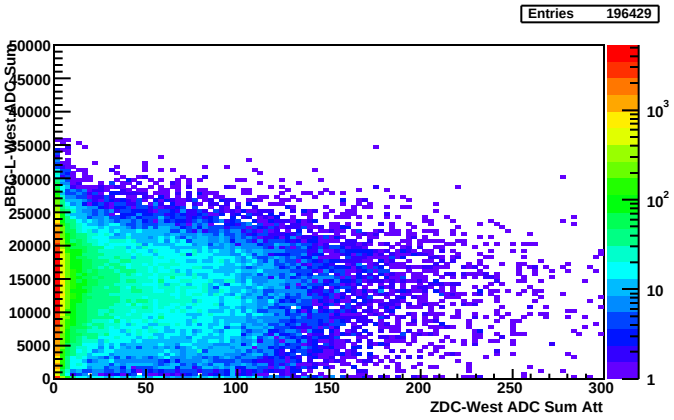

Entries 196429
TOF Mult
ZDC-East ADC Sum Att

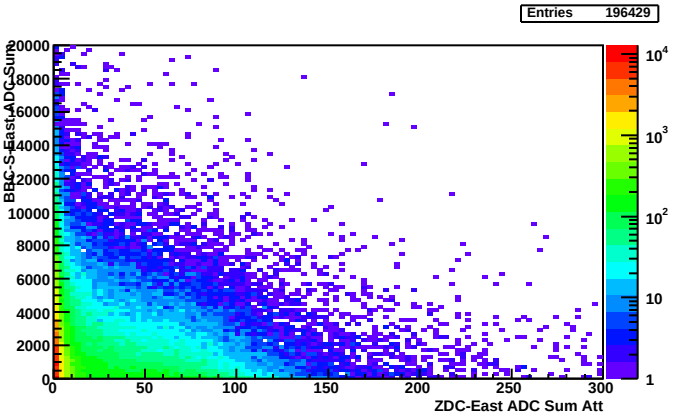

Entries 196429
TOF Mult
ZDC-West ADC Sum Att

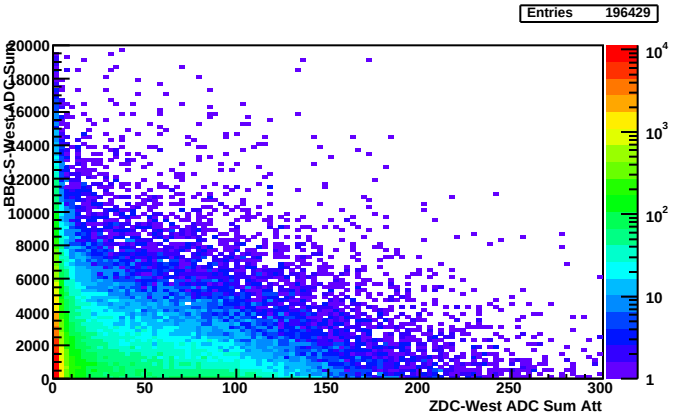

Entries 196429
BBC-L-East ADC Sum
BBC-S-East ADC Sum

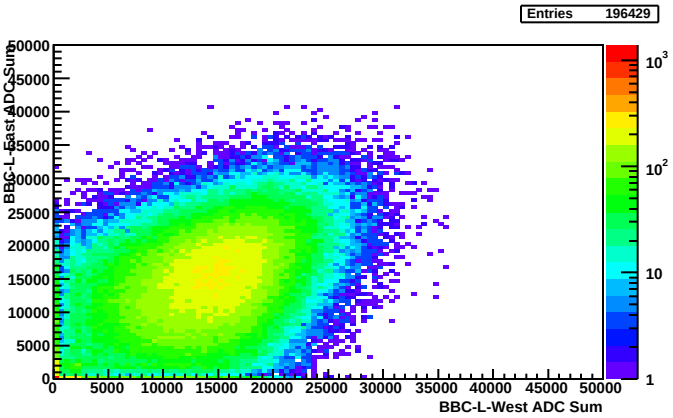

Entries 196429
BBC-L-West ADC Sum
BBC-S-West ADC Sum

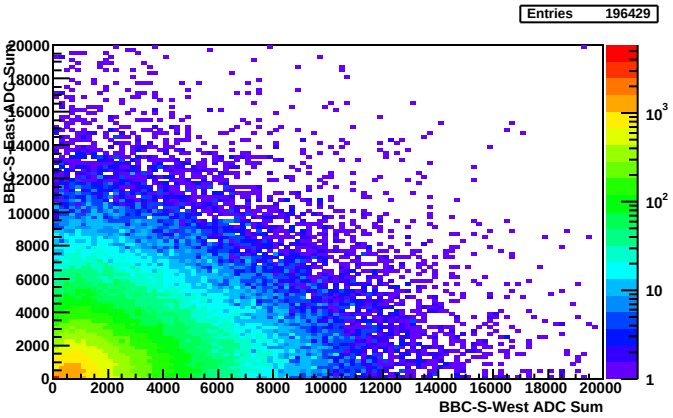

Entries 196429
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

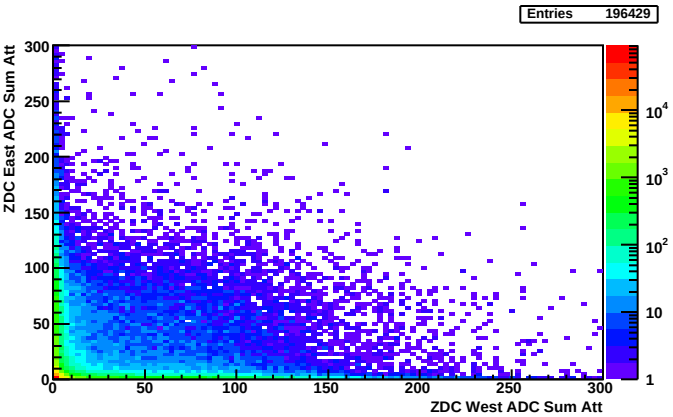

Entries 196429
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

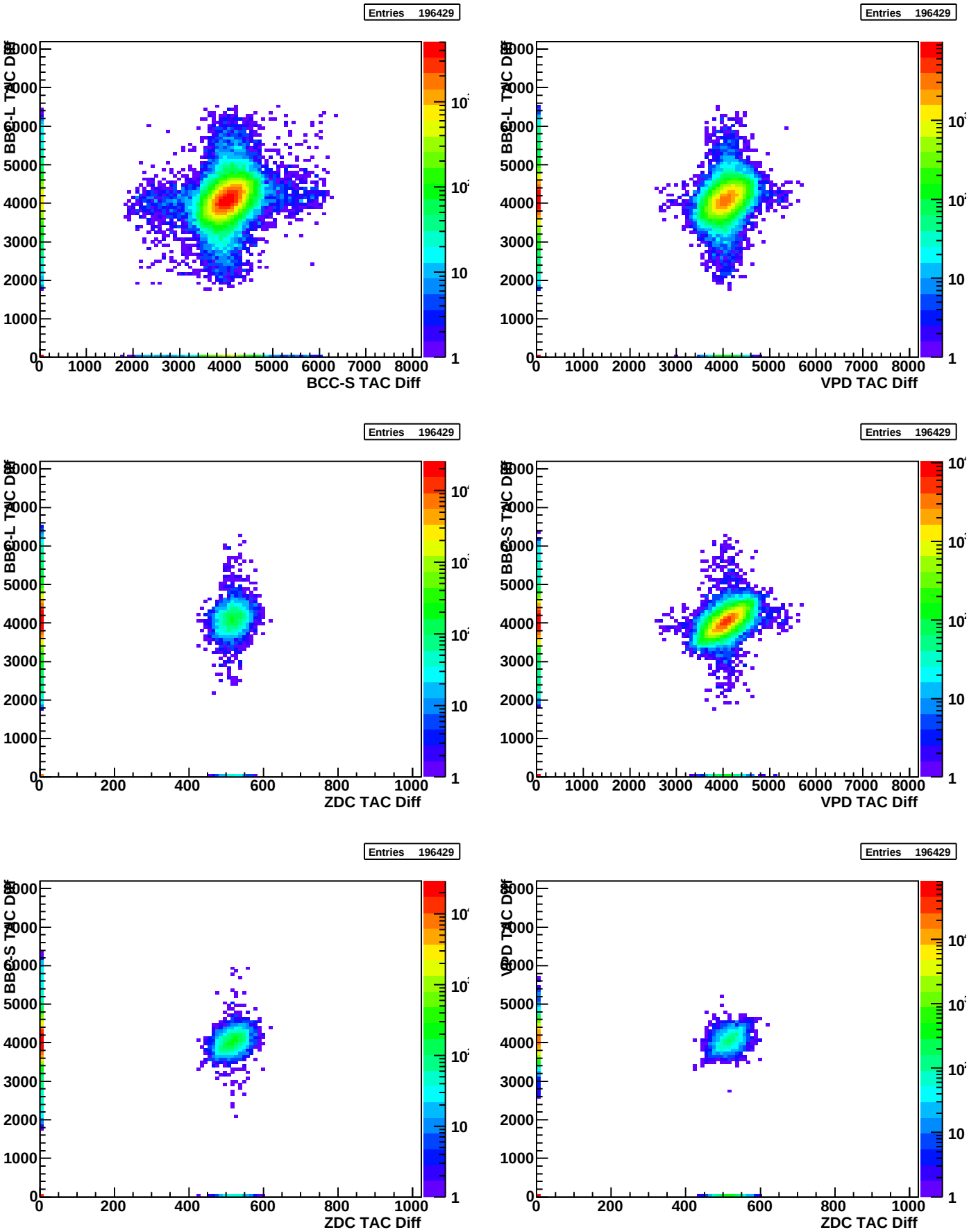

Entries 196429
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

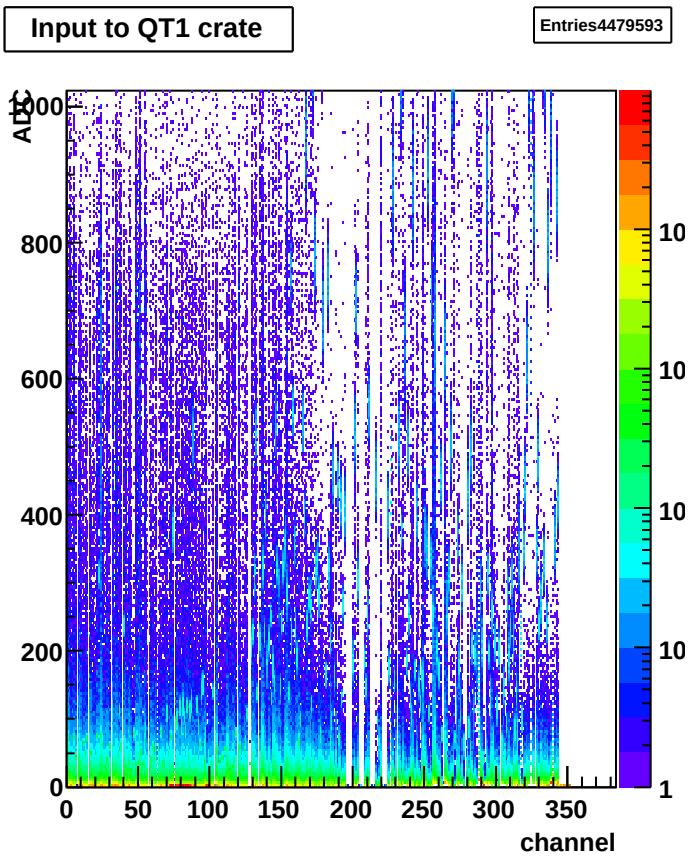

Entries 196429
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

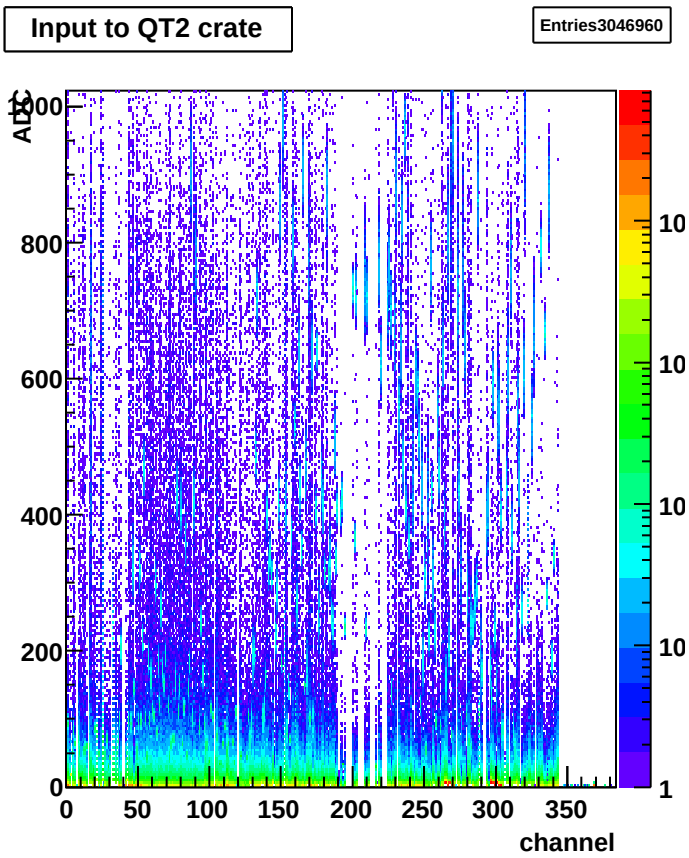

Entries 196429
BBC-L-East ADC Sum
BBC-L-West ADC Sum

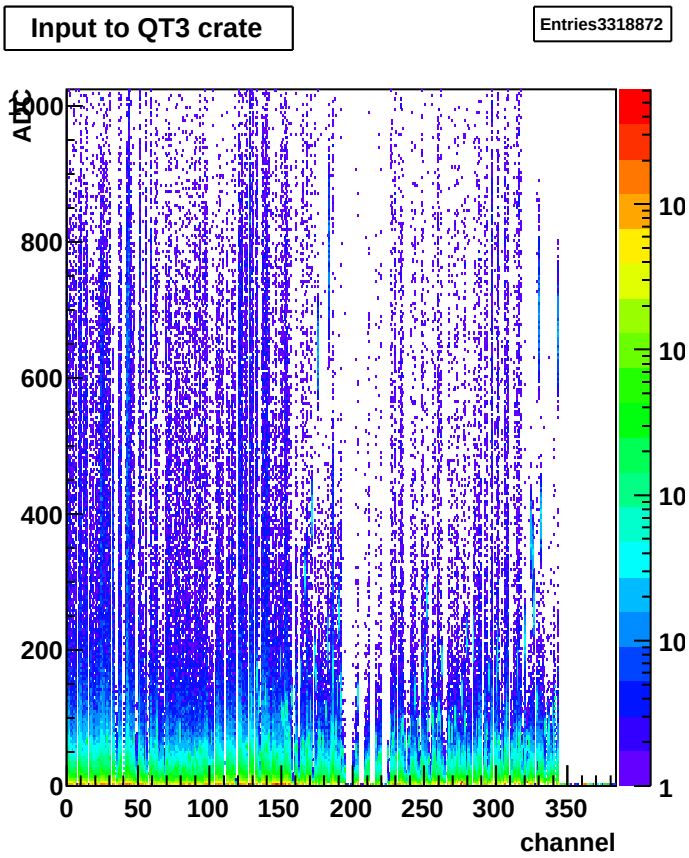

Entries 196429
BBC-S-East ADC Sum
BBC-S-West ADC Sum

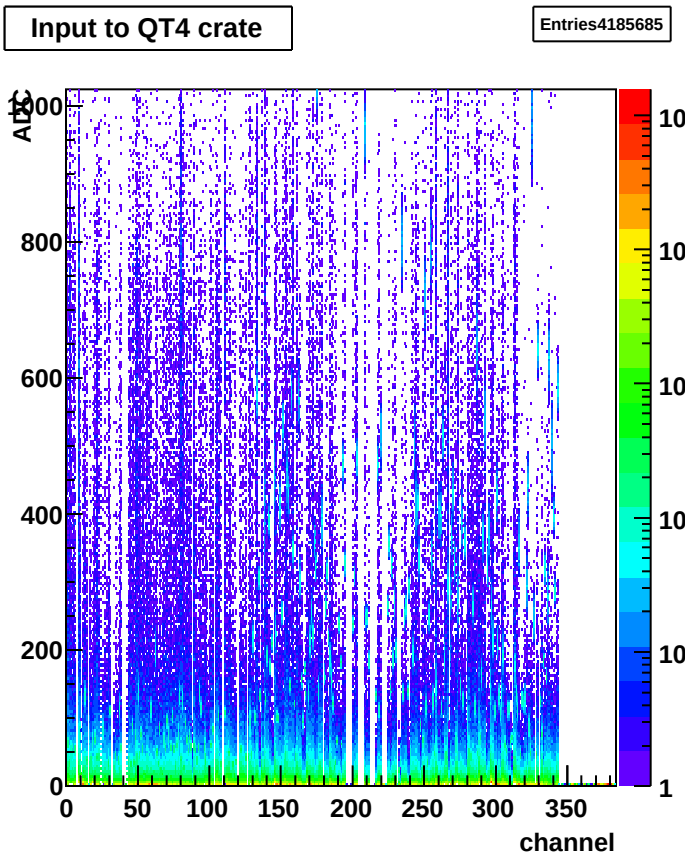

Entries 196429
ZDC East ADC Sum Att
ZDC West ADC Sum Att

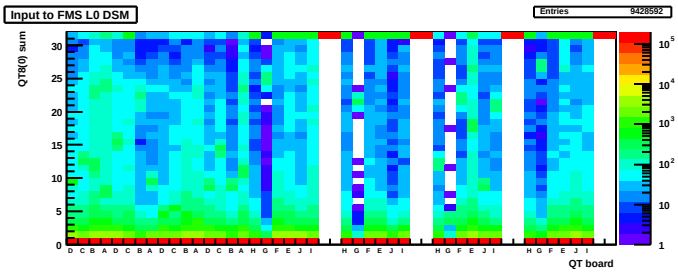

Entries 196429
BBC-L TAC Diff
BCC-S TAC Diff
Entries 196429
BBC-L TAC Diff
VPD TAC Diff
Entries 196429
BBC-L TAC Diff
ZDC TAC Diff
Entries 196429
BBC-S TAC Diff
VPD TAC Diff
Entries 196429
BBC-S TAC Diff
ZDC TAC Diff
Entries 196429
VPD TAC Diff
ZDC TAC Diff

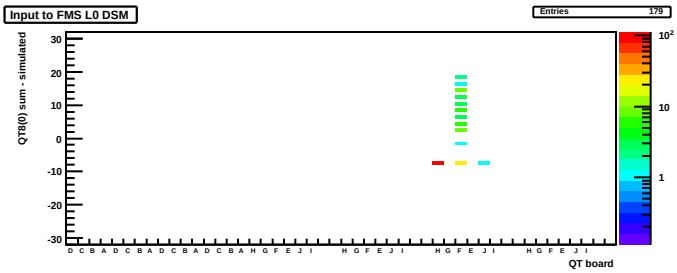

Input to QT1 crate
Entries4479593
ADC
channel

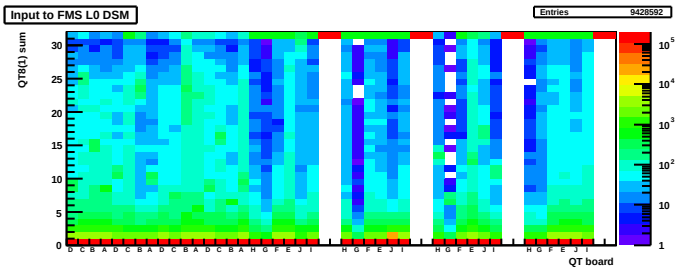

Input to QT2 crate
Entries3046960
ADC
channel

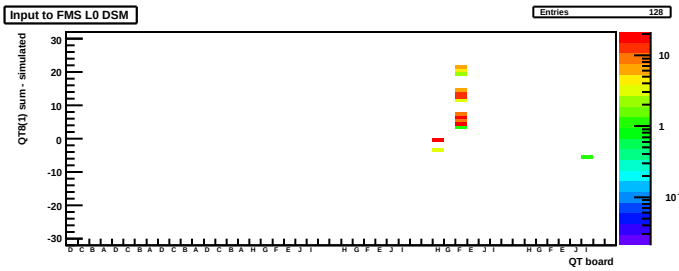

Input to QT3 crate
Entries3318872
ADC
channel

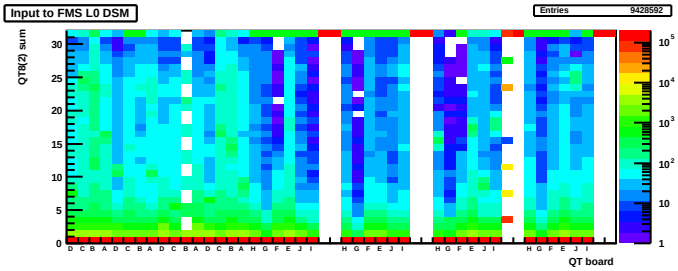

Input to QT4 crate
Entries4185685
ADC
channel

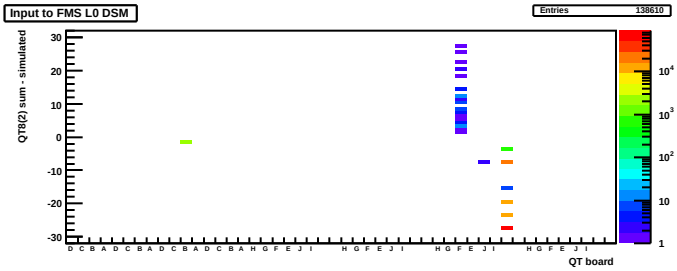

Input to FMS L0 DSM
Entries 9428592
QT8(0) sum
QT board

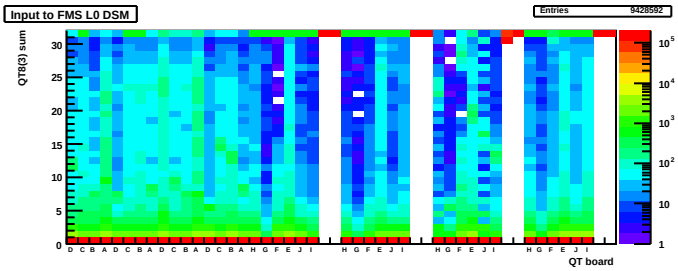

Input to FMS L0 DSM
Entries 179
QT8(0) sum - simulated
QT board

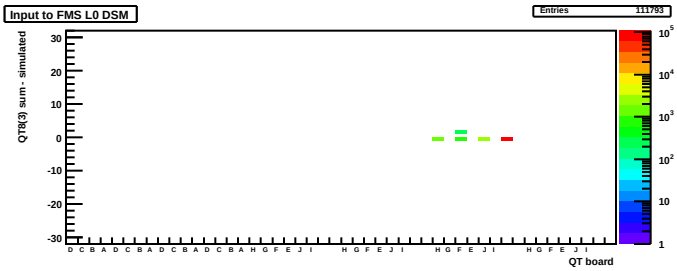

Input to FMS L0 DSM
Entries 9428592
QT8(1) sum
QT board

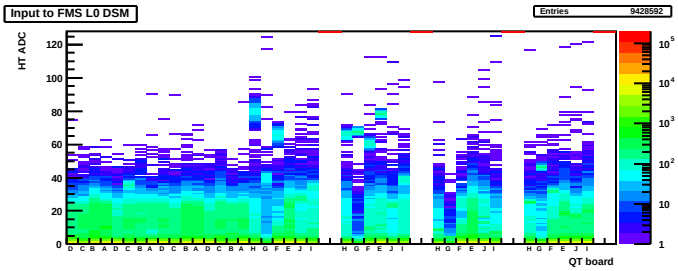

Input to FMS L0 DSM
Entries 128
QT8(1) sum - simulated
QT board

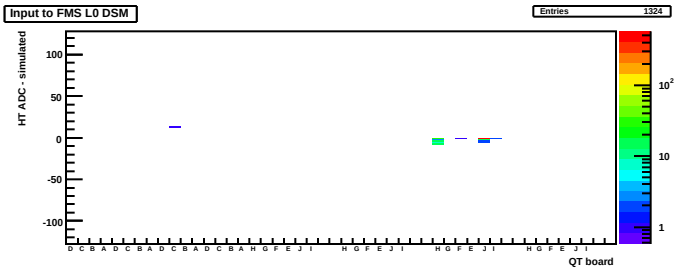

Input to FMS L0 DSM
Entries 9428592
QT8(2) sum
QT board

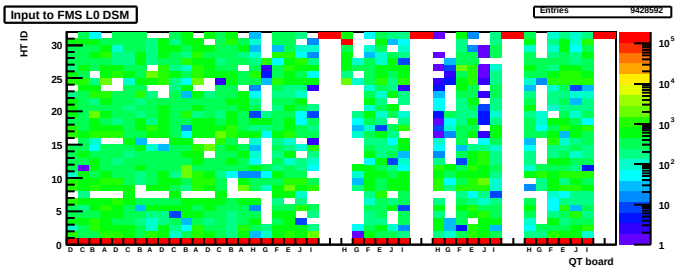

Input to FMS L0 DSM
Entries 138610
QT8(2) sum - simulated
QT board

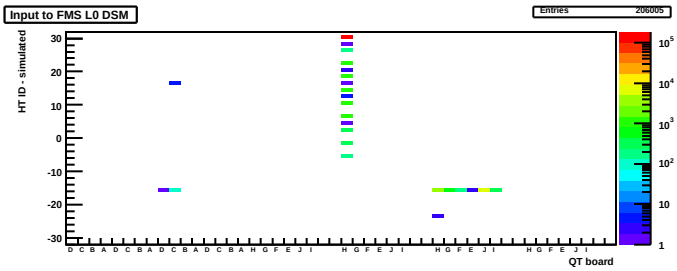

Input to FMS L0 DSM
Entries 9428592
QT8(3) sum
QT board

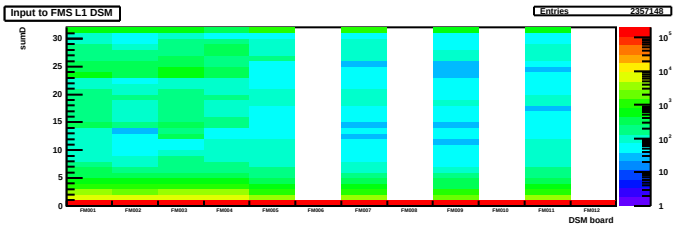

Input to FMS L0 DSM
Entries 111793
QT8(3) sum - simulated
QT board

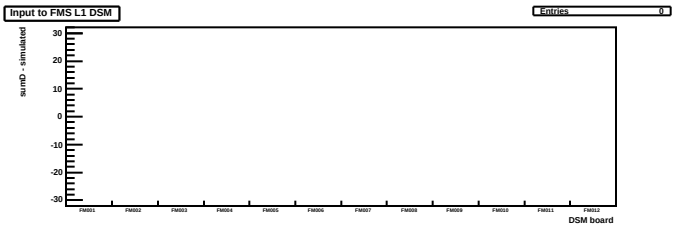

Input to FMS L0 DSM
Entries 9428592
HT ADC
QT board

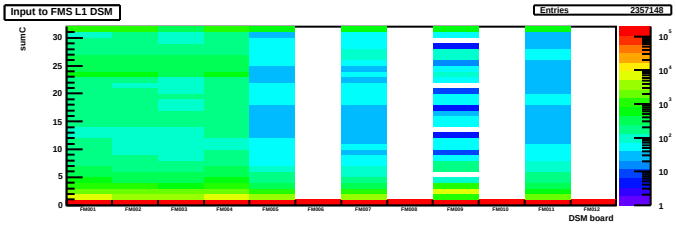

Input to FMS L0 DSM
Entries 1324
HT ADC - simulated
QT board

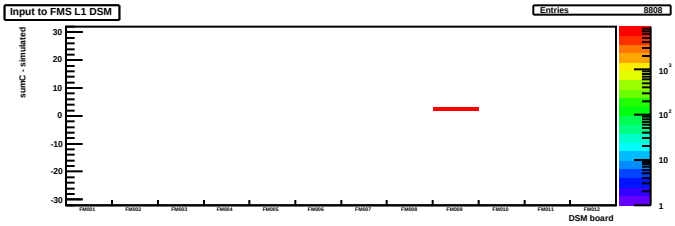

Input to FMS L0 DSM
Entries 9428592
HT ID
QT board

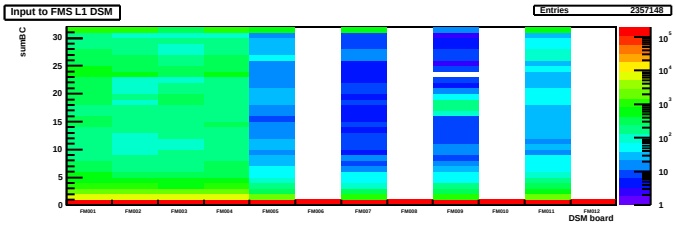

Input to FMS L0 DSM
Entries 206005
HT ID - simulated
QT board

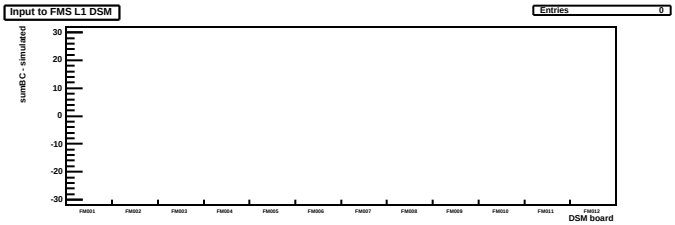

Input to FMS L1 DSM
Entries 2357148
sumD
DSM board

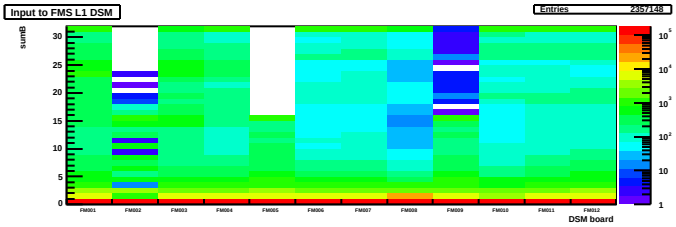

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

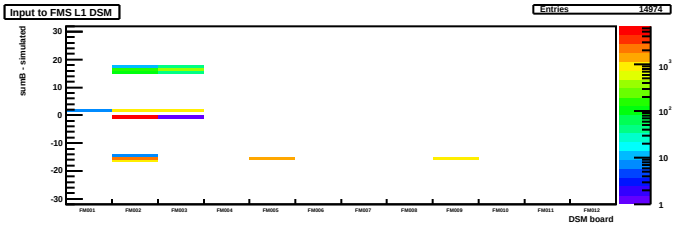

Input to FMS L1 DSM
Entries 2357148
sumC
DSM board

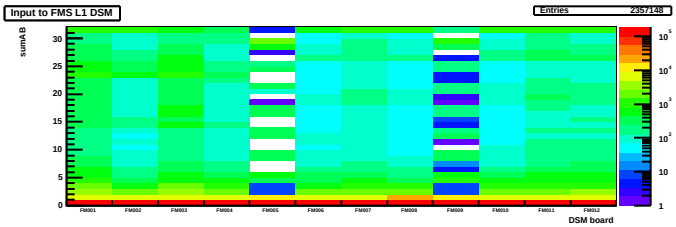

Input to FMS L1 DSM
Entries 8808
sumC - simulated
DSM board

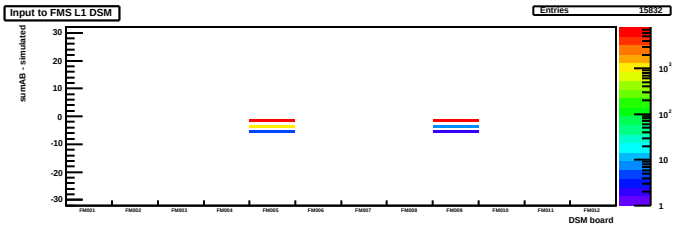

Input to FMS L1 DSM
Entries 2357148
sumBC
DSM board

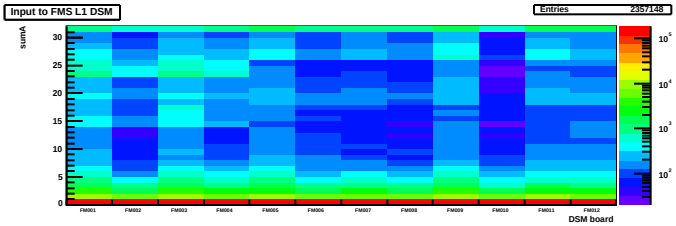

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

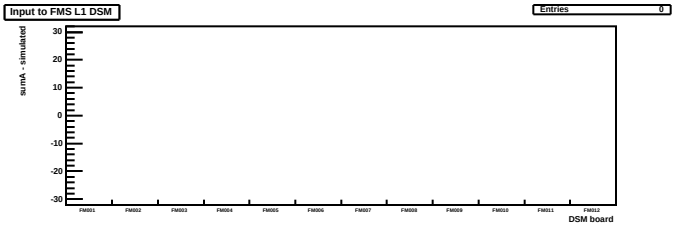

Input to FMS L1 DSM
Entries 2357148
sumB
DSM board

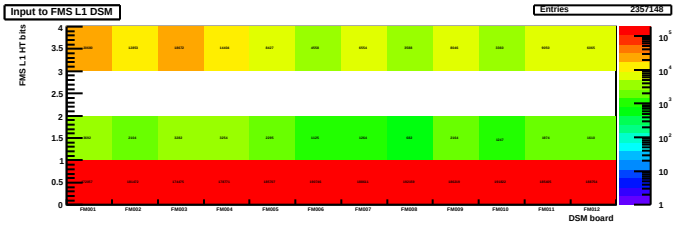

Input to FMS L1 DSM
Entries 14974
sumB - simulated
DSM board

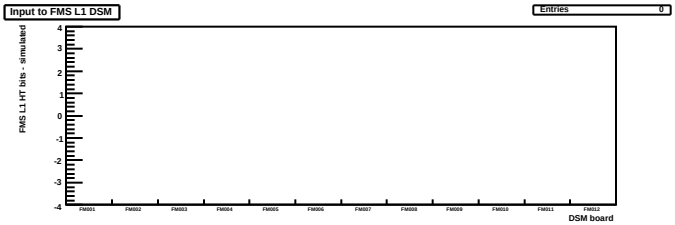

Input to FMS L1 DSM
Entries 2357148
sumAB
DSM board

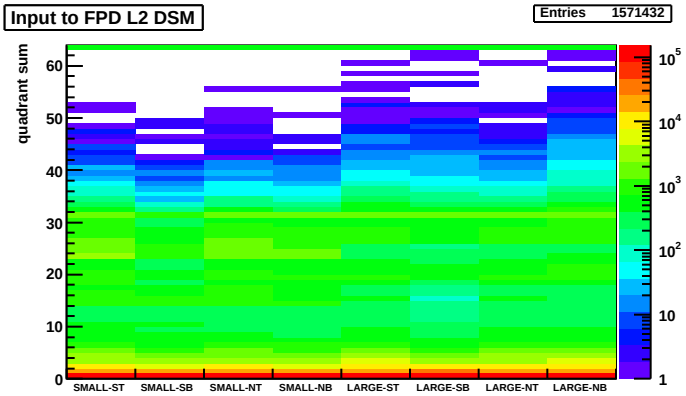

Input to FMS L1 DSM
Entries 15832
sumAB - simulated
DSM board

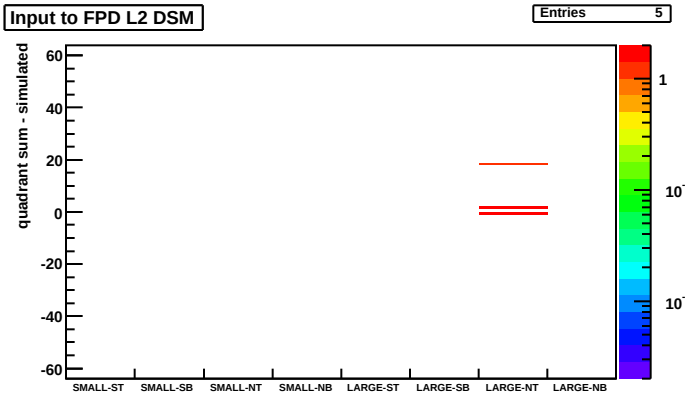

Input to FMS L1 DSM
Entries 2357148
sumA
DSM board

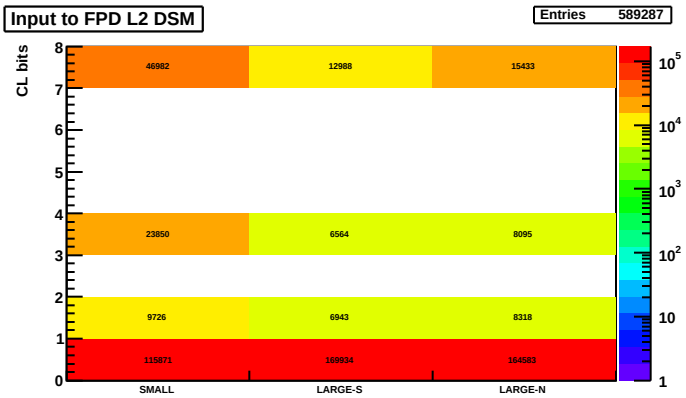

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

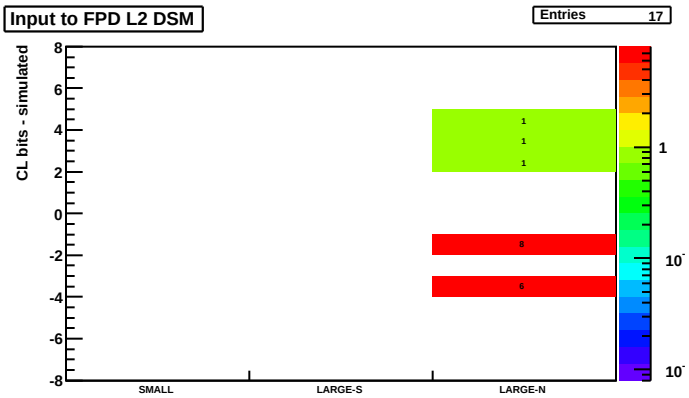

Input to FMS L1 DSM
Entries 2357148
FMS L1 HT bits
DSM board

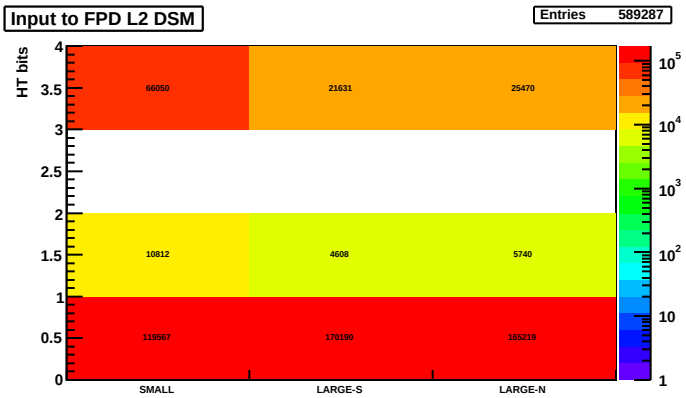

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

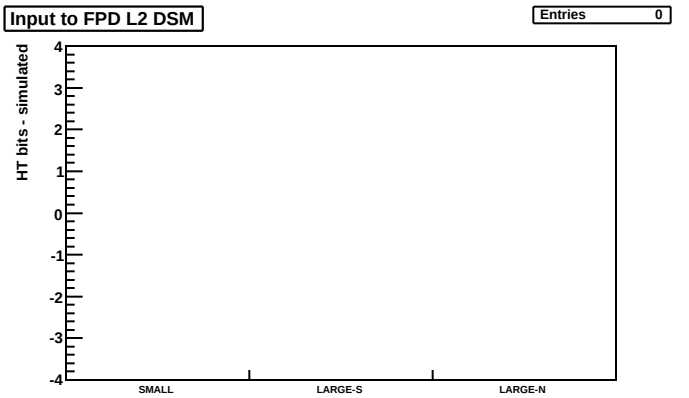

Input to FPD L2 DSM
Entries 1571432
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

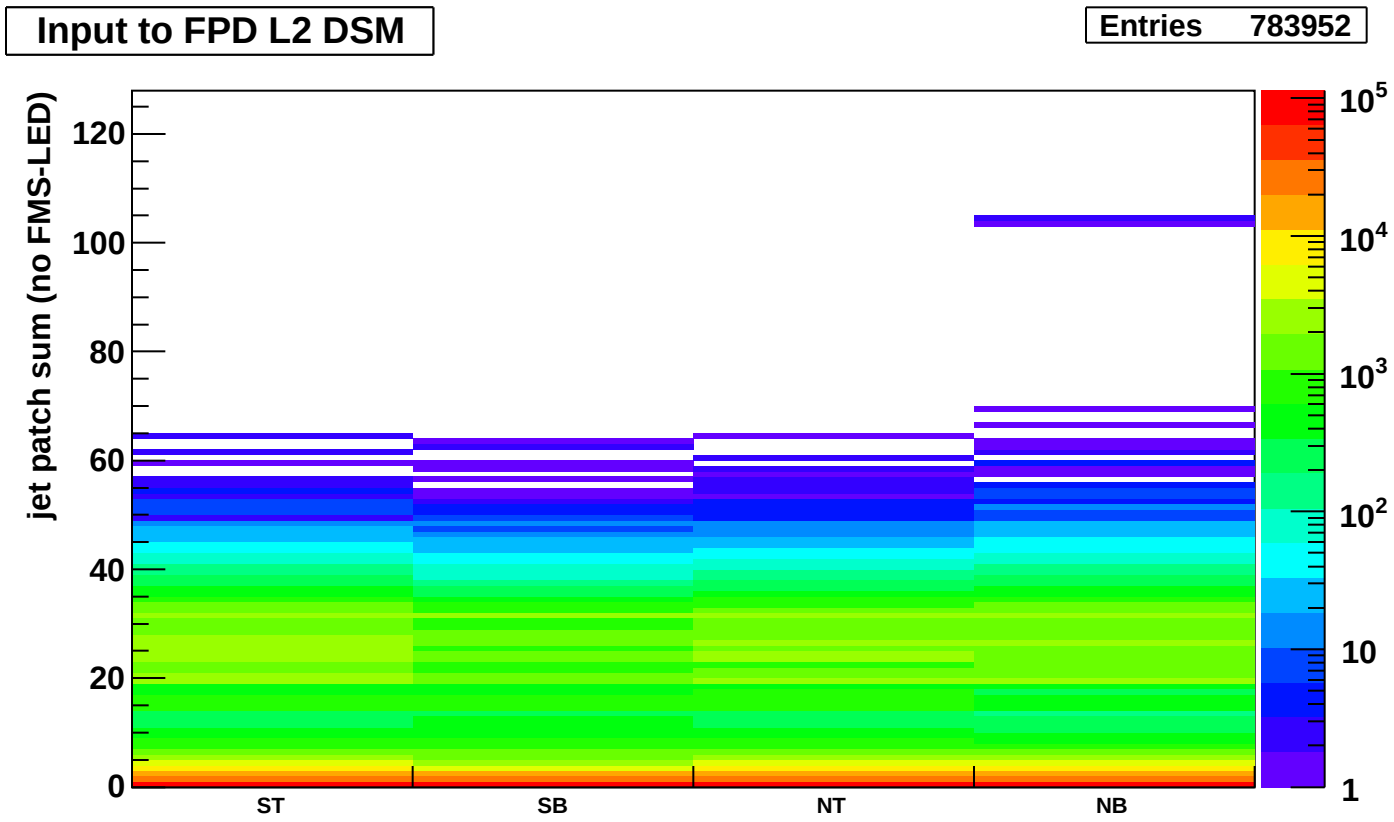

Input to FPD L2 DSM
Entries 5
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

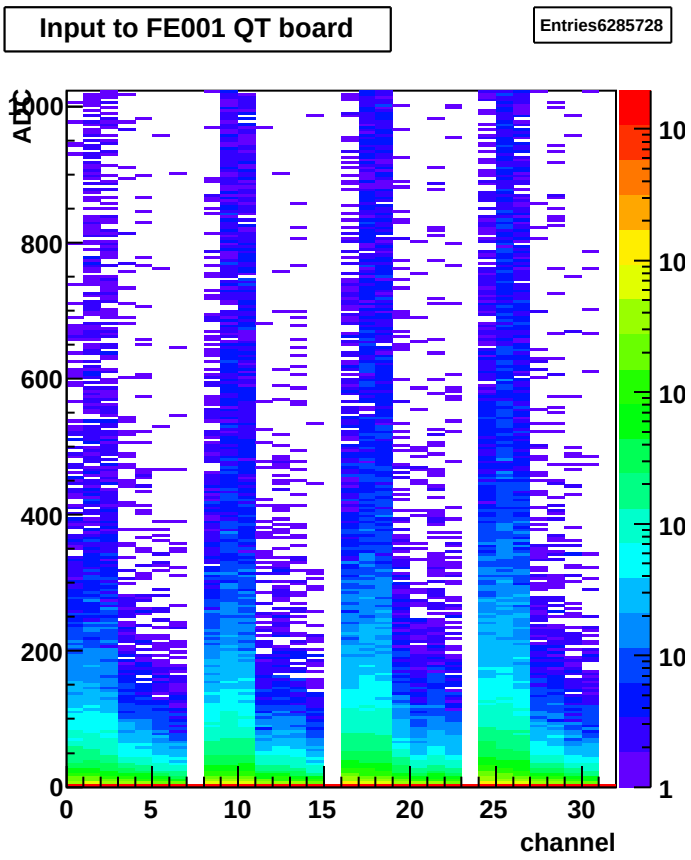

Input to FPD L2 DSM
Entries 589287
CL bits
46982
12988
15433
23850
6564
8095
9726
6943
8318
115871
169934
164583
SMALL
LARGE-S
LARGE-N

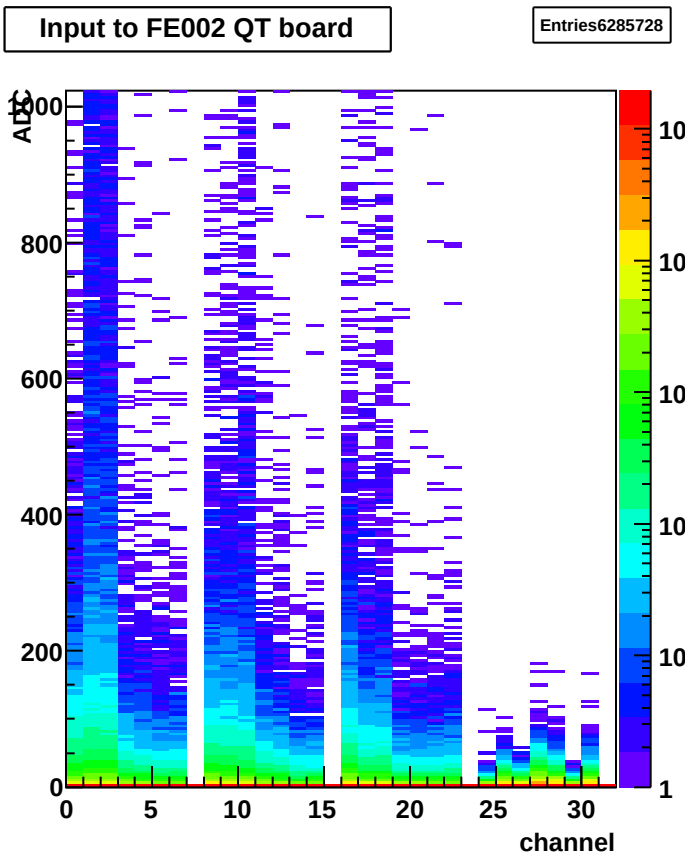

Input to FPD L2 DSM
Entries 17
CL bits - simulated
1
1
1
8
6
SMALL
LARGE-S
LARGE-N

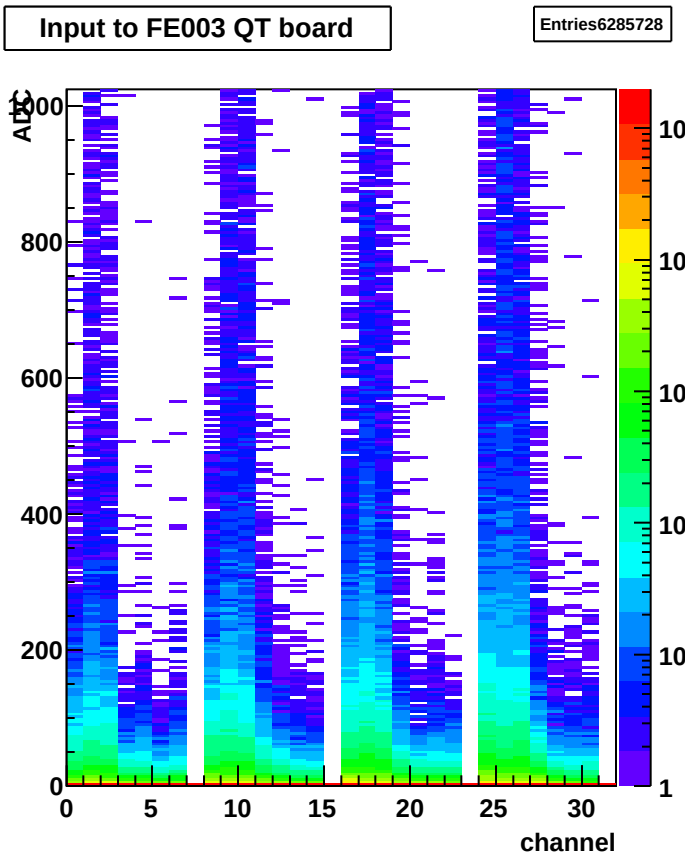

Input to FPD L2 DSM
Entries 589287
HT bits
66050
21631
25470
10812
4608
5740
119567
170190
165219
SMALL
LARGE-S
LARGE-N

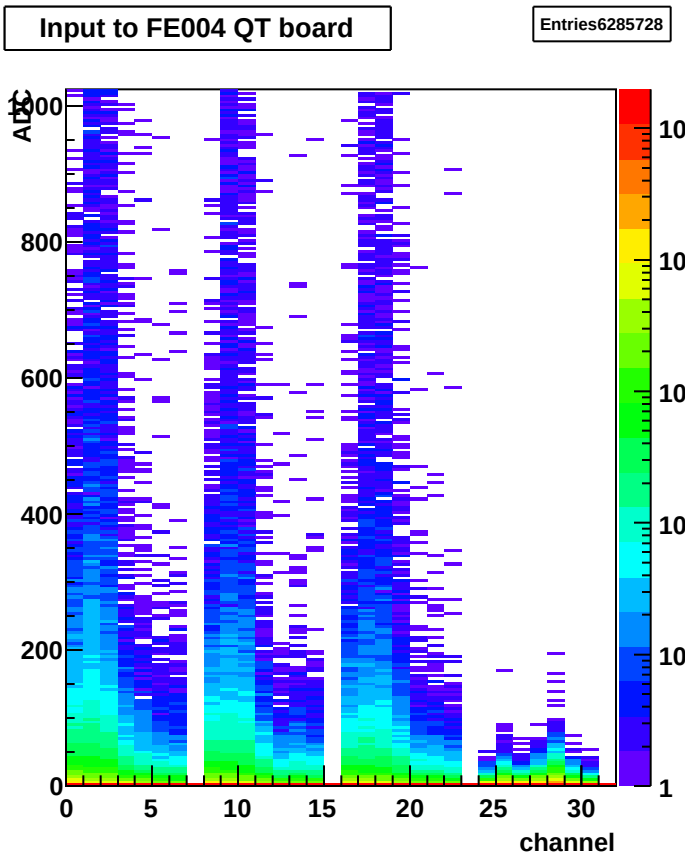

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

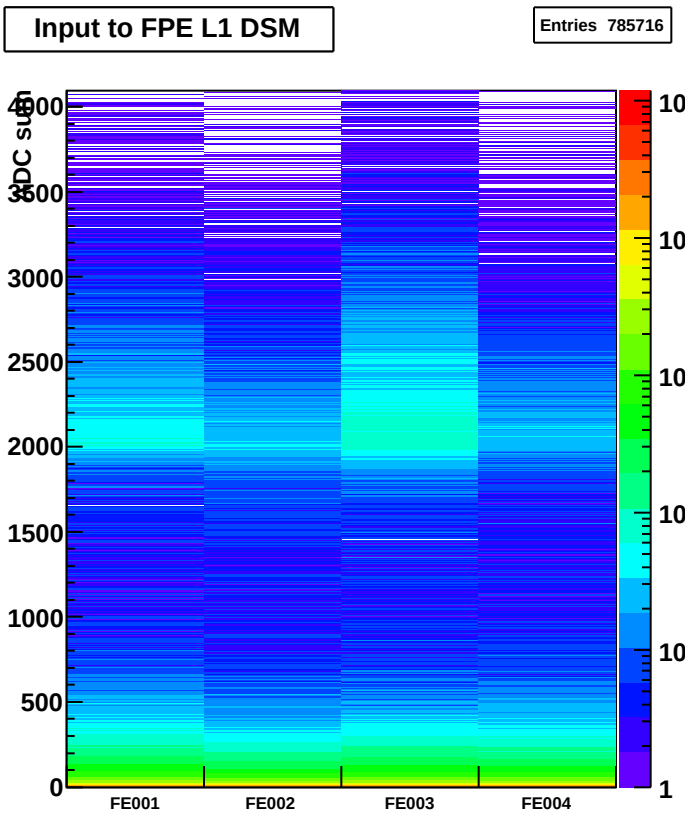

Input to FPD L2 DSM
Entries 783952
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

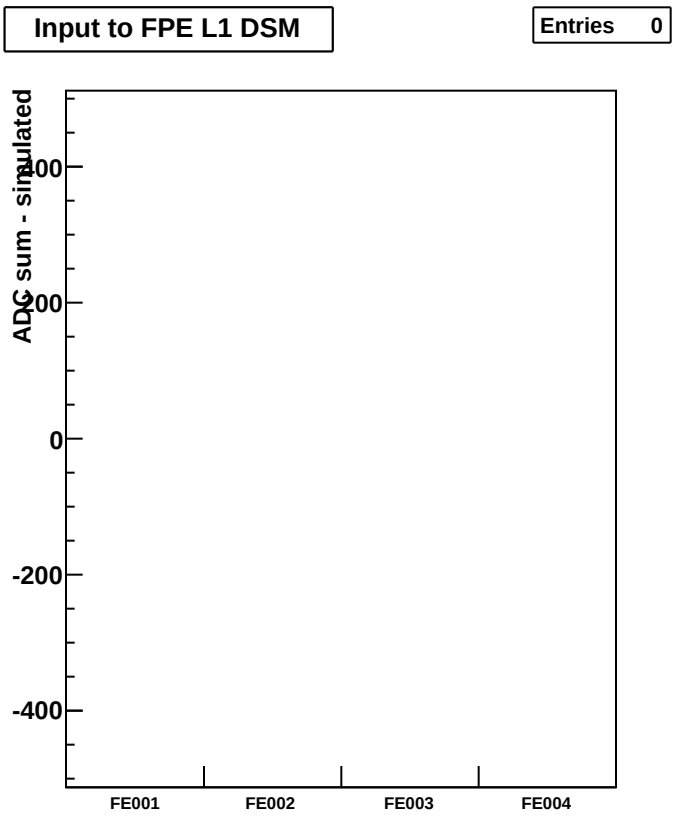

Input to FE001 QT board
Entries6285728
ADC
channel


Input to FE002 QT board
Entries6285728
ADC
channel

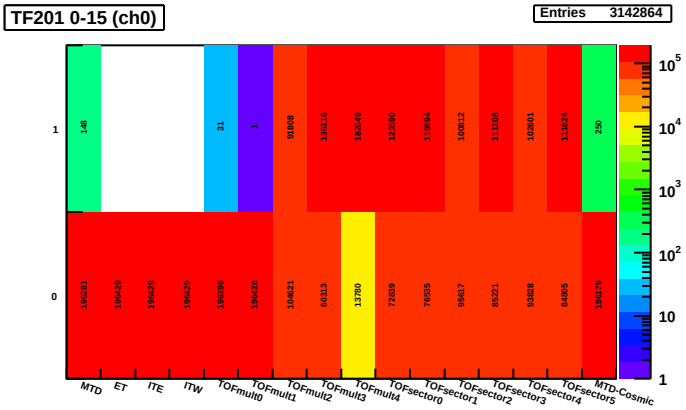

Input to FE003 QT board
Entries6285728
ADC
channel


Input to FE004 QT board
Entries6285728
ADC
channel

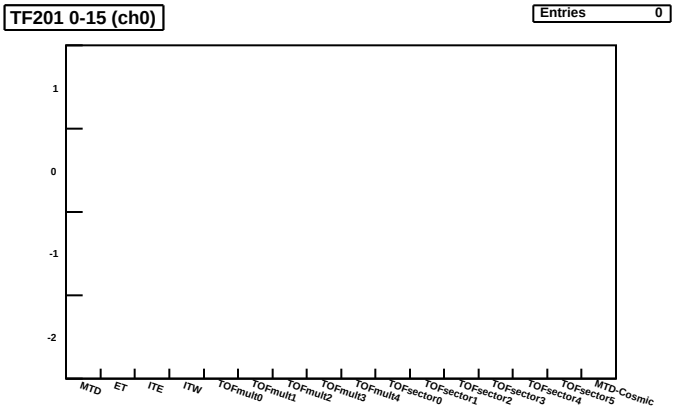

Input to FPE L1 DSM
Entries 785716
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
100
200
0
-200
-400
FE001
FE002
FE003
FE004

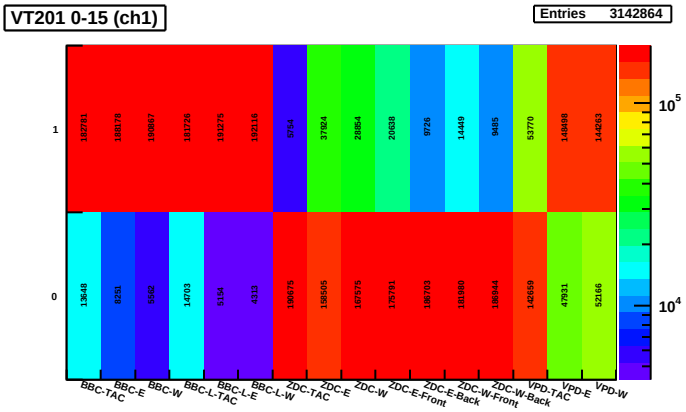
TF201 0-15 (ch0)



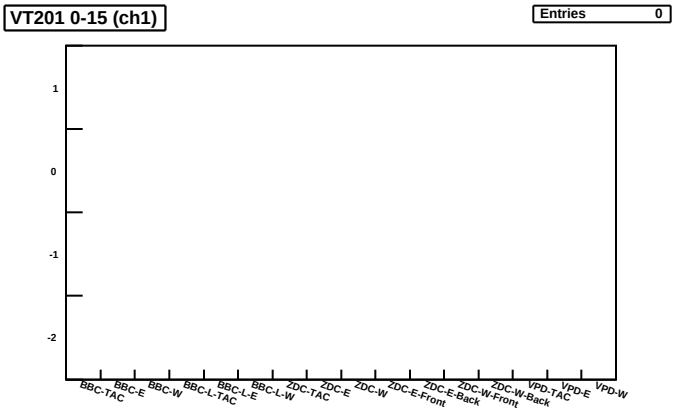
TF201 0-15 (ch0)



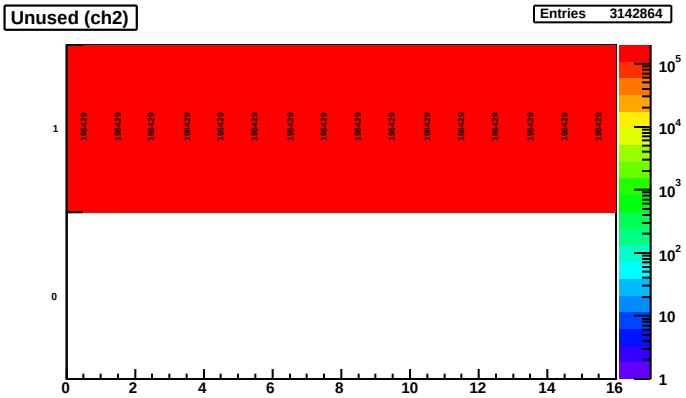
VT201 0-15 (ch1)

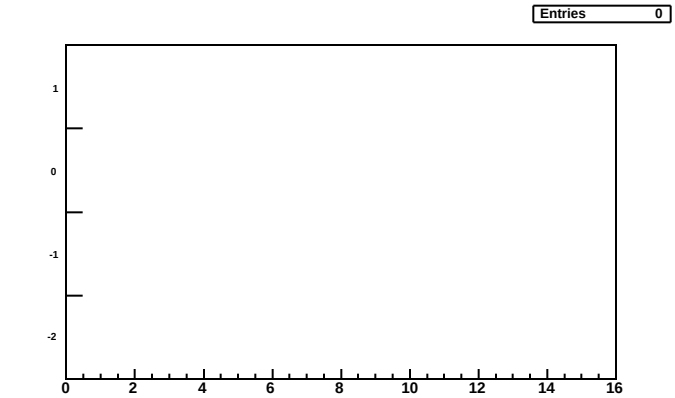


VT201 0-15 (ch1)

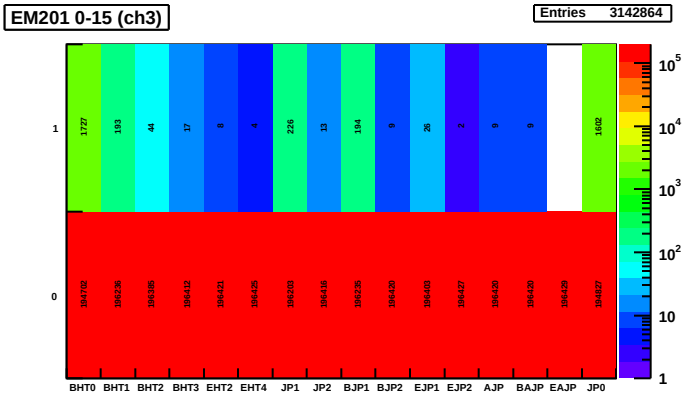


Unused (ch2)

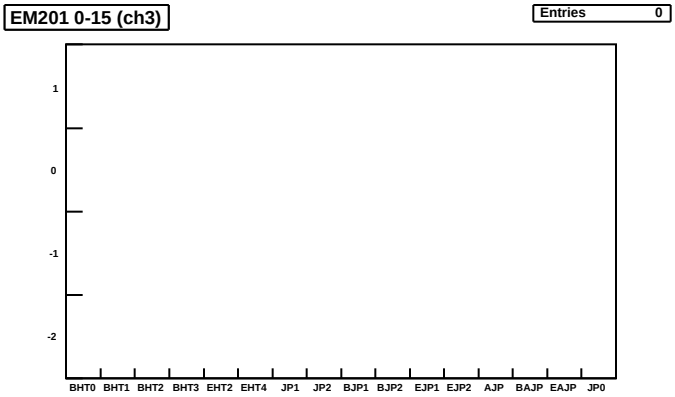




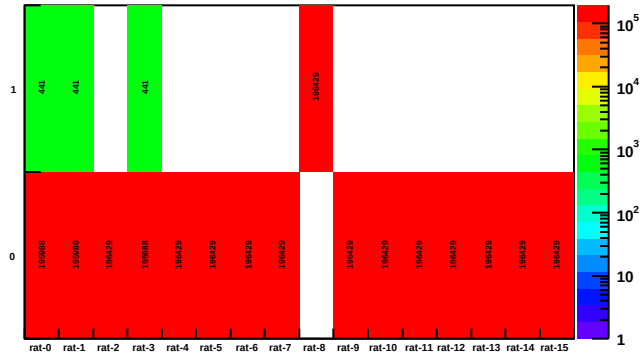
EM201 0-15 (ch3)

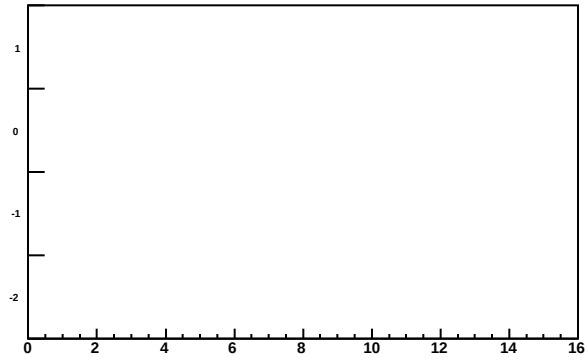


EM201 0-15 (ch3)

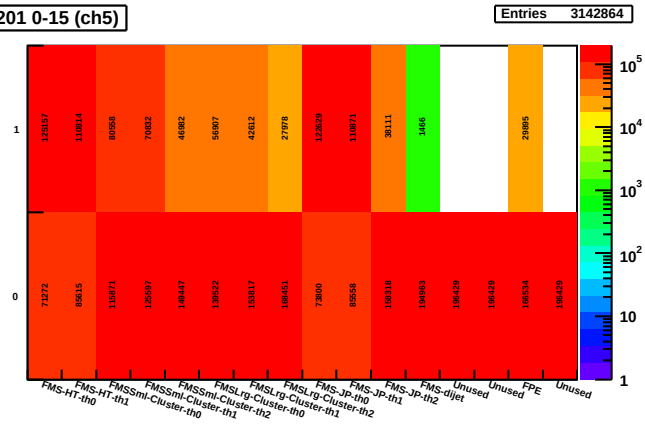


RAT board (ch4)

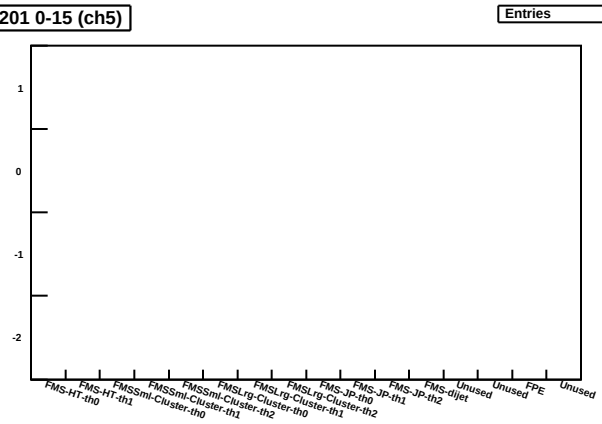




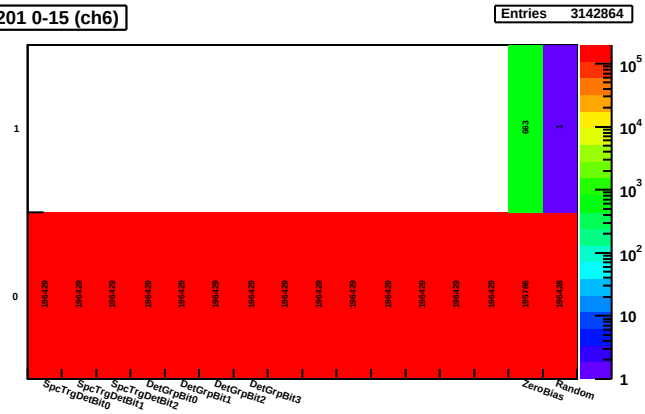
FP201 0-15 (ch5)

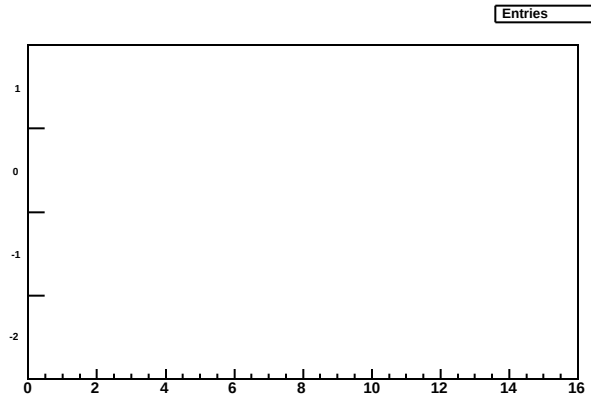


FP201 0-15 (ch5)

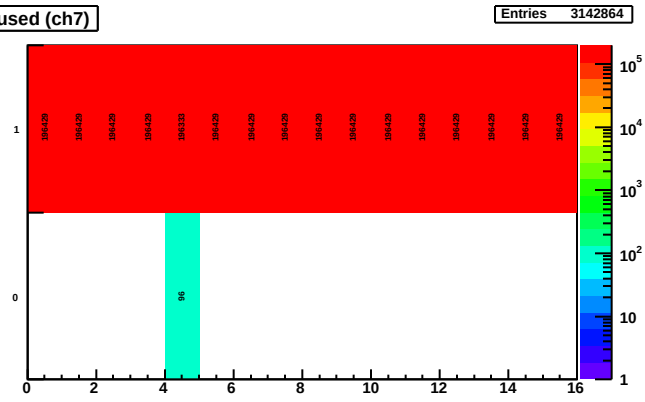


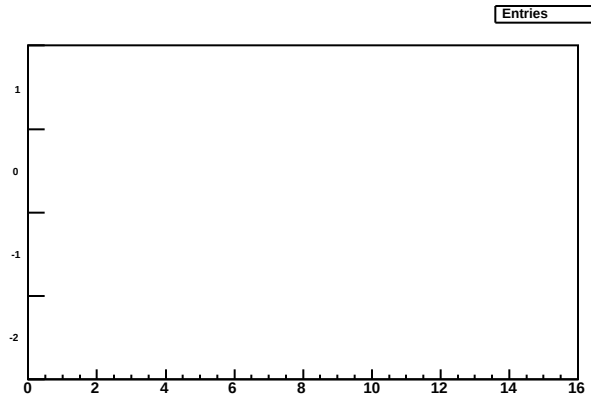
ST201 0-15 (ch6)

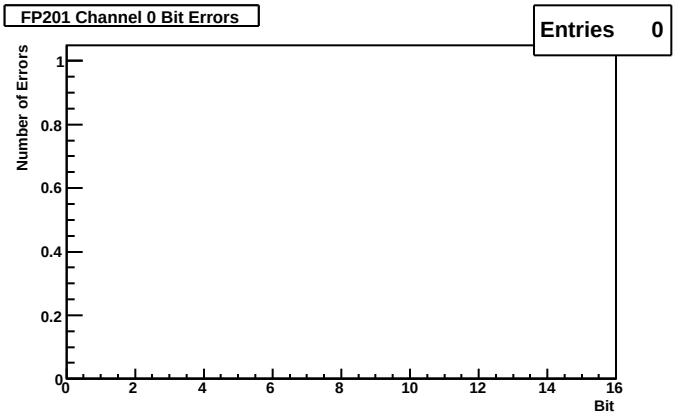


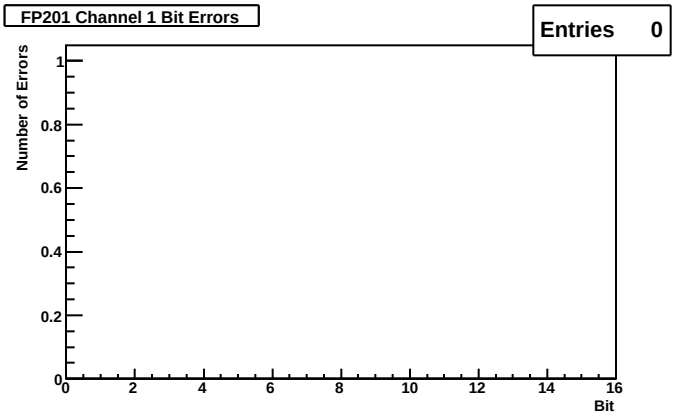


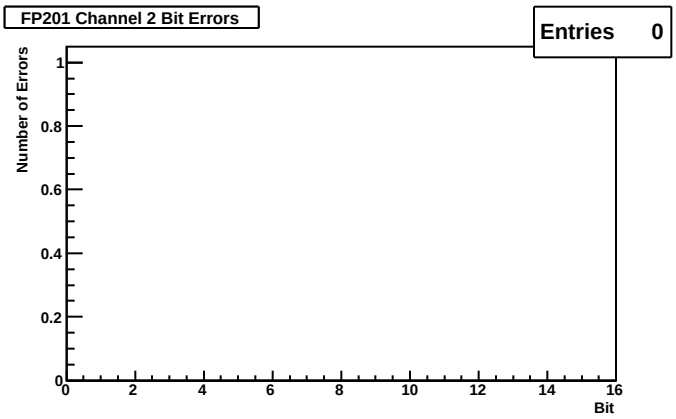
Unused (ch7)

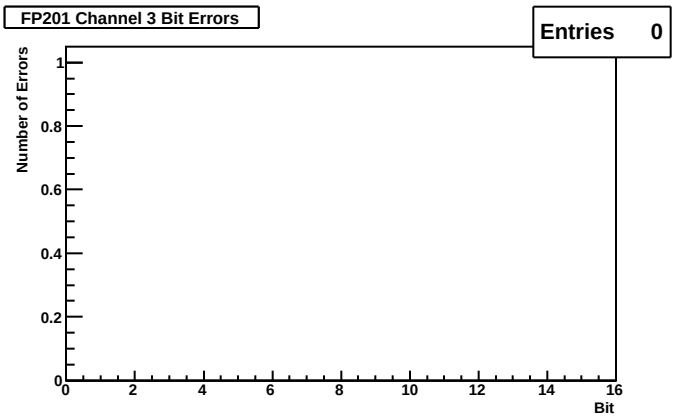


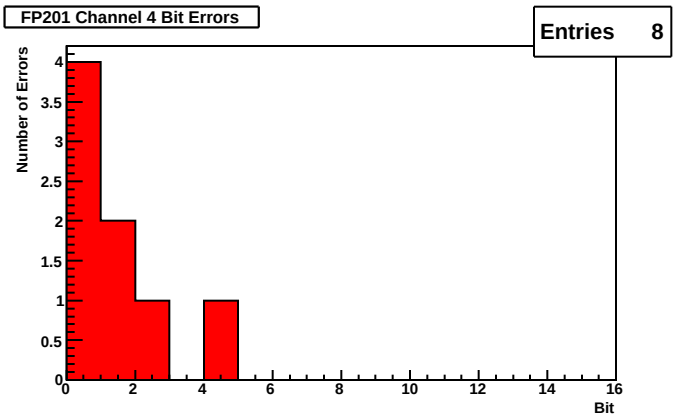


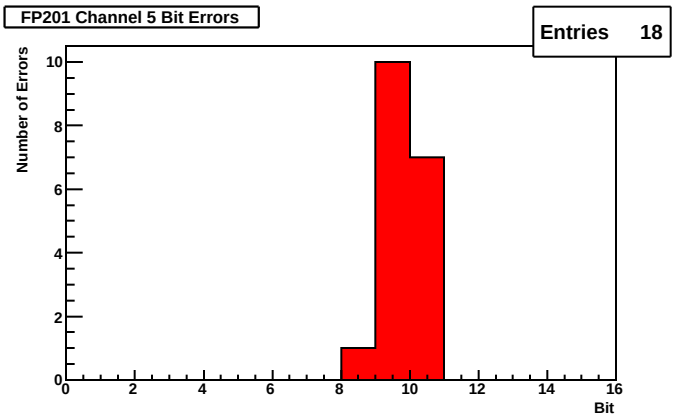

FP201 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

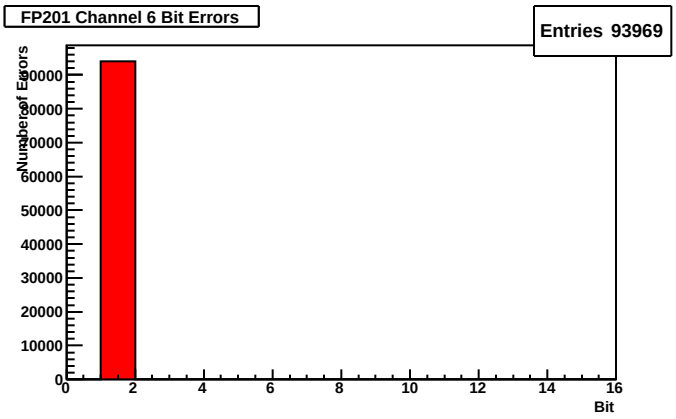

FP201 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

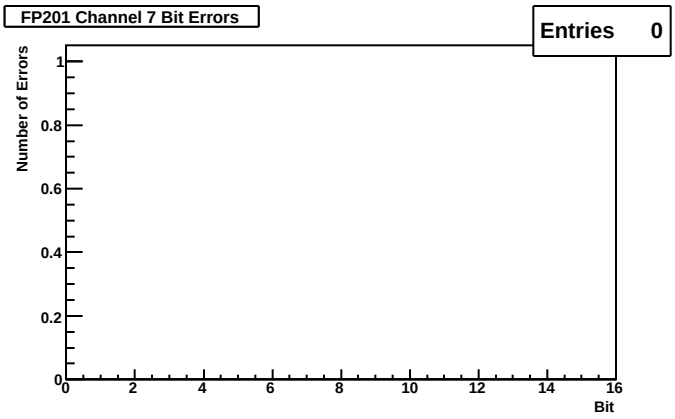

FP201 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

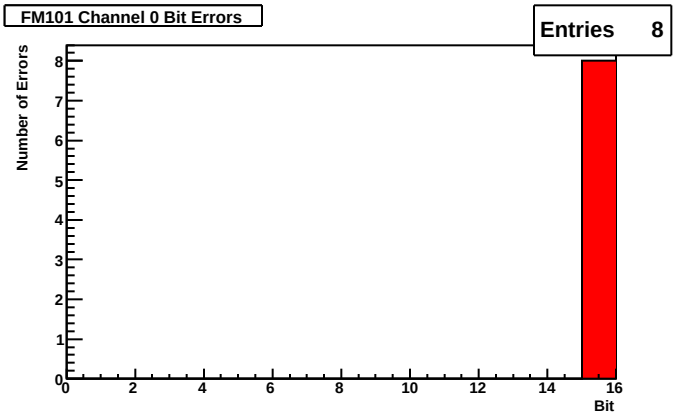

FP201 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

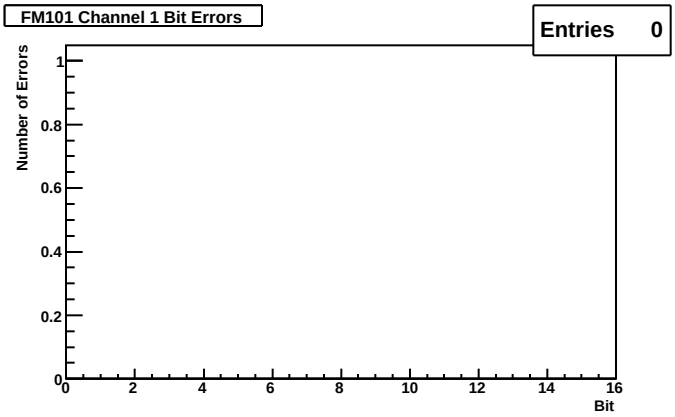

FP201 Channel 4 Bit Errors
Entries 8
Number of Errors
Bit

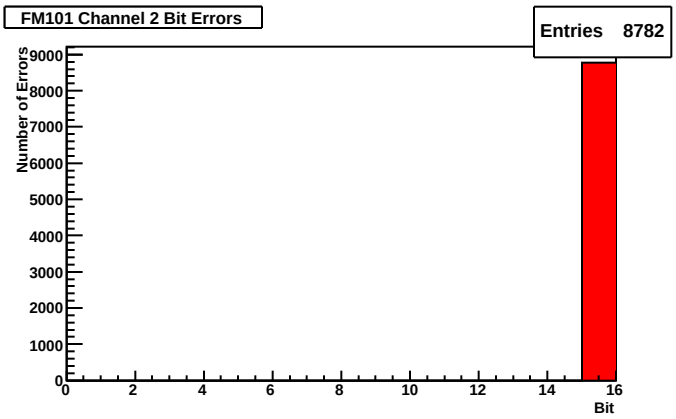

FP201 Channel 5 Bit Errors
Entries 18
Number of Errors
Bit

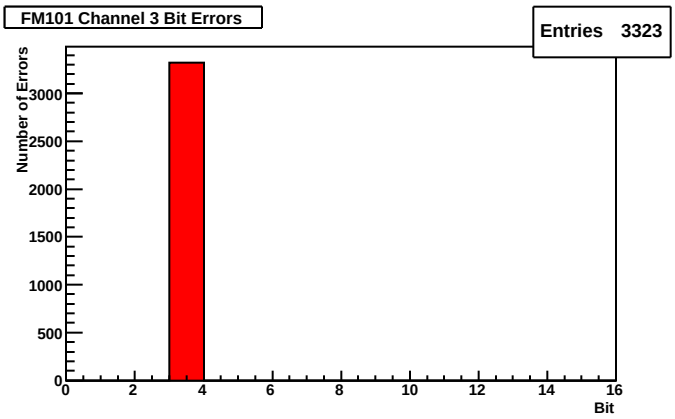

FP201 Channel 6 Bit Errors
Entries 93969
Number of Errors
Bit

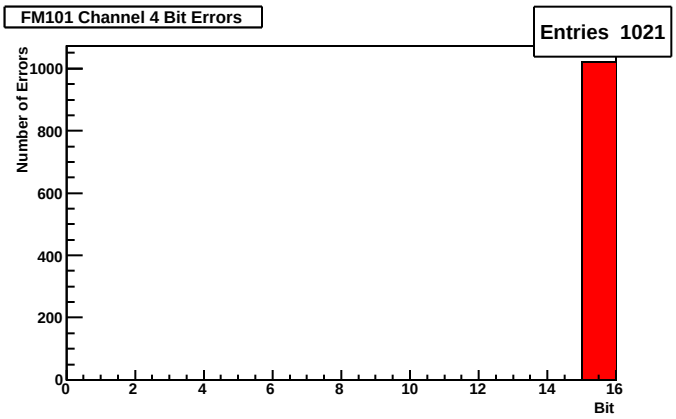

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

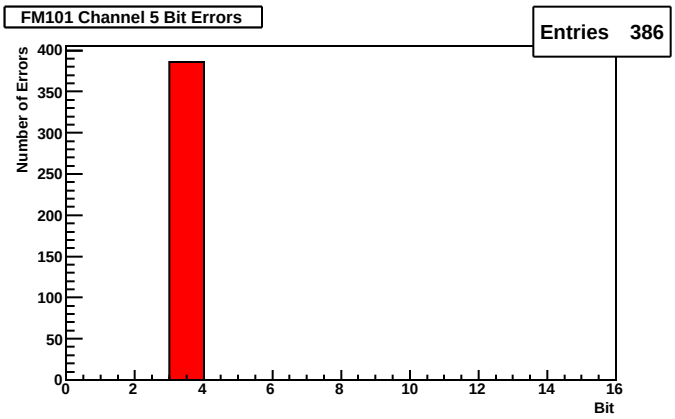

FM101 Channel 0 Bit Errors
Entries 8
Number of Errors
Bit

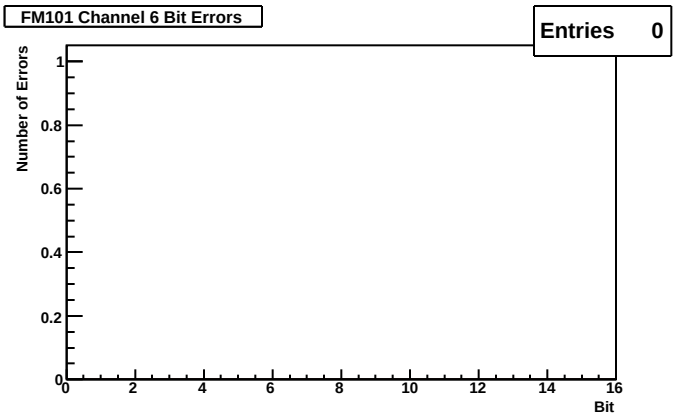

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

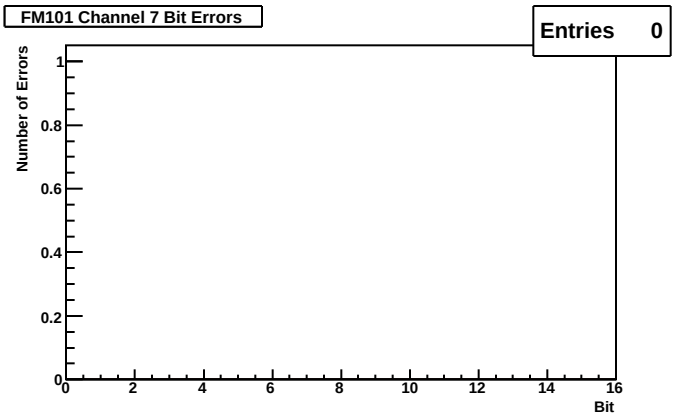

FM101 Channel 2 Bit Errors
Entries 8782
Number of Errors
Bit

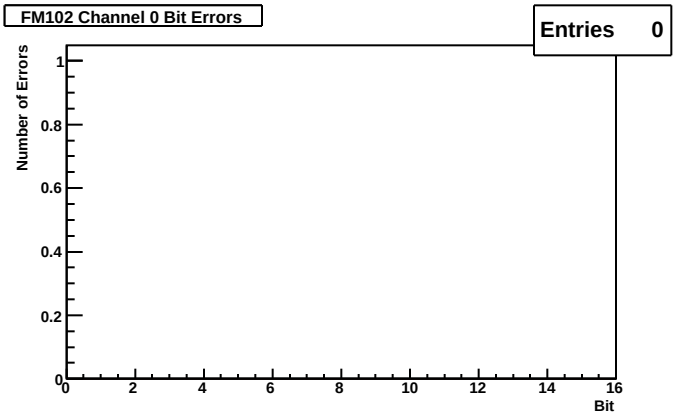

FM101 Channel 3 Bit Errors
Entries 3323
Number of Errors
Bit

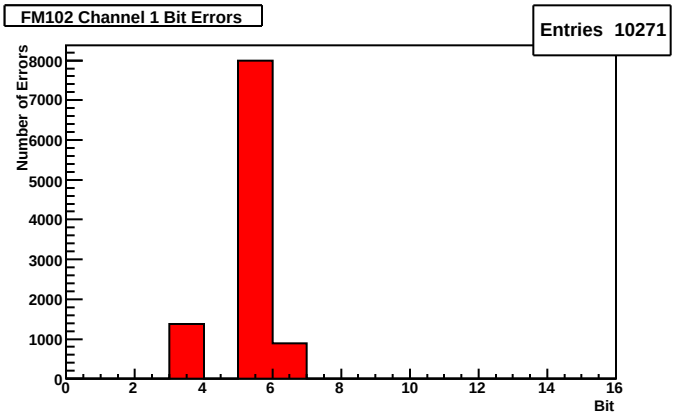

FM101 Channel 4 Bit Errors
Entries 1021
Number of Errors
Bit

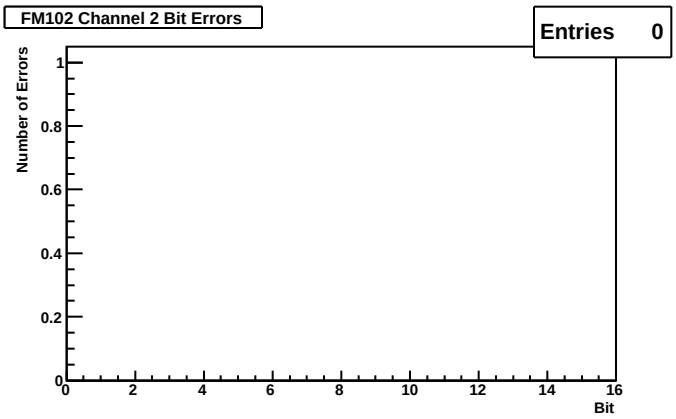

FM101 Channel 5 Bit Errors
Entries 386
Number of Errors
Bit

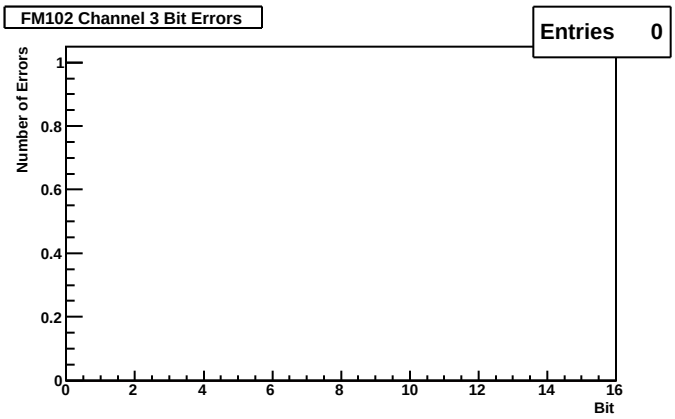

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

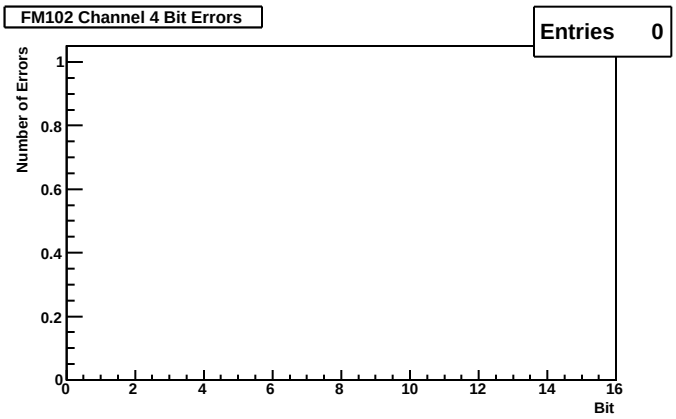

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

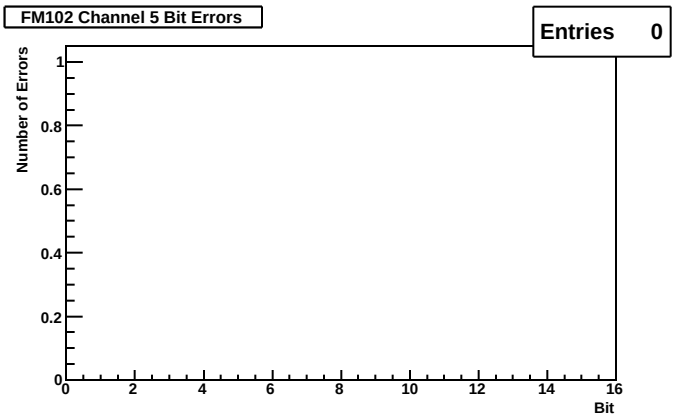

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

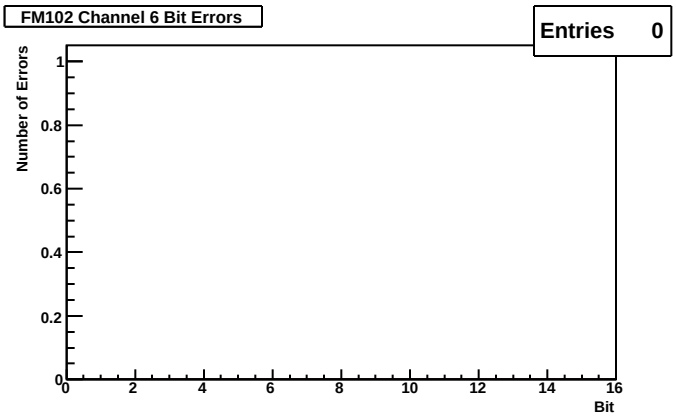

FM102 Channel 1 Bit Errors
Entries 10271
Number of Errors
Bit

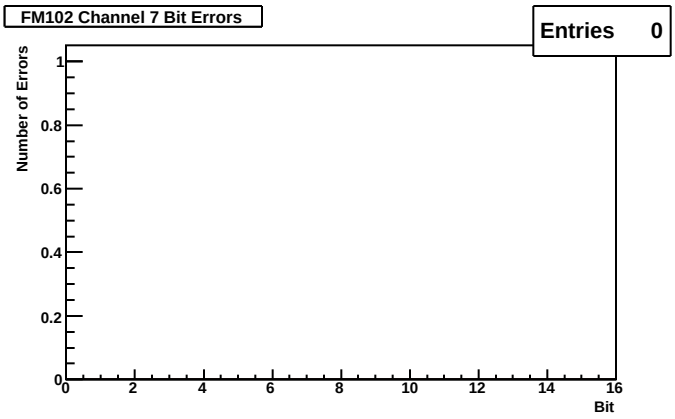

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

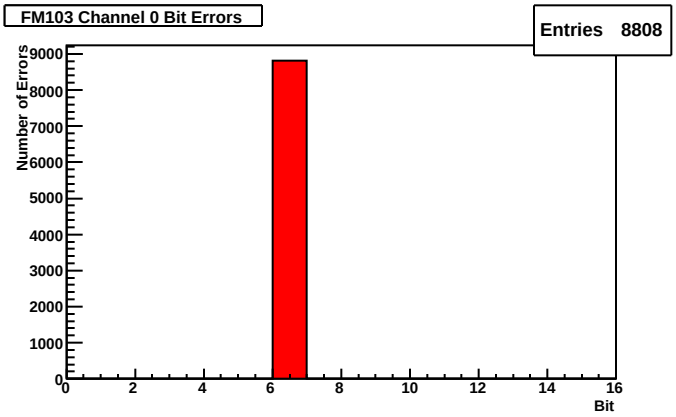

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

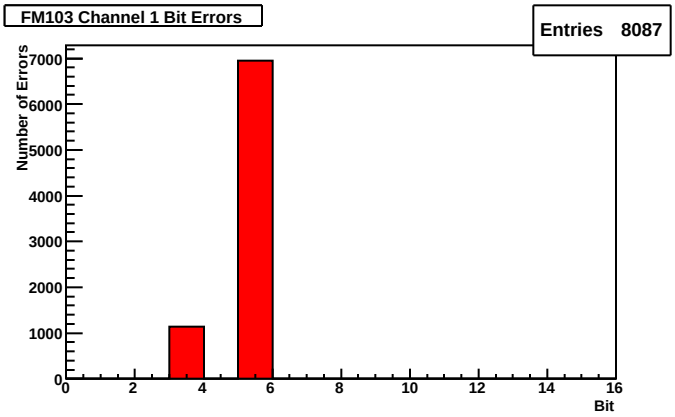

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

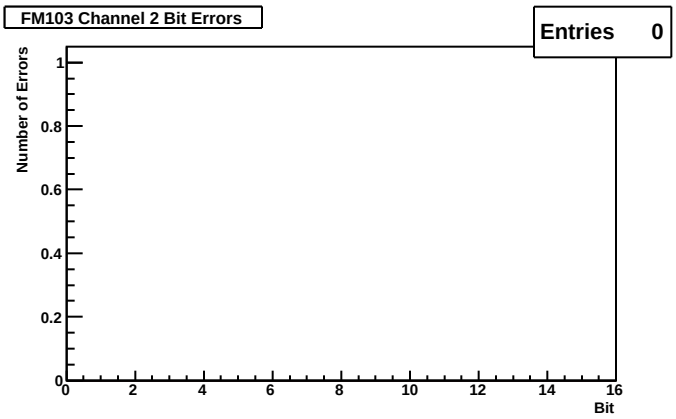

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

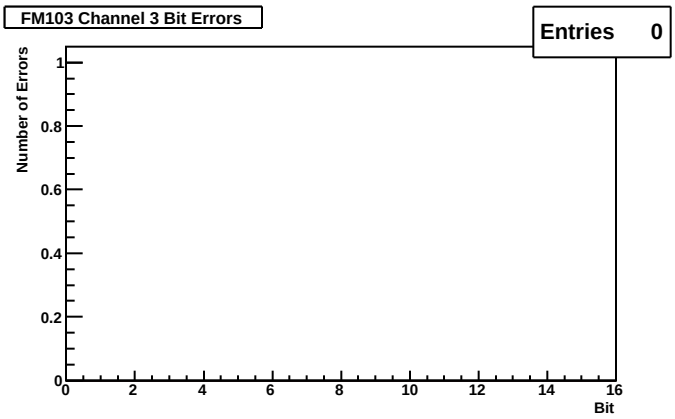

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

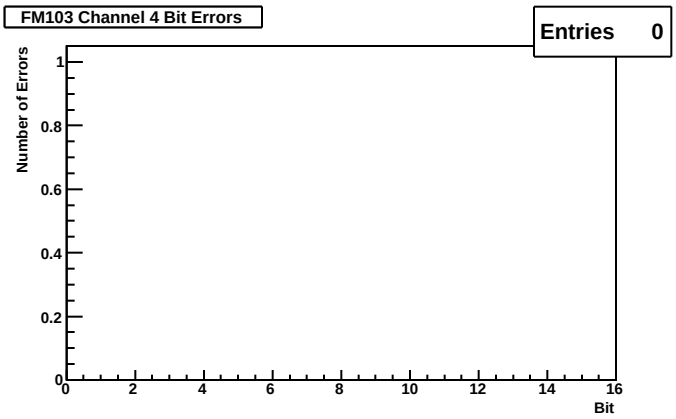

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

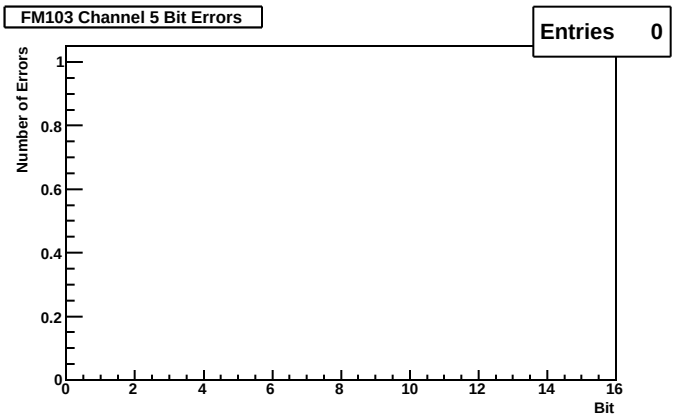

FM103 Channel 0 Bit Errors
Entries 8808
Number of Errors
Bit

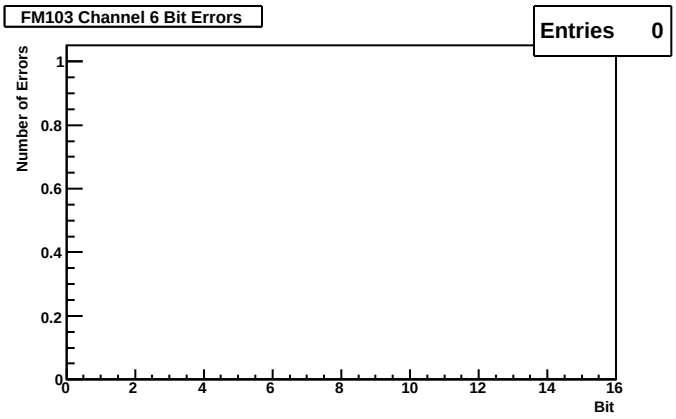

FM103 Channel 1 Bit Errors
Entries 8087
Number of Errors
Bit

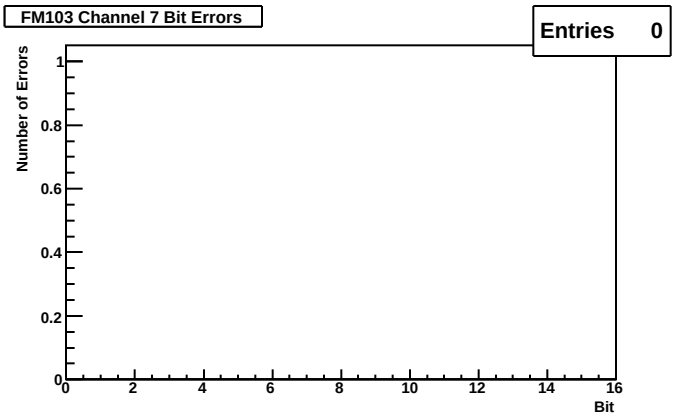

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

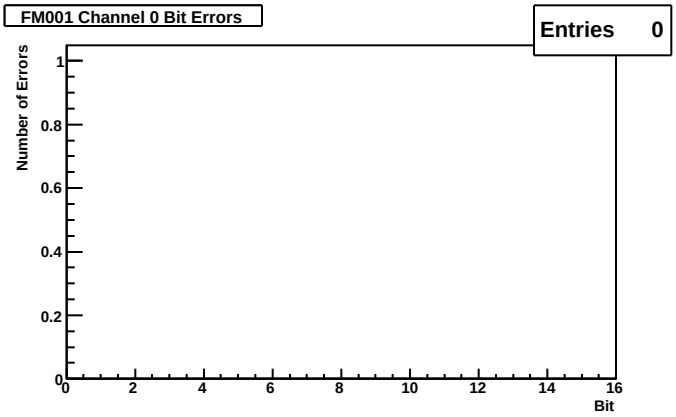

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

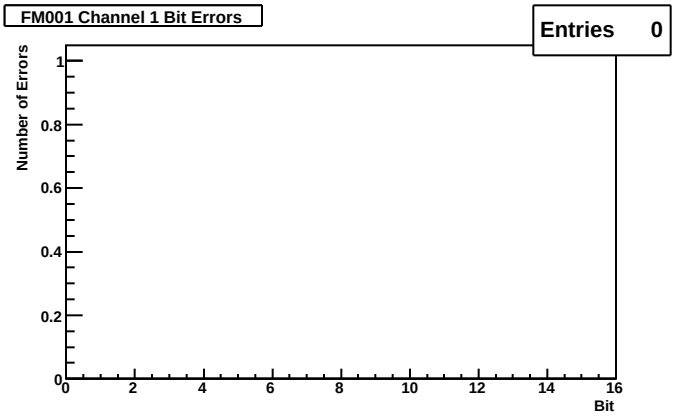

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

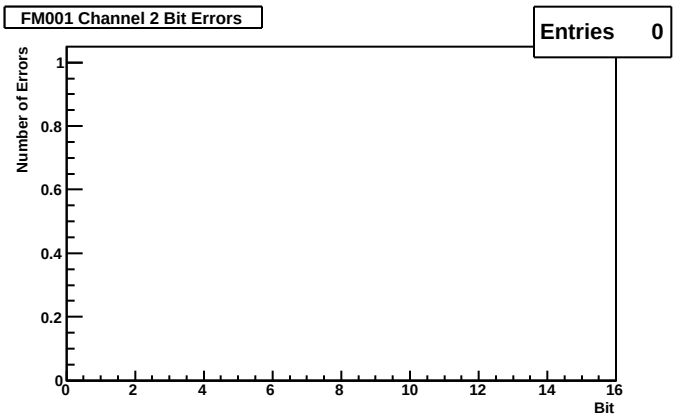

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

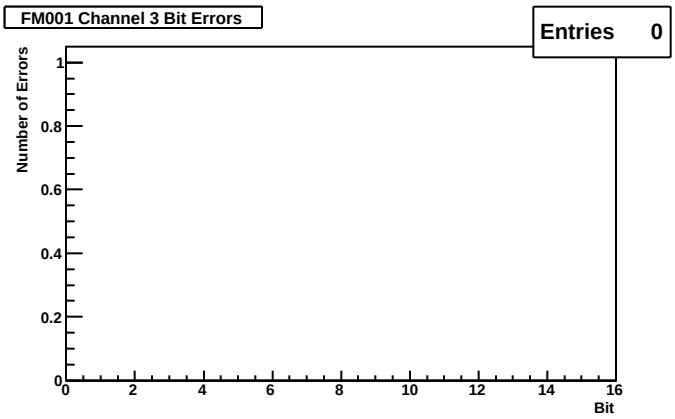

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

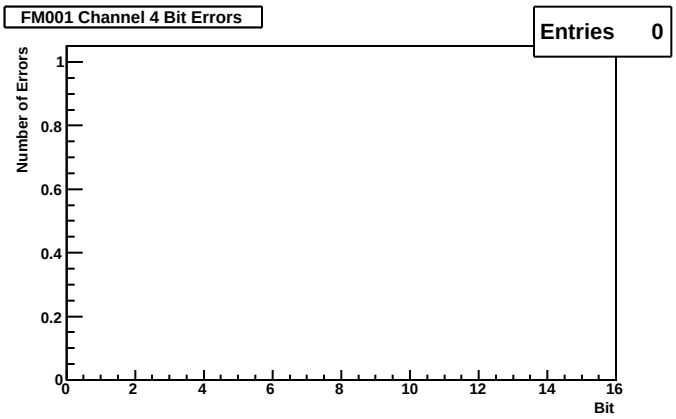

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

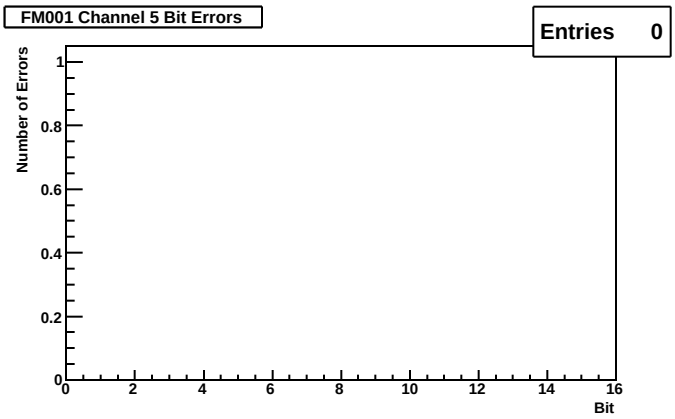

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

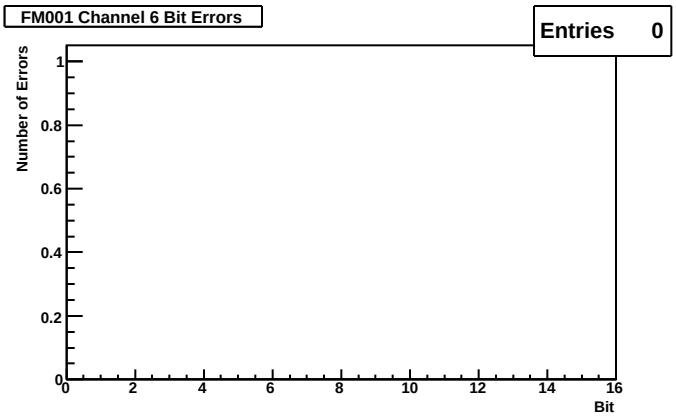

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

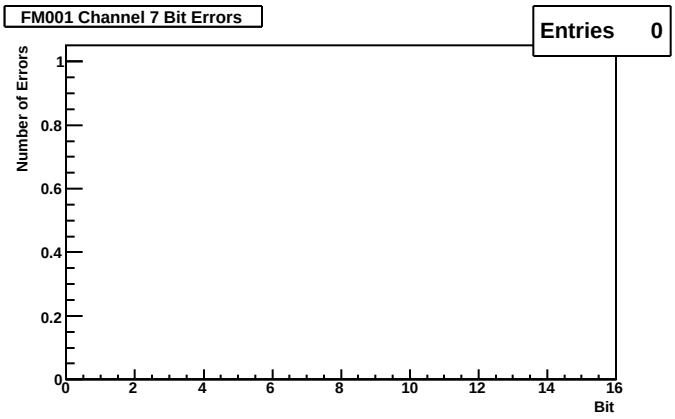

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

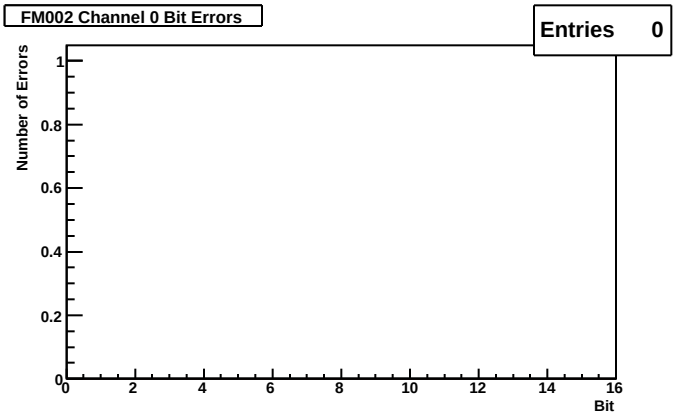

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

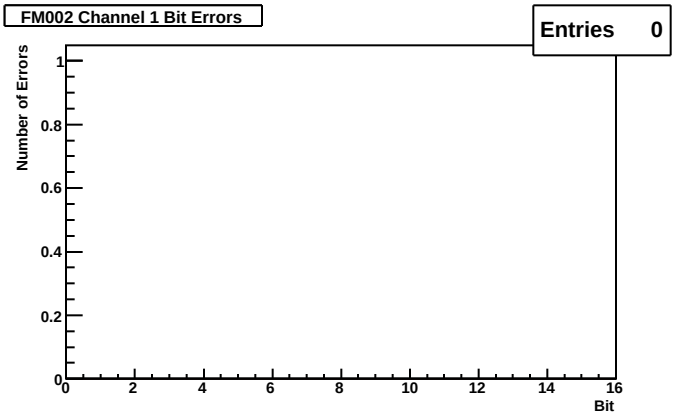

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

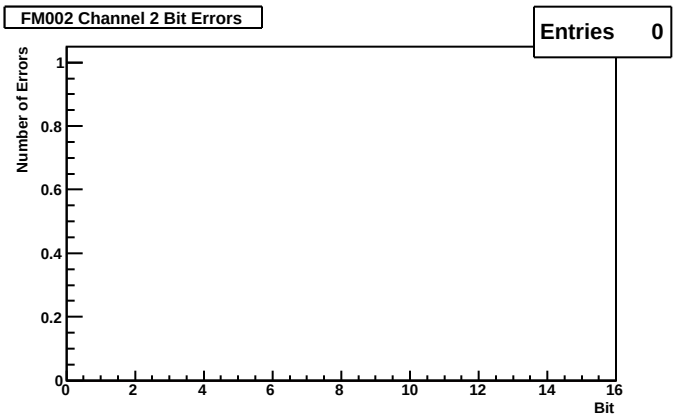

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

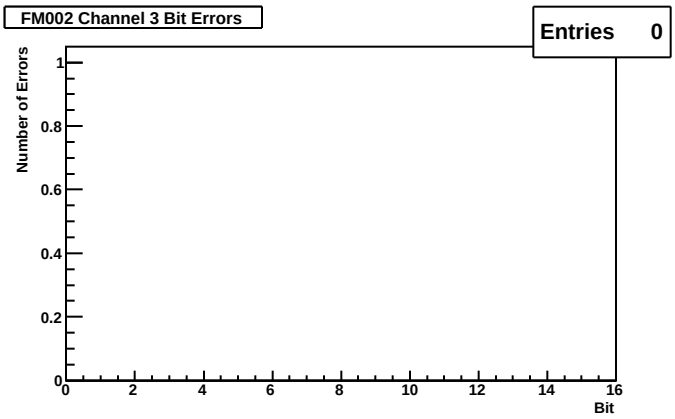

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

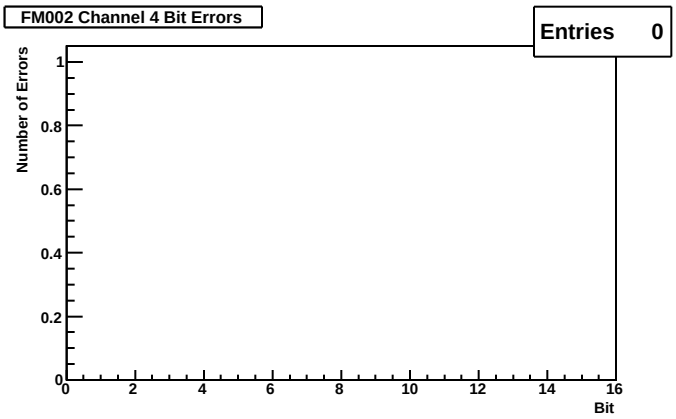

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

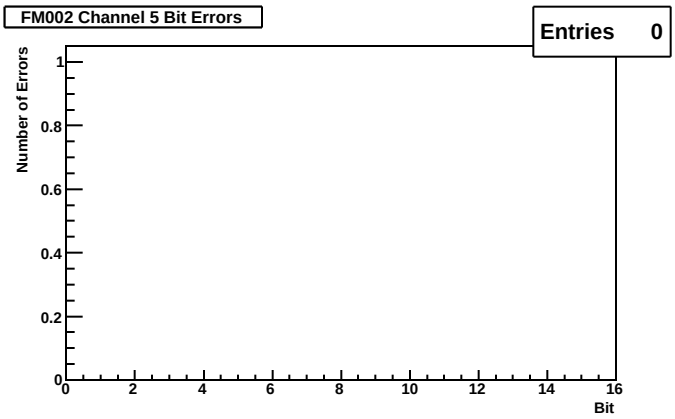

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

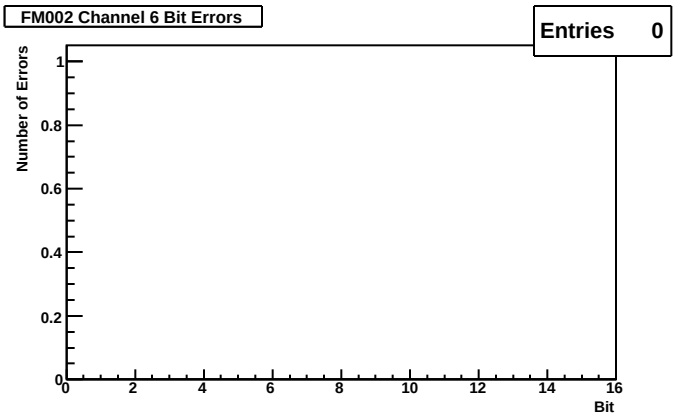

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

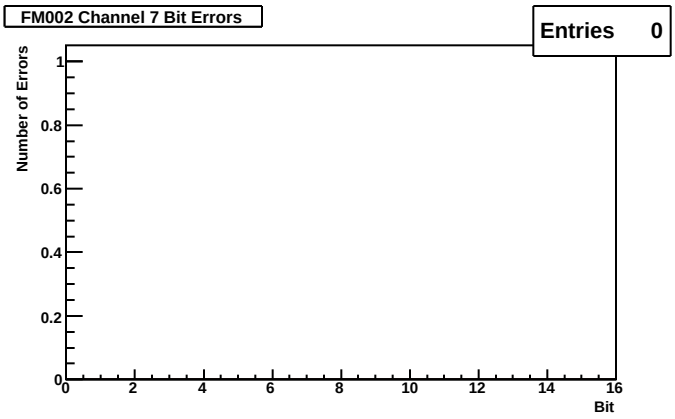

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

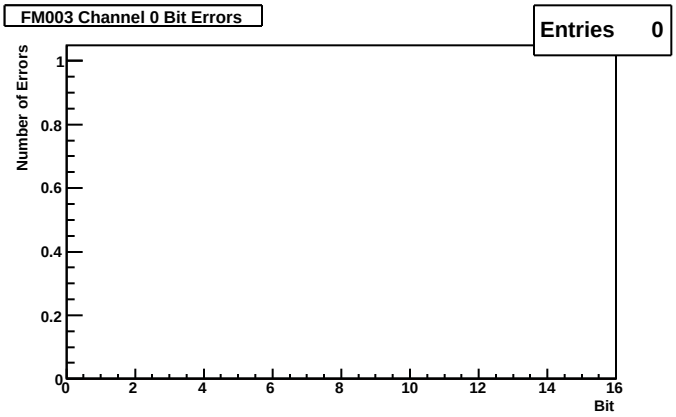

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

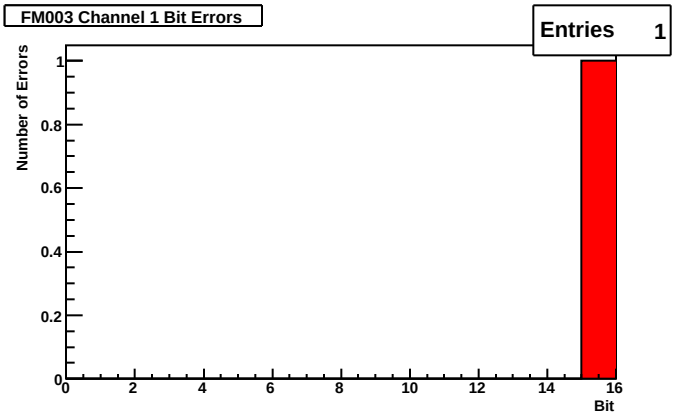

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

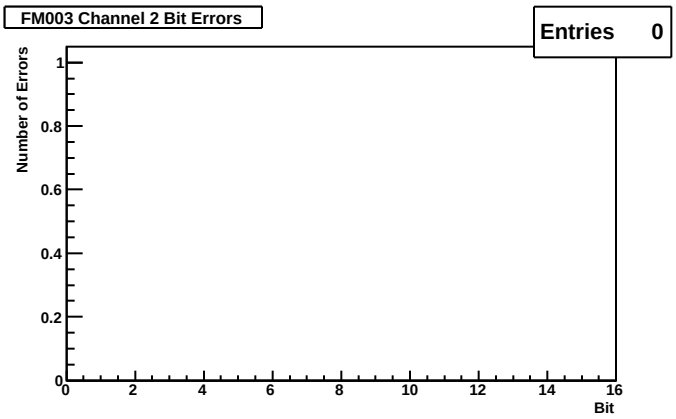

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

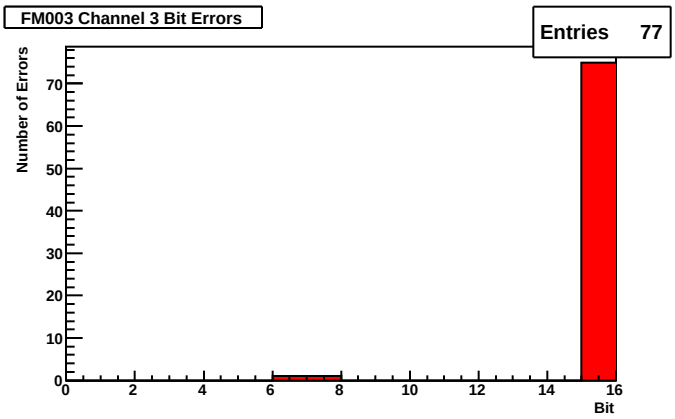

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

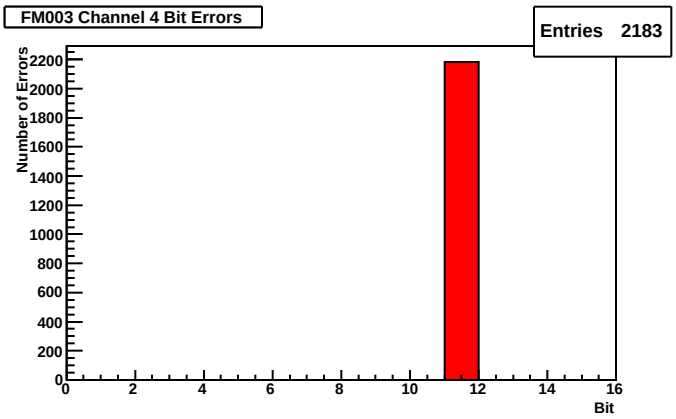

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

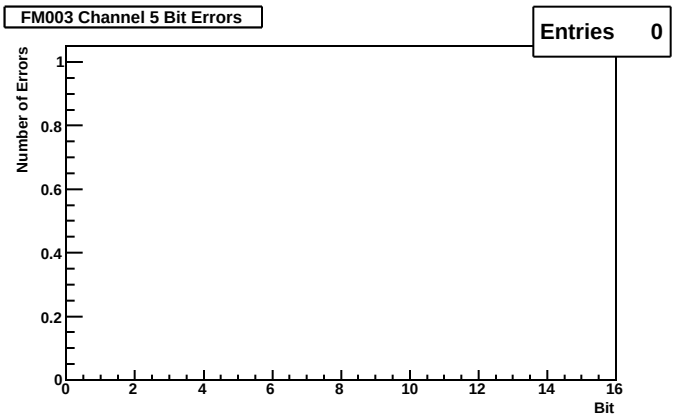

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

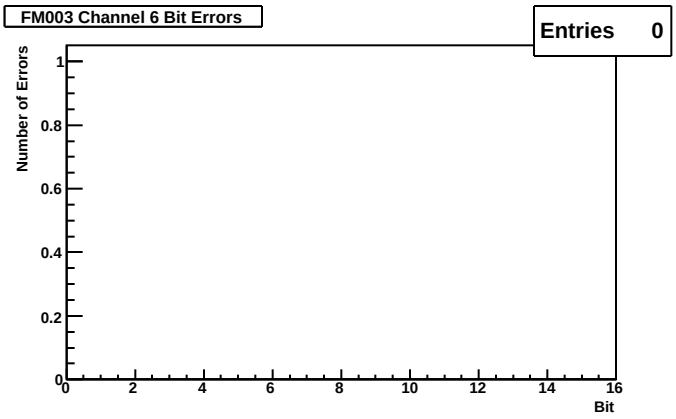

FM003 Channel 1 Bit Errors
Entries 1
Number of Errors
Bit

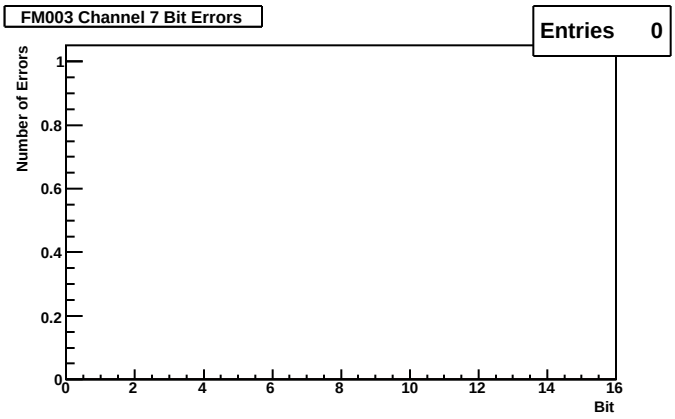

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

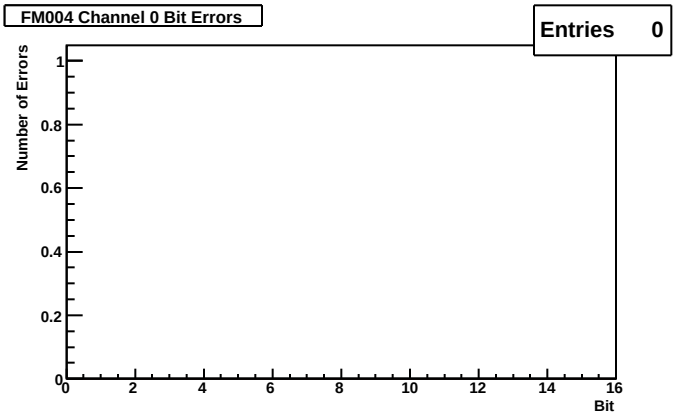

FM003 Channel 3 Bit Errors
Entries 77
Number of Errors
Bit

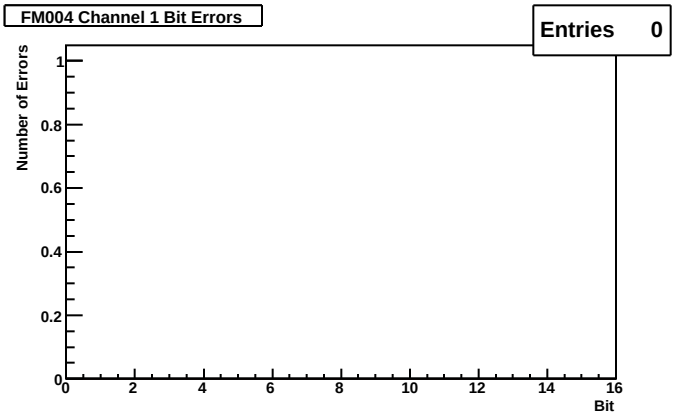

FM003 Channel 4 Bit Errors
Entries 2183
Number of Errors
Bit

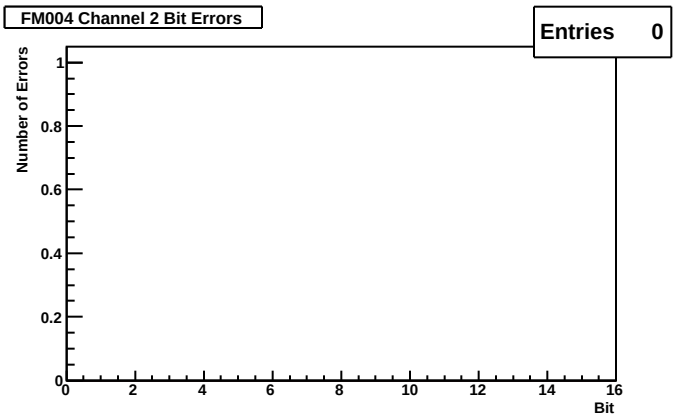

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

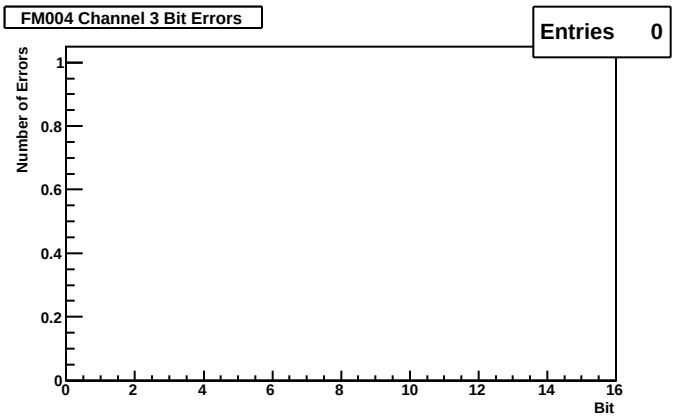

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

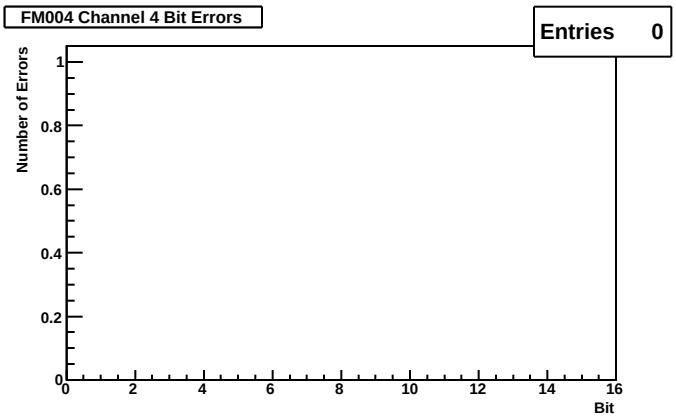

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

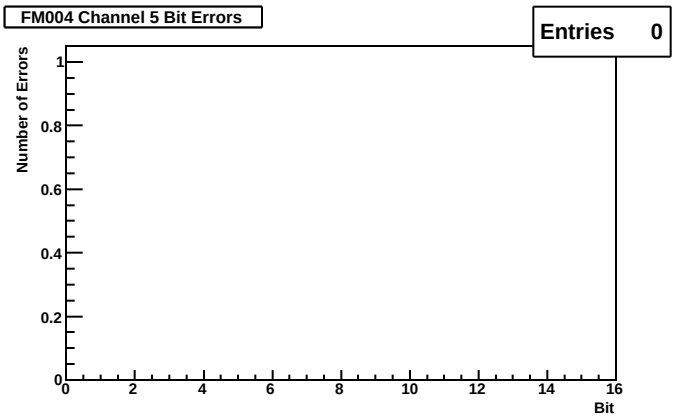

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

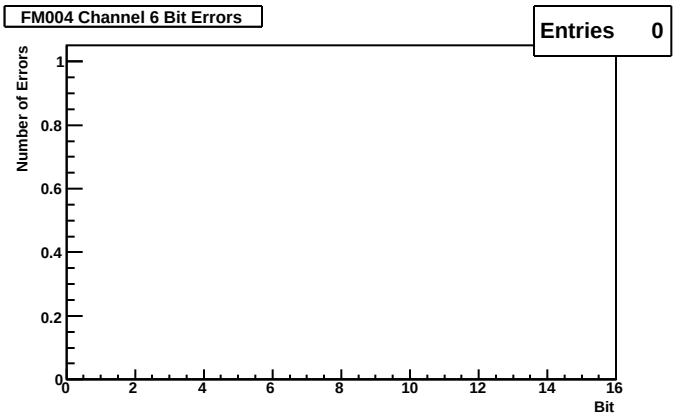

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

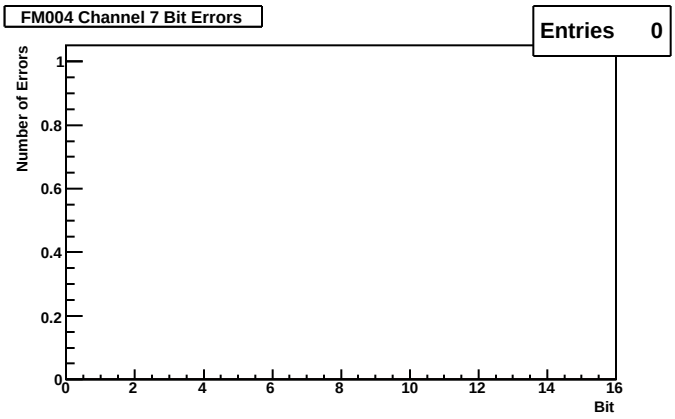

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

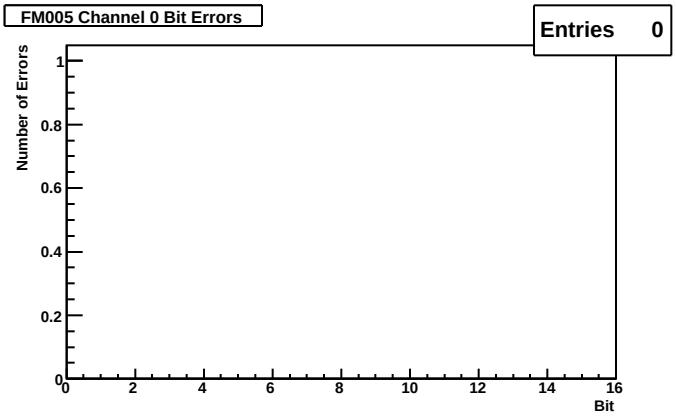

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

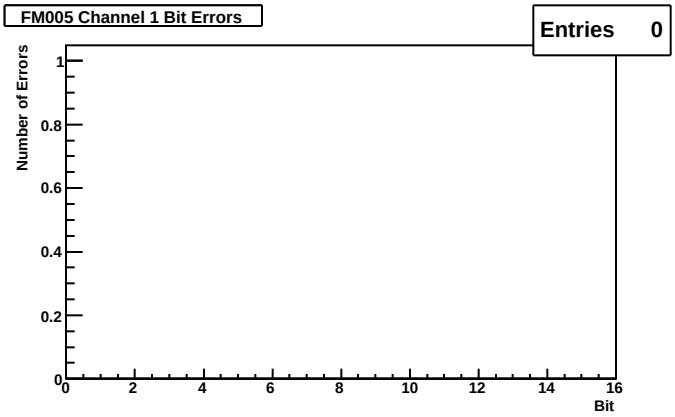

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

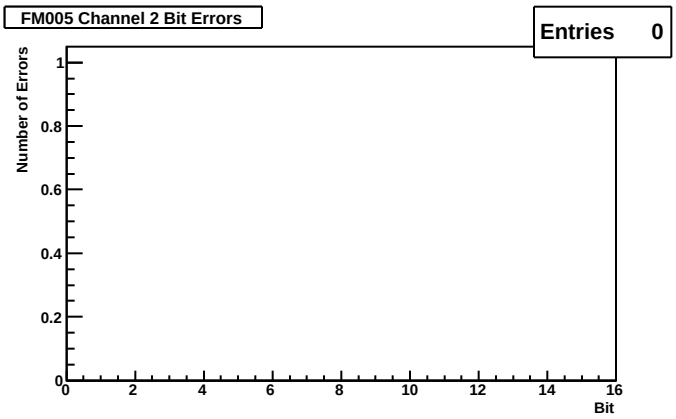

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

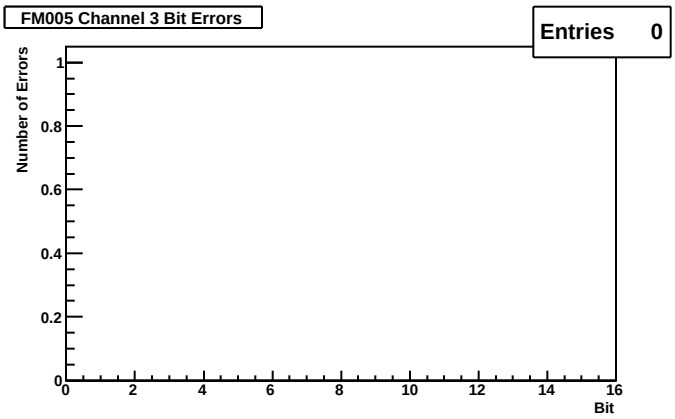

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

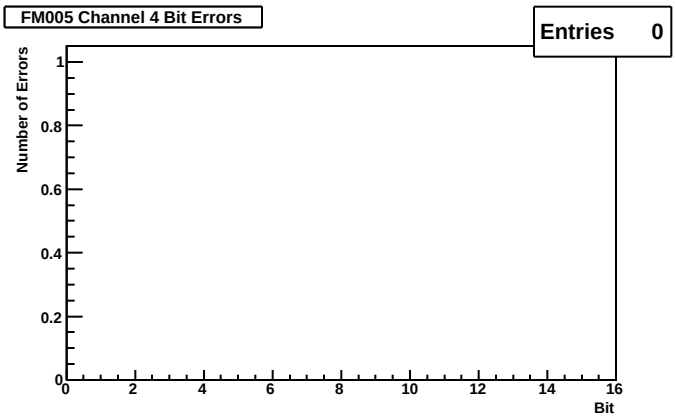

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

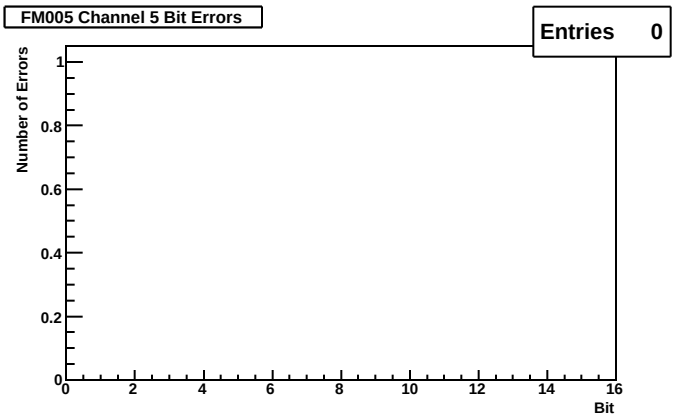

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

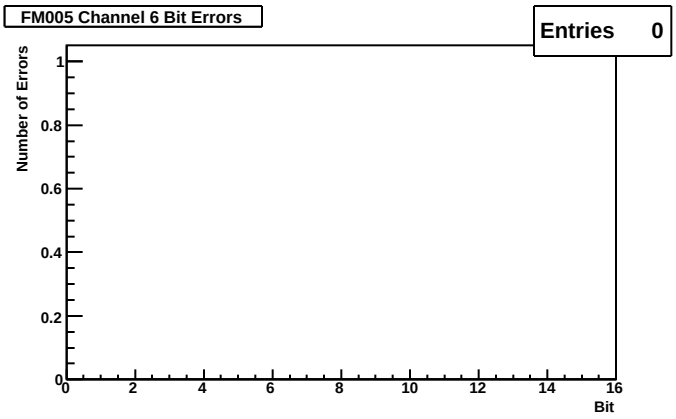

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

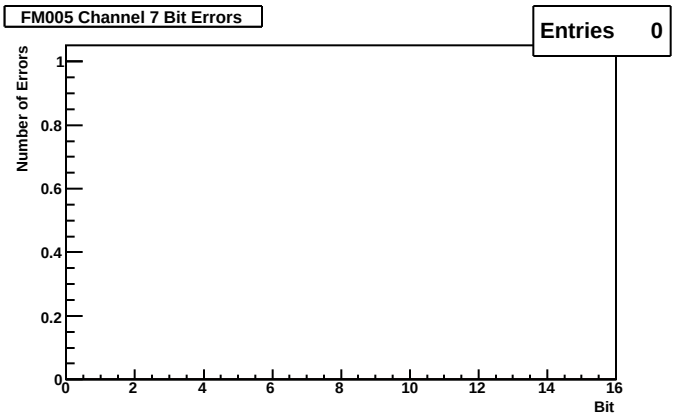

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

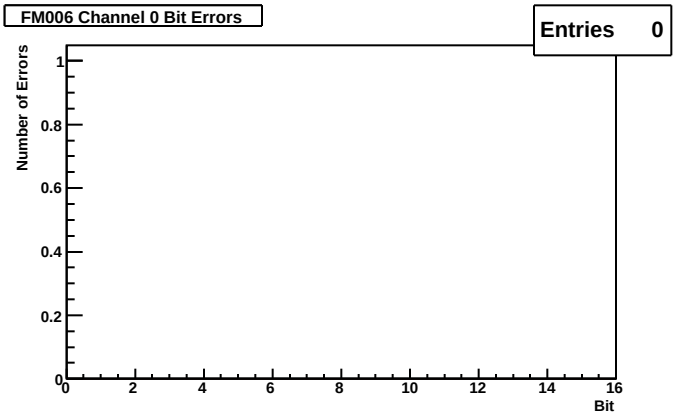

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

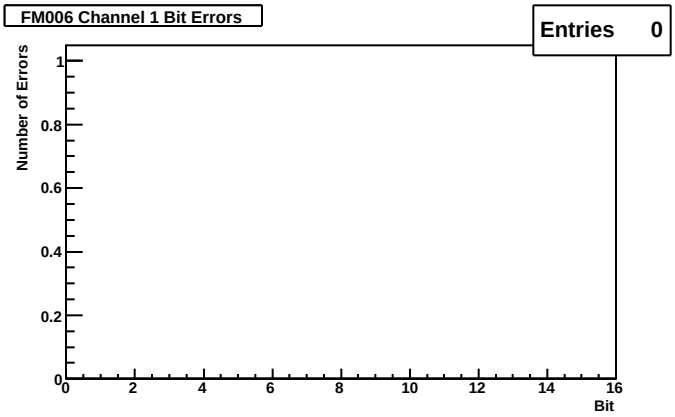

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

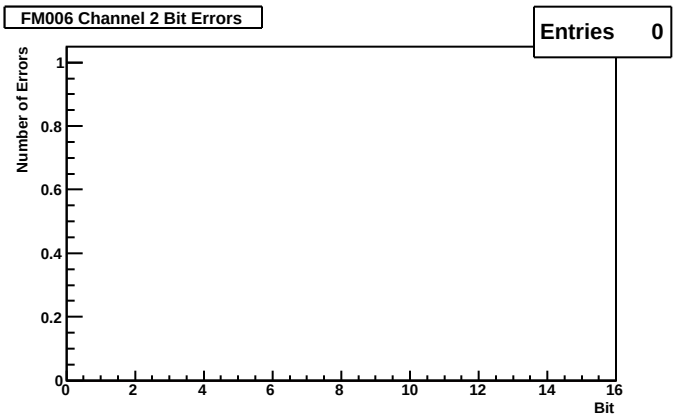

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

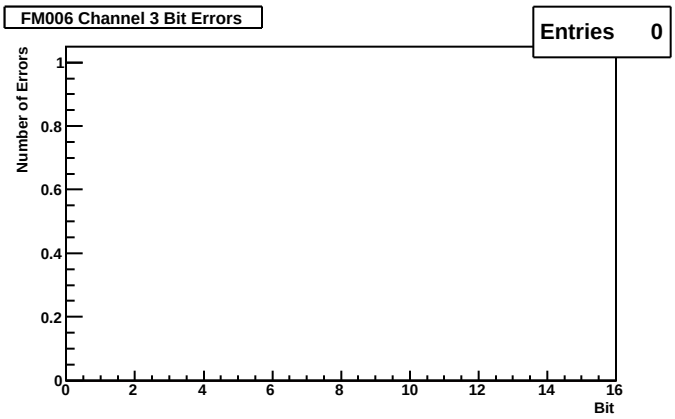

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

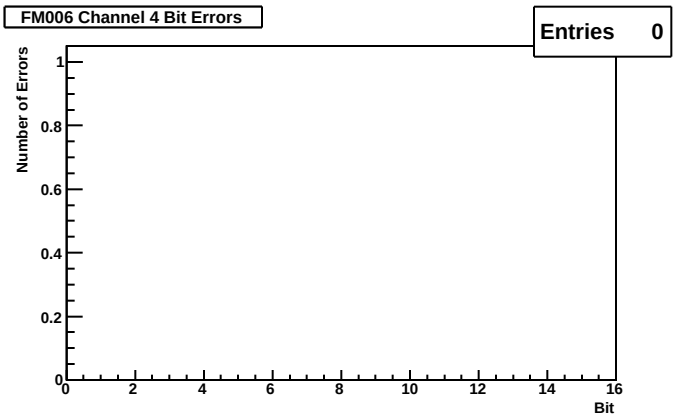

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

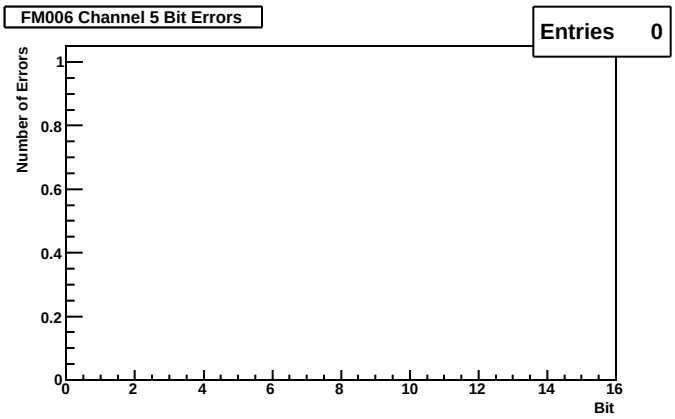

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

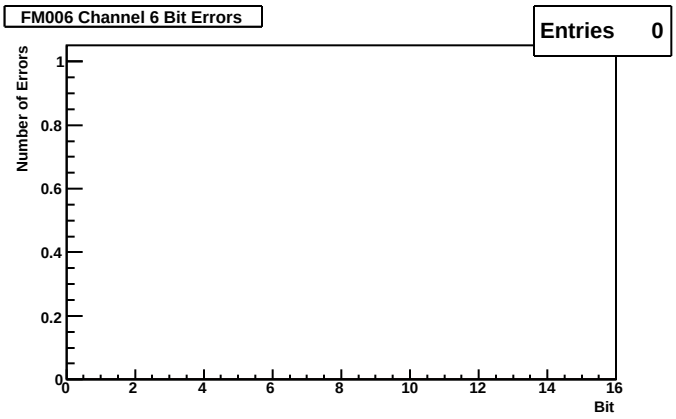

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

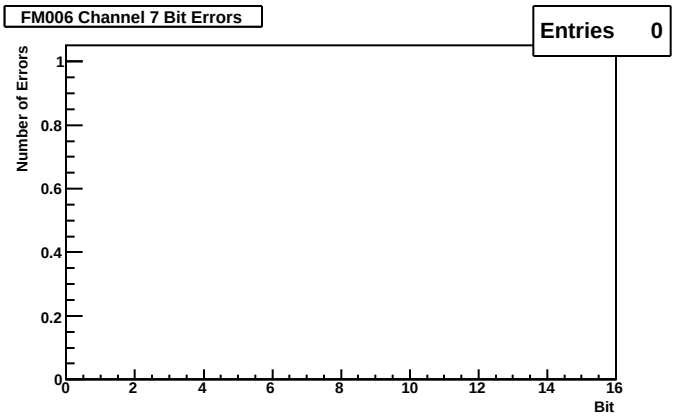

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

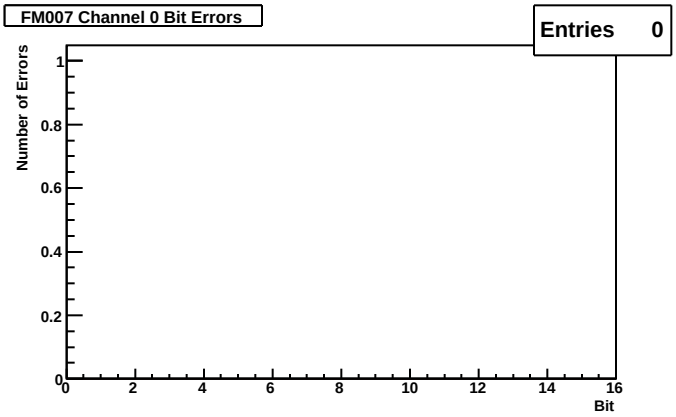

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

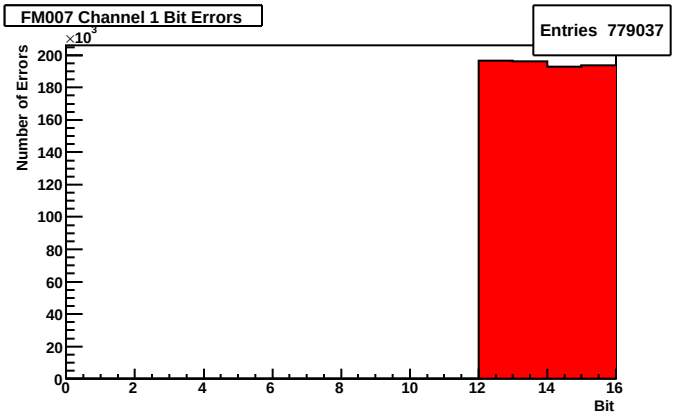

FM006 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

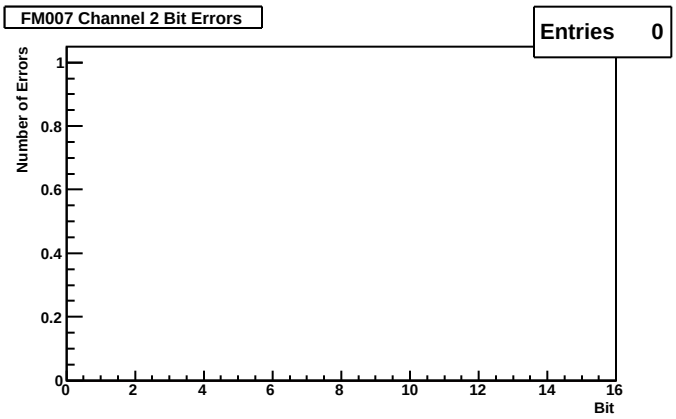

FM006 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

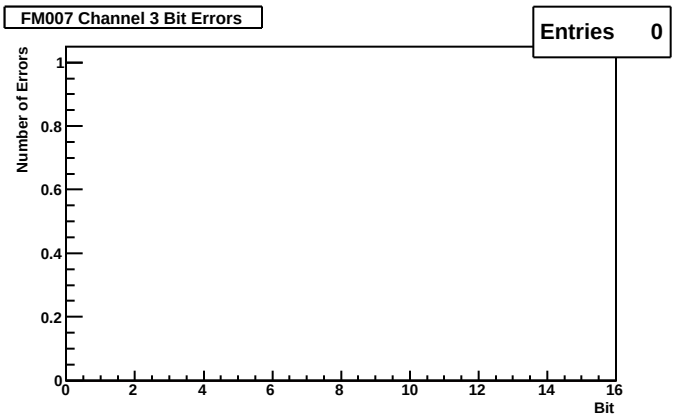

FM006 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

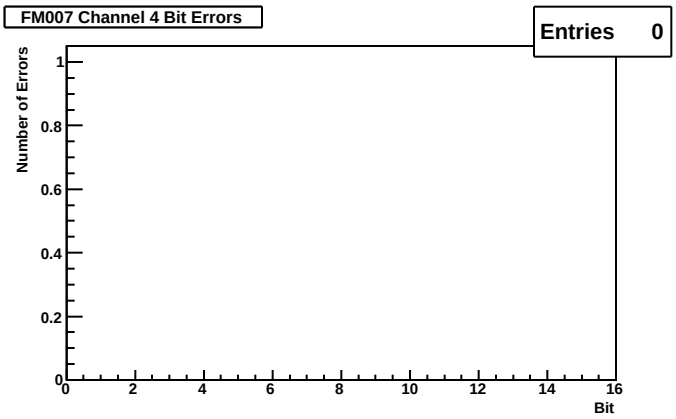

FM006 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

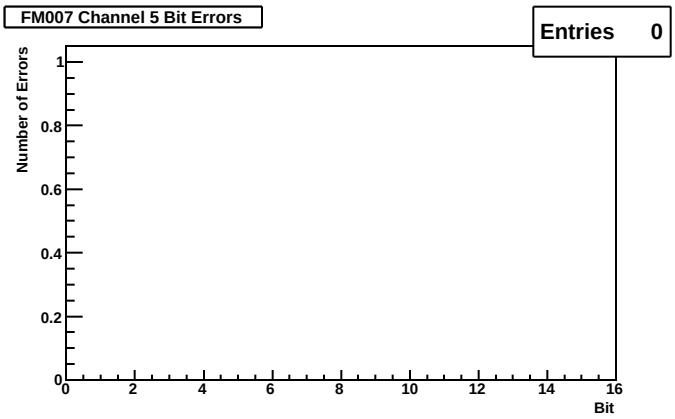

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

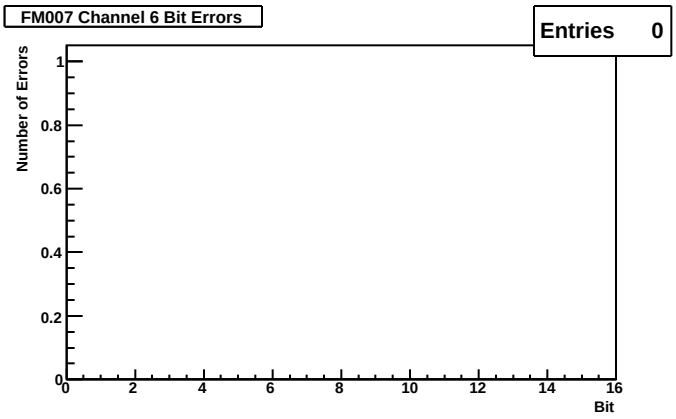

FM007 Channel 1 Bit Errors
Entries 779037
×10³
Number of Errors
Bit

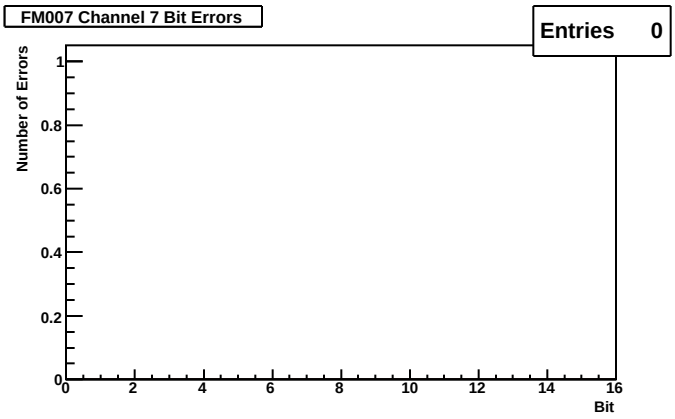

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

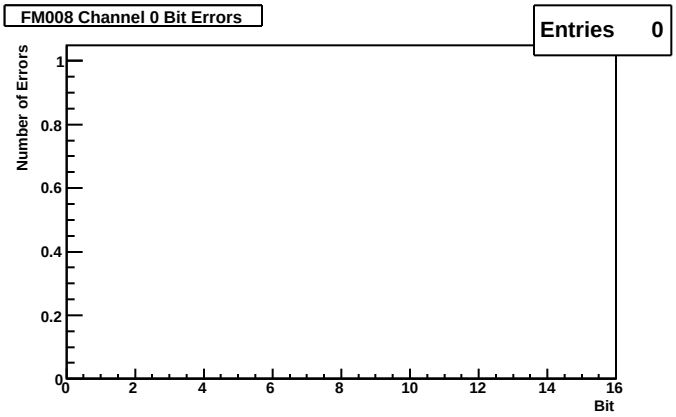

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

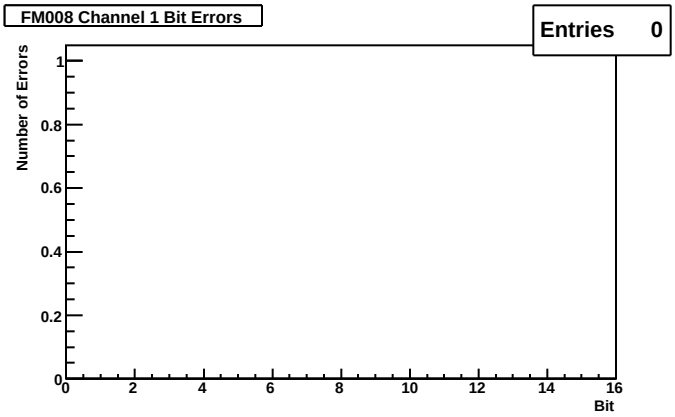

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

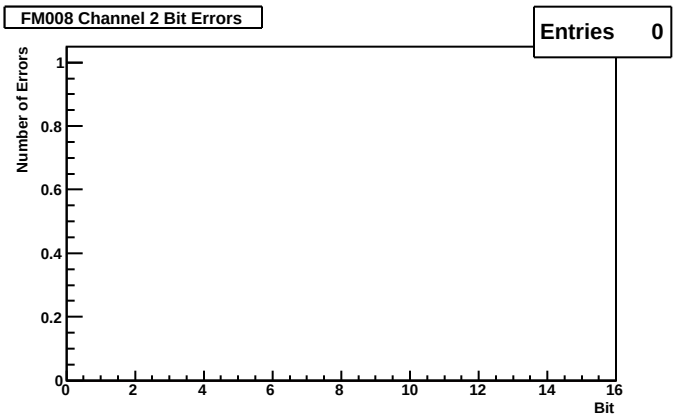

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

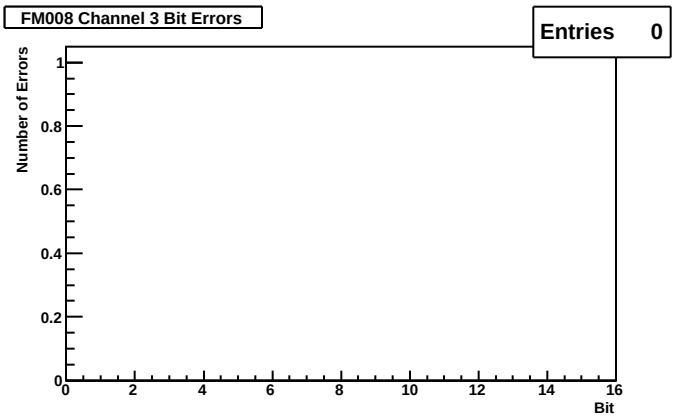

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

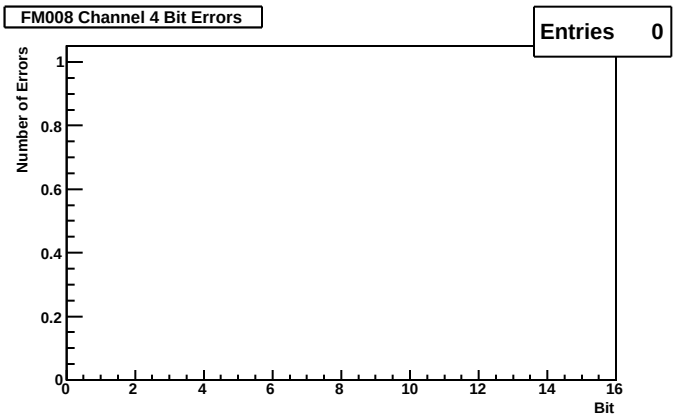

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

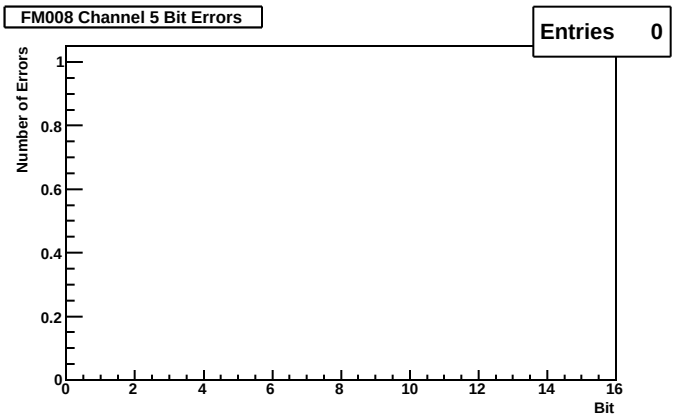

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

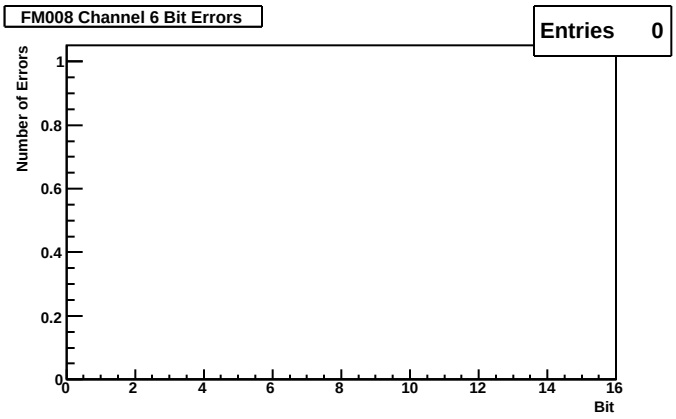

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

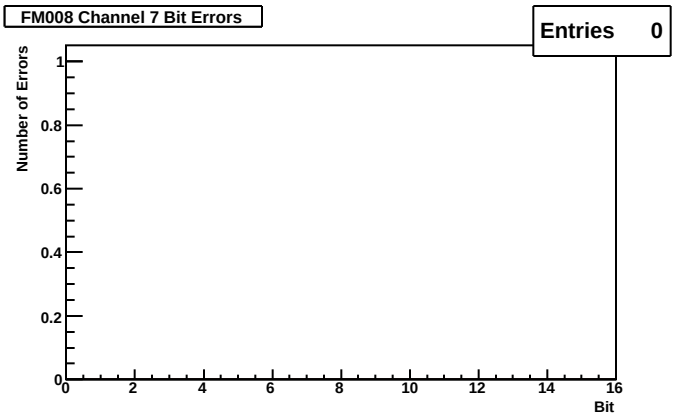

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

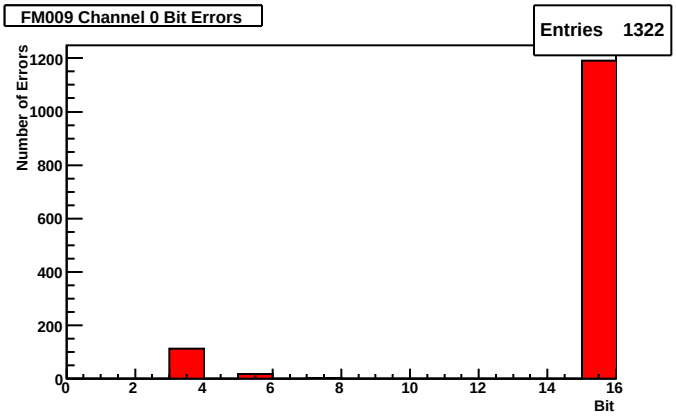

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

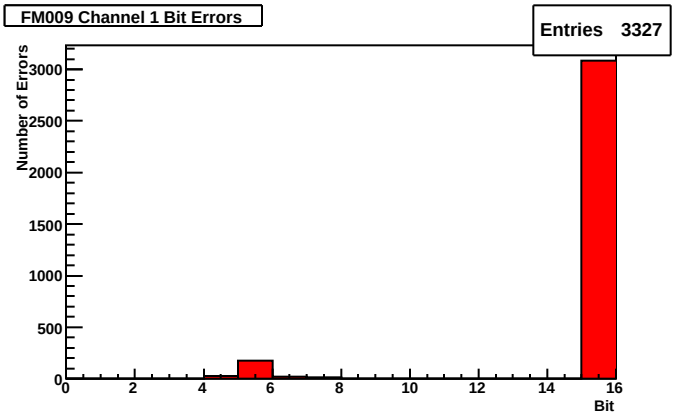

FM008 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

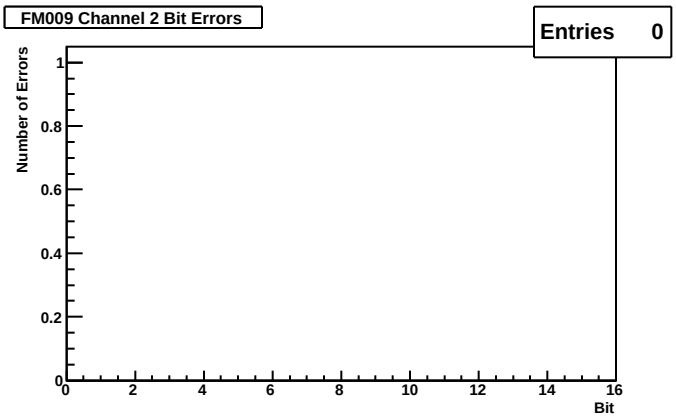

FM008 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

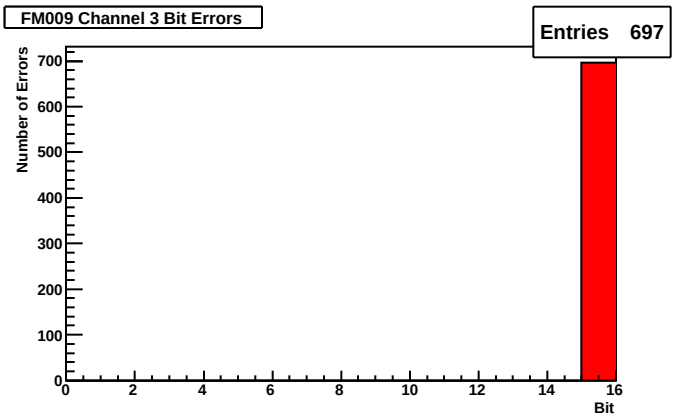

FM008 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

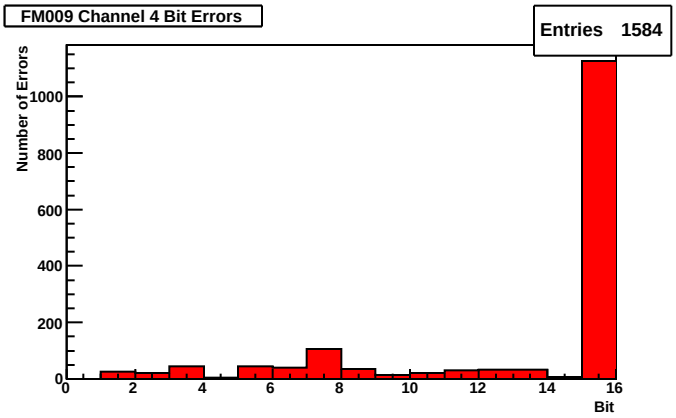

FM008 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

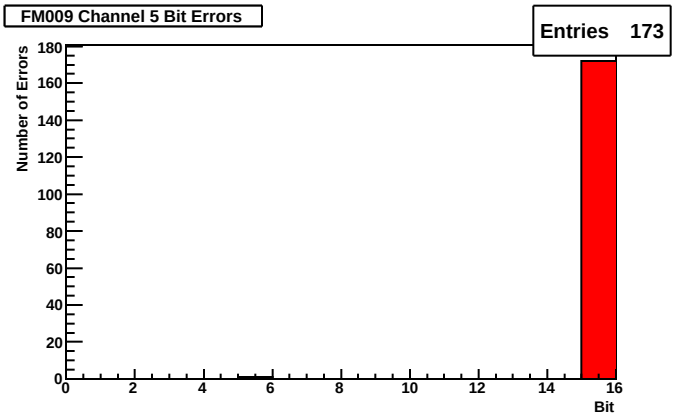

FM009 Channel 0 Bit Errors
Entries 1322
Number of Errors
Bit

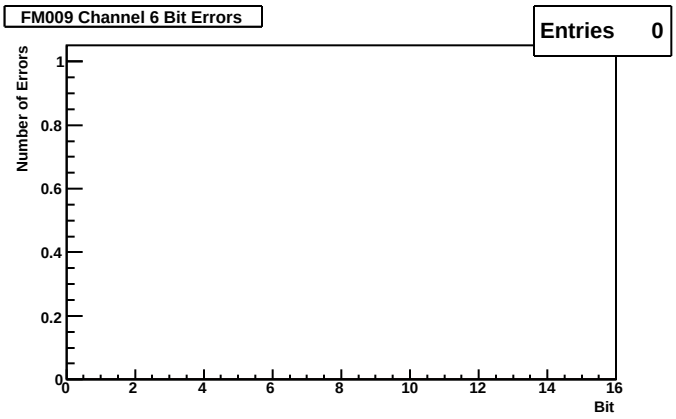

FM009 Channel 1 Bit Errors
Entries 3327
Number of Errors
Bit

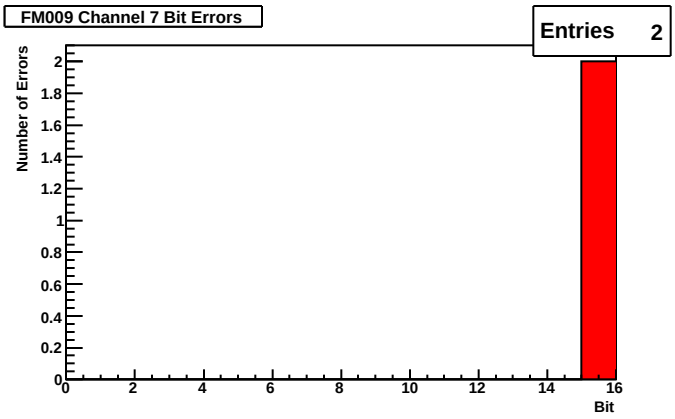

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

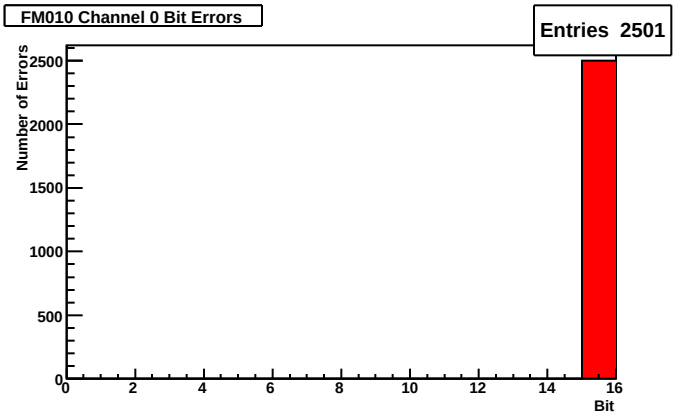

FM009 Channel 3 Bit Errors
Entries 697
Number of Errors
Bit

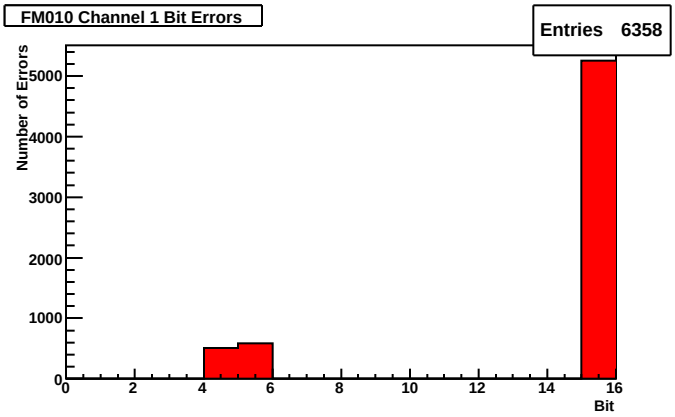

FM009 Channel 4 Bit Errors
Entries 1584
Number of Errors
Bit

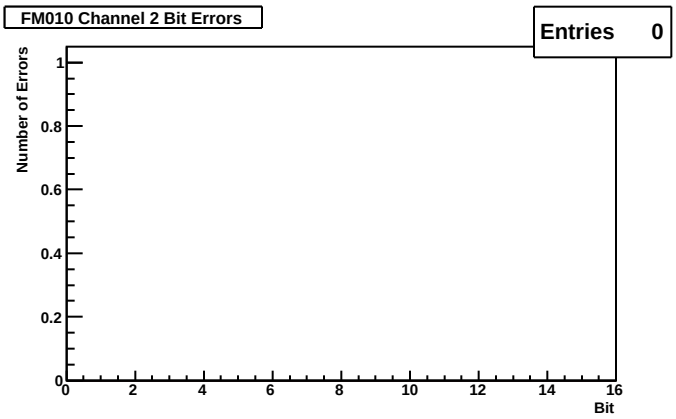

FM009 Channel 5 Bit Errors
Entries 173
Number of Errors
Bit

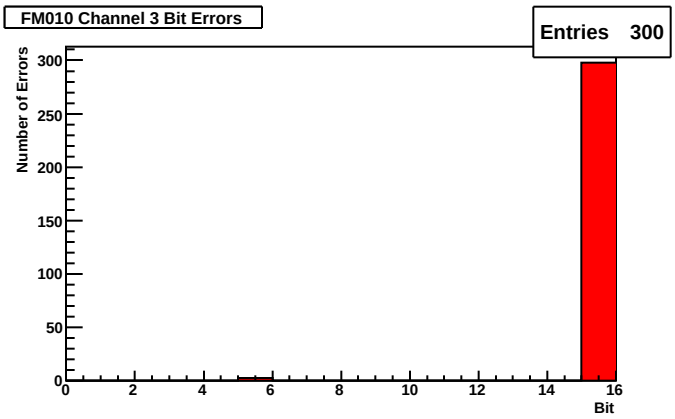

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM009 Channel 7 Bit Errors
Entries 2
Number of Errors
Bit

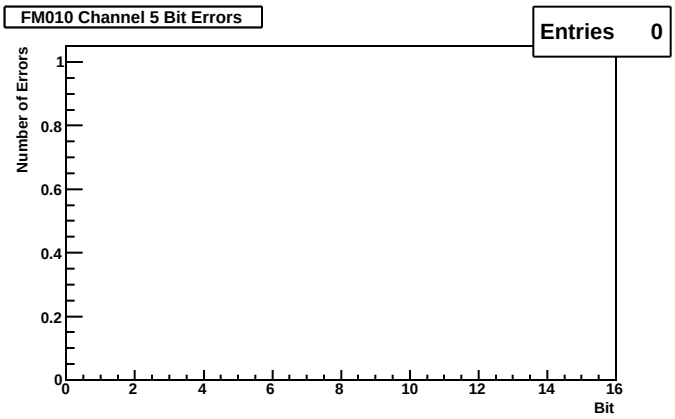

FM010 Channel 0 Bit Errors
Entries 2501
Number of Errors
Bit

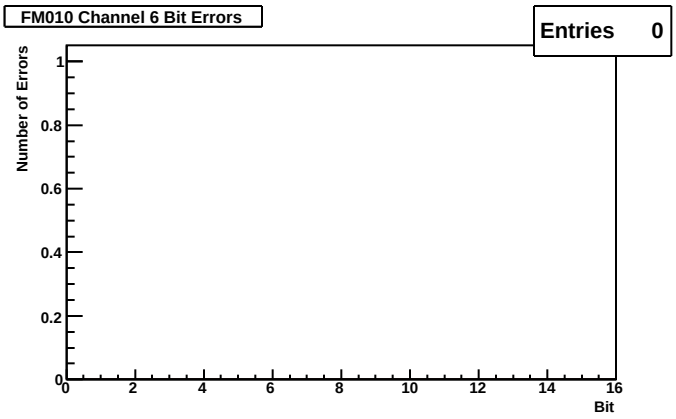

FM010 Channel 1 Bit Errors
Entries 6358
Number of Errors
Bit

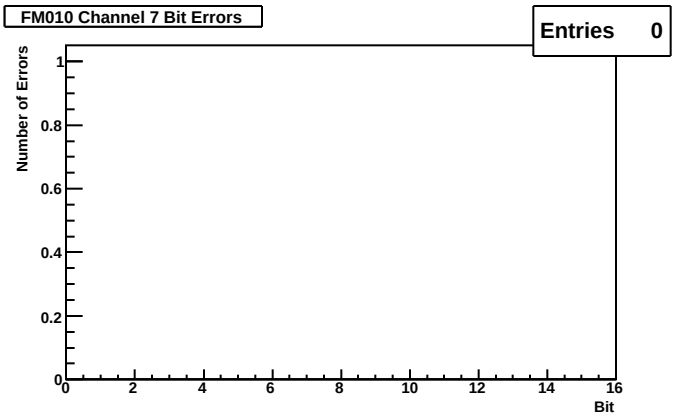

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

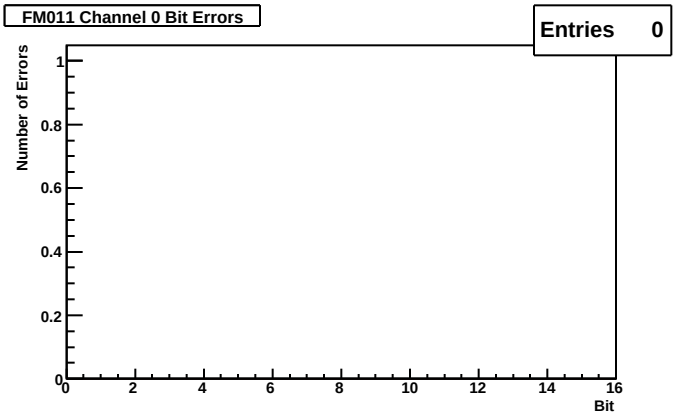

FM010 Channel 3 Bit Errors
Entries 300
Number of Errors
Bit

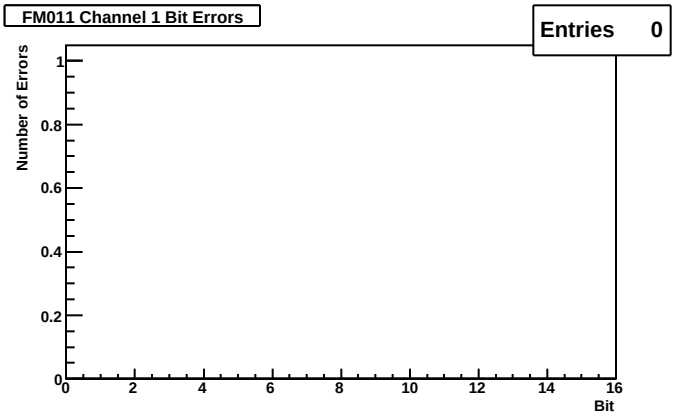
FM010 Channel 4 Bit Errors
Entries 447714
Number of Errors
Bit

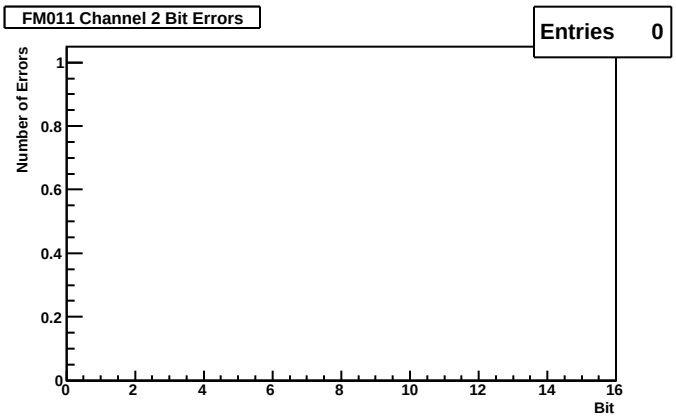

FM010 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

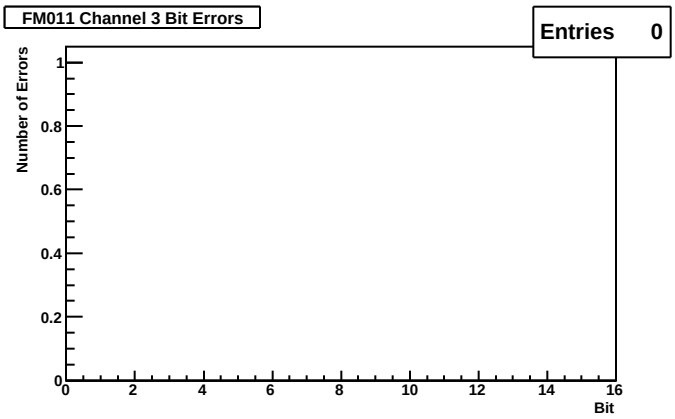

FM010 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

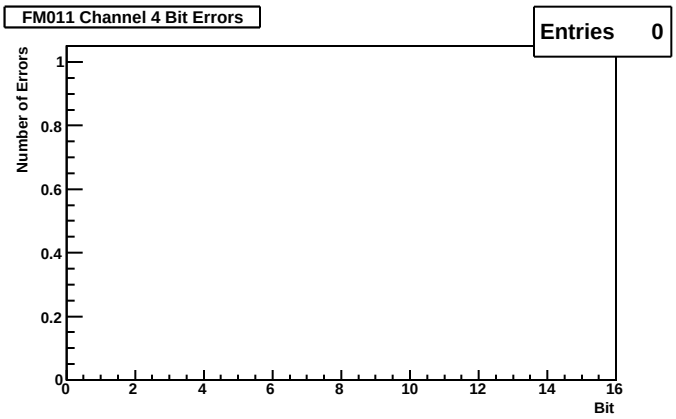

FM010 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

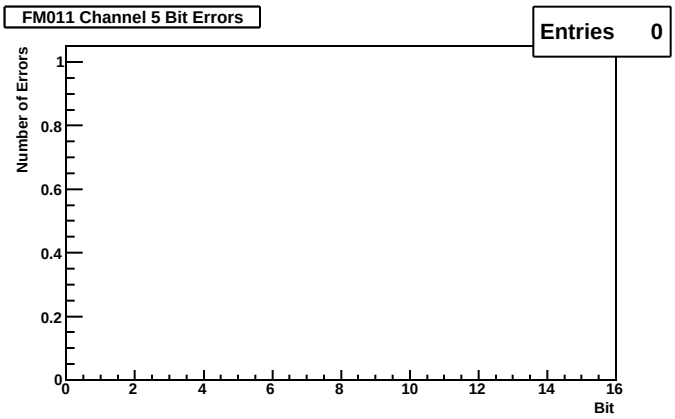

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

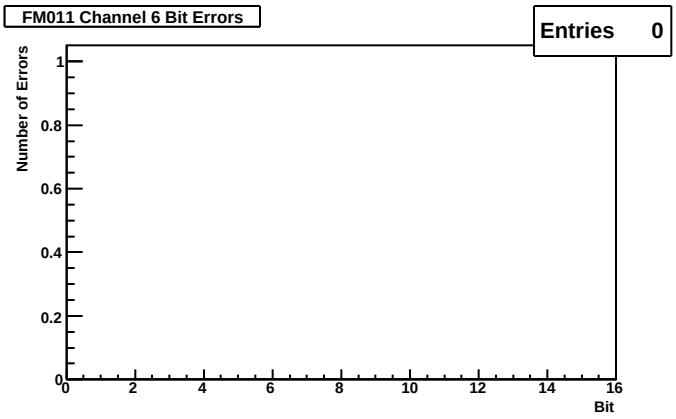

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

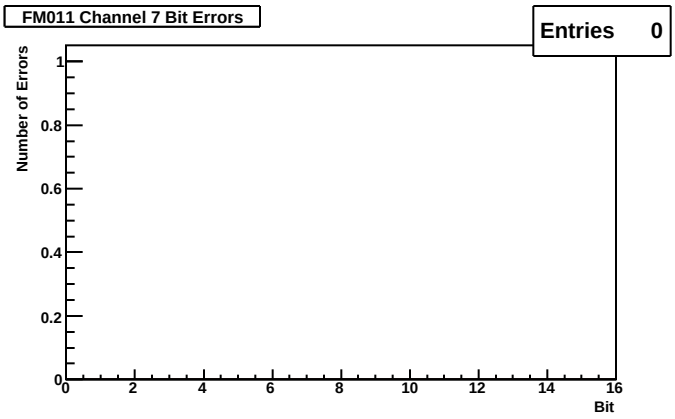

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

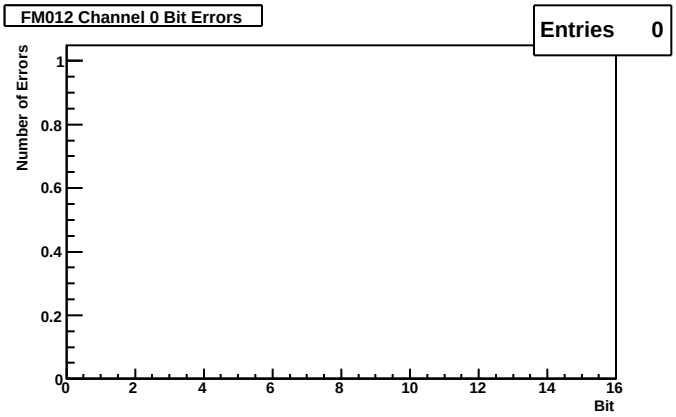

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

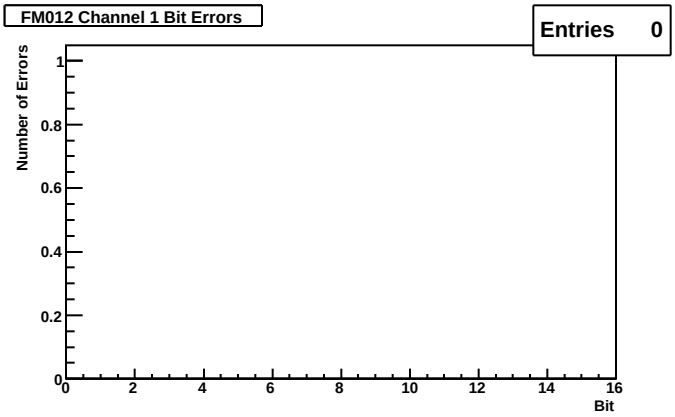

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

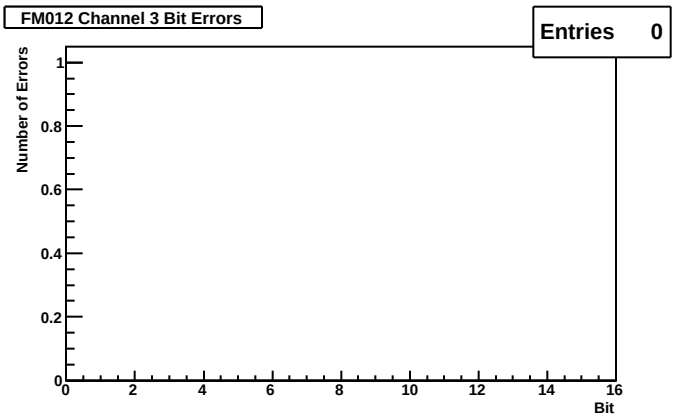

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

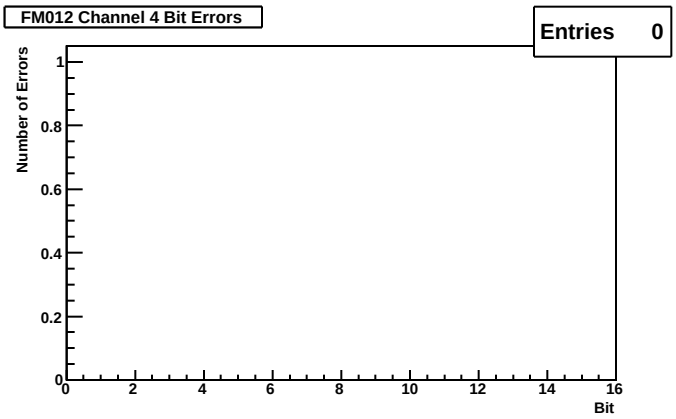

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

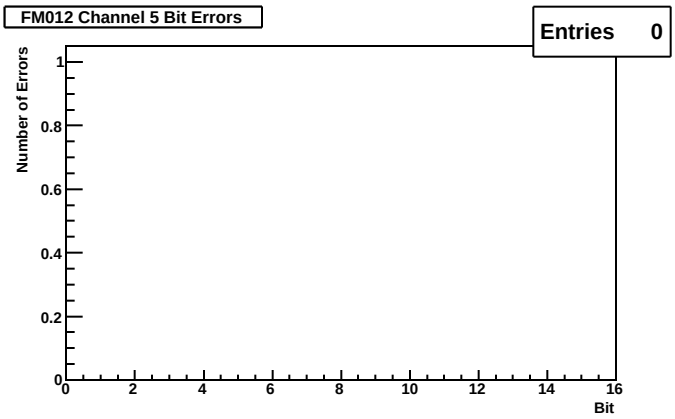

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

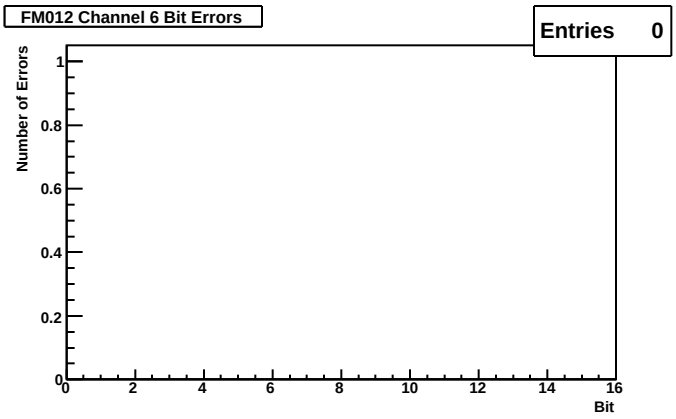

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

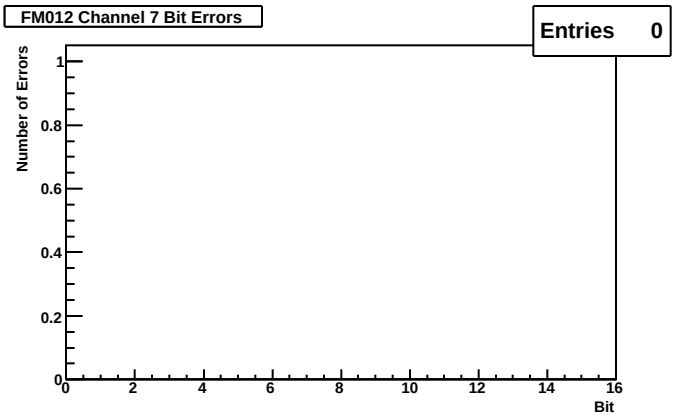
FM012 Channel 2 Bit Errors
Entries 2
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit