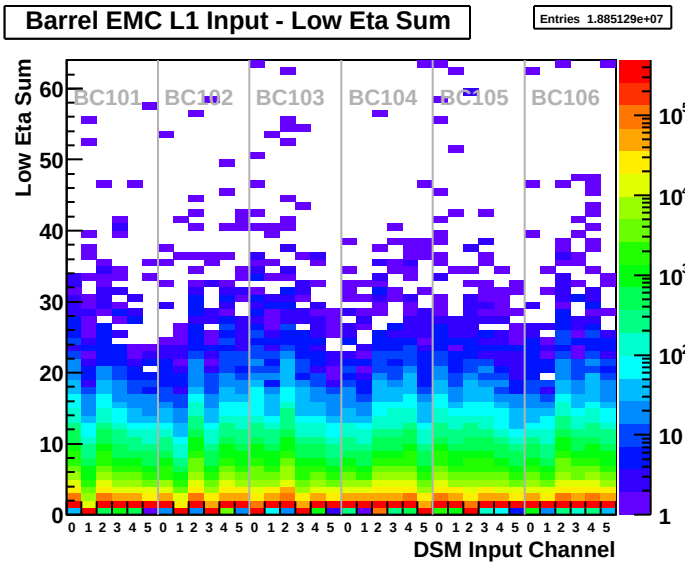
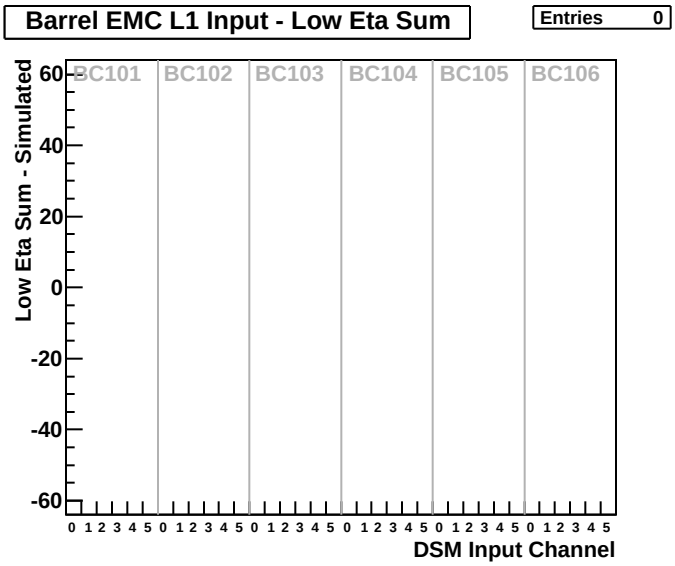
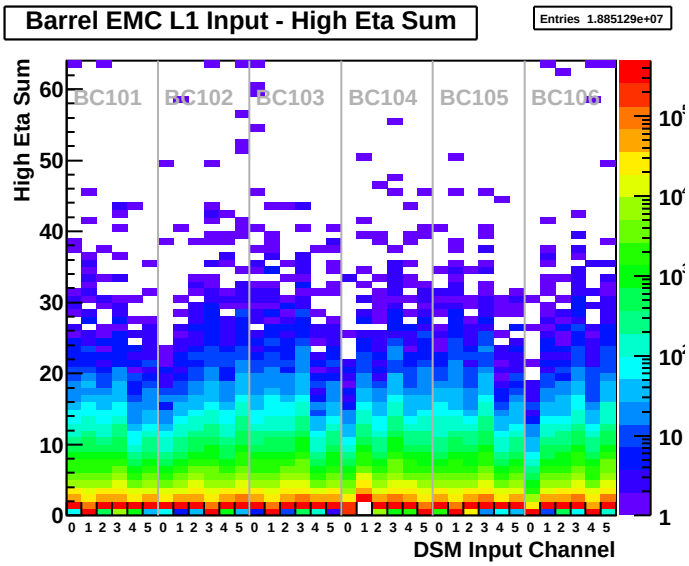
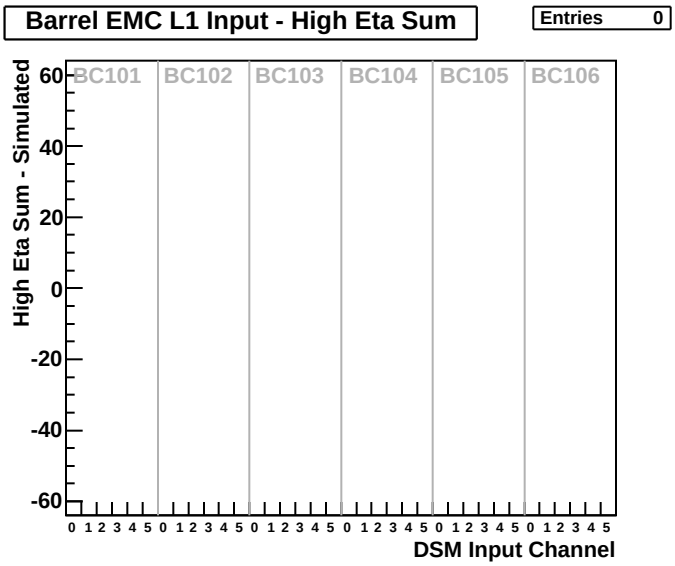
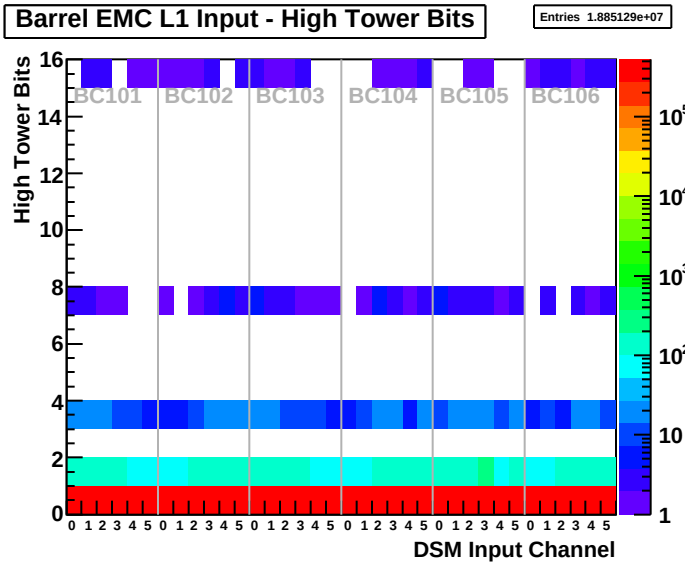
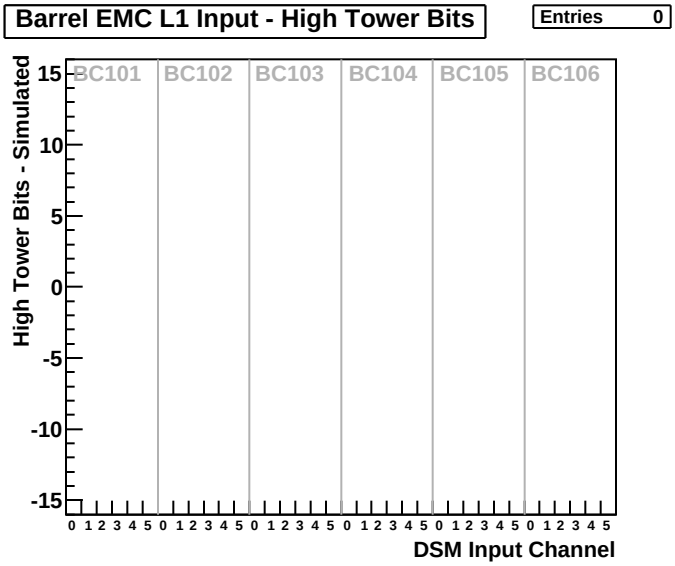

Barrel EMC L1 Input - Low Eta Sum
Entries 1.885129e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

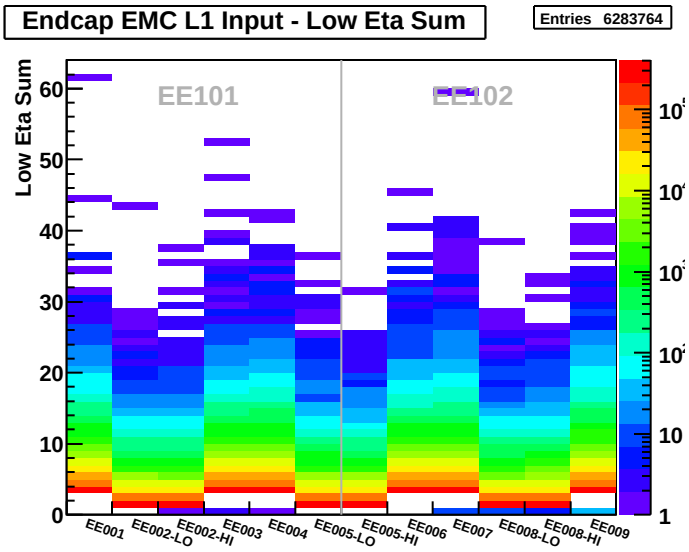

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

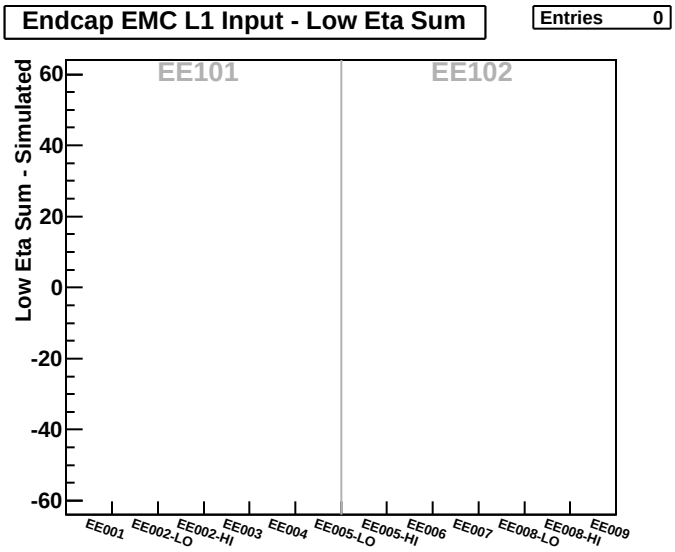

Barrel EMC L1 Input - High Eta Sum
Entries 1.885129e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

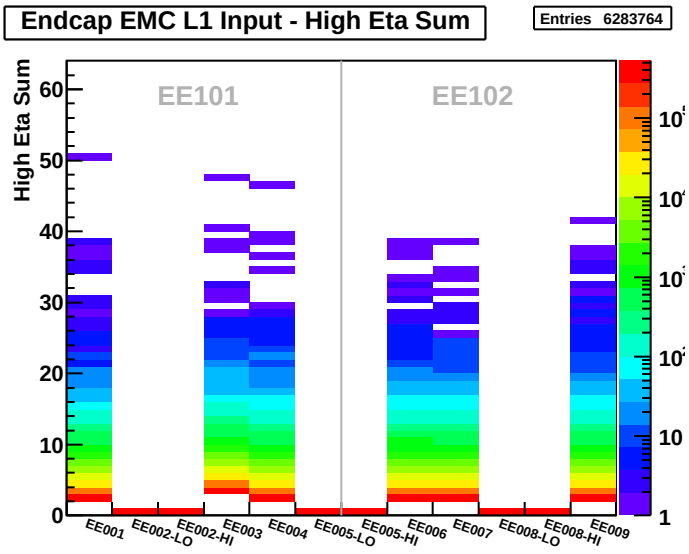

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

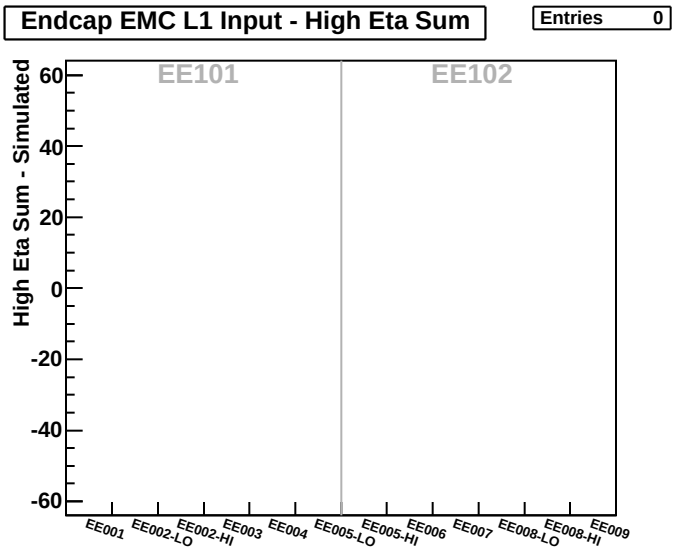

Barrel EMC L1 Input - High Tower Bits
Entries 1.885129e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

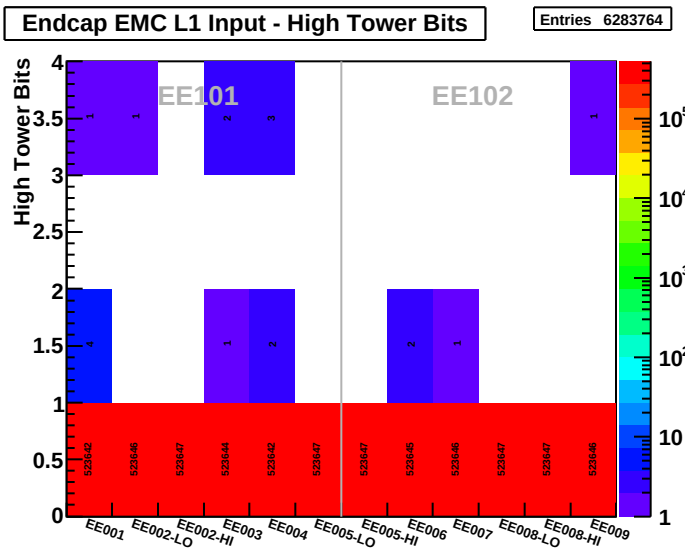

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

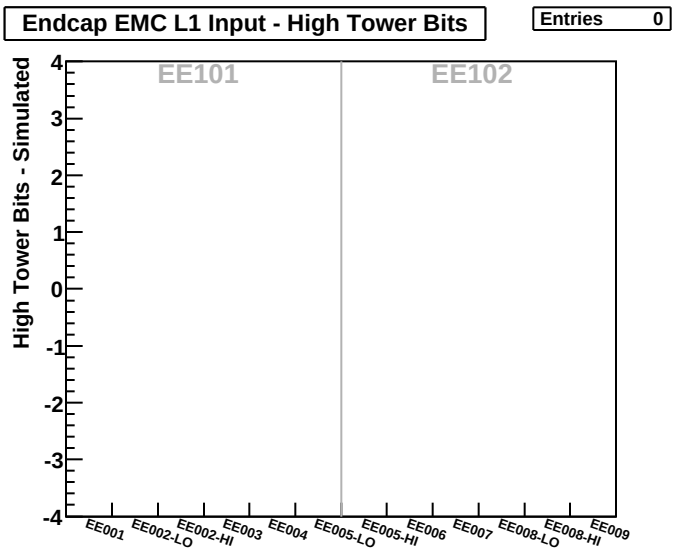

Endcap EMC L1 Input - Low Eta Sum
Entries 6283764
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

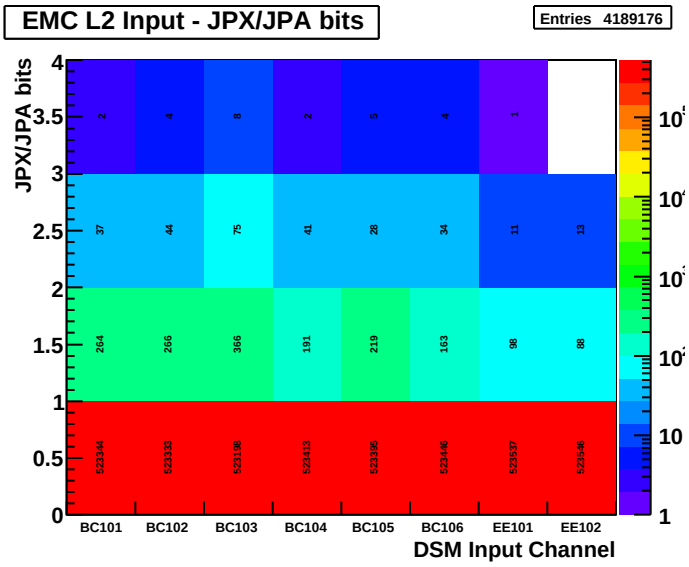

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

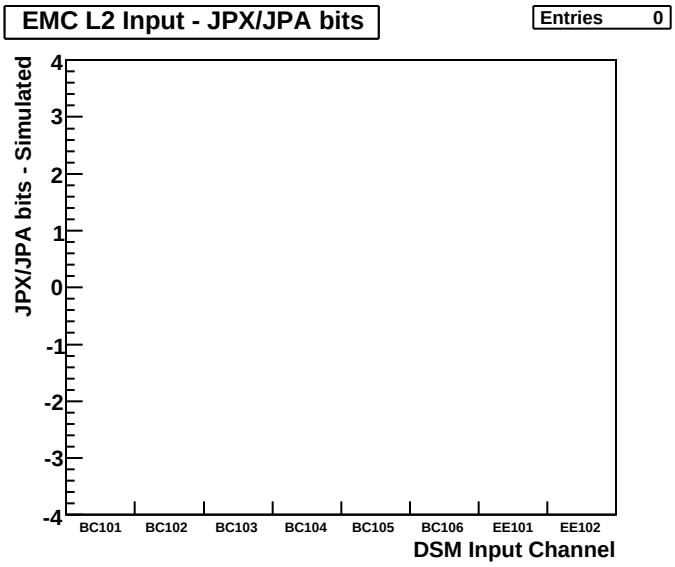

Endcap EMC L1 Input - High Eta Sum
Entries 6283764
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

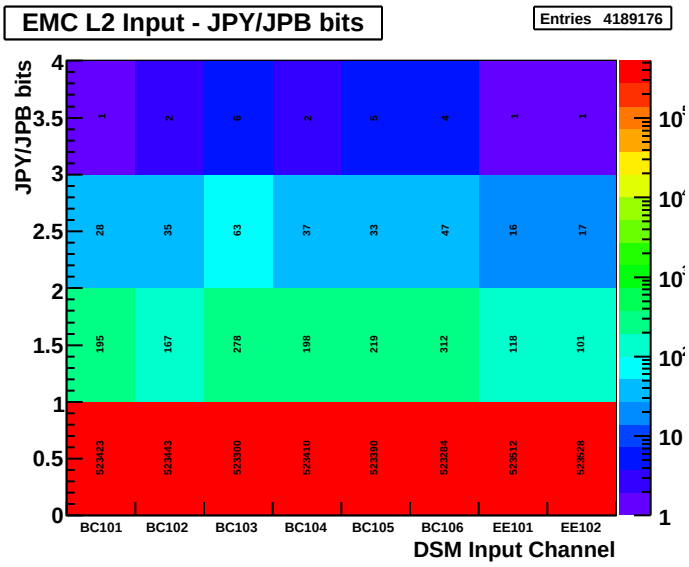

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

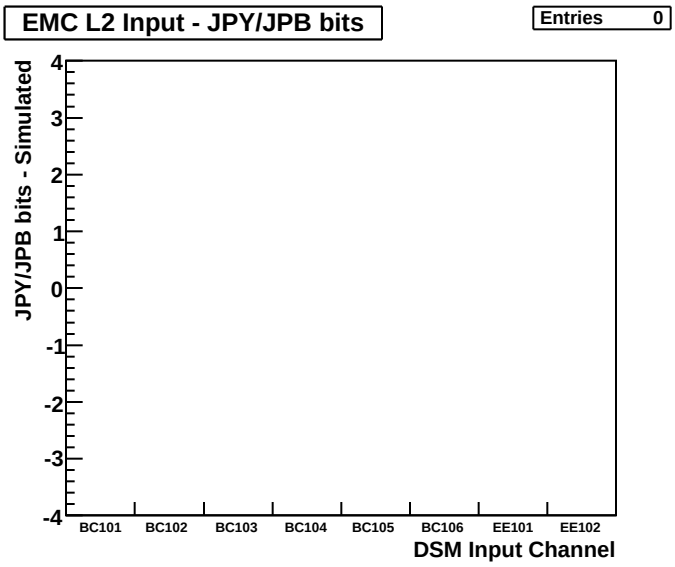

Endcap EMC L1 Input - High Tower Bits
Entries 6283764
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

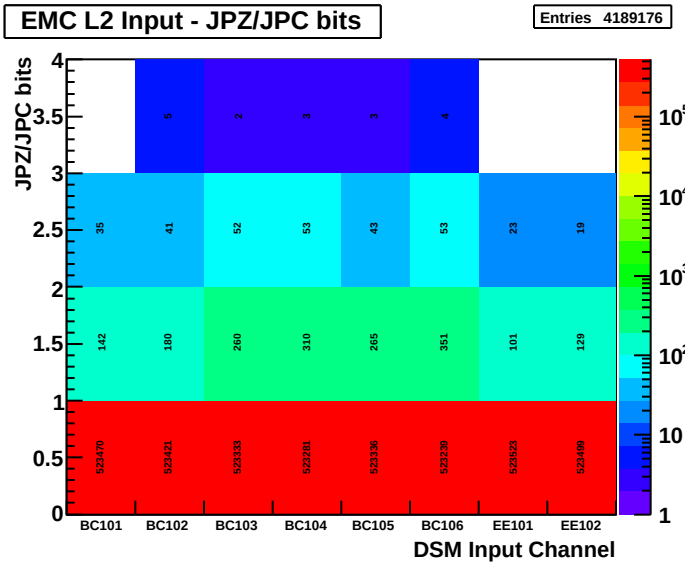

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

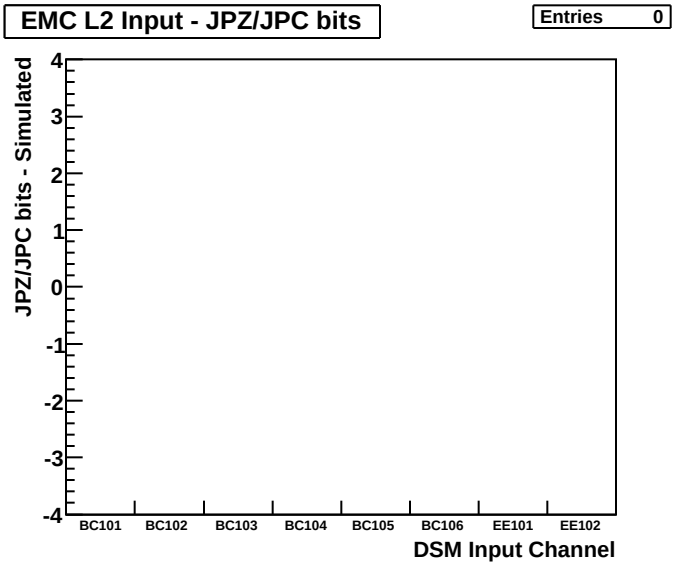

EMC L2 Input - JPX/JPA bits
Entries 4189176
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 4 8 2 5 4 1
37 44 75 41 28 34 11 13
264 266 366 191 219 163 98 88
523344 523333 522198 522413 523395 523446 523537 523546

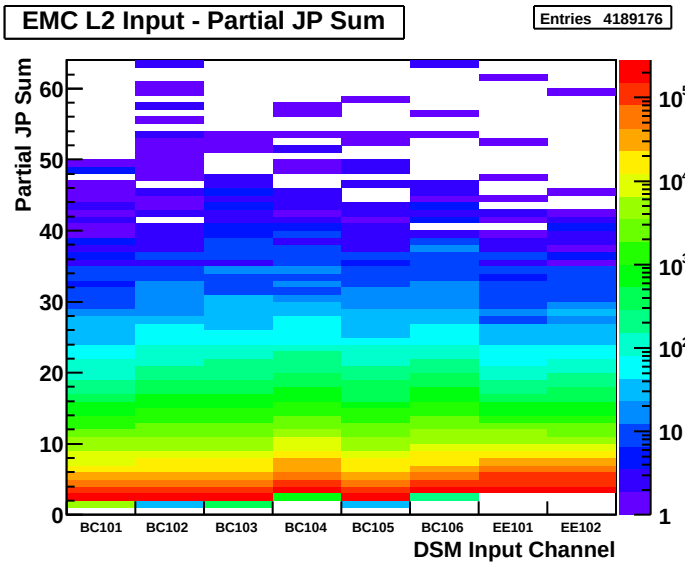

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

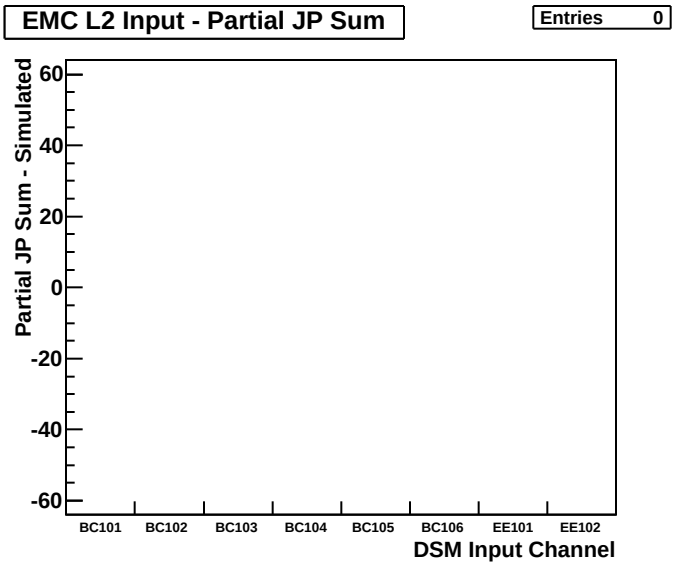

EMC L2 Input - JPY/JPB bits
Entries 4189176
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 2 6 2 5 4 1 1
28 35 63 37 33 47 16 17
195 167 278 198 219 312 118 101
523423 523443 522300 523410 523390 523284 523512 523528

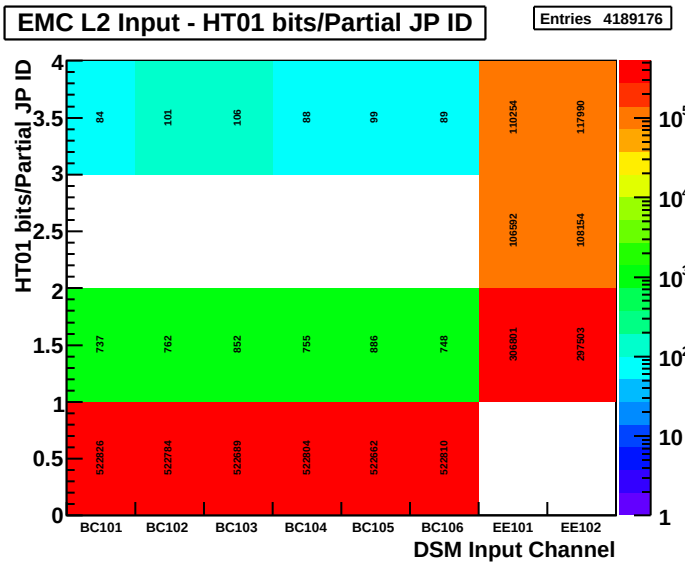

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

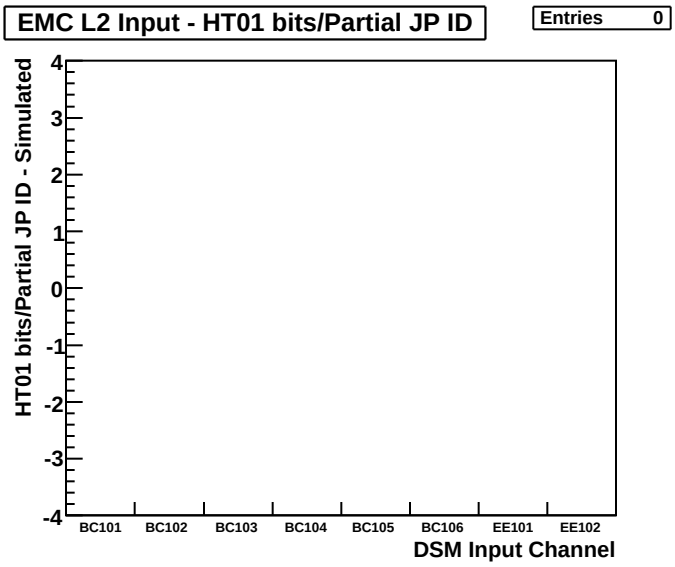

EMC L2 Input - JPZ/JPC bits
Entries 4189176
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5 2 3 3 4
35 41 52 53 43 53 23 19
142 180 260 310 265 351 101 129
523470 523421 523333 523281 523336 523239 523523 523499

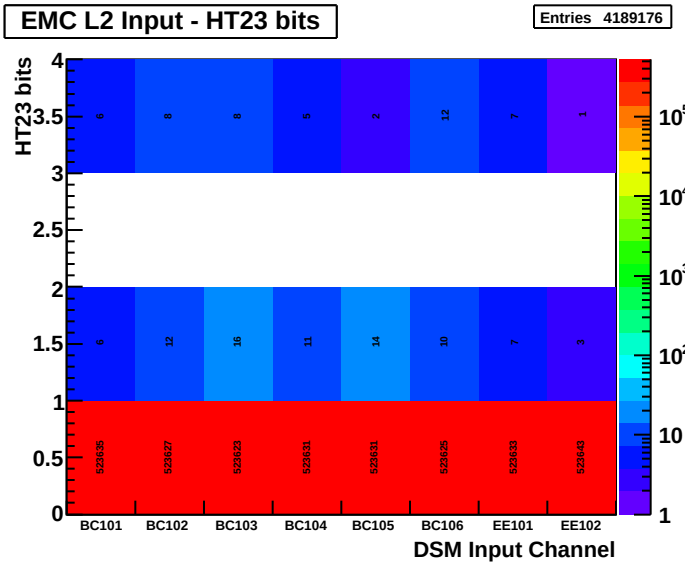

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

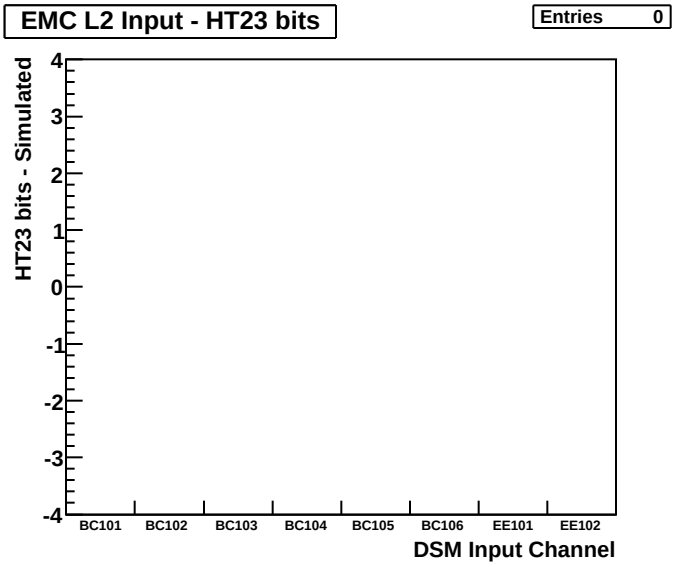

EMC L2 Input - Partial JP Sum
Entries 4189176
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

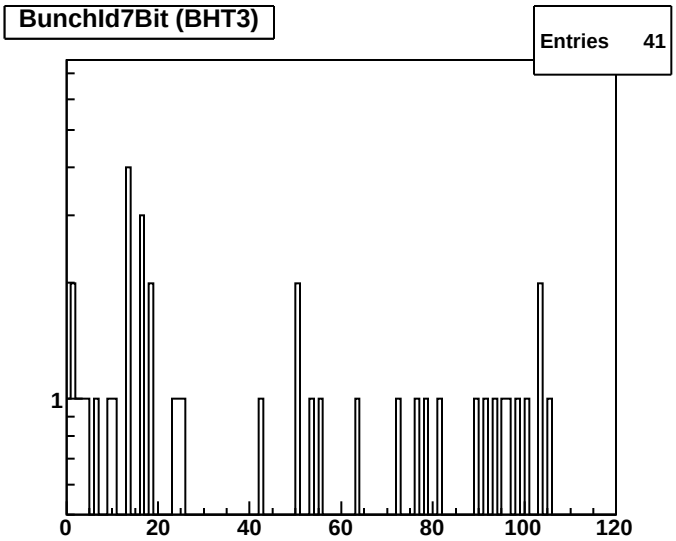

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

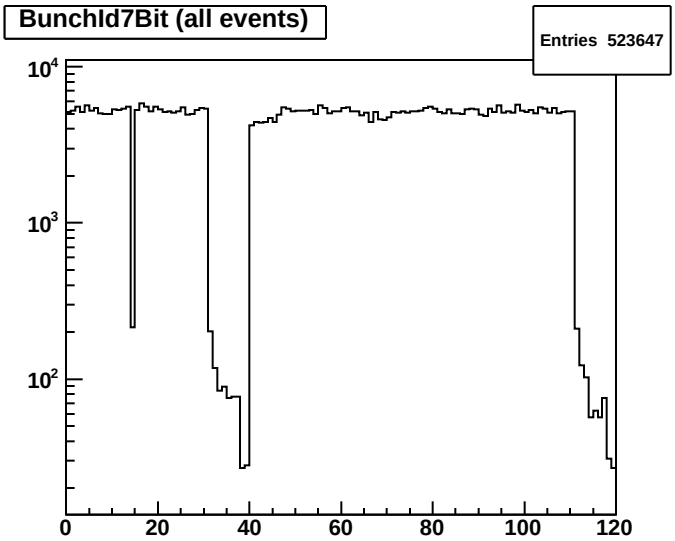

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4189176
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

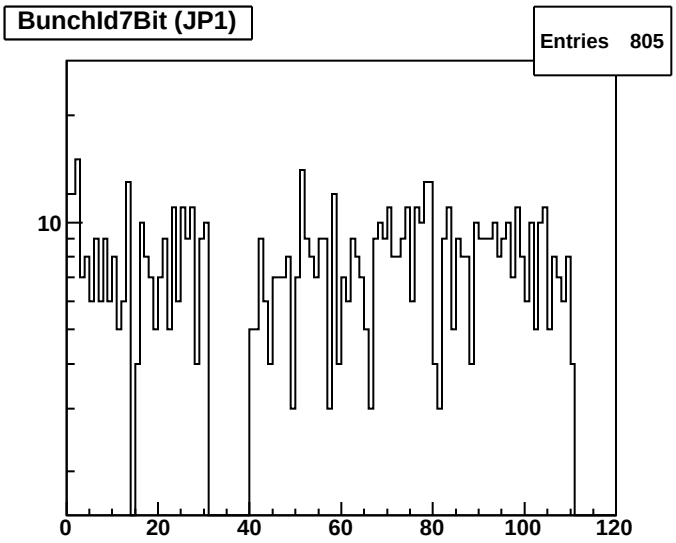

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

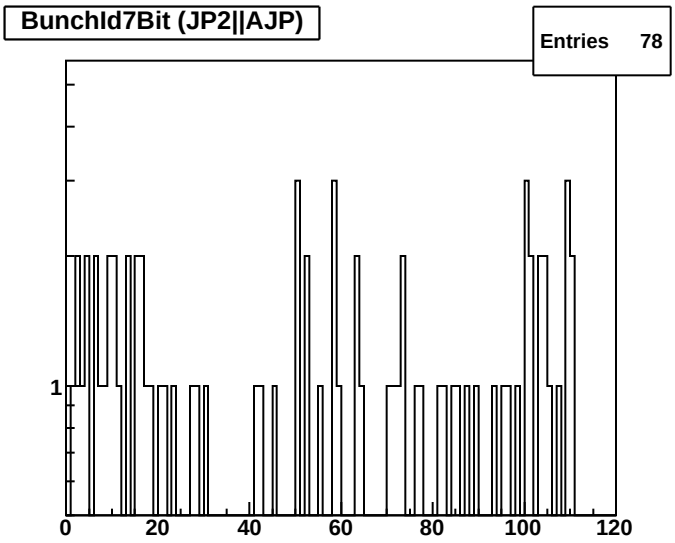

EMC L2 Input - HT23 bits
Entries 4189176
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

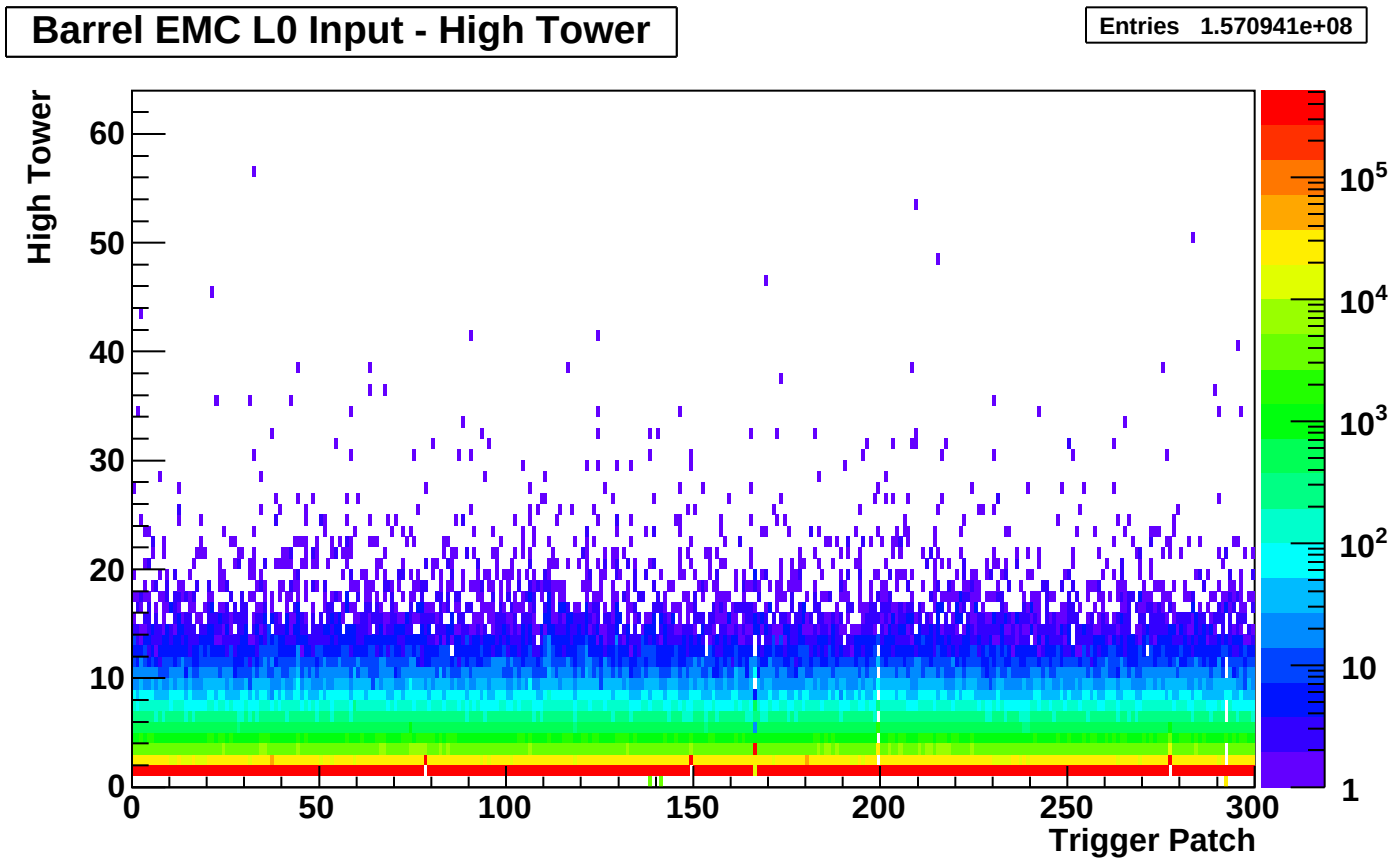

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

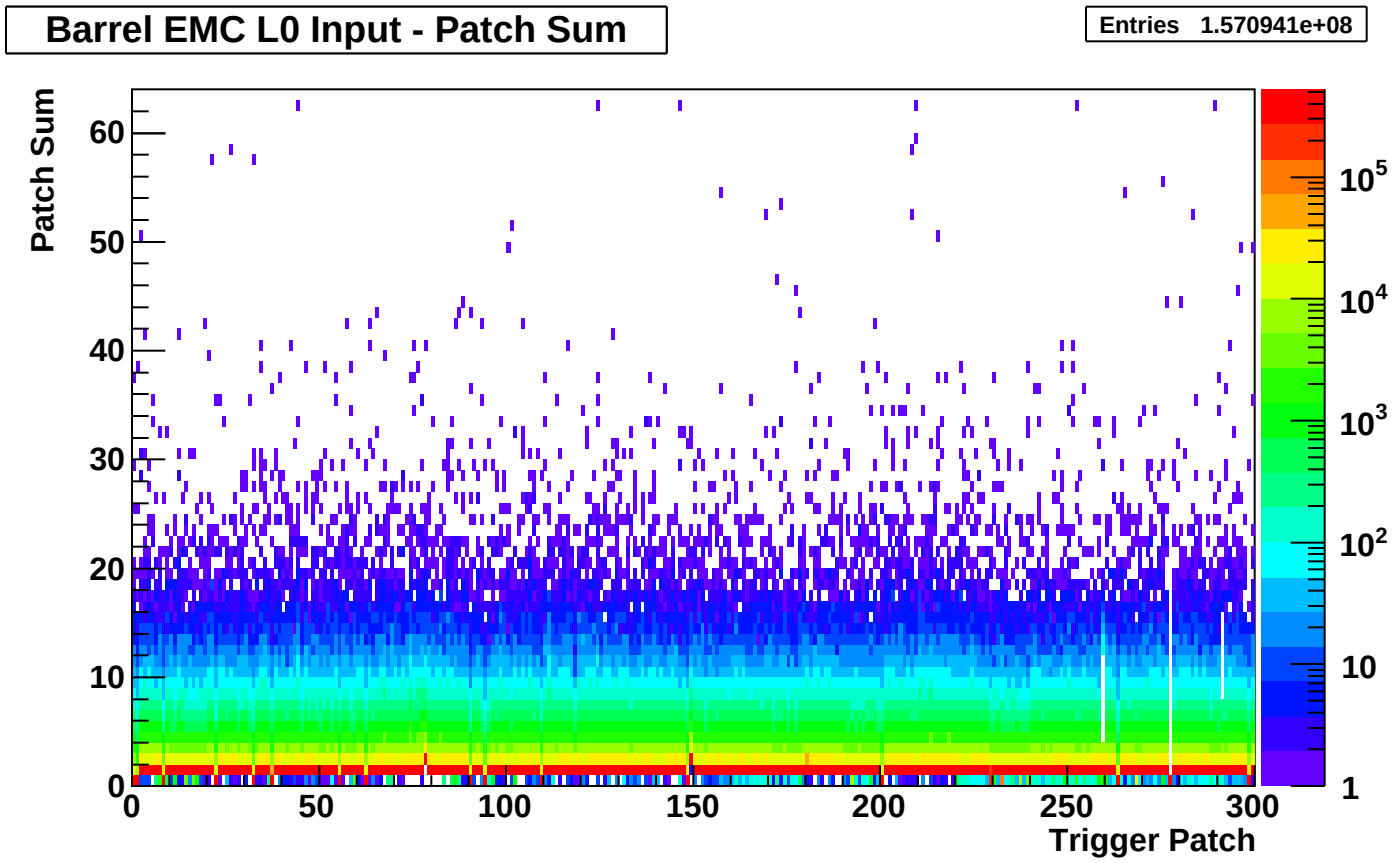

BunchId7Bit (BHT3)
Entries 41
1
0
20
40
60
80
100
120

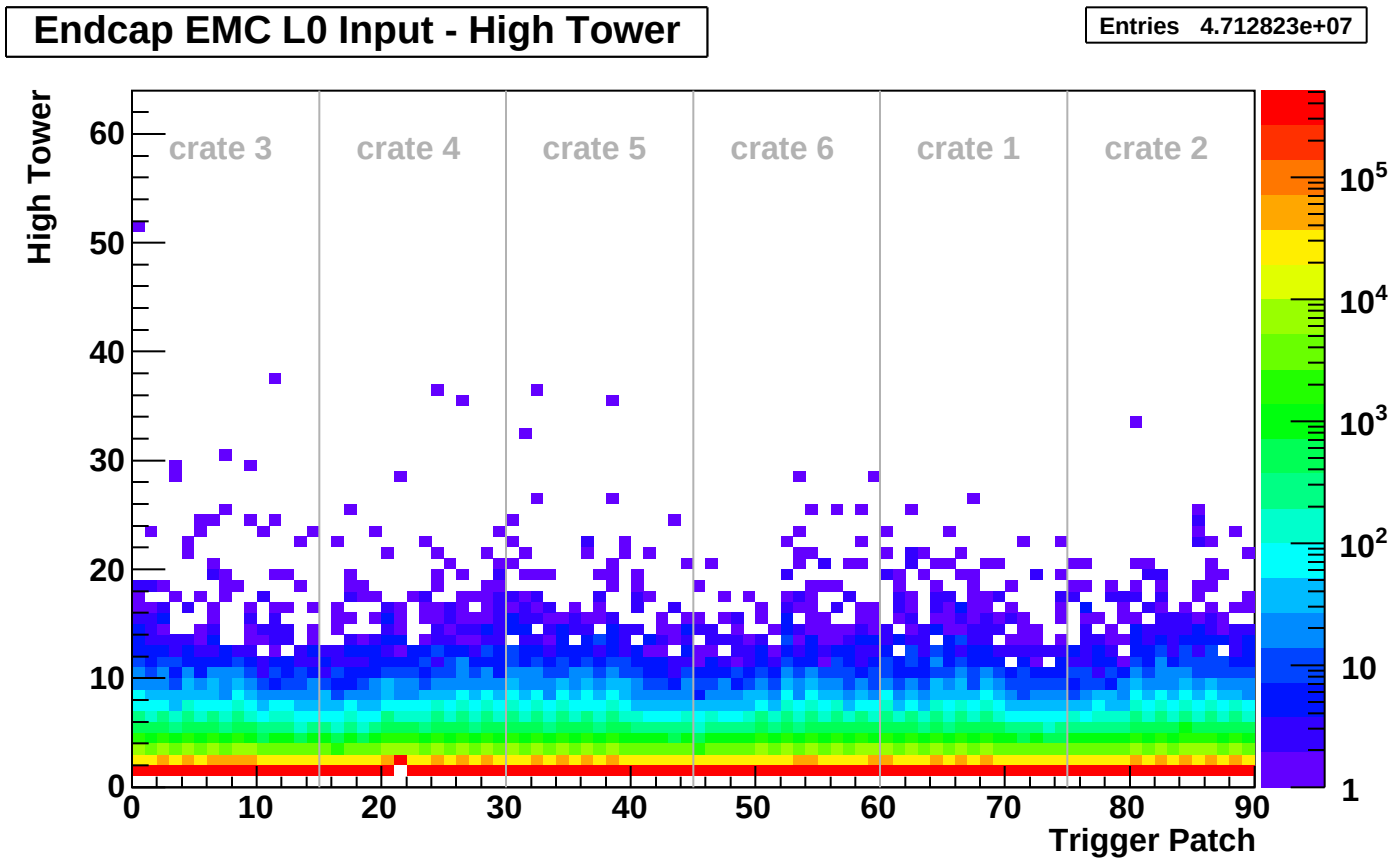

BunchId7Bit (all events)
Entries 523647
10^4
10^3
10^2
0
20
40
60
80
100
120

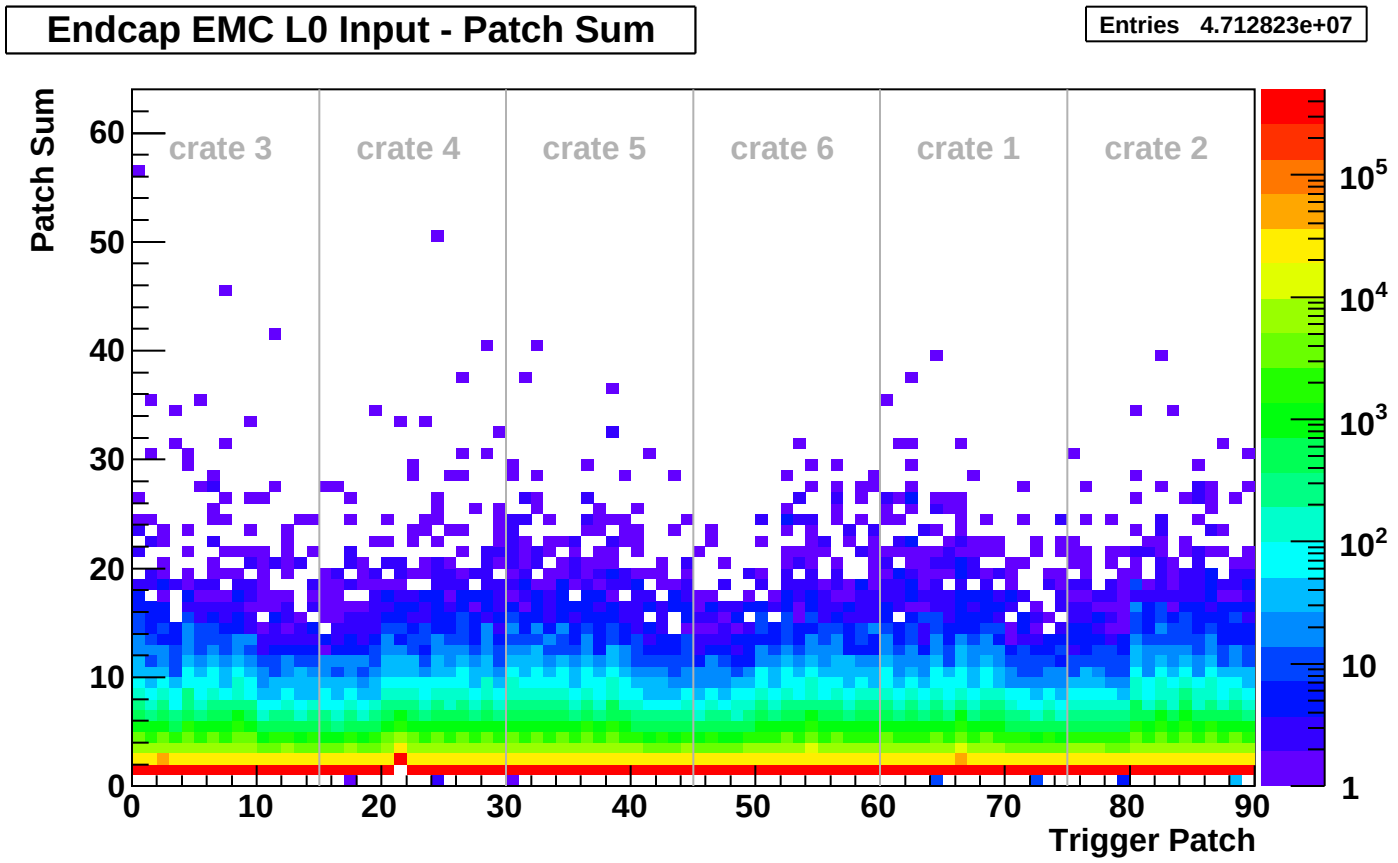

BunchId7Bit (JP1)
Entries 805
10
0
20
40
60
80
100
120

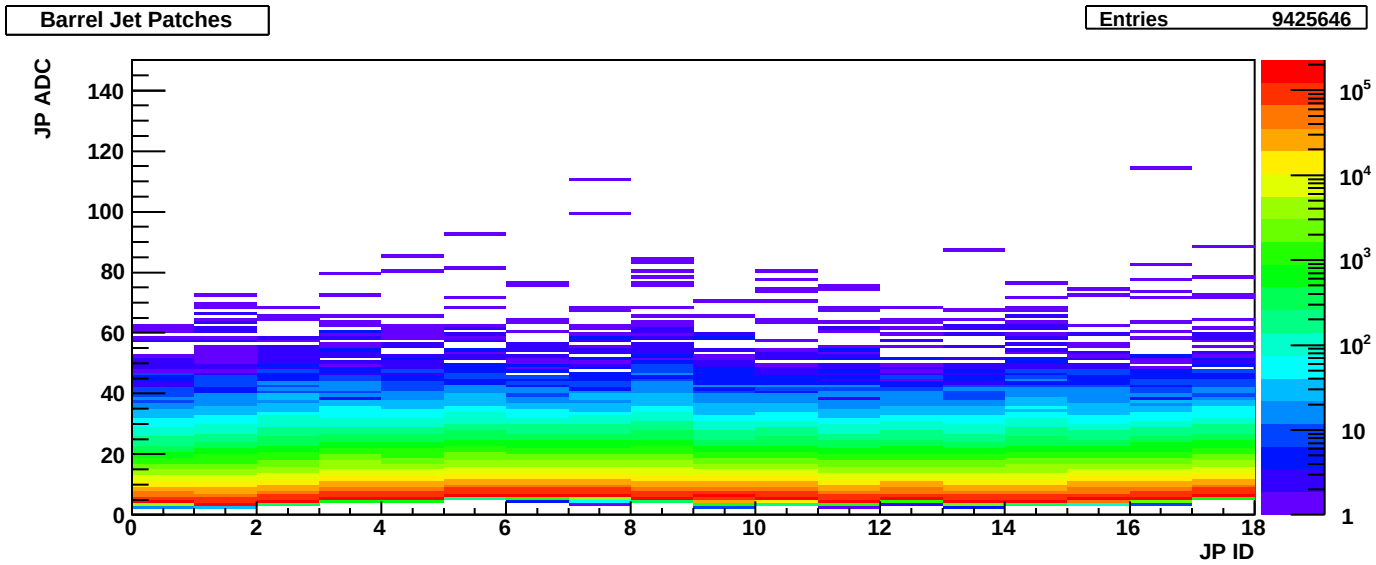

BunchId7Bit (JP2||AJP)
Entries 78
1
0
20
40
60
80
100
120

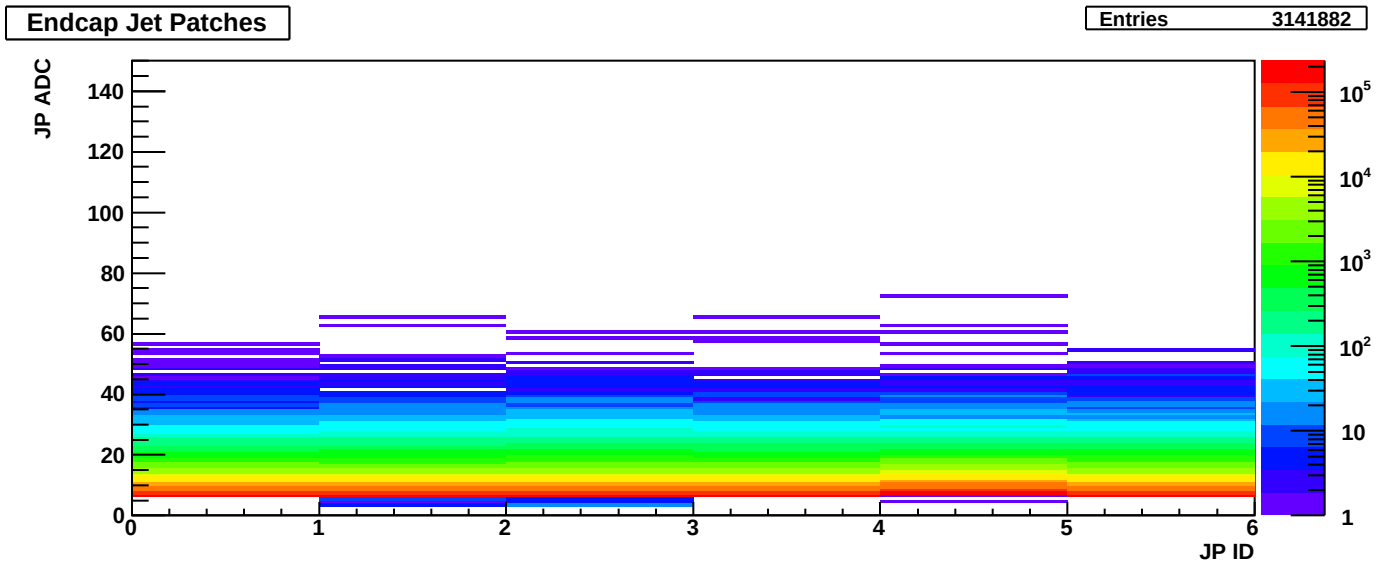

Barrel EMC L0 Input - High Tower
Entries 1.570941e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

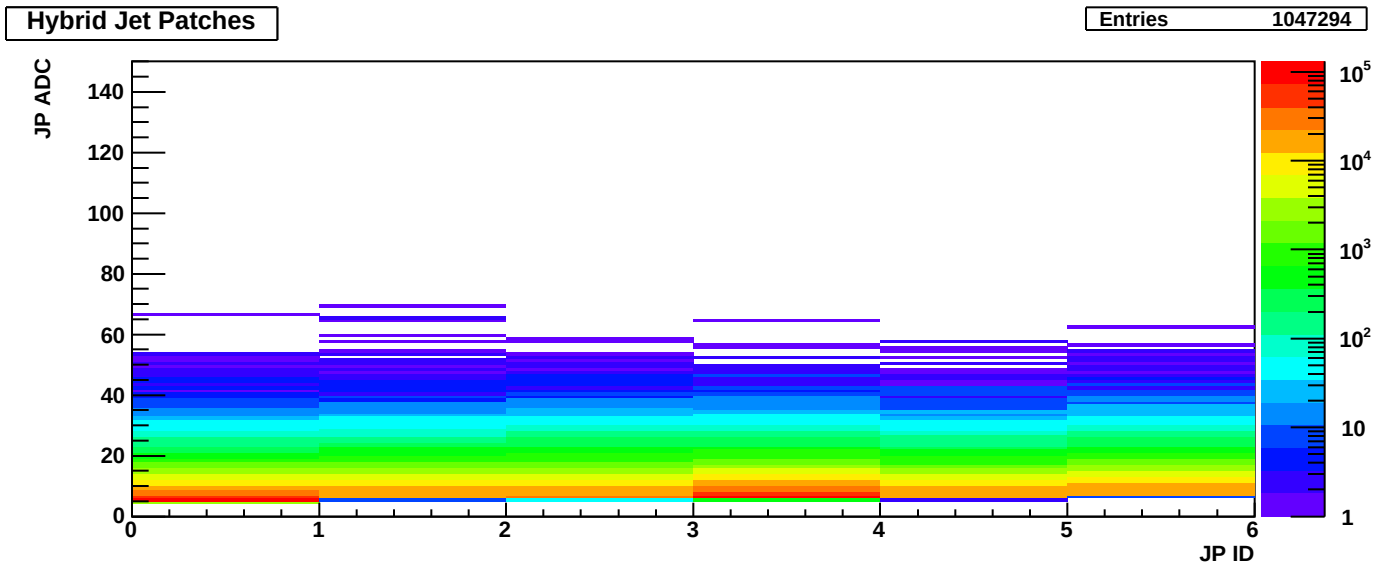

Barrel EMC L0 Input - Patch Sum
Entries 1.570941e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

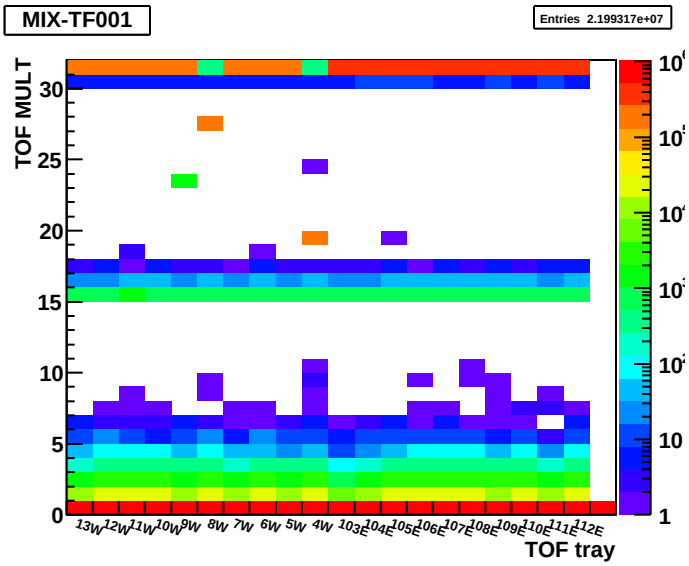

Endcap EMC L0 Input - High Tower
Entries 4.712823e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

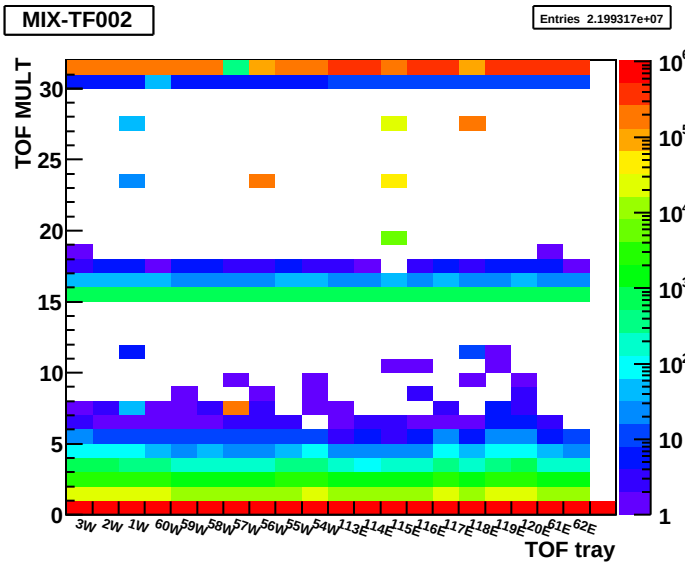

Endcap EMC L0 Input - Patch Sum
Entries 4.712823e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

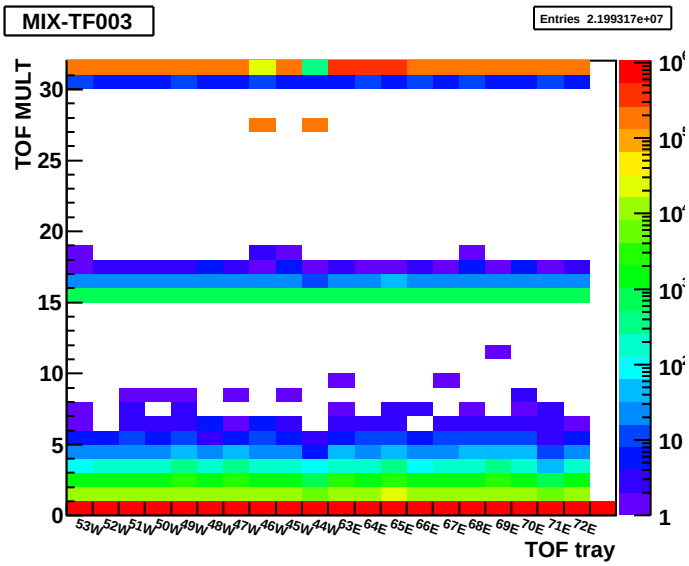

Barrel Jet Patches
Entries 9425646
JP ADC
JP ID

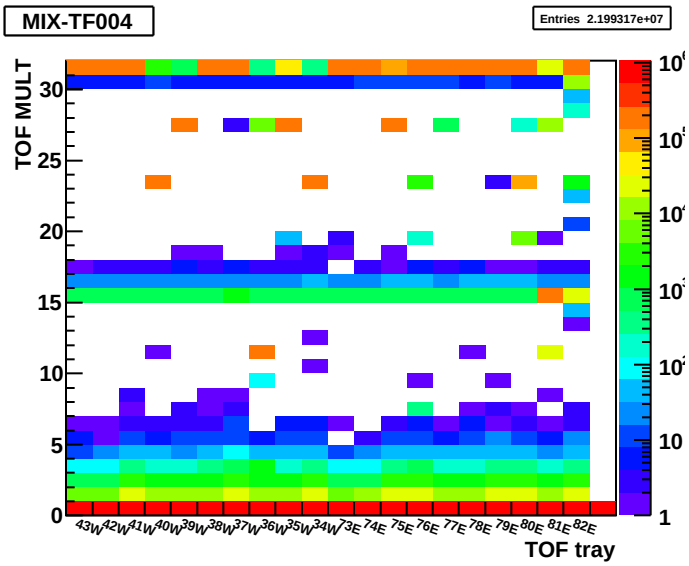

Endcap Jet Patches
Entries 3141882
JP ADC
JP ID

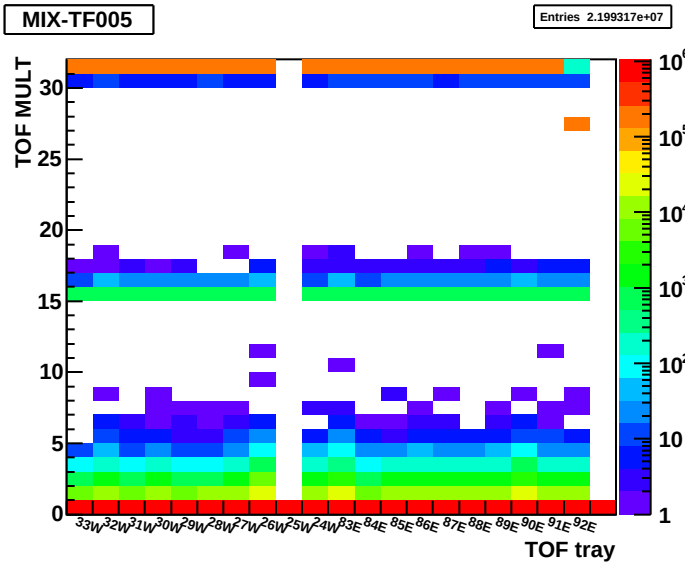

Hybrid Jet Patches
Entries 1047294
JP ADC
JP ID

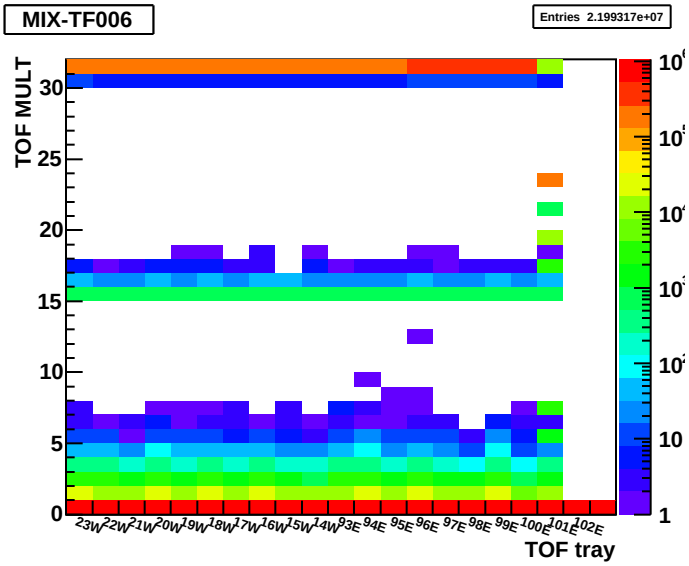

MIX-TF001
Entries 2.199317e+07
TOF MULT
TOF tray

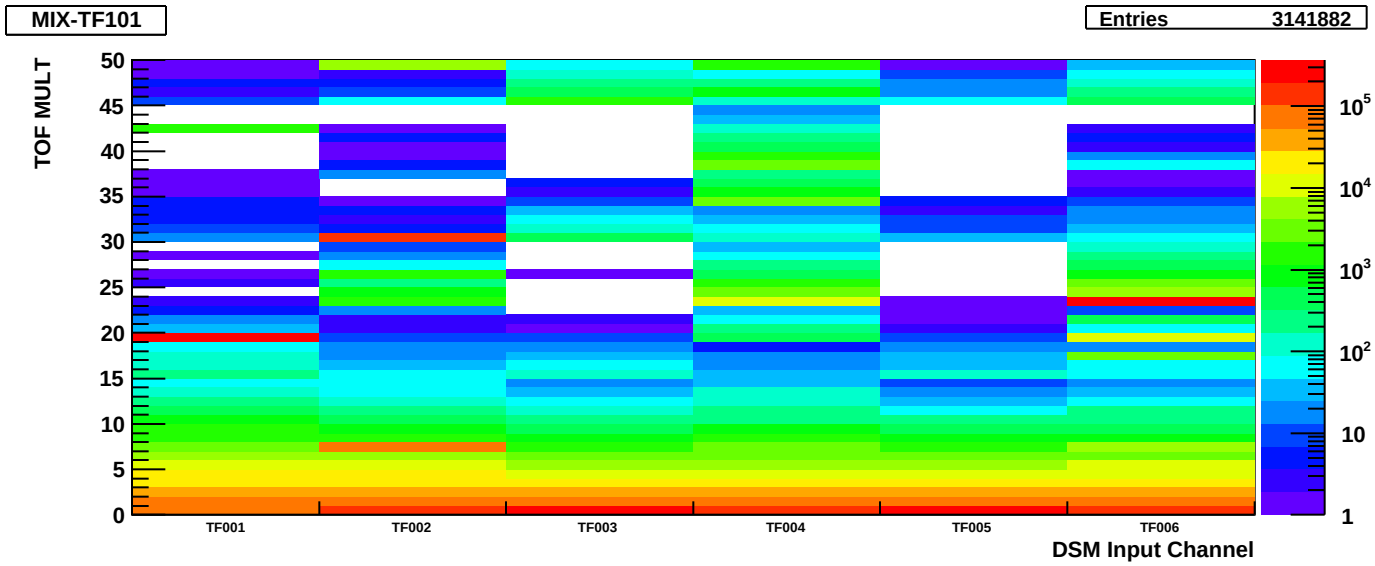

MIX-TF002
Entries 2.199317e+07
TOF MULT
TOF tray

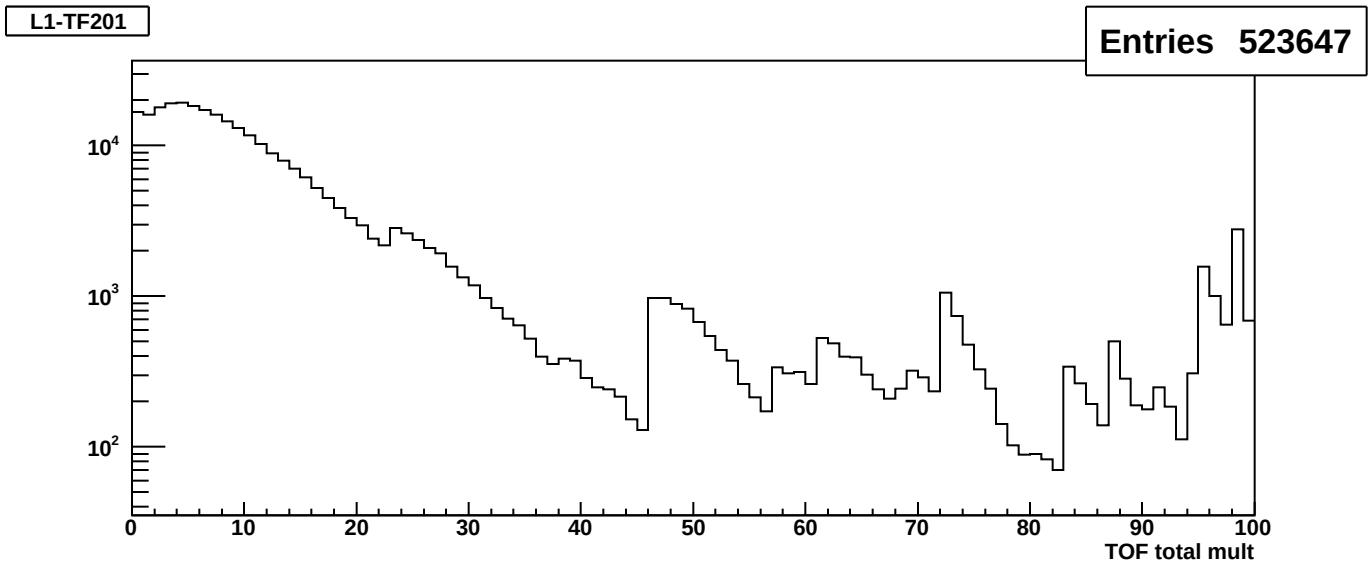

MIX-TF003
Entries 2.199317e+07
TOF MULT
TOF tray

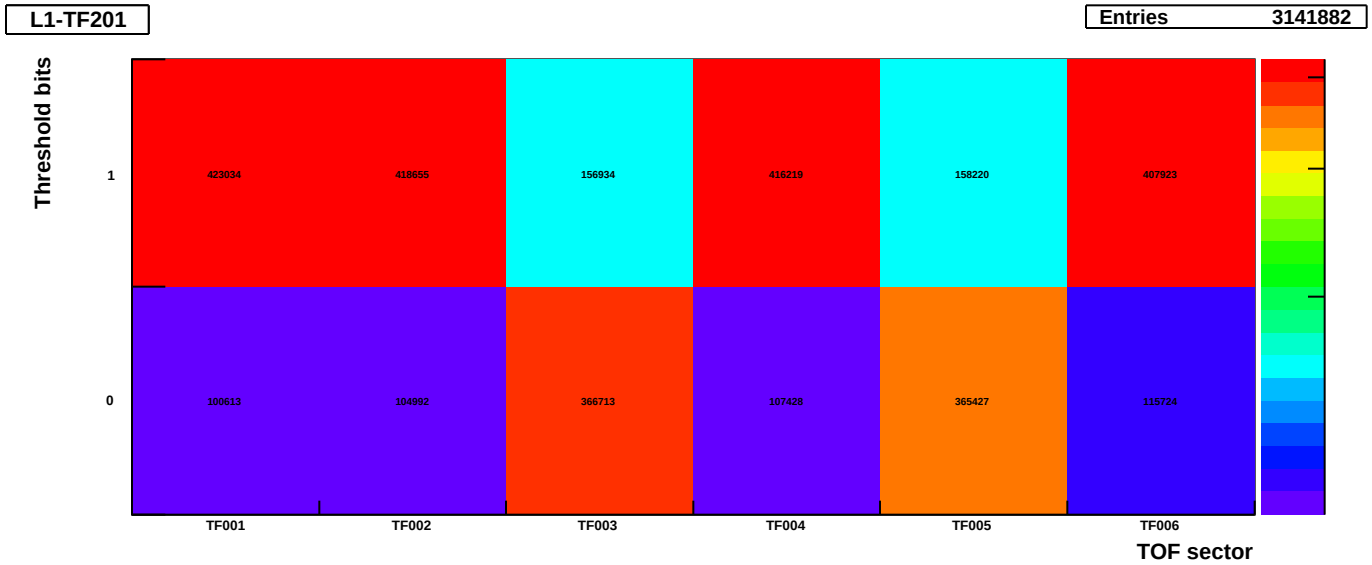

MIX-TF004
Entries 2.199317e+07
TOF MULT
TOF tray

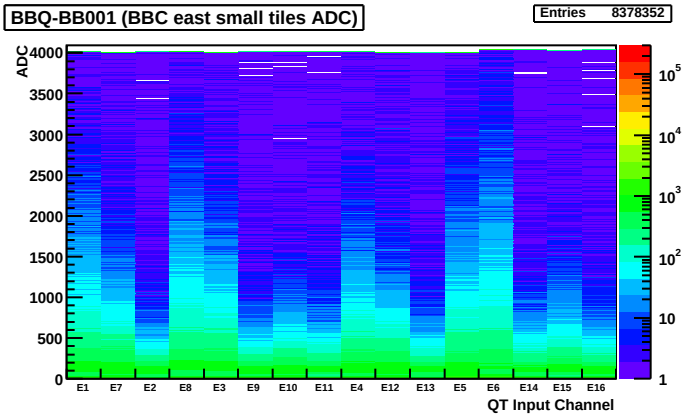

MIX-TF005
Entries 2.199317e+07
TOF MULT
TOF tray

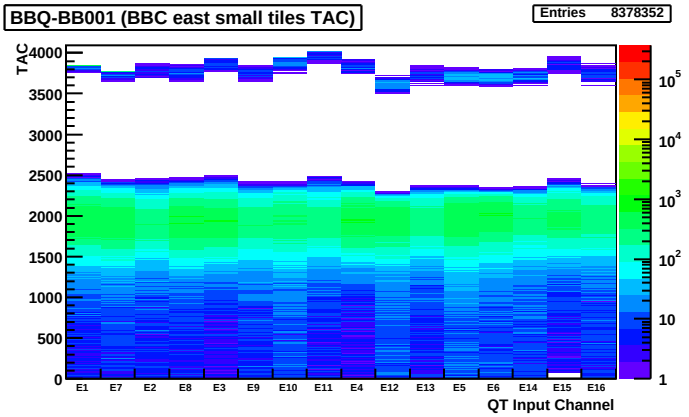

MIX-TF006
Entries 2.199317e+07
TOF MULT
TOF tray

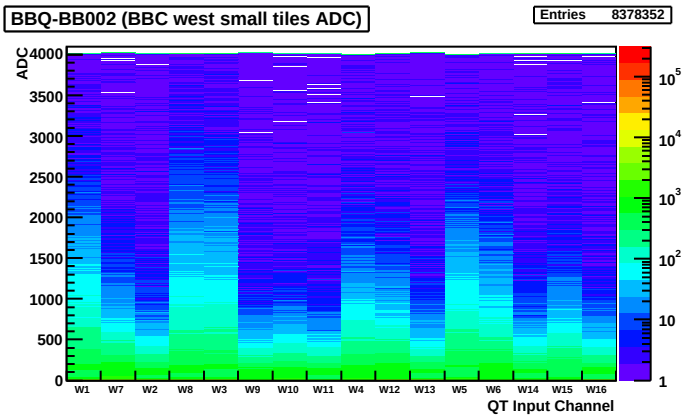

MIX-TF101
Entries 3141882
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

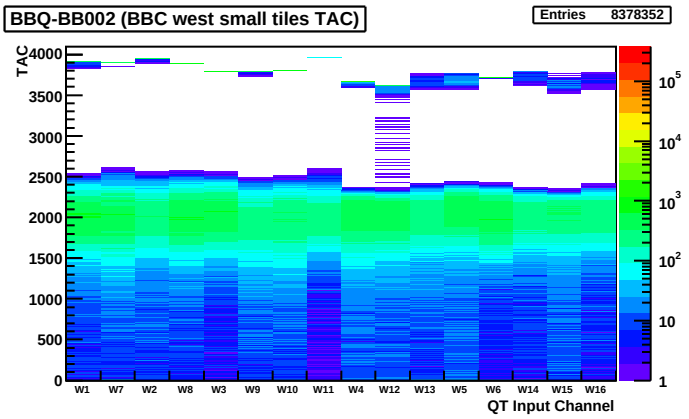

L1-TF201
Entries 523647
TOF total mult

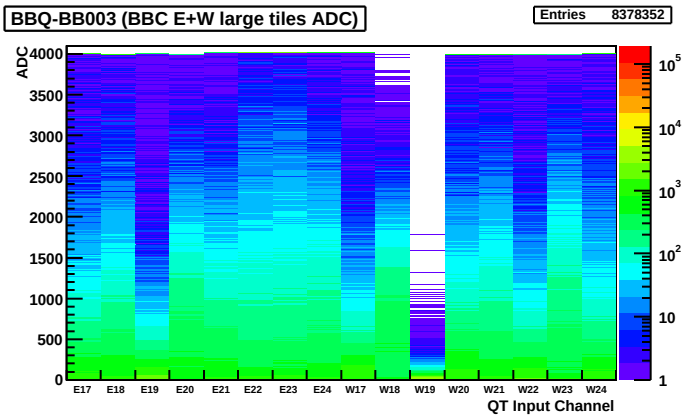

L1-TF201
Entries 3141882
Threshold bits
423034
418655
156934
416219
158220
407923
100613
104992
366713
107428
365427
115724
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

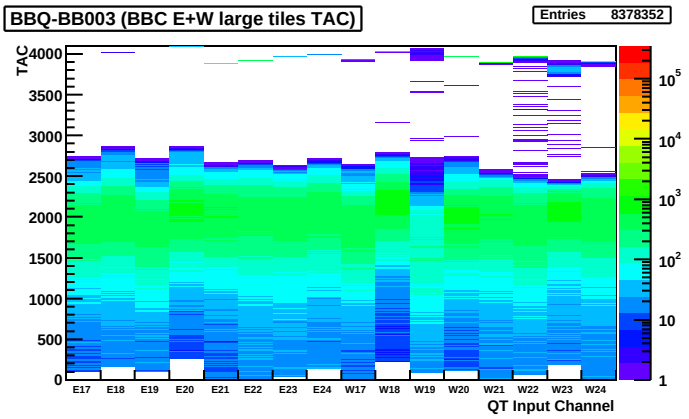

BBQ-BB001 (BBC east small tiles ADC)
Entries 8378352
ADC
QT Input Channel

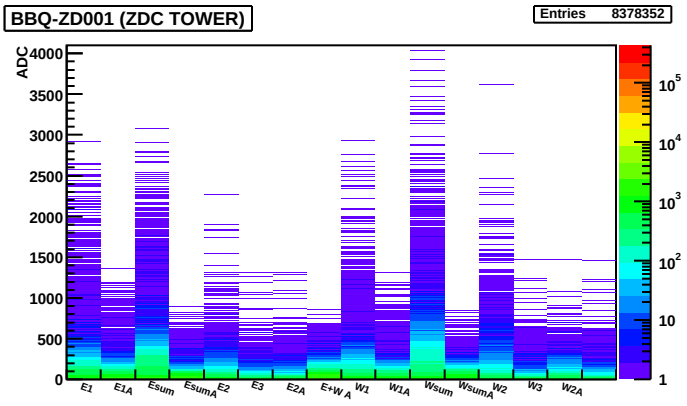

BBQ-BB001 (BBC east small tiles TAC)
Entries 8378352
TAC
QT Input Channel

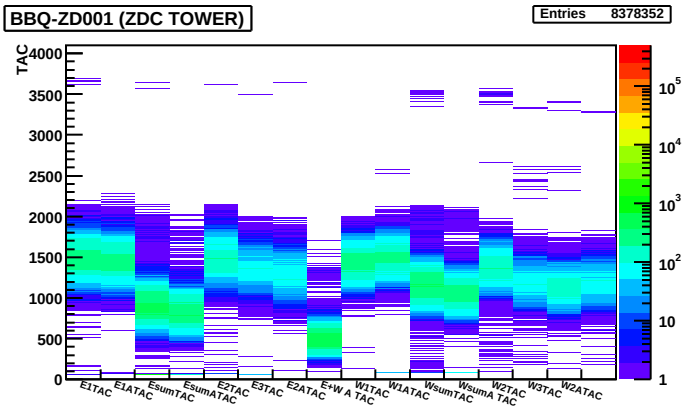

BBQ-BB002 (BBC west small tiles ADC)
Entries 8378352
ADC
QT Input Channel

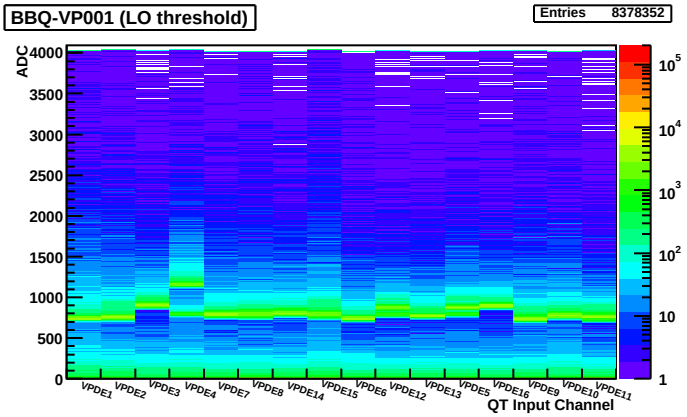

BBQ-BB002 (BBC west small tiles TAC)
Entries 8378352
TAC
QT Input Channel

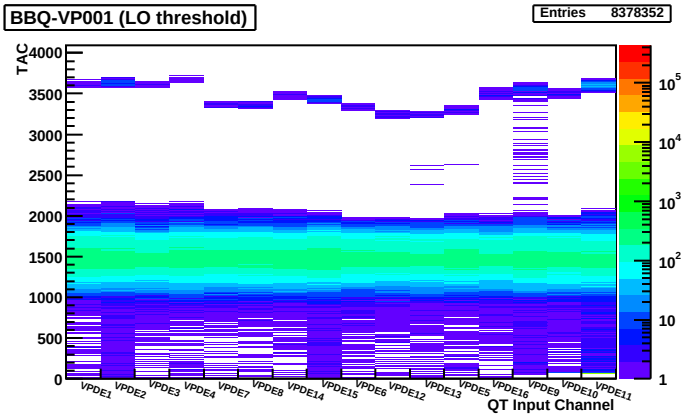

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8378352
ADC
QT Input Channel

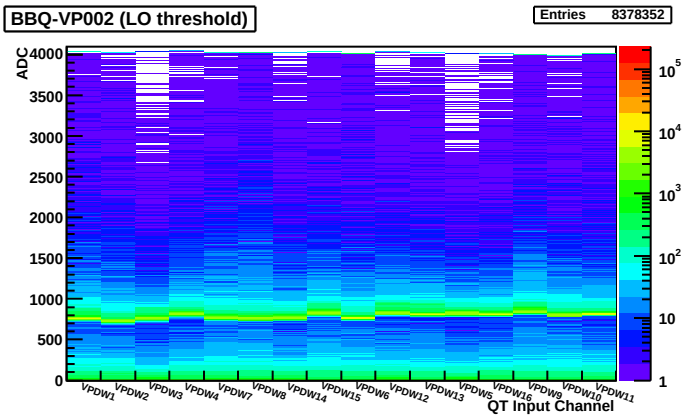

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8378352
TAC
QT Input Channel

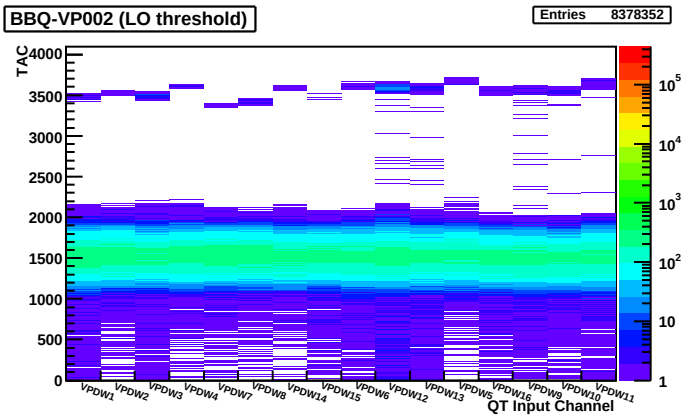

BBQ-ZD001 (ZDC TOWER)
Entries 8378352
ADC

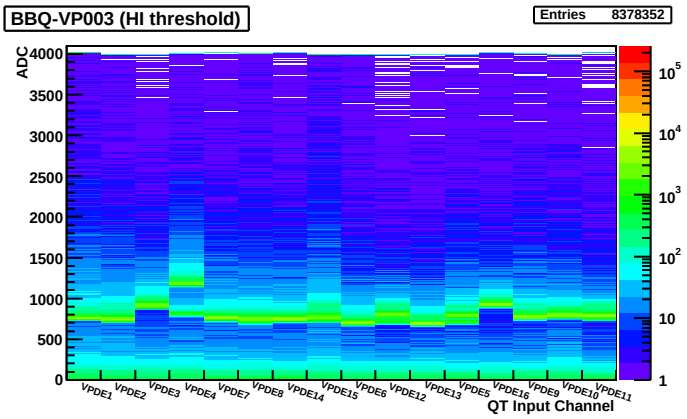

BBQ-ZD001 (ZDC TOWER)
Entries 8378352
TAC

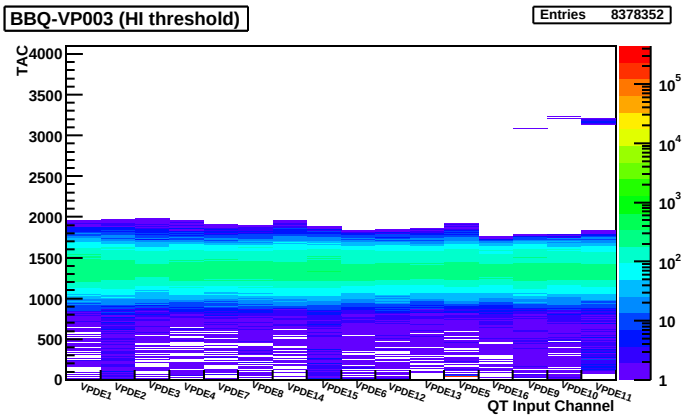

BBQ-VP001 (LO threshold)
Entries 8378352
ADC
QT Input Channel

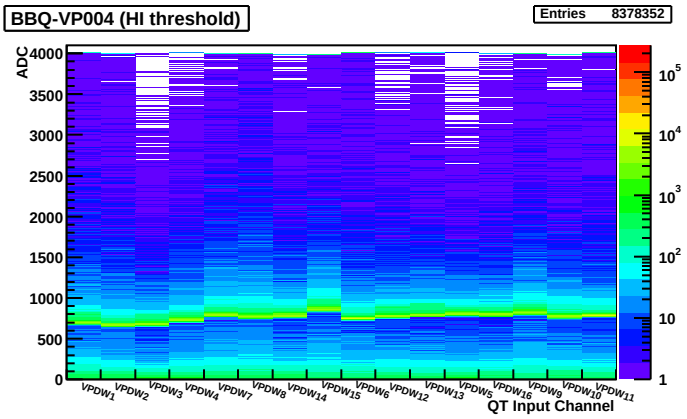

BBQ-VP001 (LO threshold)
Entries 8378352
TAC
QT Input Channel

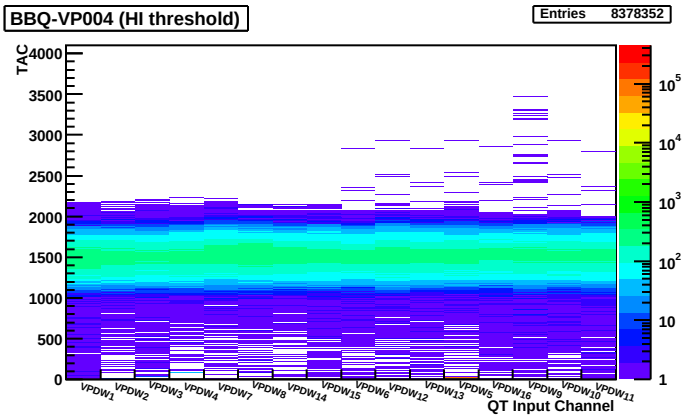

BBQ-VP002 (LO threshold)
Entries 8378352
ADC
QT Input Channel

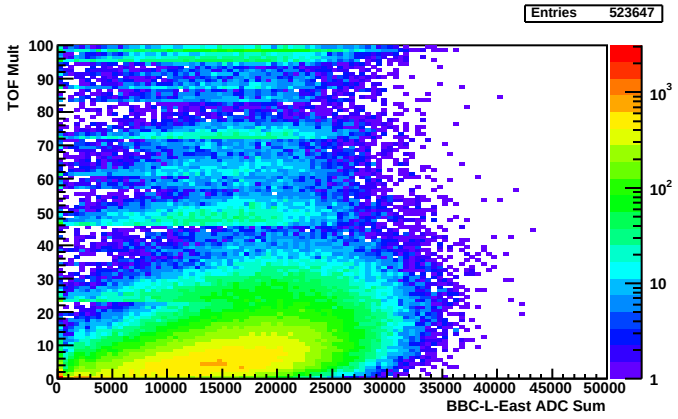

BBQ-VP002 (LO threshold)
Entries 8378352
TAC
QT Input Channel

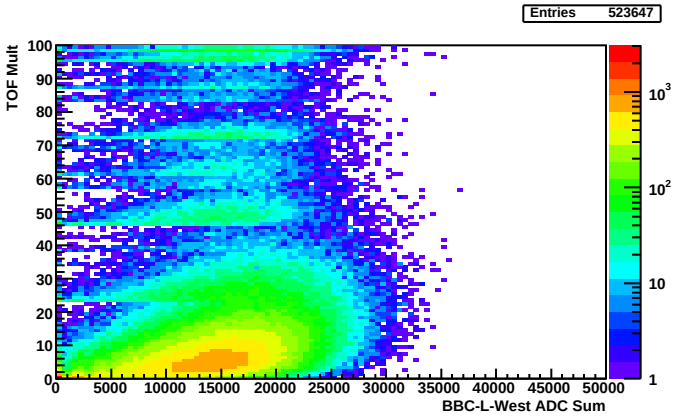

BBQ-VP003 (HI threshold)
Entries 8378352
ADC
QT Input Channel

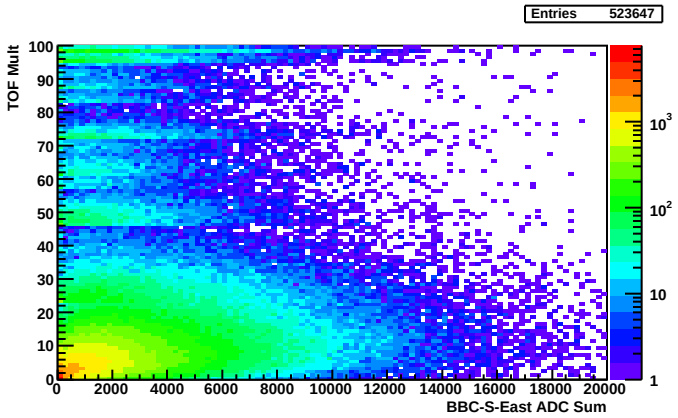

BBQ-VP003 (HI threshold)
Entries 8378352
TAC
QT Input Channel

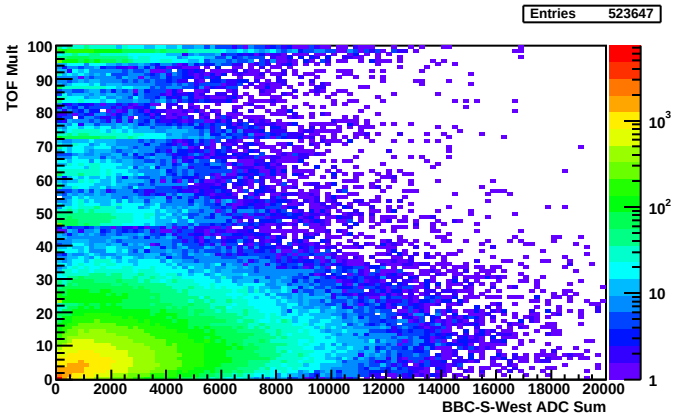

BBQ-VP004 (HI threshold)
Entries 8378352
ADC
QT Input Channel

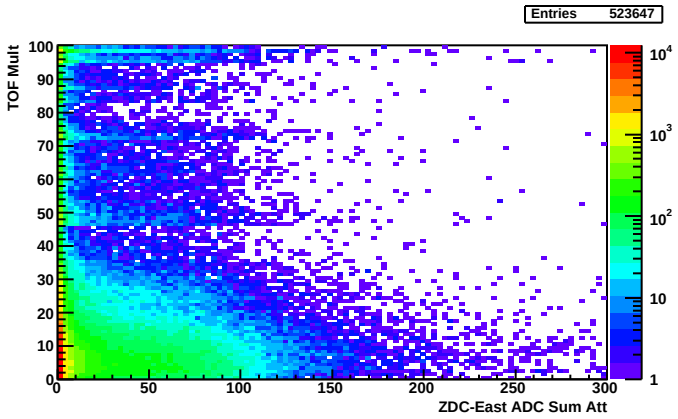

BBQ-VP004 (HI threshold)
Entries 8378352
TAC
QT Input Channel

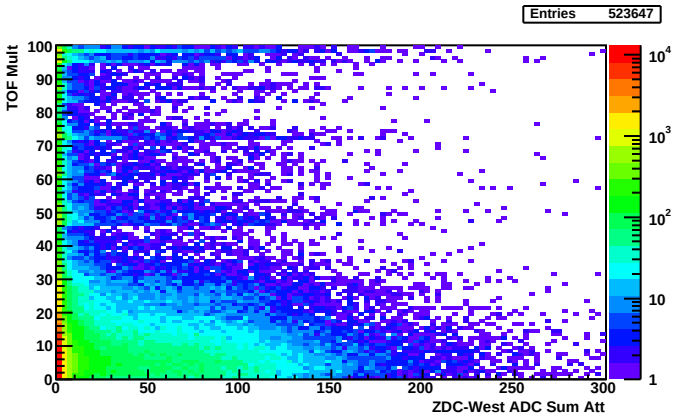

Entries 523647
TOF Mult
BBC-L-East ADC Sum

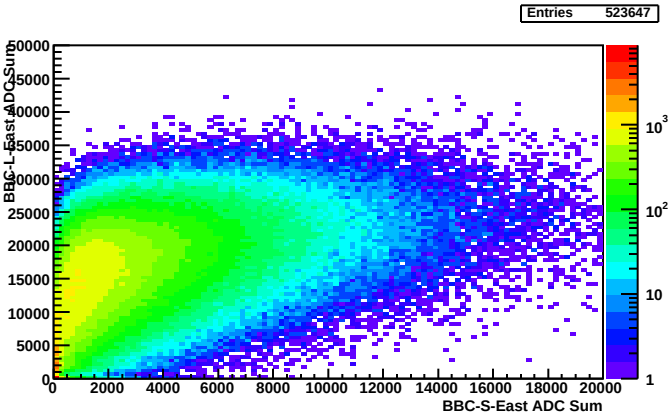

Entries 523647
TOF Mult
BBC-L-West ADC Sum

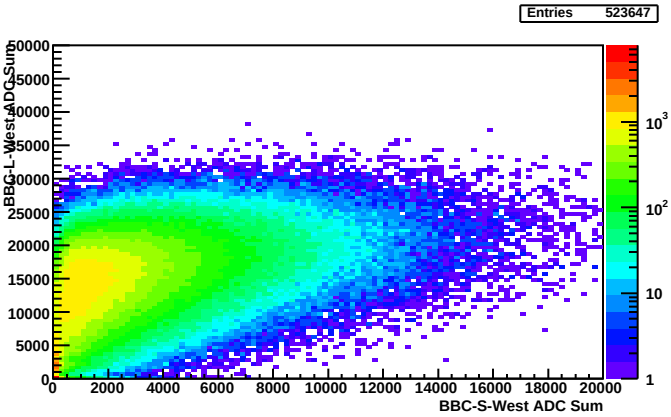

Entries 523647
TOF Mult
BBC-S-East ADC Sum

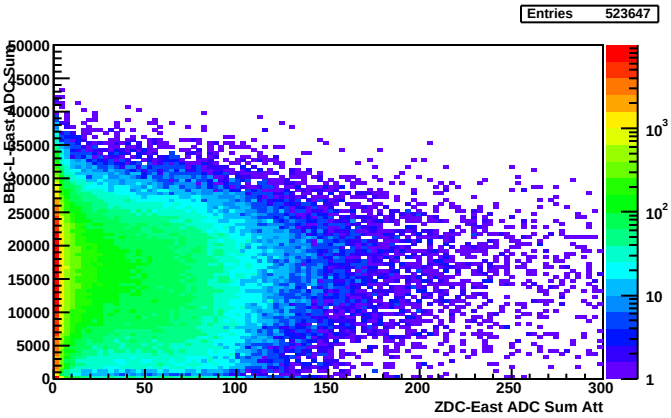

Entries 523647
TOF Mult
BBC-S-West ADC Sum


Entries 523647
TOF Mult
ZDC-East ADC Sum Att


Entries 523647
TOF Mult
ZDC-West ADC Sum Att


Entries 523647
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 523647
BBC-L-West ADC Sum
BBC-S-West ADC Sum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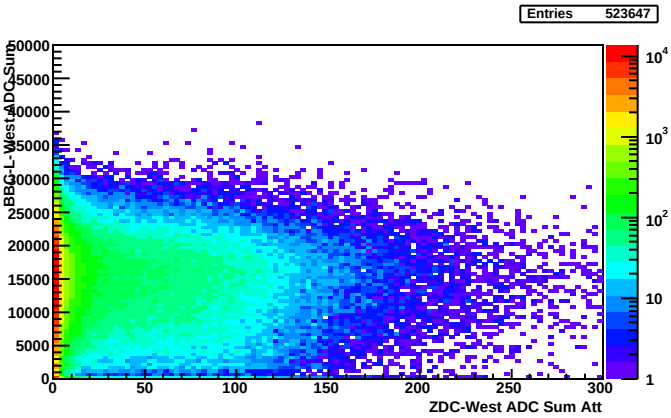
Entries 523647
BBC-L-West ADC Sum
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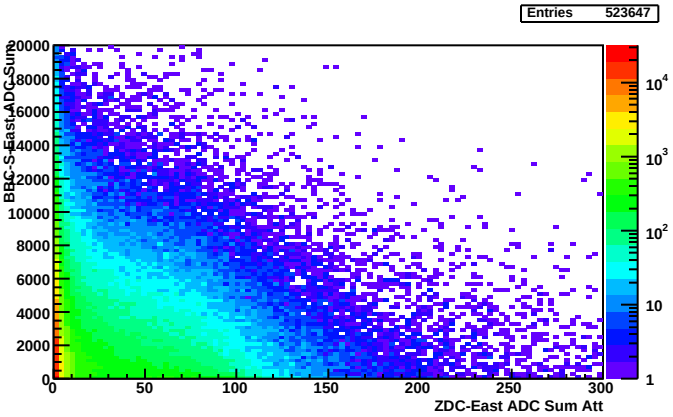
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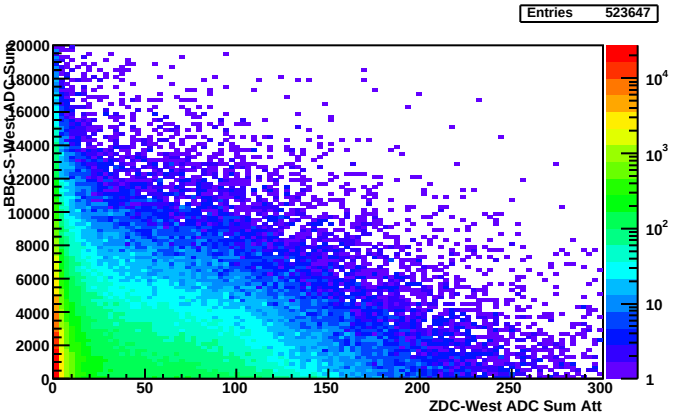
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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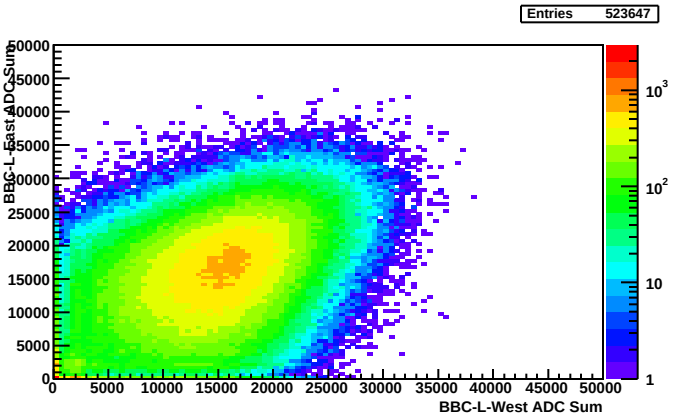
ZDC-West ADC Sum Att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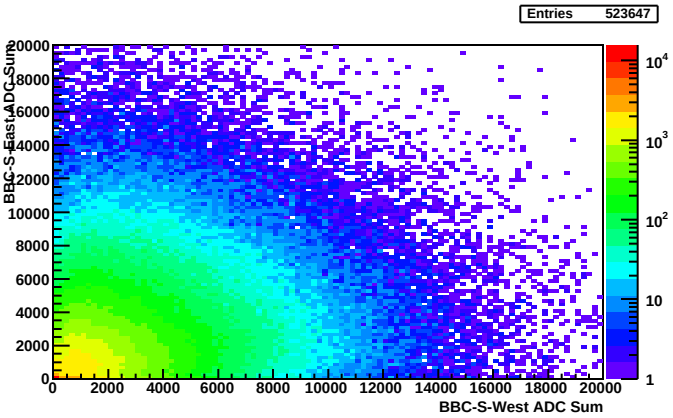
BBC-L-East ADC Sum
BBC-L-West ADC Sum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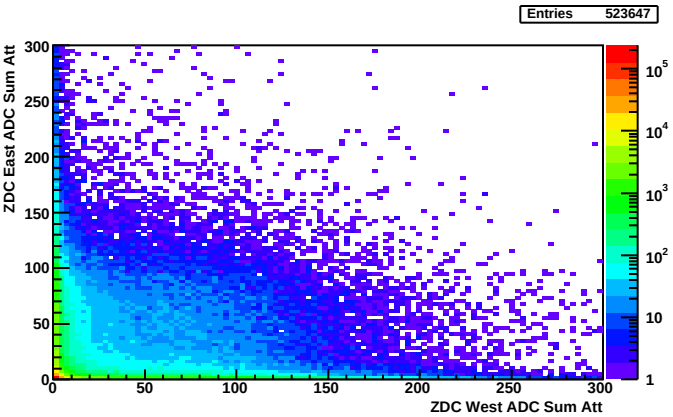
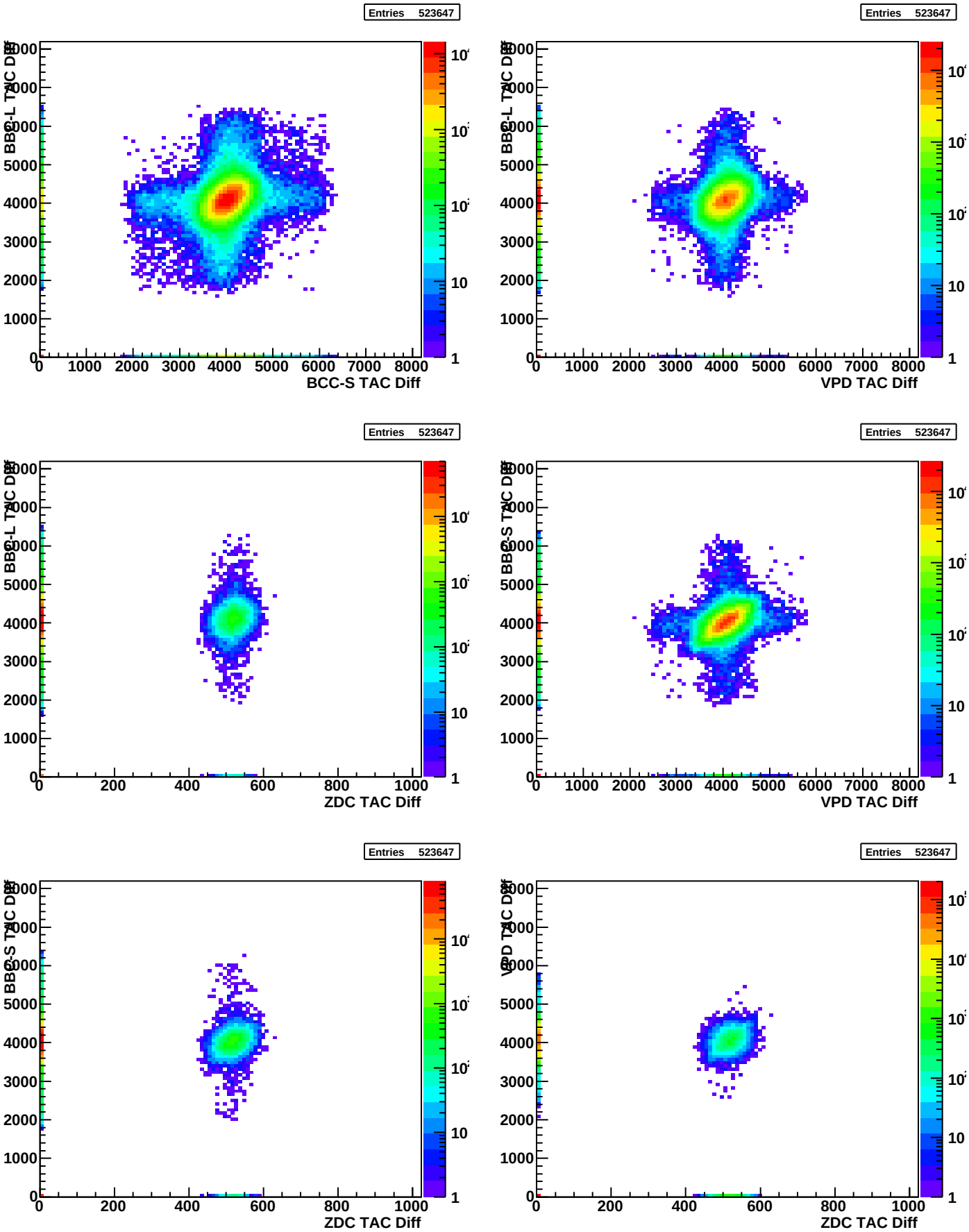
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 523647
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 523647
BBC-L TAC Diff
BCC-S TAC Diff
Entries 523647
BBC-L TAC Diff
VPD TAC Diff
Entries 523647
BBC-L TAC Diff
ZDC TAC Diff
Entries 523647
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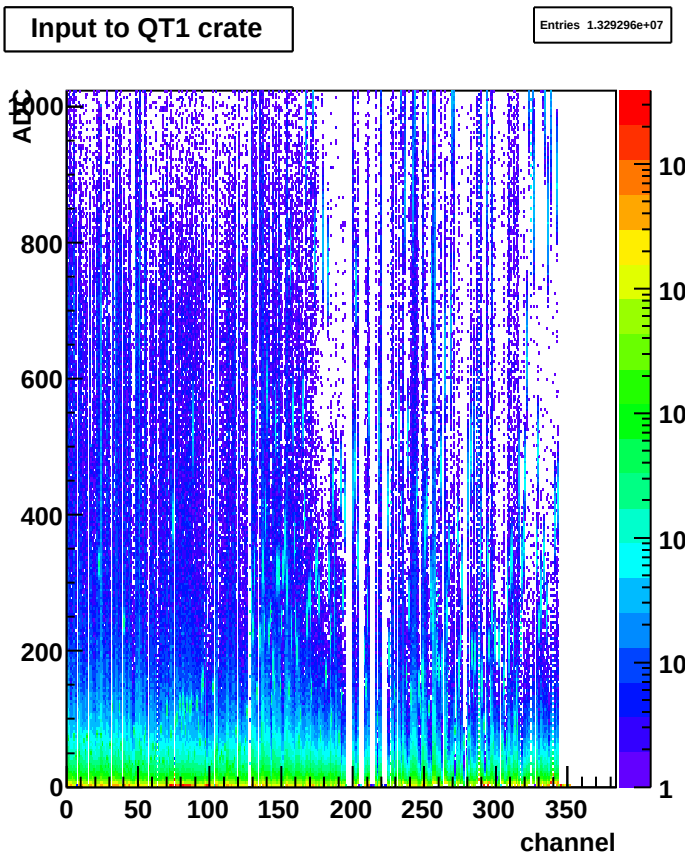
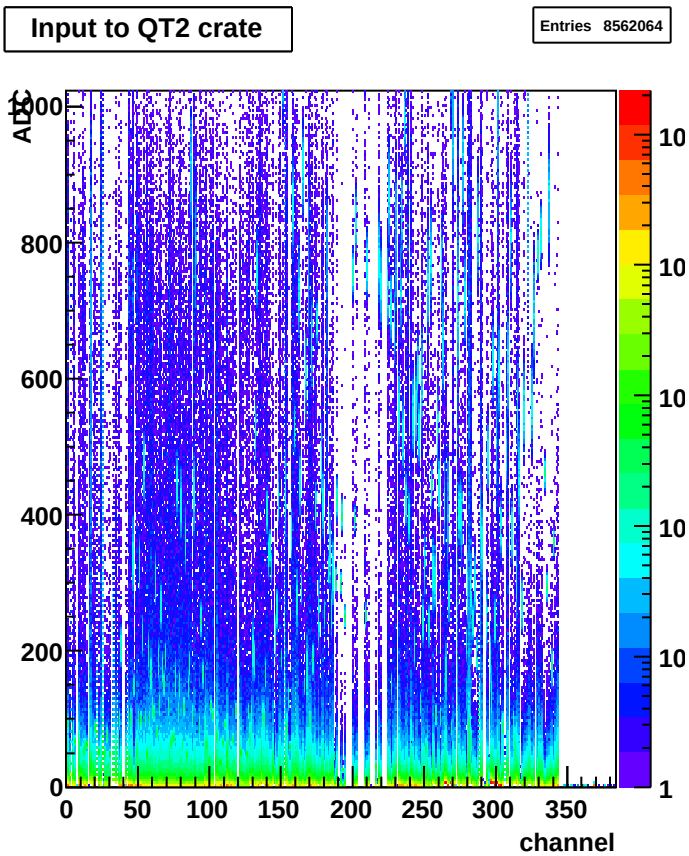
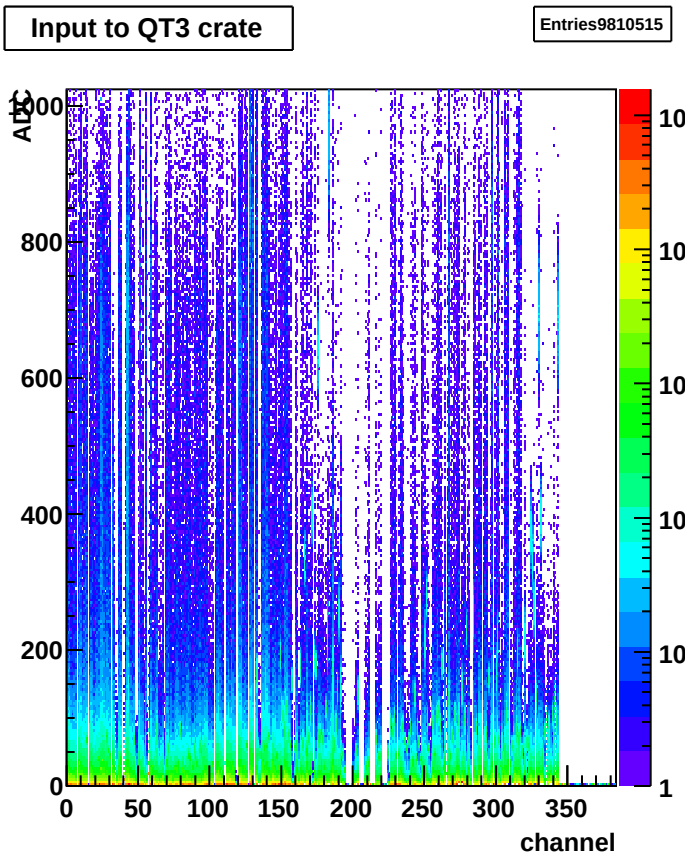
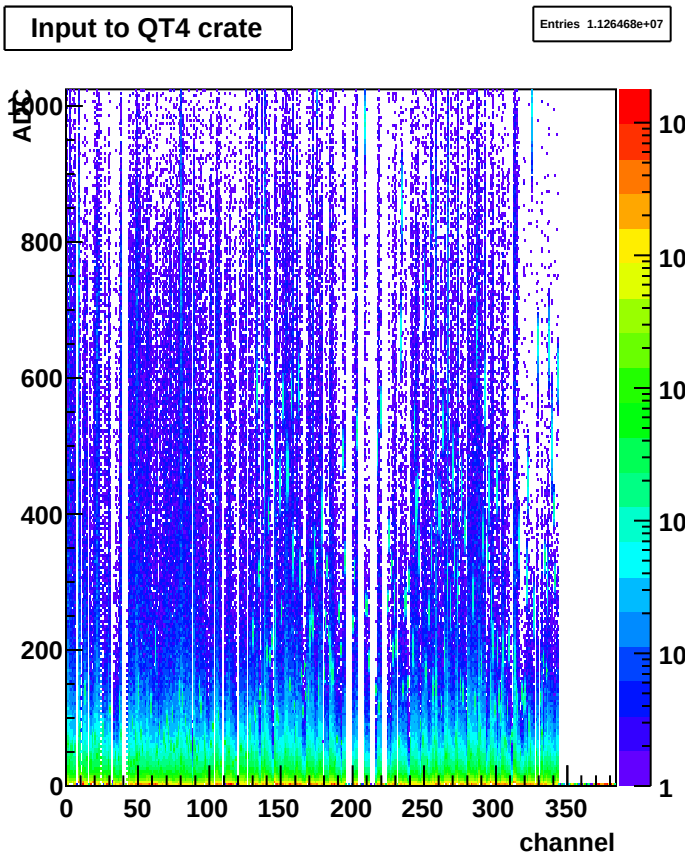
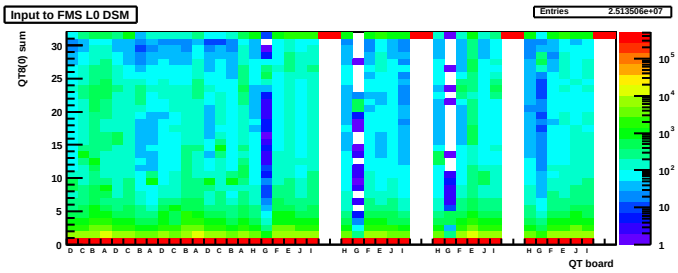
VPD TAC Diff
Entries 523647
BBC-S TAC Diff
ZDC TAC Diff
Entries 523647
VPD TAC Diff
ZDC TAC Diff

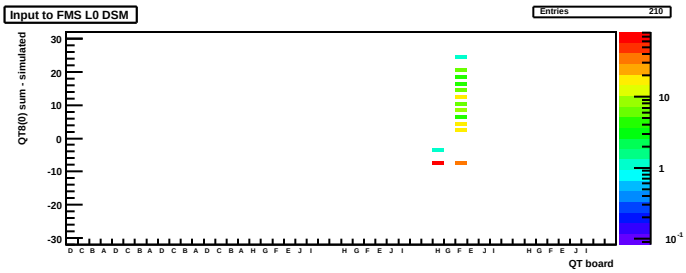

Input to QT1 crate
Entries 1.329296e+07
ADC
channel

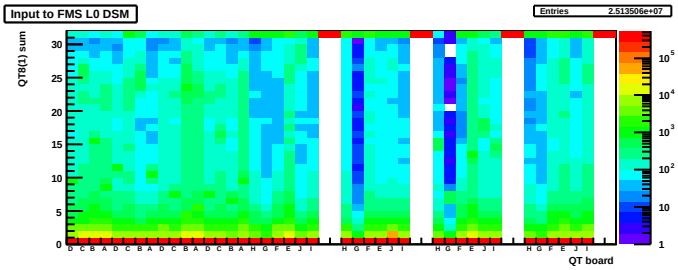

Input to QT2 crate
Entries 8562064
ADC
channel

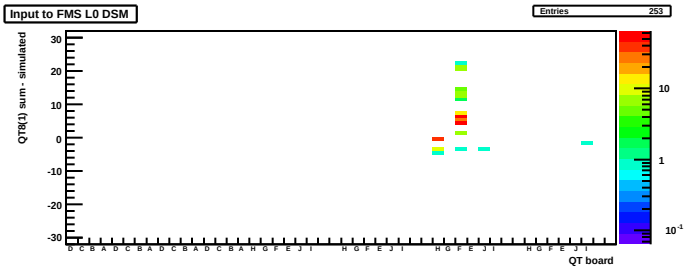

Input to QT3 crate
Entries9810515
ADC
channel

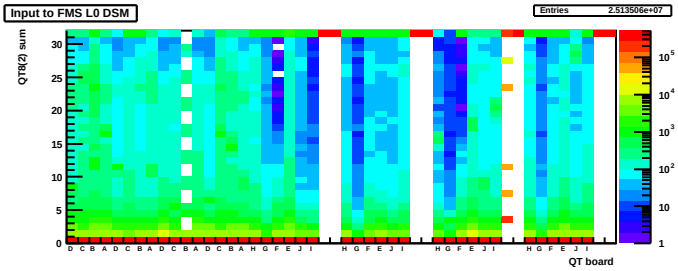

Input to QT4 crate
Entries 1.126468e+07
ADC
channel

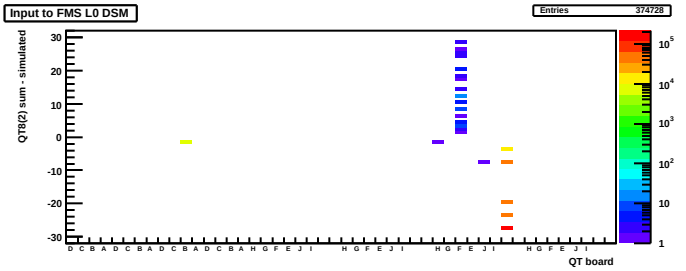

Input to FMS L0 DSM
Entries 2.513506e+07
QT8(0) sum
QT board

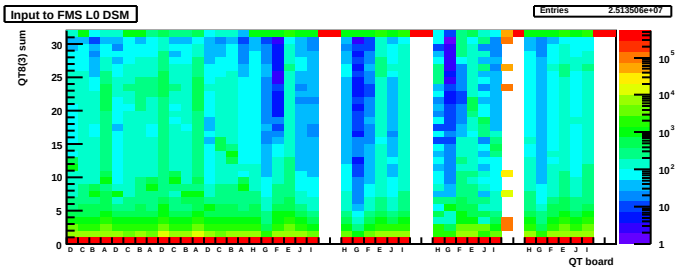

Input to FMS L0 DSM
Entries 210
QT8(0) sum - simulated
QT board

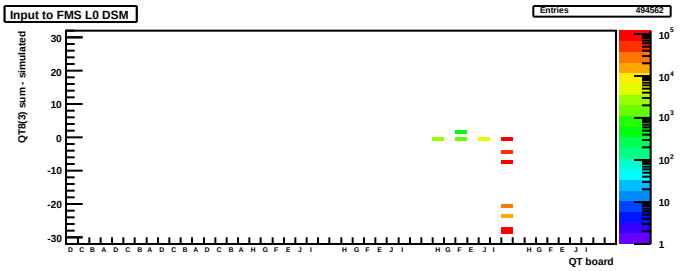

Input to FMS L0 DSM
Entries 2.513506e+07
QT8(1) sum
QT board

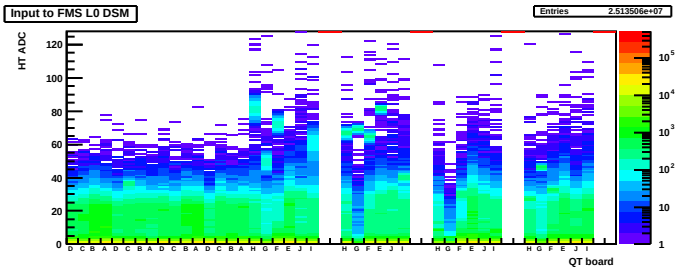

Input to FMS L0 DSM
Entries 253
QT8(1) sum - simulated
QT board

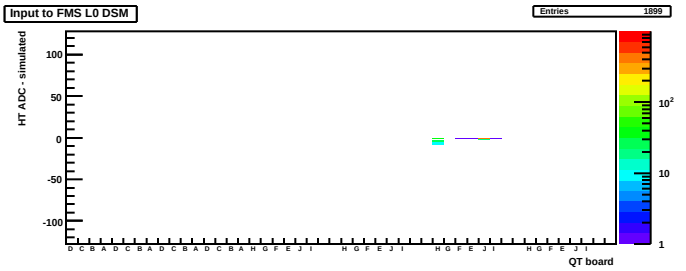

Input to FMS L0 DSM
Entries 2.513506e+07
QT8(2) sum
QT board

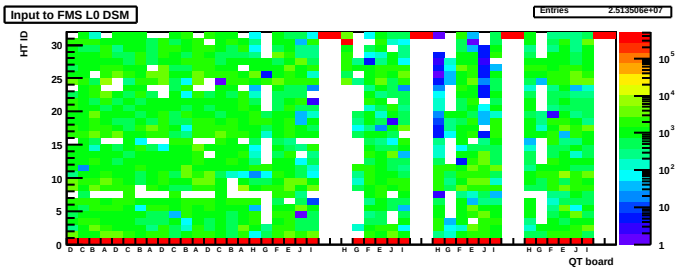

Input to FMS L0 DSM
Entries 374728
QT8(2) sum - simulated
QT board

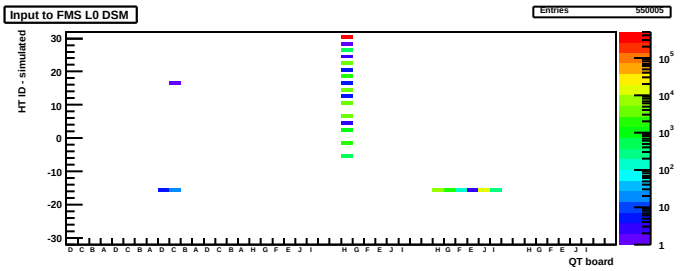

Input to FMS L0 DSM
Entries 2.513506e+07
QT8(3) sum
QT board

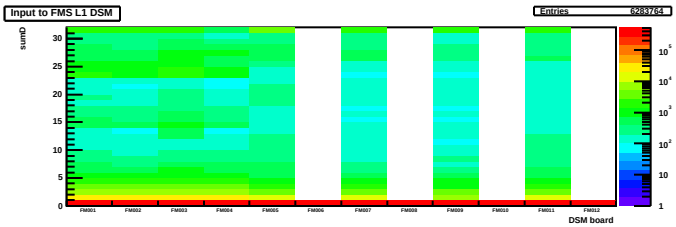

Input to FMS L0 DSM
Entries 494562
QT8(3) sum - simulated
QT board

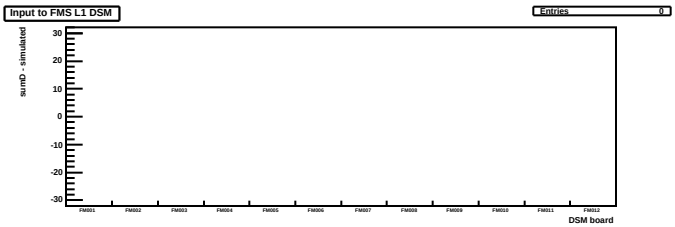

Input to FMS L0 DSM
Entries 2.513506e+07
HT ADC
QT board

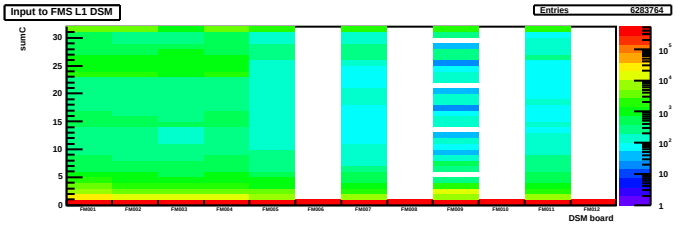

Input to FMS L0 DSM
Entries 1899
HT ADC - simulated
QT board

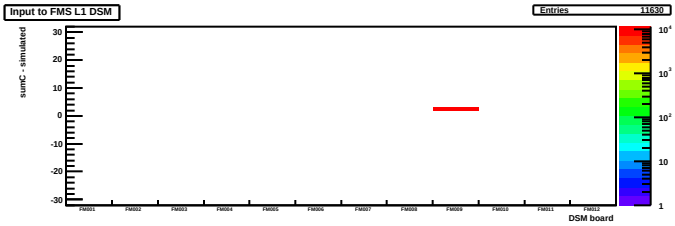

Input to FMS L0 DSM
Entries 2.513506e+07
HT ID
QT board

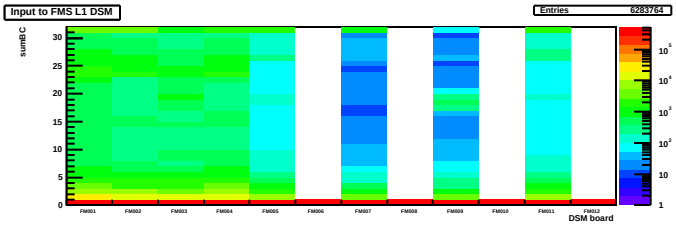

Input to FMS L0 DSM
Entries 550005
HT ID - simulated
QT board

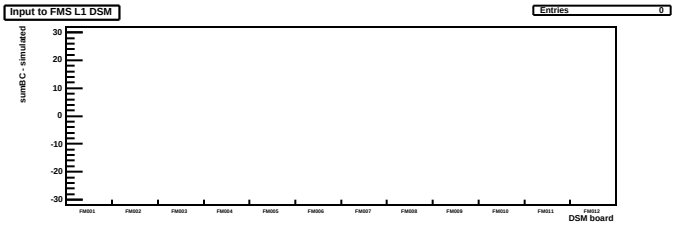

Input to FMS L1 DSM
Entries 6283764
sumD
DSM board

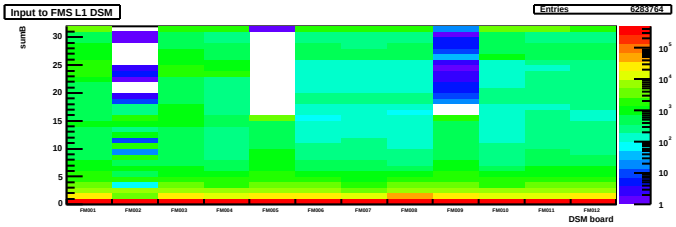

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

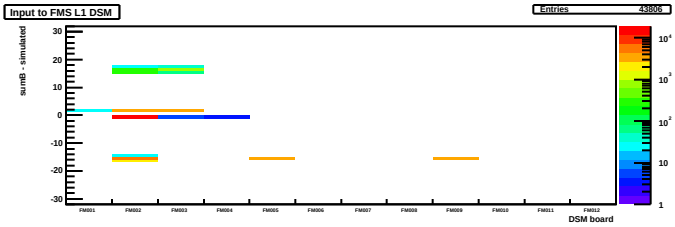

Input to FMS L1 DSM
Entries 6283764
sumC
DSM board

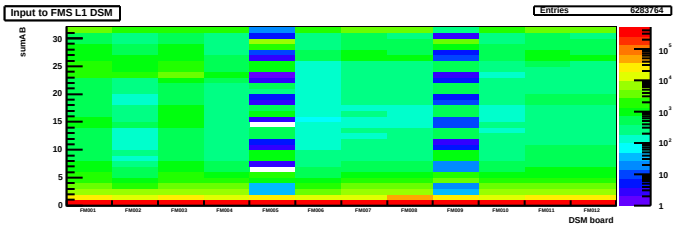

Input to FMS L1 DSM
Entries 11630
sumC - simulated
DSM board

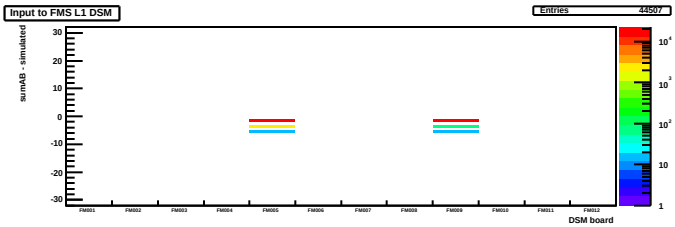

Input to FMS L1 DSM
Entries 6283764
sumBC
DSM board

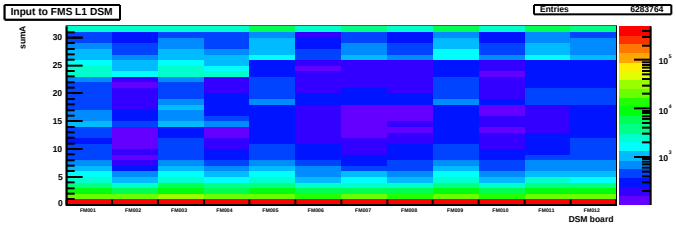

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

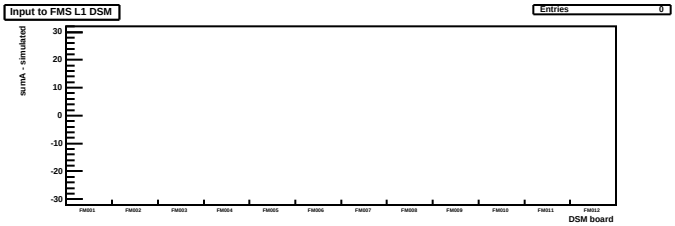

Input to FMS L1 DSM
Entries 6283764
sumB
DSM board

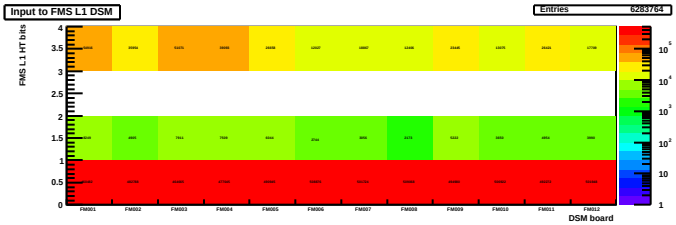

Input to FMS L1 DSM
Entries 43806
sumB - simulated
DSM board

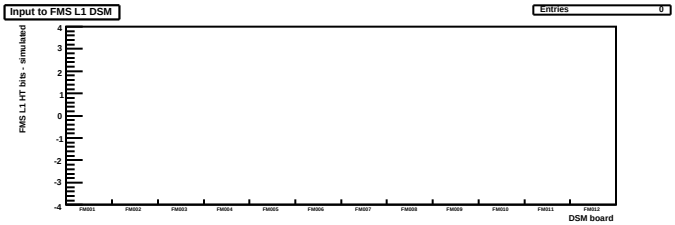

Input to FMS L1 DSM
Entries 6283764
sumAB
DSM board

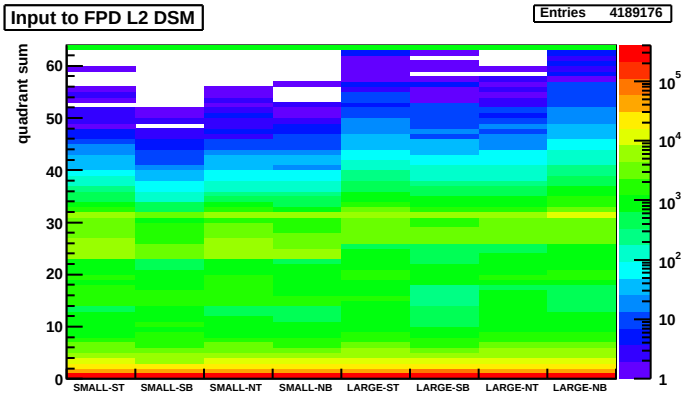

Input to FMS L1 DSM
Entries 44507
sumAB - simulated
DSM board

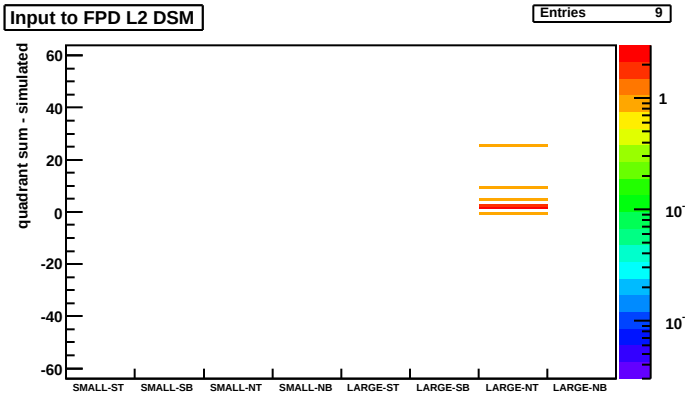

Input to FMS L1 DSM
Entries 6283764
sumA
DSM board

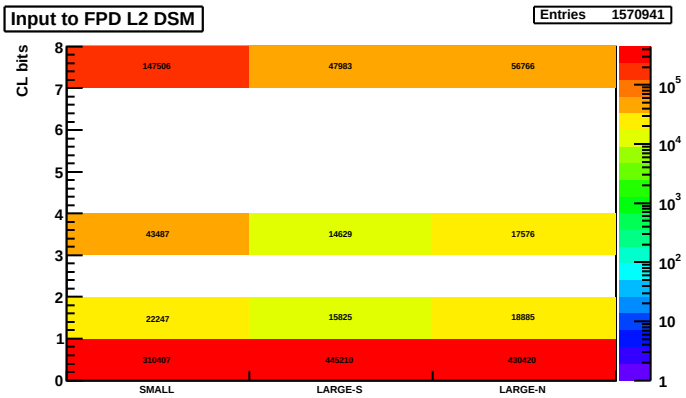

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

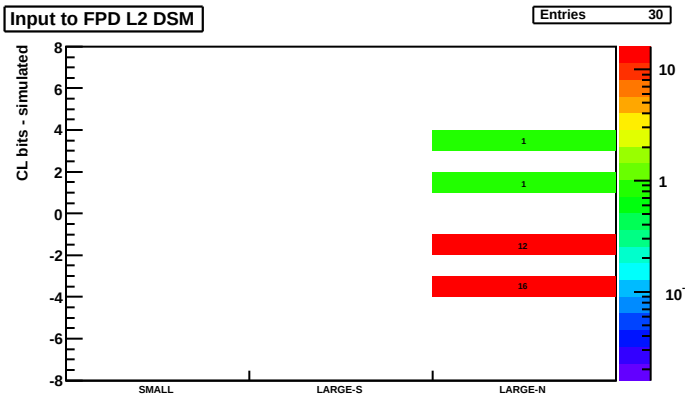

Input to FMS L1 DSM
Entries 6283764
FMS L1 HT bits
DSM board

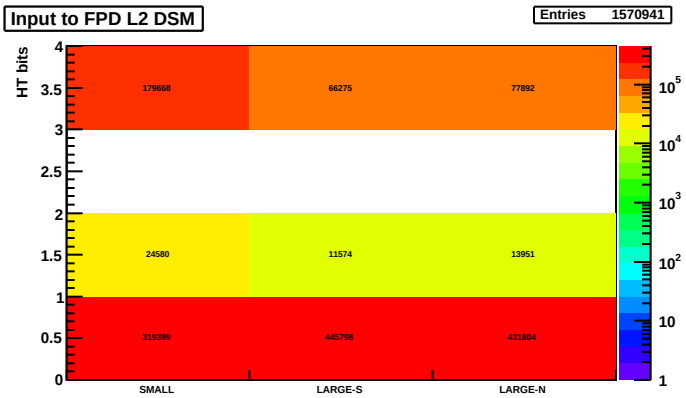

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

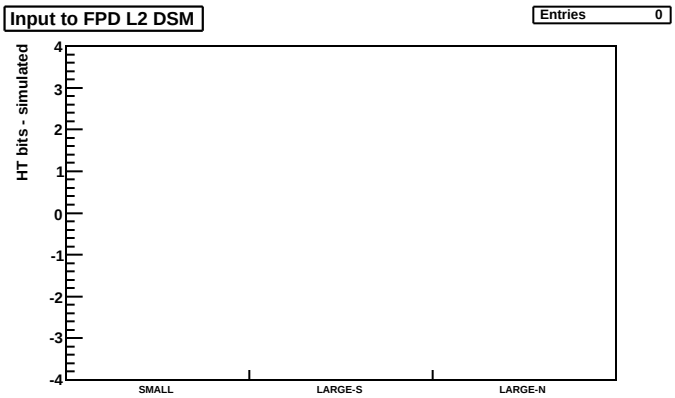

Input to FPD L2 DSM
Entries 4189176
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

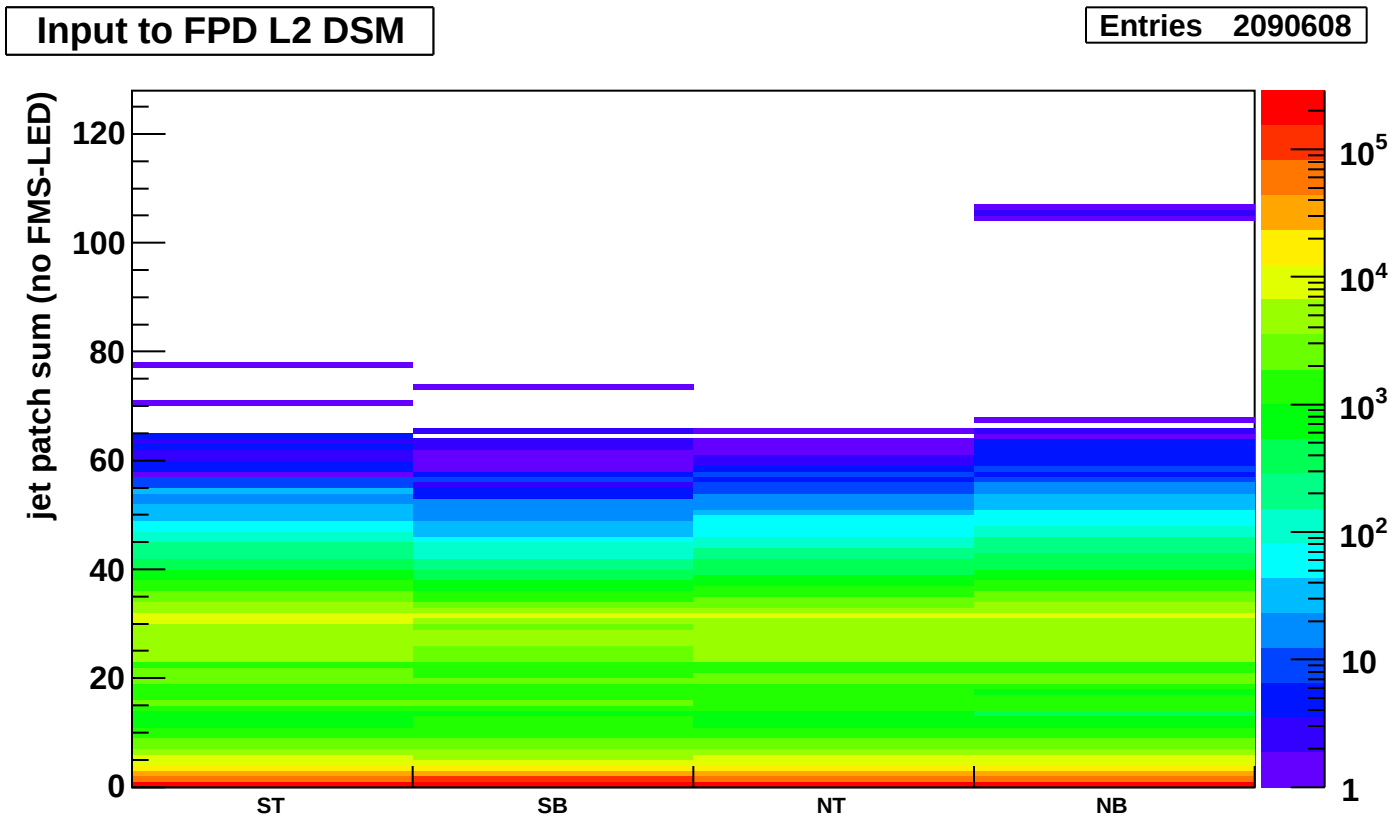

Input to FPD L2 DSM
Entries 9
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

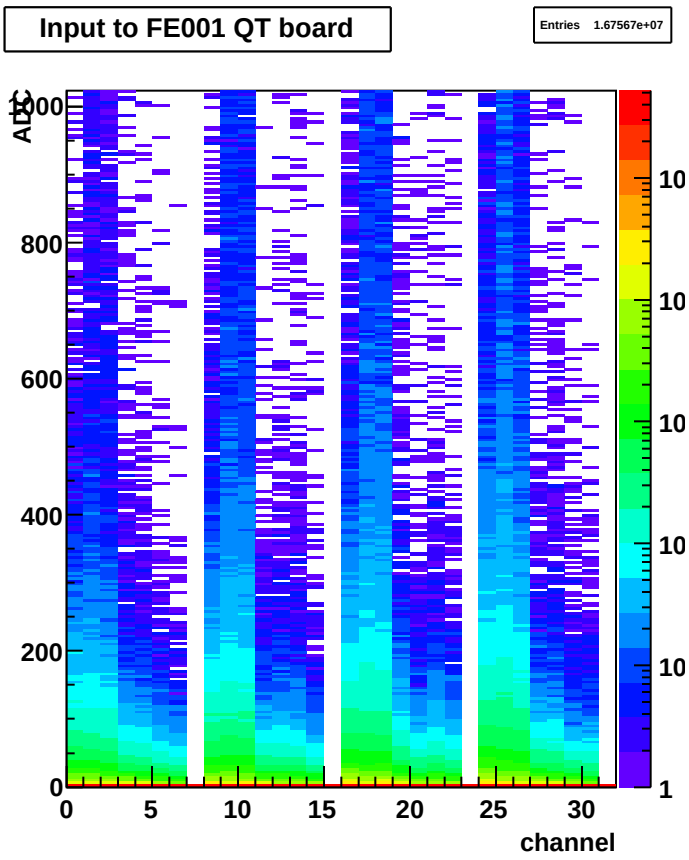

Input to FPD L2 DSM
Entries 1570941
CL bits
147506 47983 56766
43487 14629 17576
22247 15825 18885
310407 445210 430420
SMALL LARGE-S LARGE-N

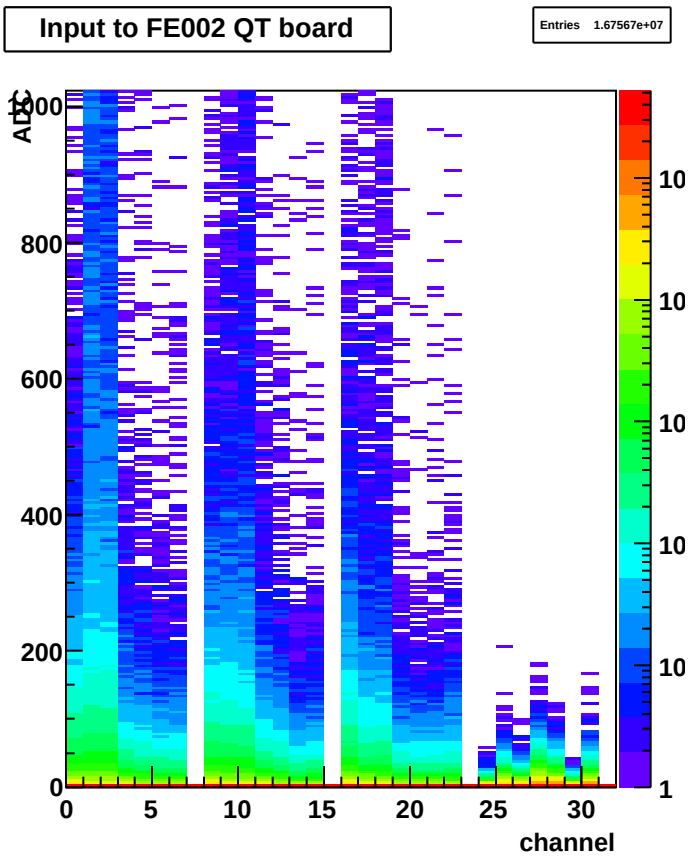

Input to FPD L2 DSM
Entries 30
CL bits - simulated
1
1
12
16
SMALL LARGE-S LARGE-N

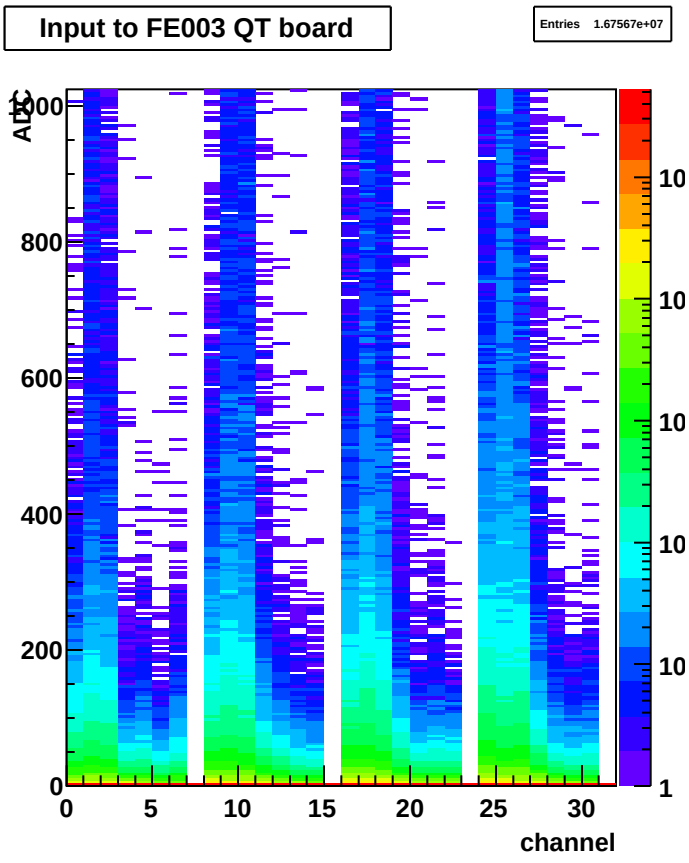

Input to FPD L2 DSM
Entries 1570941
HT bits
179668 66275 77892
24580 11574 13951
319399 445798 431804
SMALL LARGE-S LARGE-N

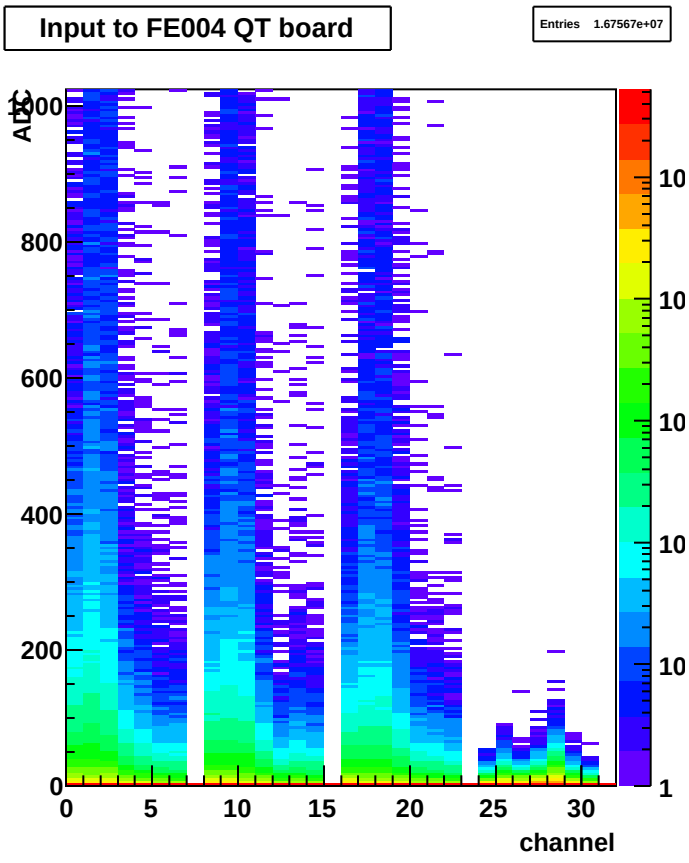

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

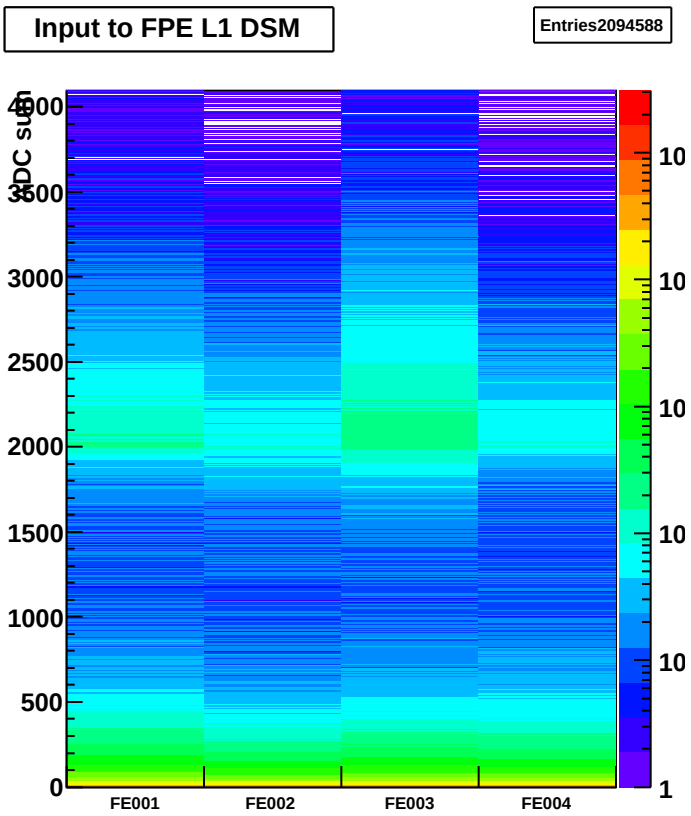

Input to FPD L2 DSM
Entries 2090608
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

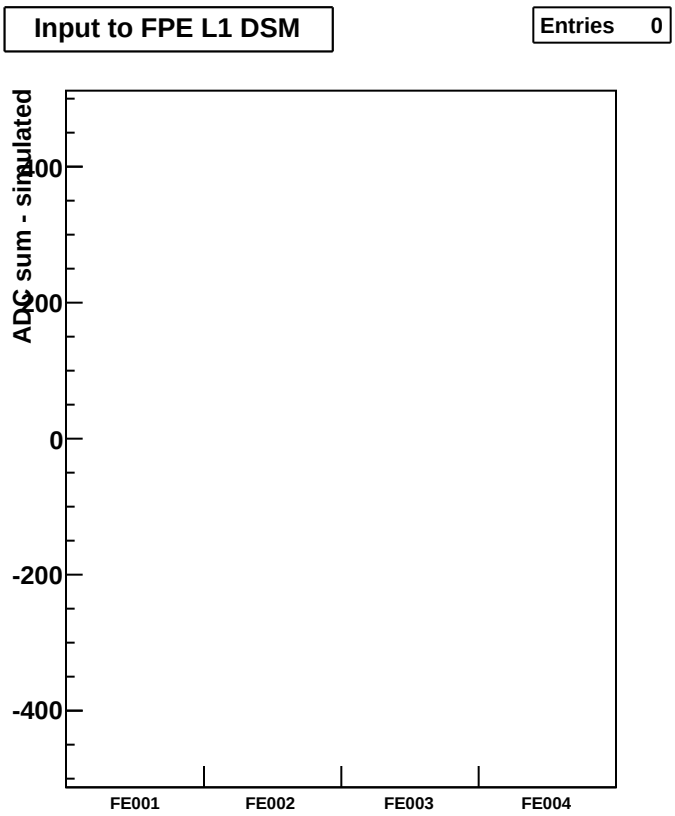

Input to FE001 QT board
Entries 1.67567e+07
ADC
channel


Input to FE002 QT board
Entries 1.67567e+07
ADC
channel

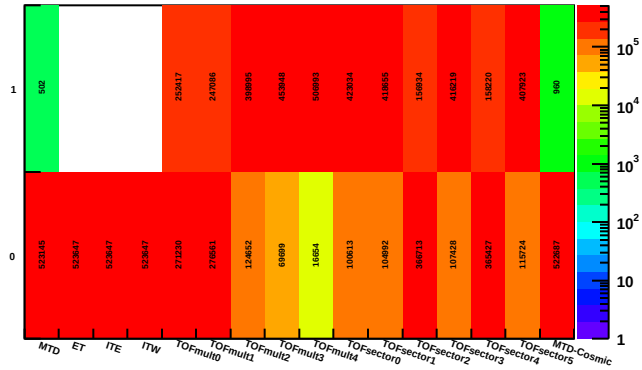

Input to FE003 QT board
Entries 1.67567e+07
ADC
channel


Input to FE004 QT board
Entries 1.67567e+07
ADC
channel

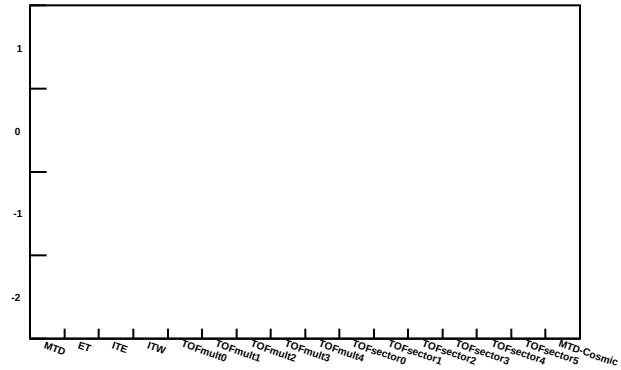

Input to FPE L1 DSM
Entries2094588
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004

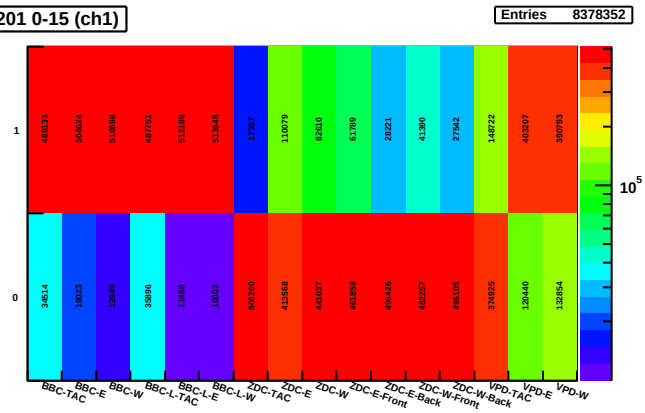
TF201 0-15 (ch0)



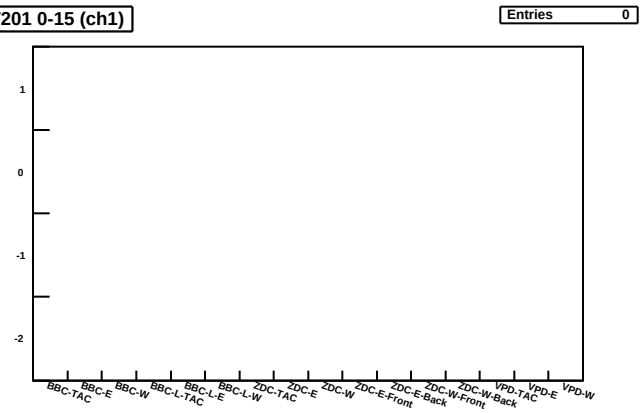
TF201 0-15 (ch0)



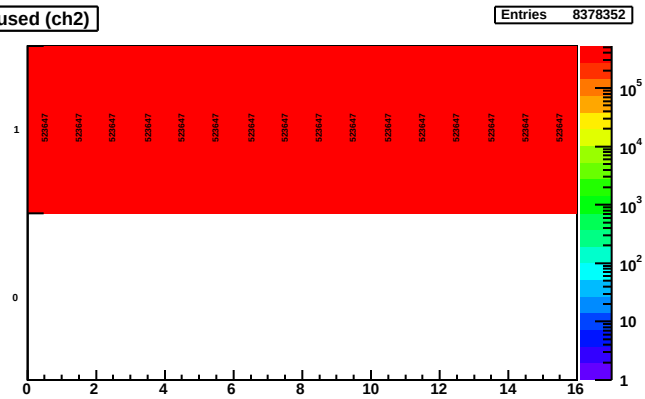
VT201 0-15 (ch1)

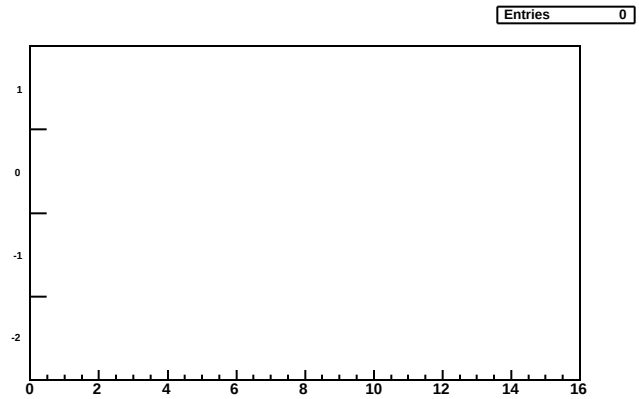


VT201 0-15 (ch1)

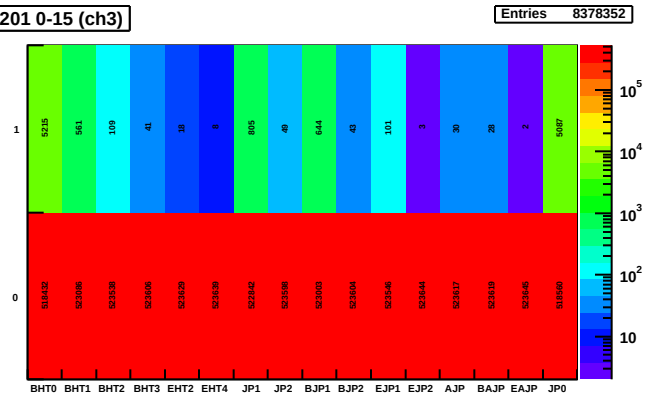


Unused (ch2)

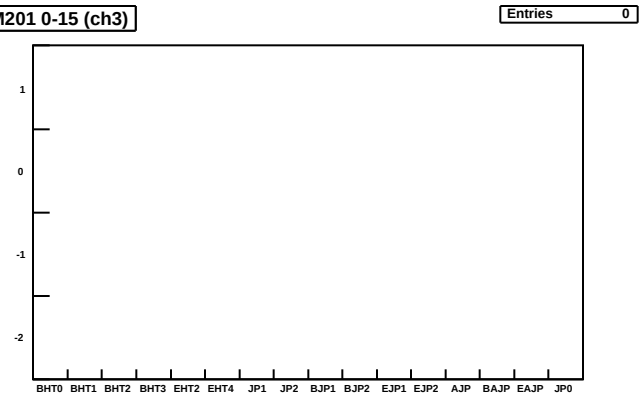




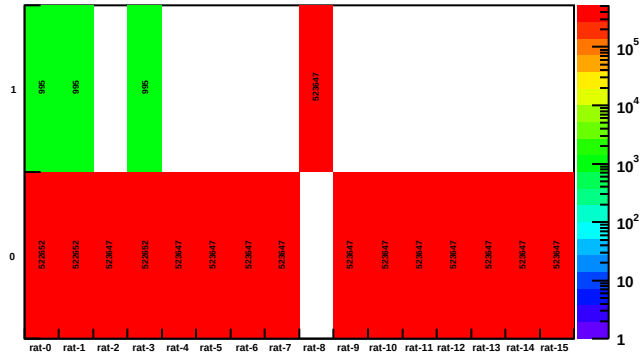
EM201 0-15 (ch3)

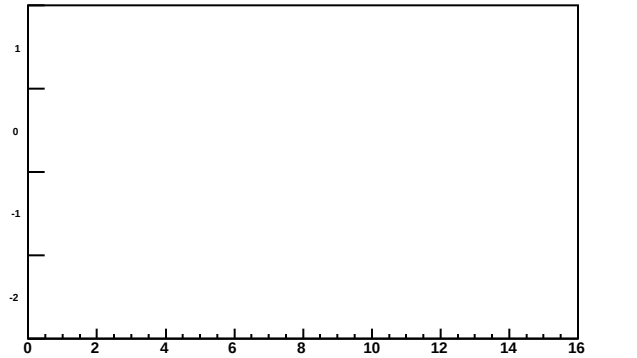


EM201 0-15 (ch3)

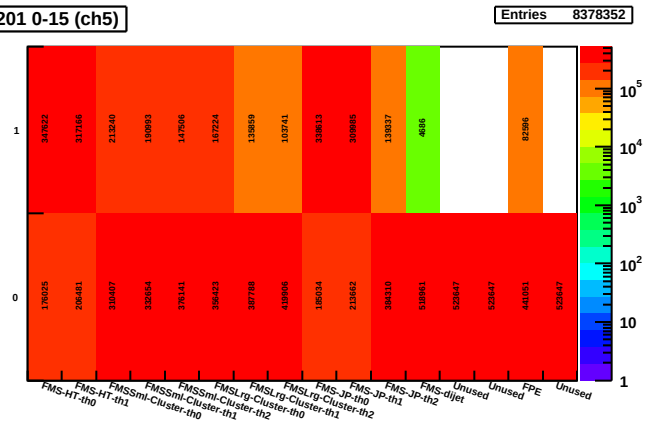


RAT board (ch4)

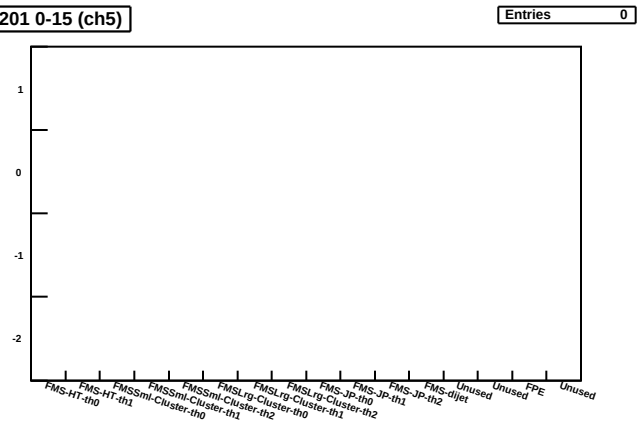




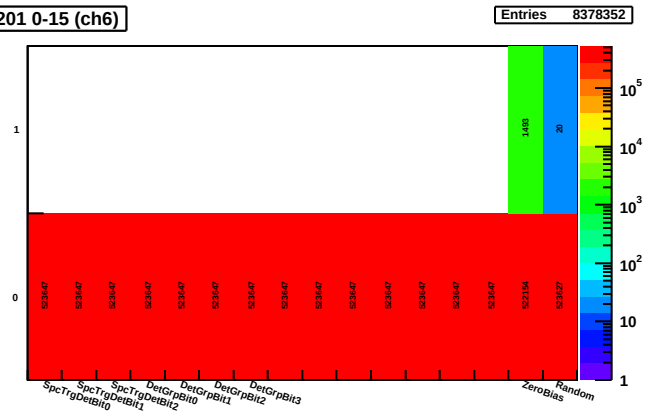
FP201 0-15 (ch5)

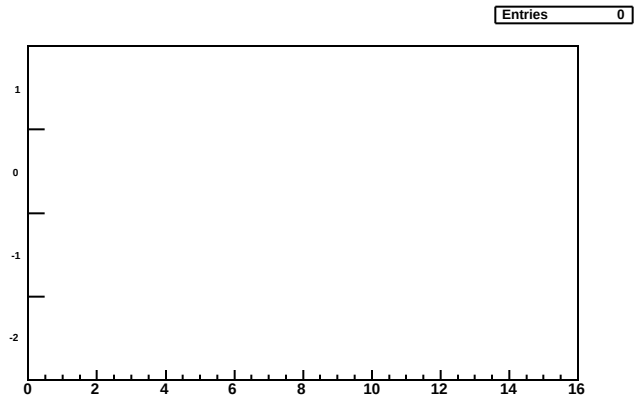


FP201 0-15 (ch5)

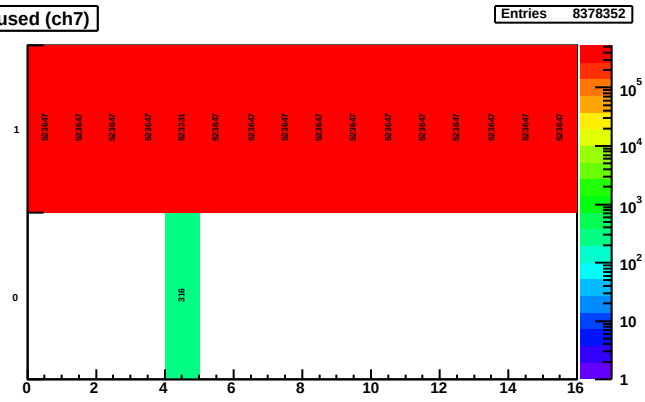


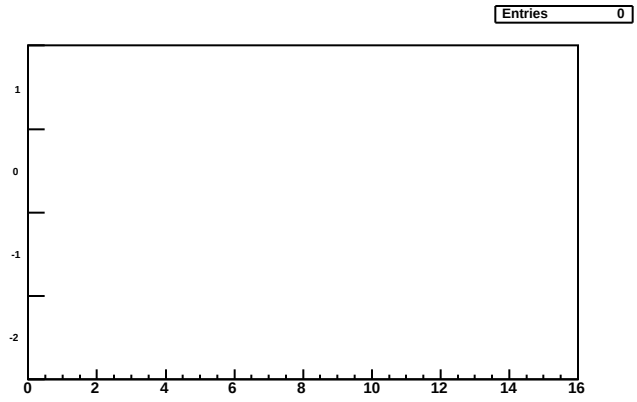
ST201 0-15 (ch6)

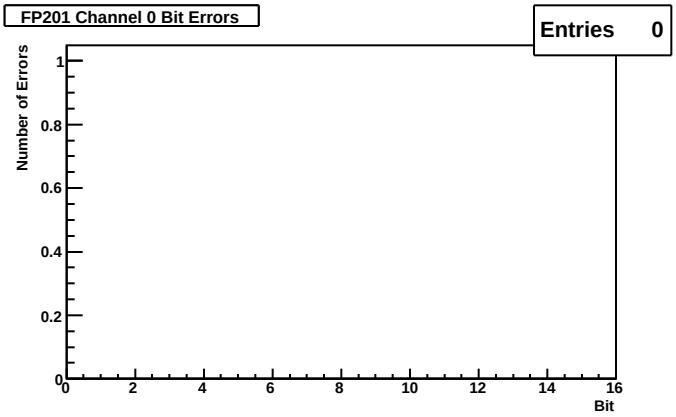


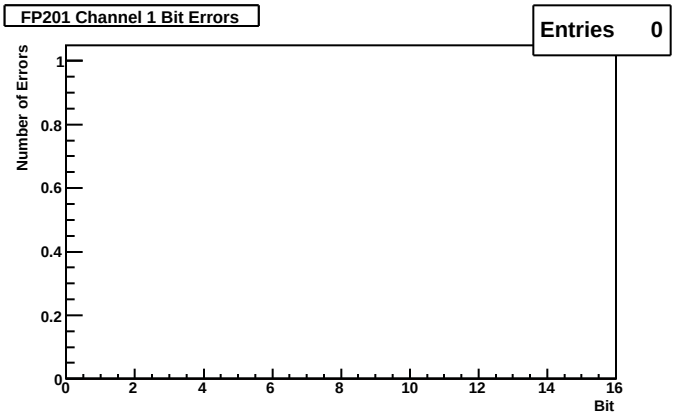


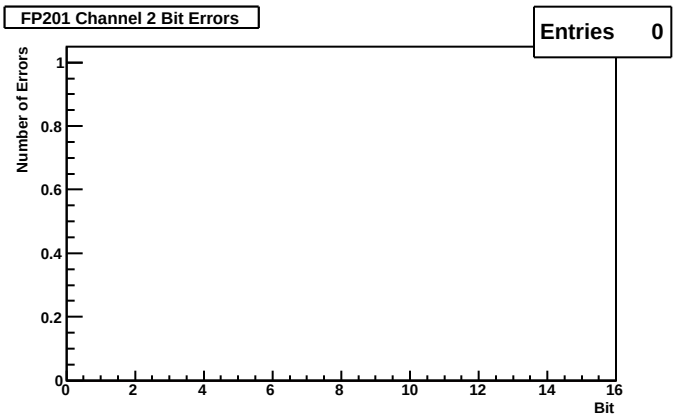
Unused (ch7)

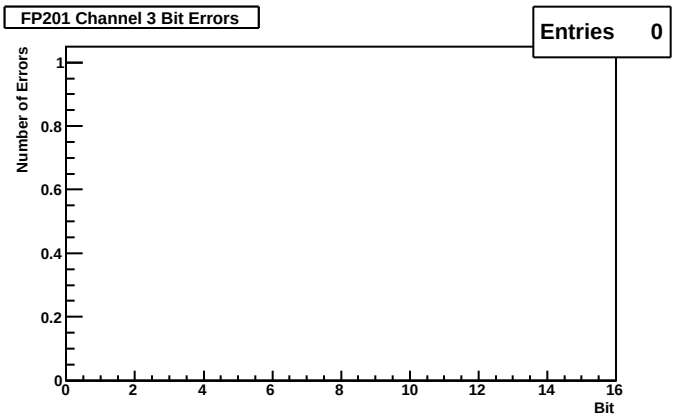


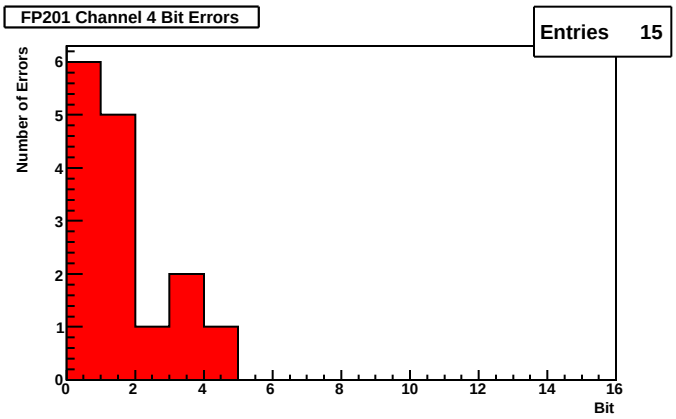


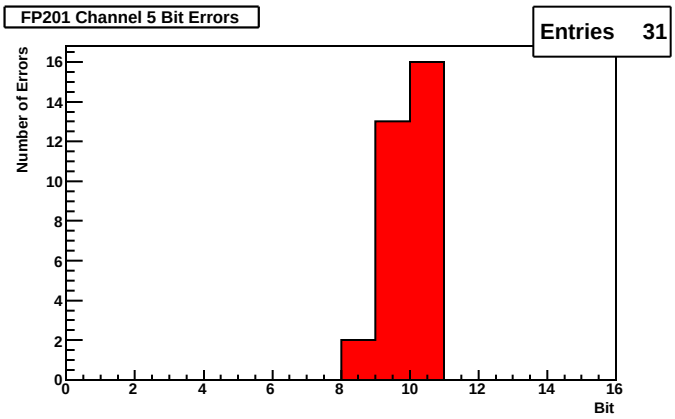

FP201 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

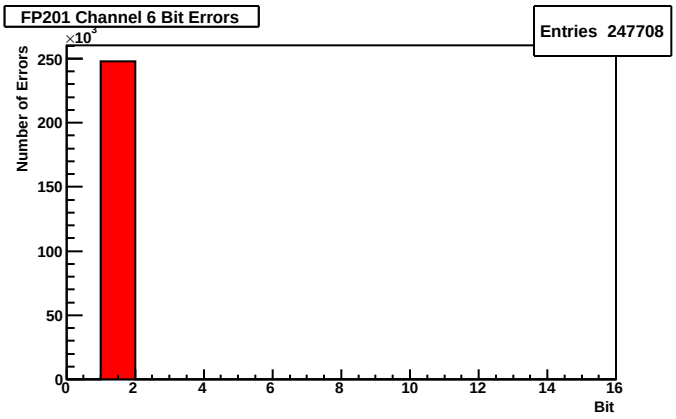

FP201 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

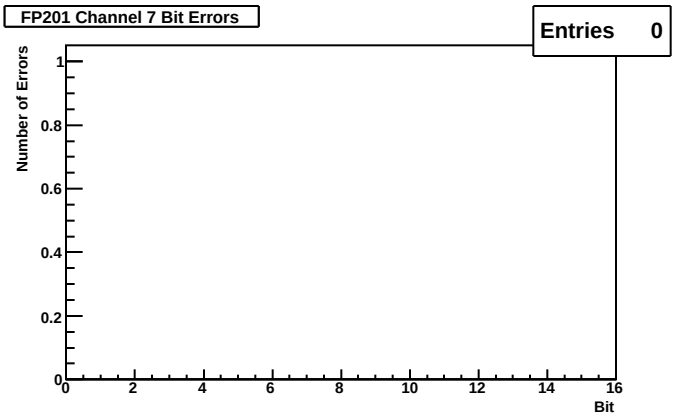

FP201 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

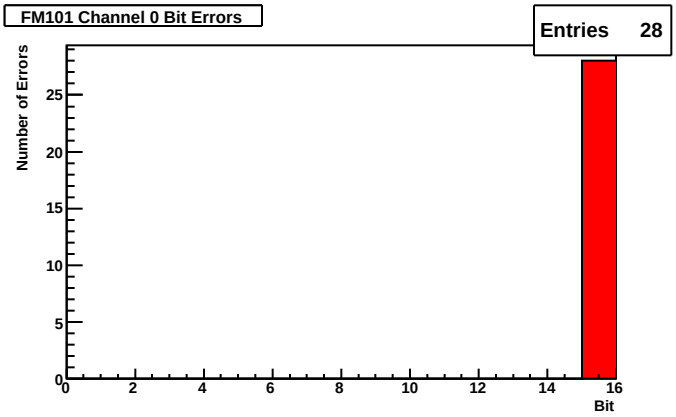

FP201 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

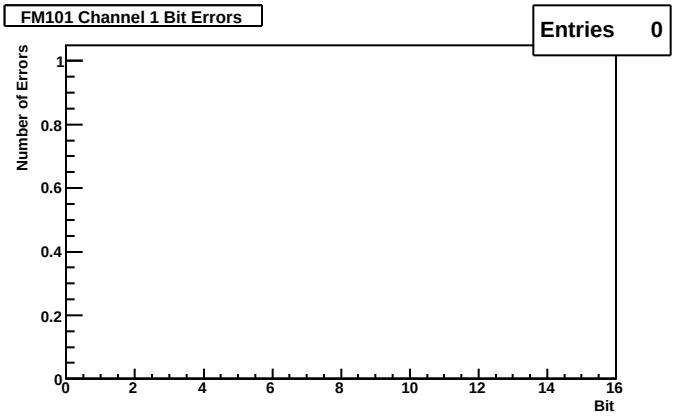

FP201 Channel 4 Bit Errors
Entries 15
Number of Errors
Bit

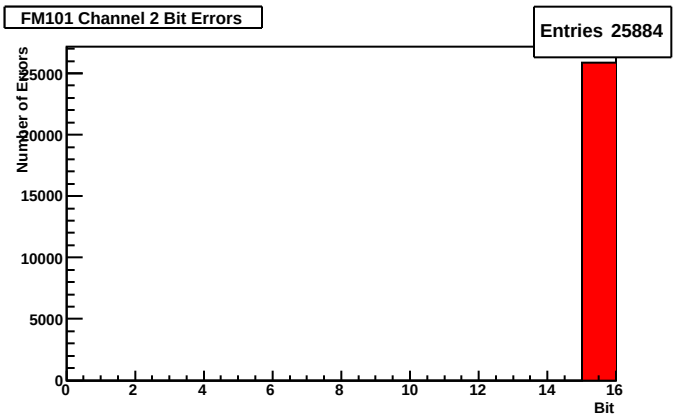

FP201 Channel 5 Bit Errors
Entries 31
Number of Errors
Bit

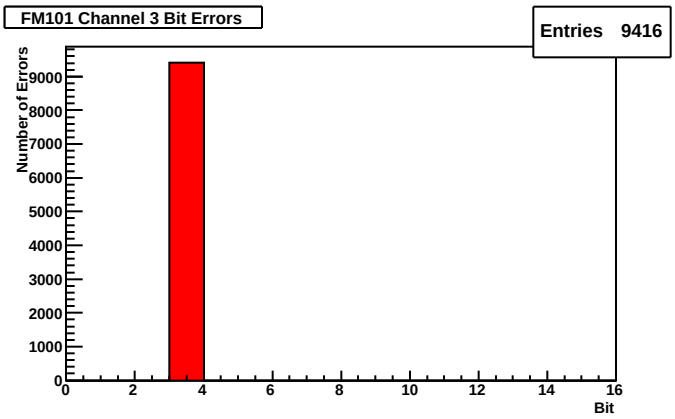

FP201 Channel 6 Bit Errors
Entries 247708
Number of Errors
Bit

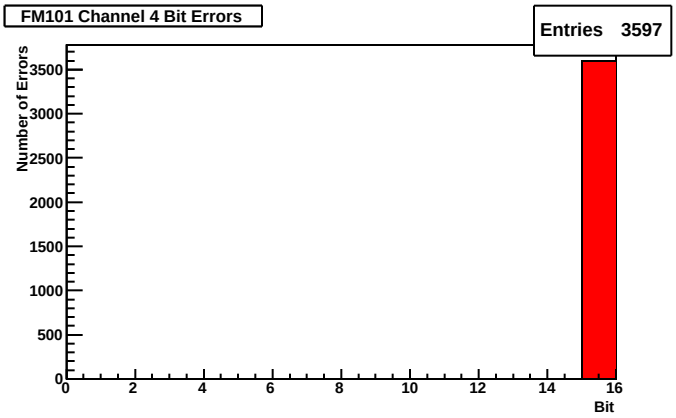

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

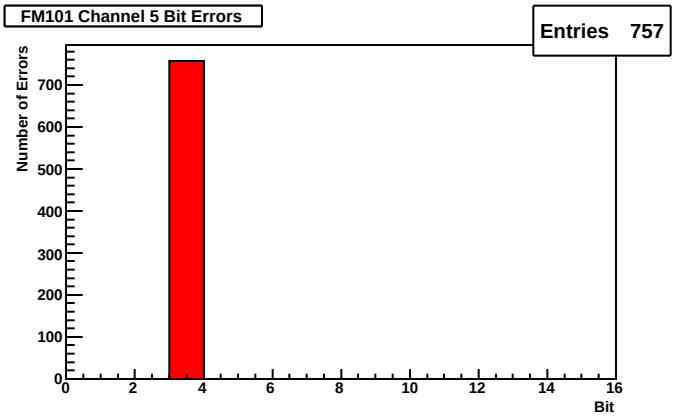

FM101 Channel 0 Bit Errors
Entries 28
Number of Errors
Bit

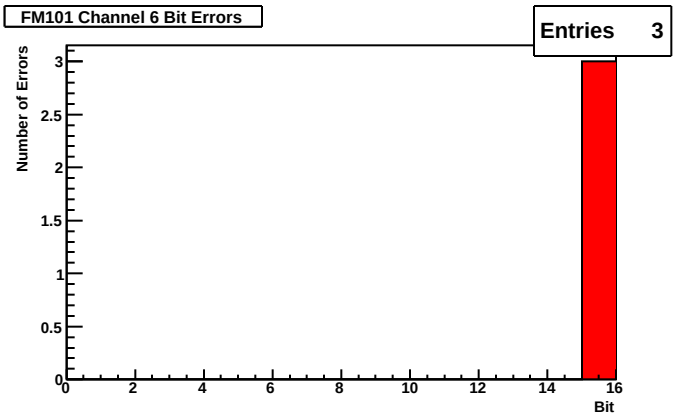

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

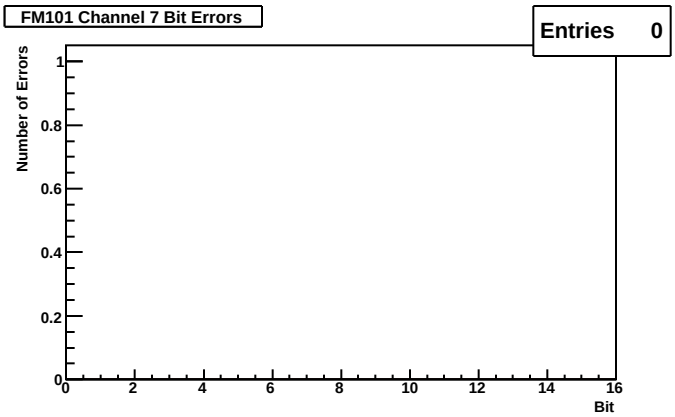

FM101 Channel 2 Bit Errors
Entries 25884
Number of Errors
Bit

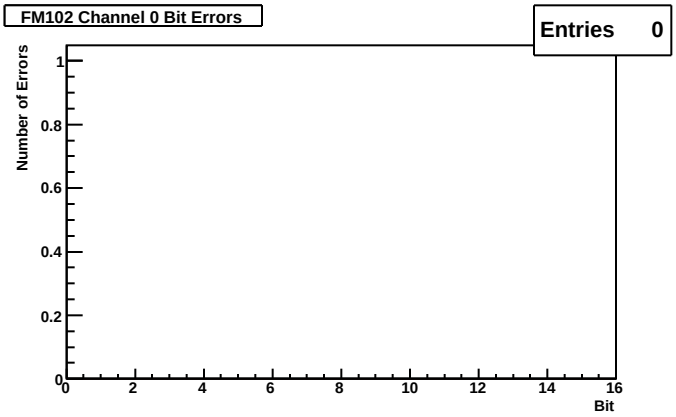

FM101 Channel 3 Bit Errors
Entries 9416
Number of Errors
Bit

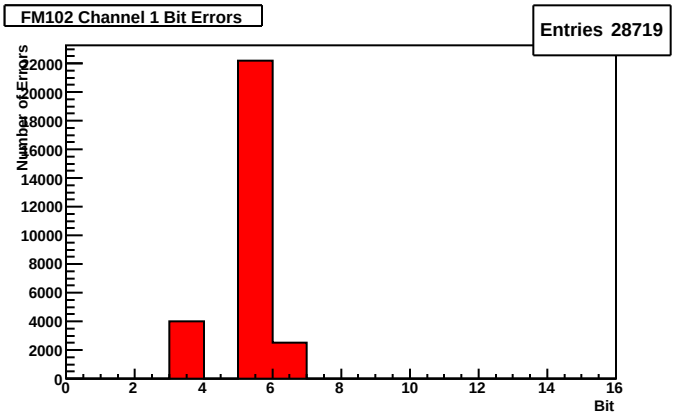

FM101 Channel 4 Bit Errors
Entries 3597
Number of Errors
Bit

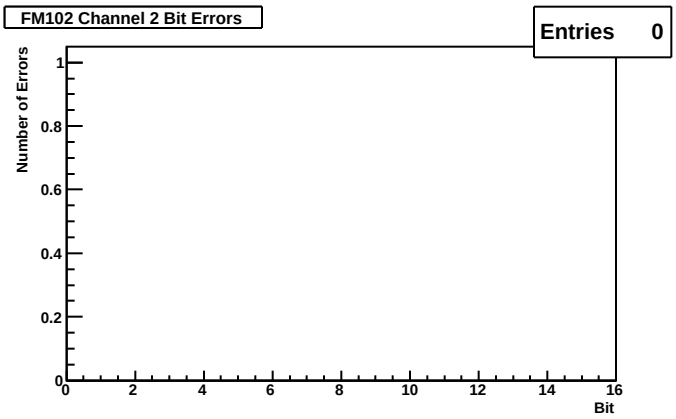

FM101 Channel 5 Bit Errors
Entries 757
Number of Errors
Bit

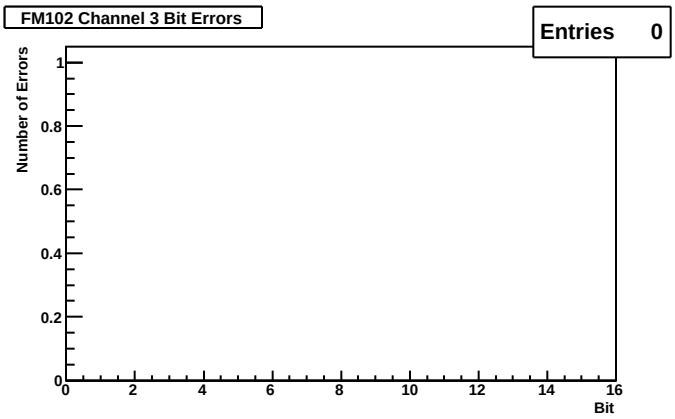

FM101 Channel 6 Bit Errors
Entries 3
Number of Errors
Bit

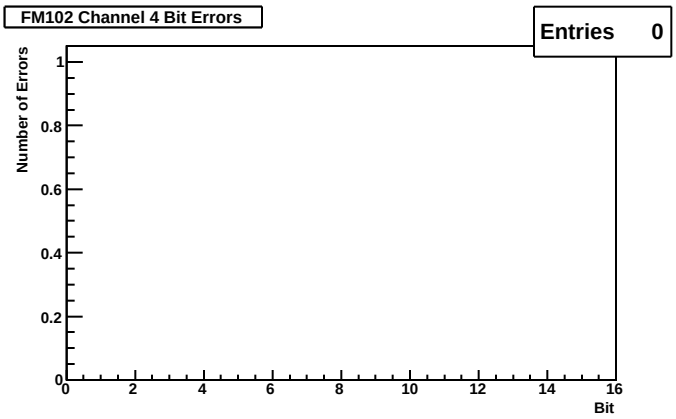

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

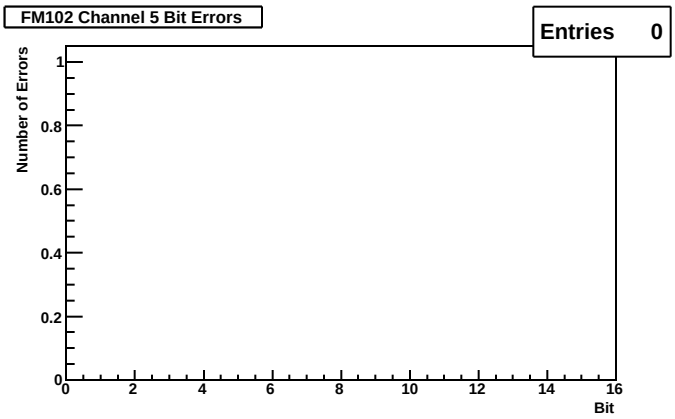

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

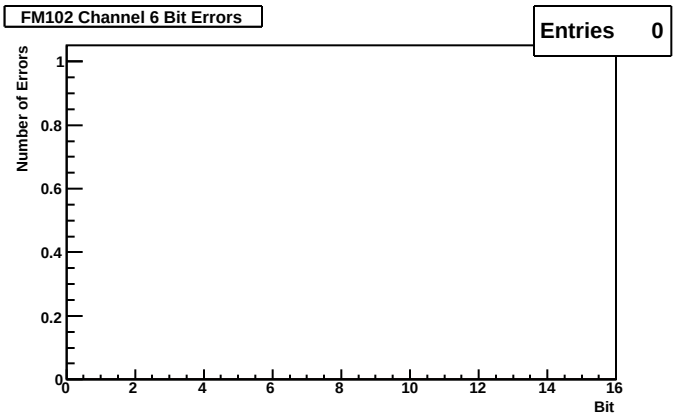

FM102 Channel 1 Bit Errors
Entries 28719
Number of Errors
Bit

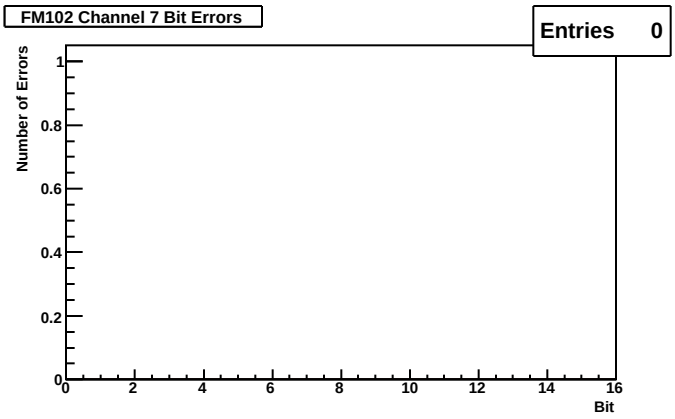

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

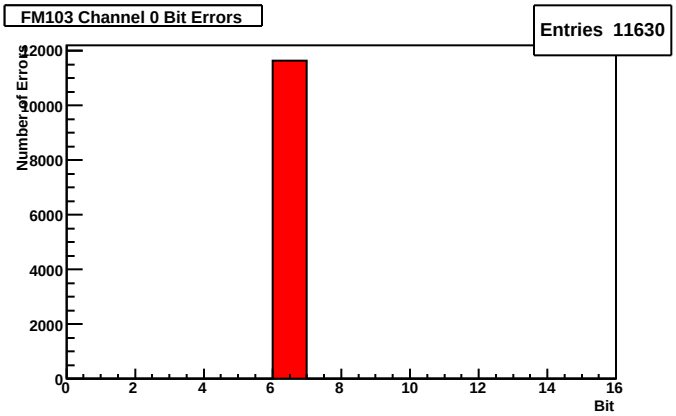

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

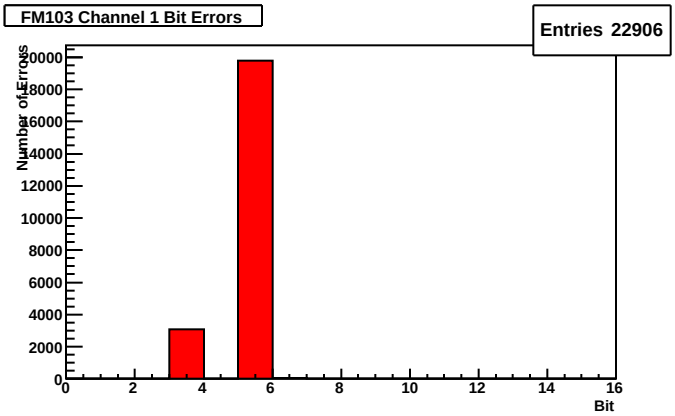

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

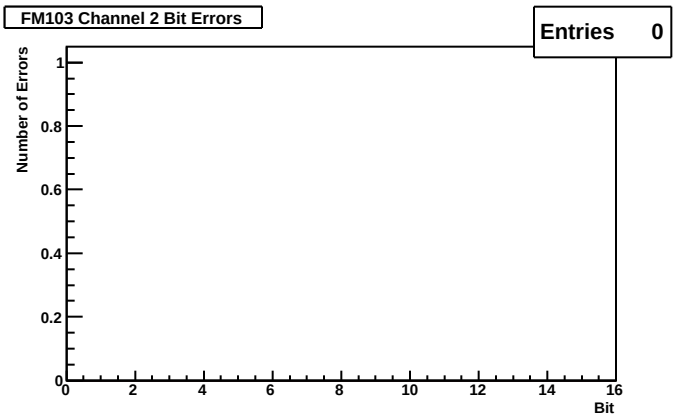

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

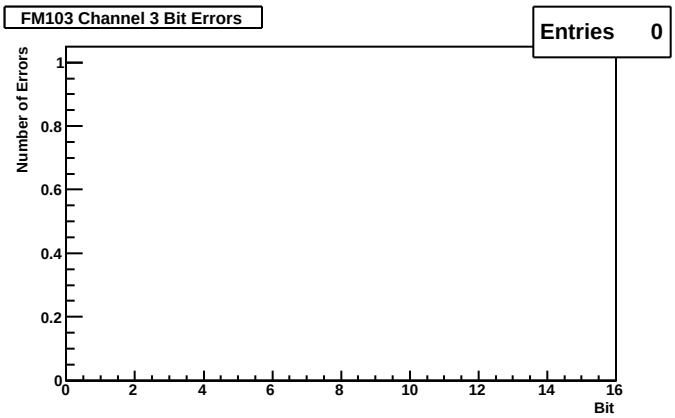

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

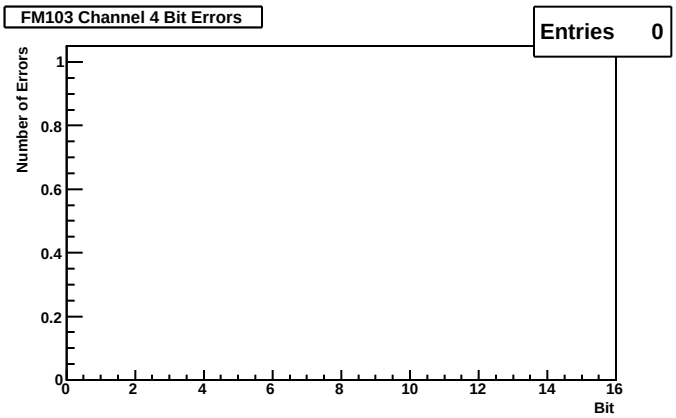

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

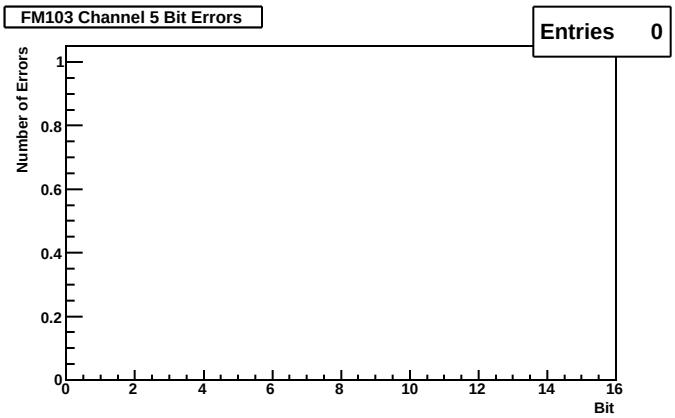

FM103 Channel 0 Bit Errors
Entries 11630
Number of Errors
Bit

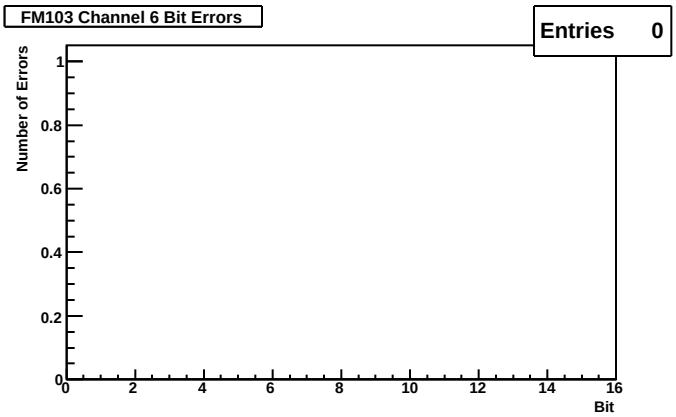

FM103 Channel 1 Bit Errors
Entries 22906
Number of Errors
Bit

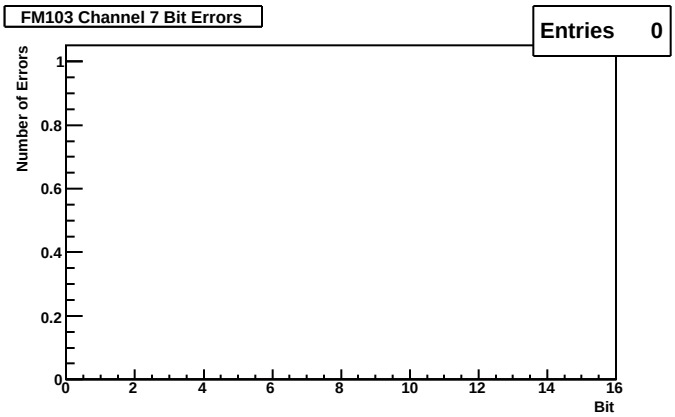

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

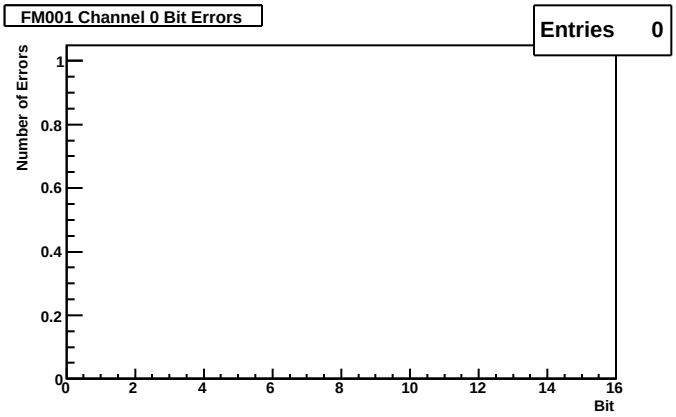

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

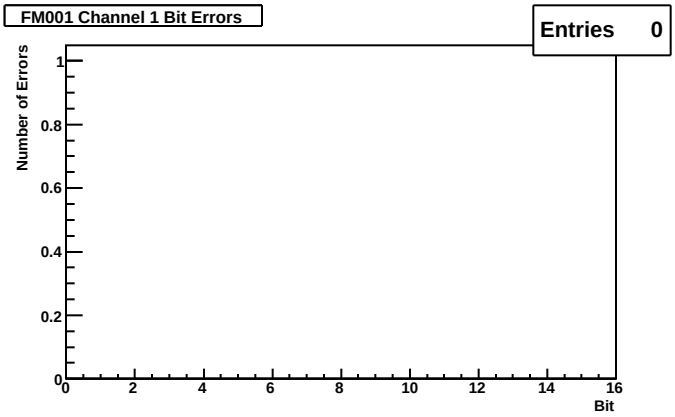

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

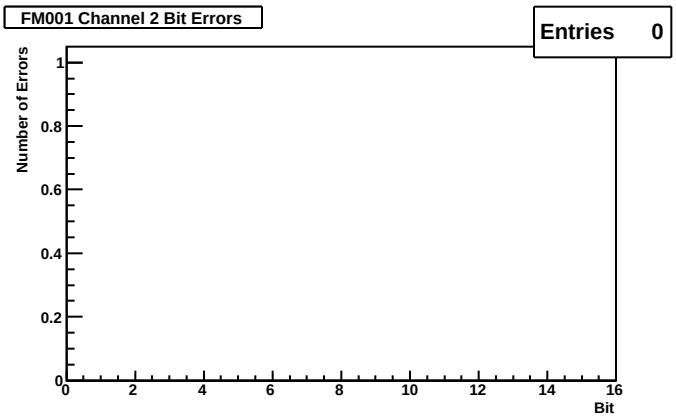

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

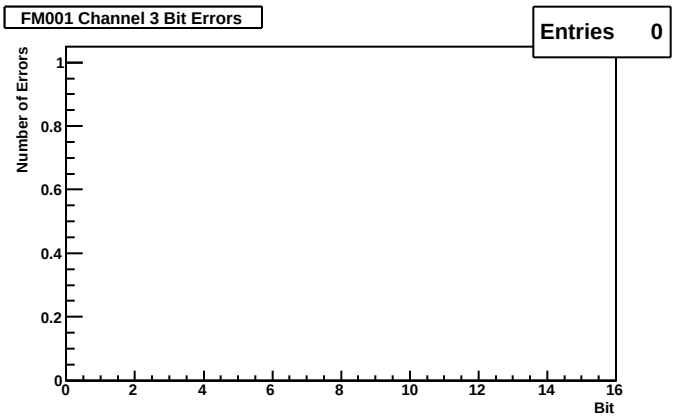

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

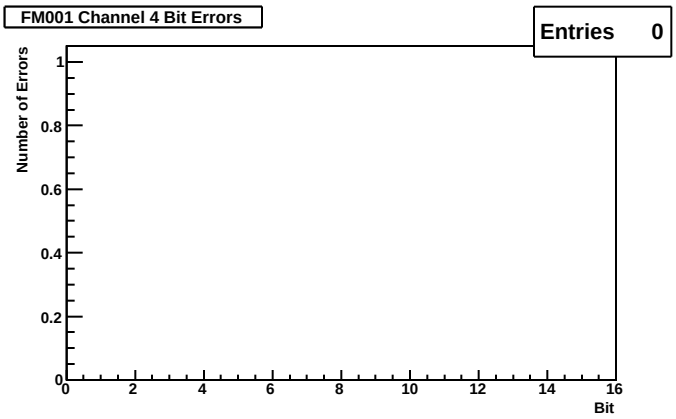

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

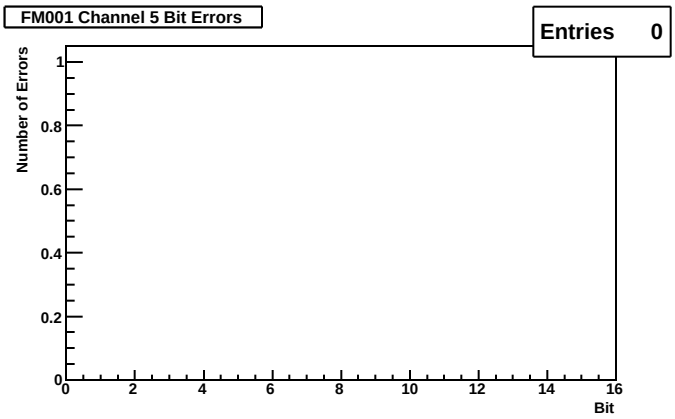

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

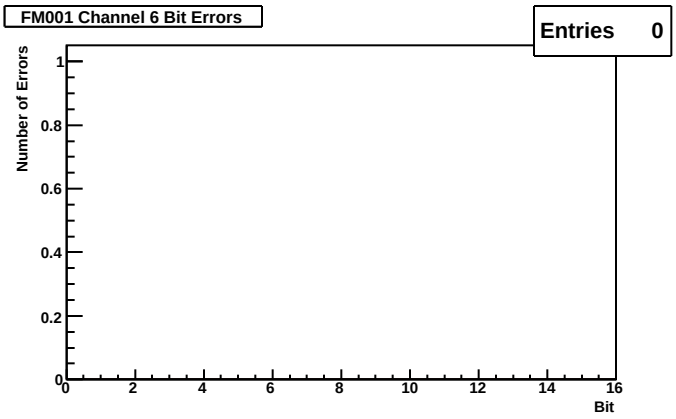

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

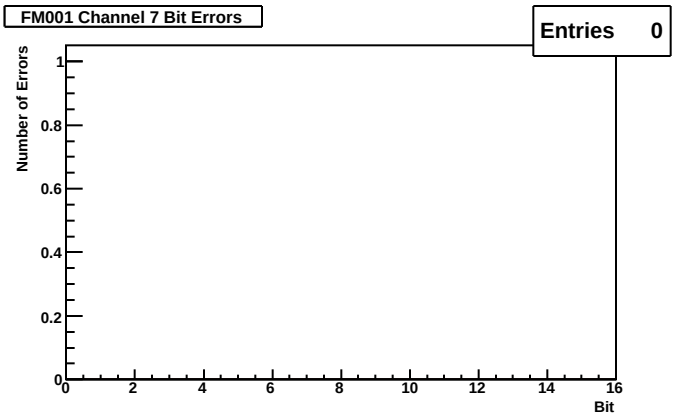

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

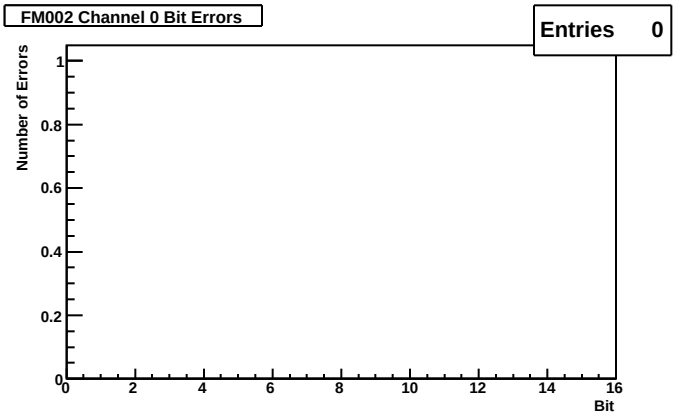

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

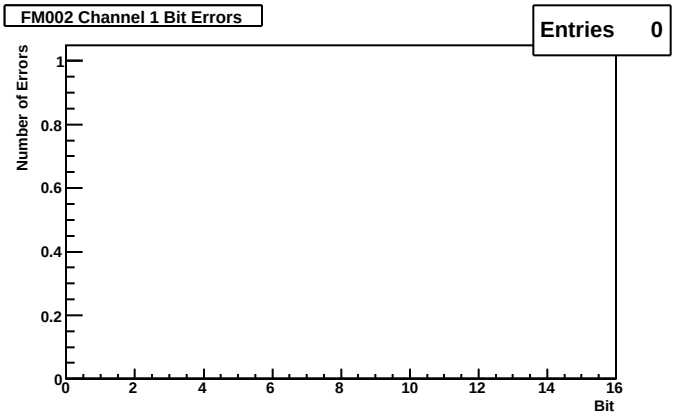

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

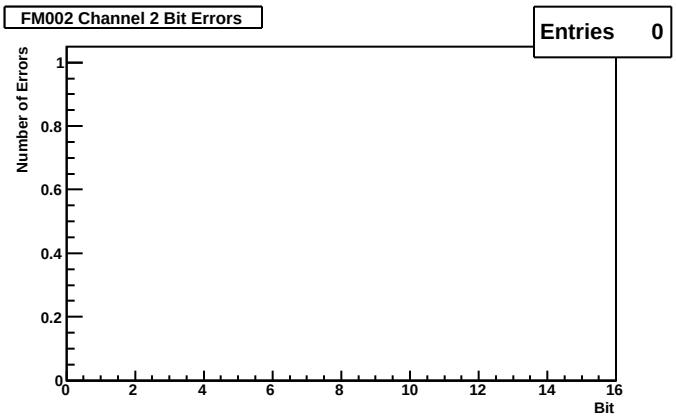

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

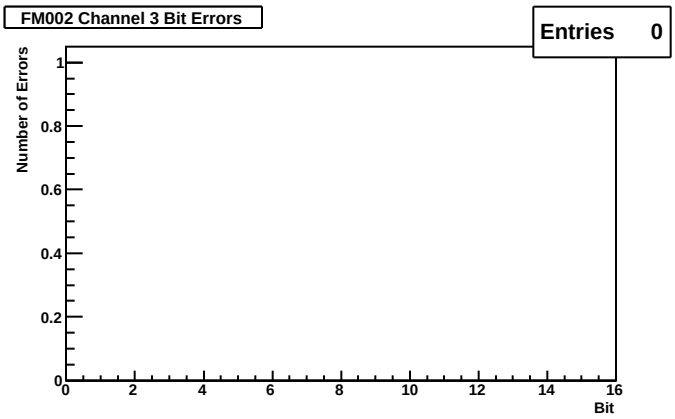

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

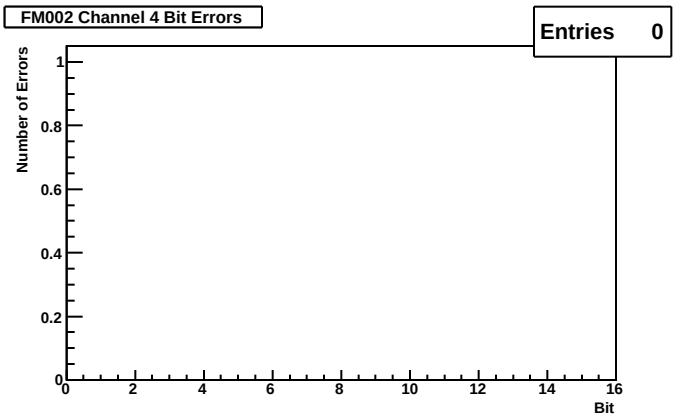

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

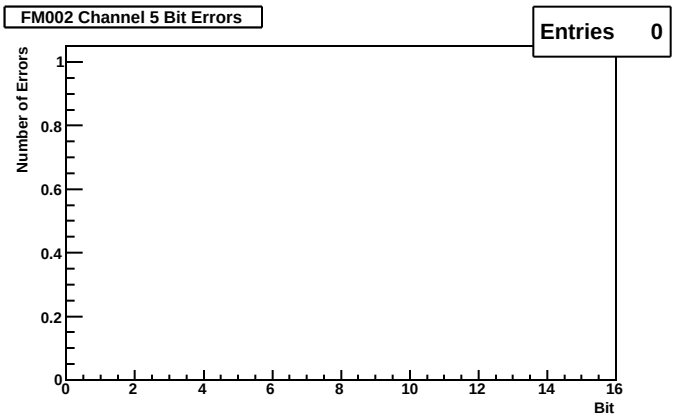

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

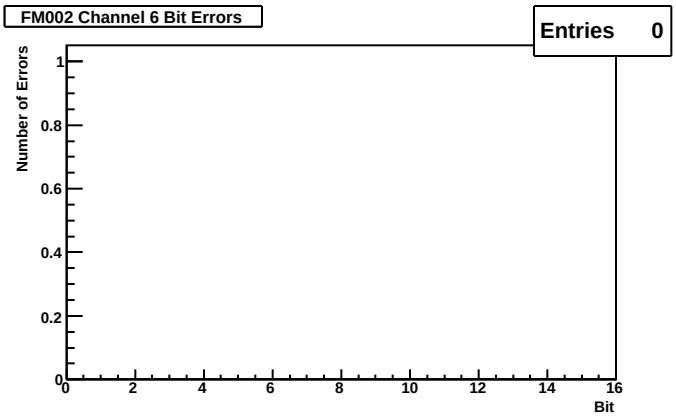

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

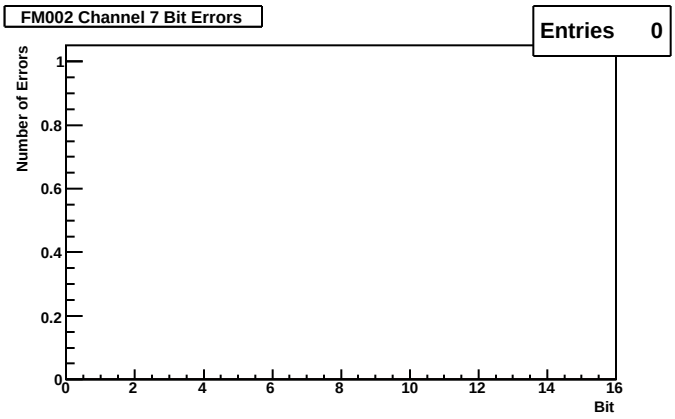

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

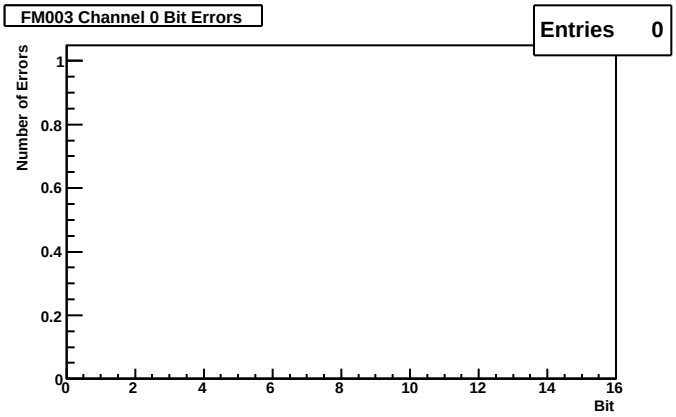

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

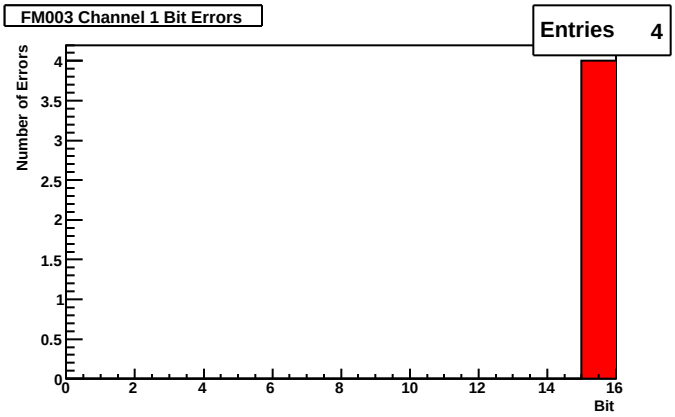

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

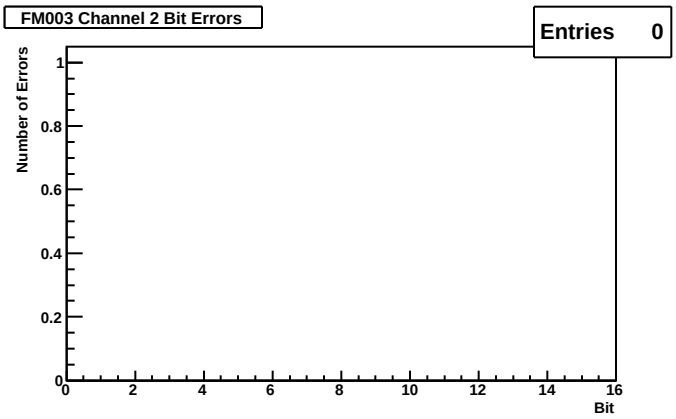

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

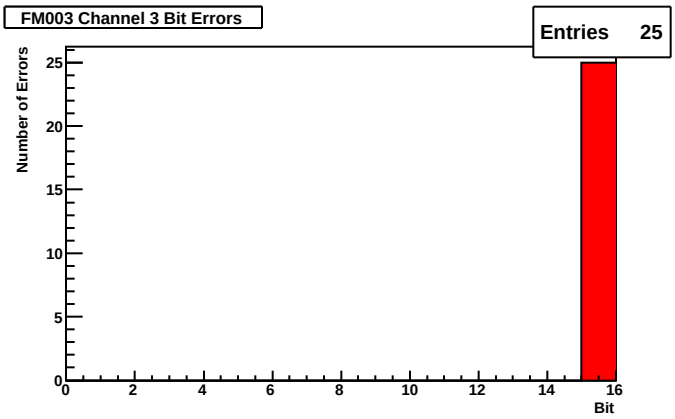

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

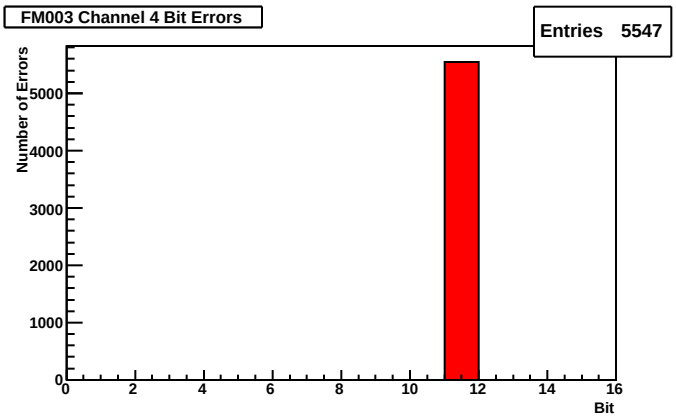

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

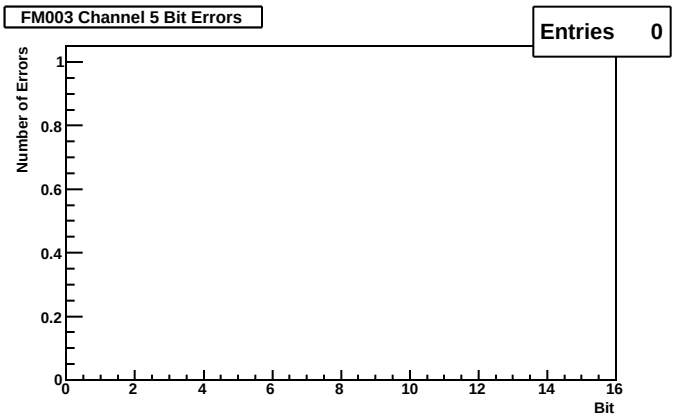

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

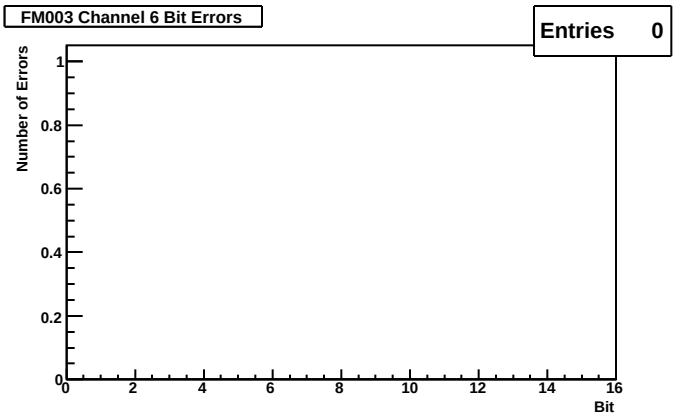

FM003 Channel 1 Bit Errors
Entries 4
Number of Errors
Bit

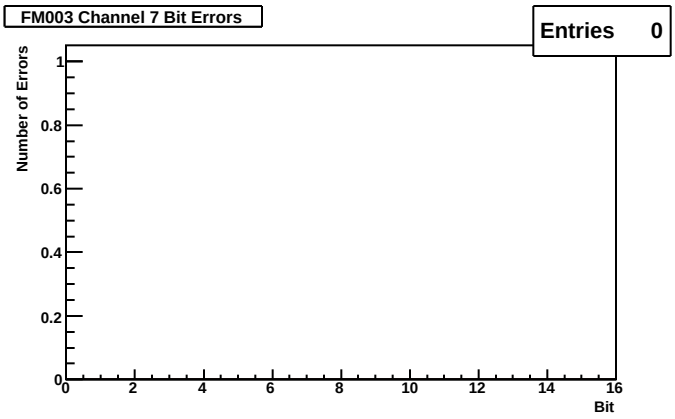

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

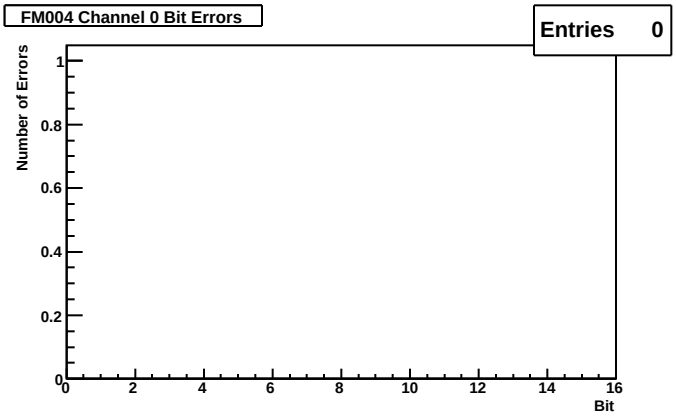

FM003 Channel 3 Bit Errors
Entries 25
Number of Errors
Bit

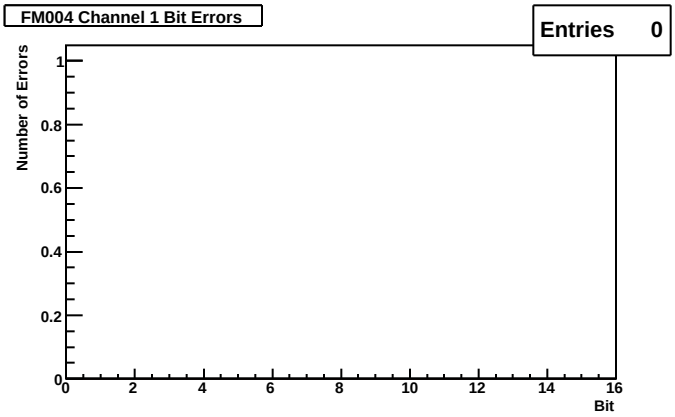

FM003 Channel 4 Bit Errors
Entries 5547
Number of Errors
Bit

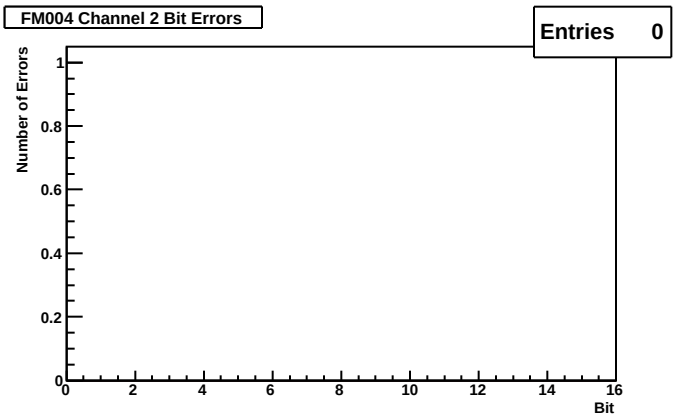

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

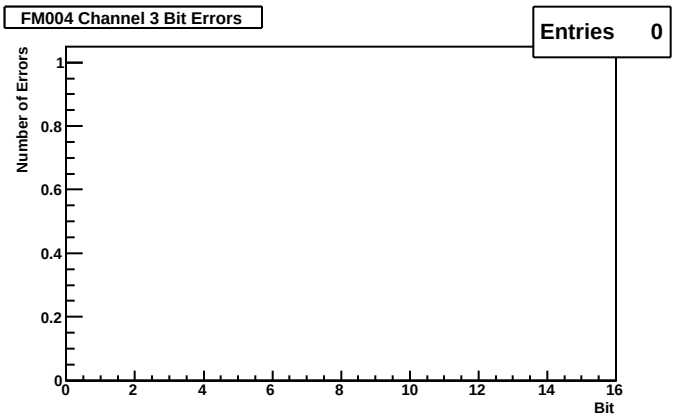

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

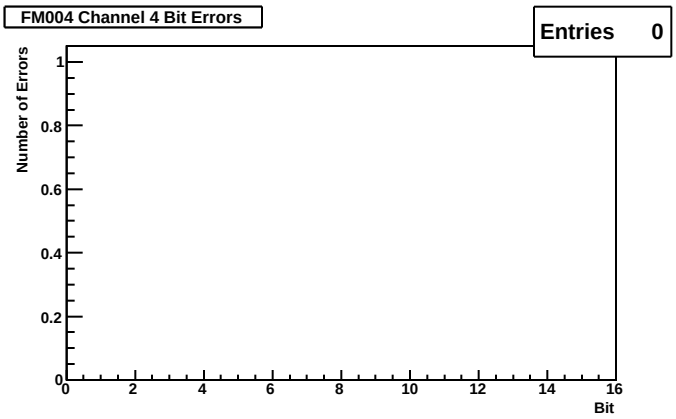

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

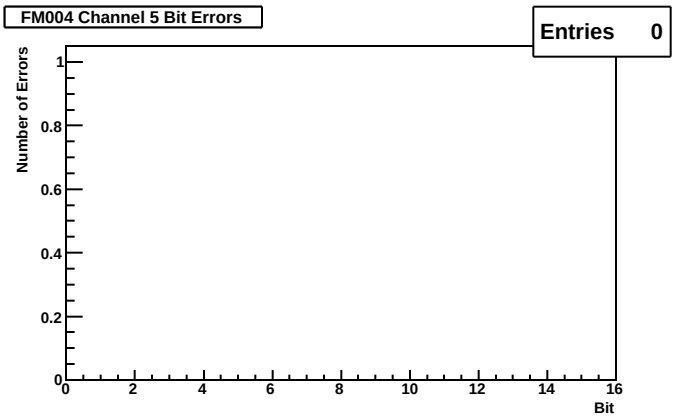

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

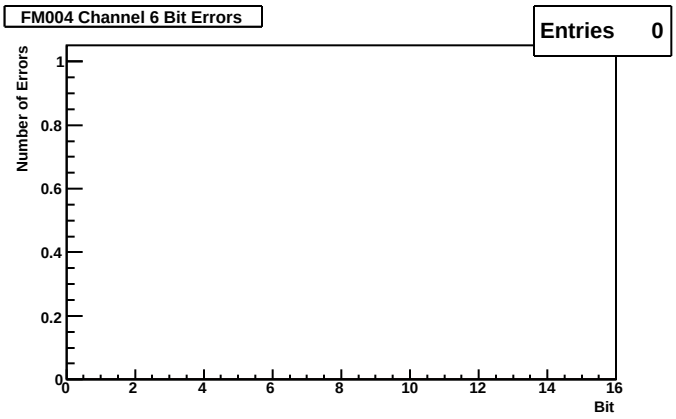

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

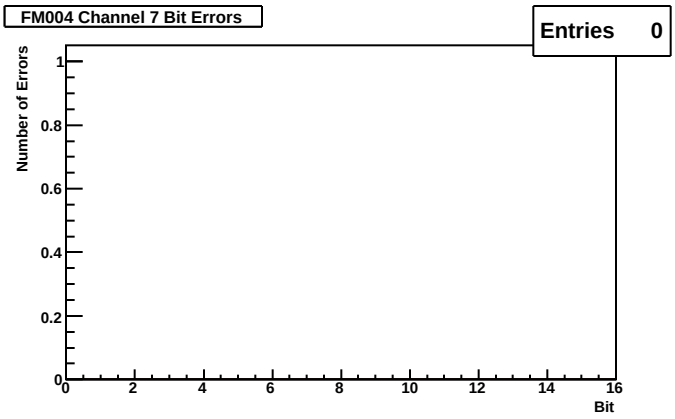

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

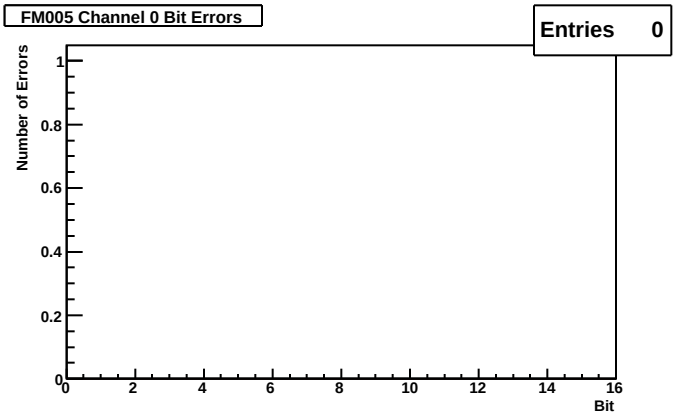

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

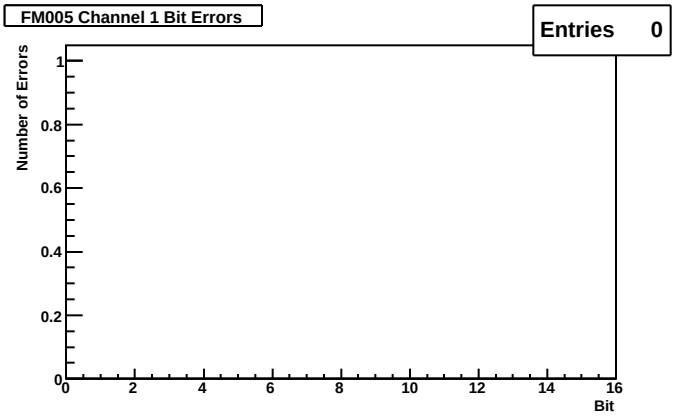

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

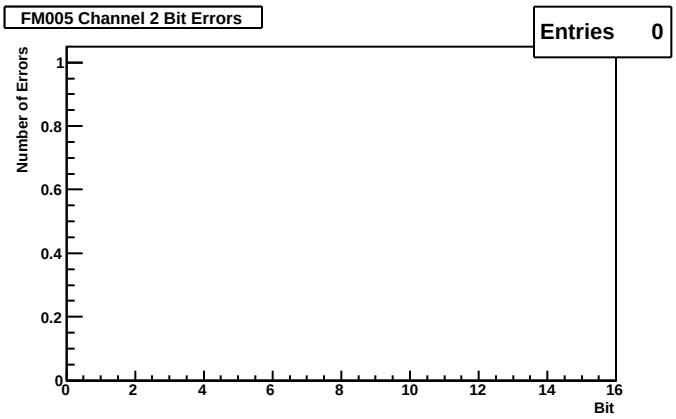

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

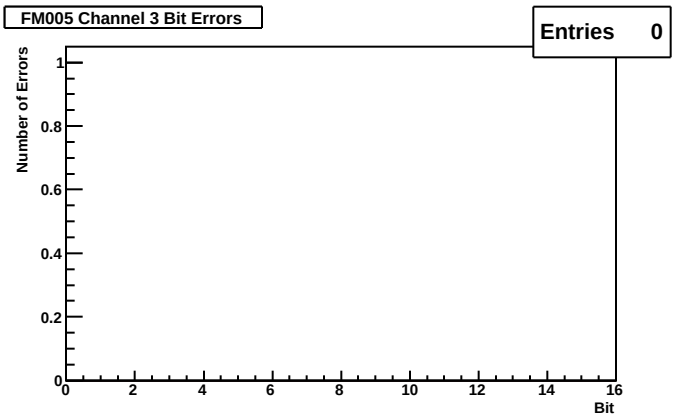

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

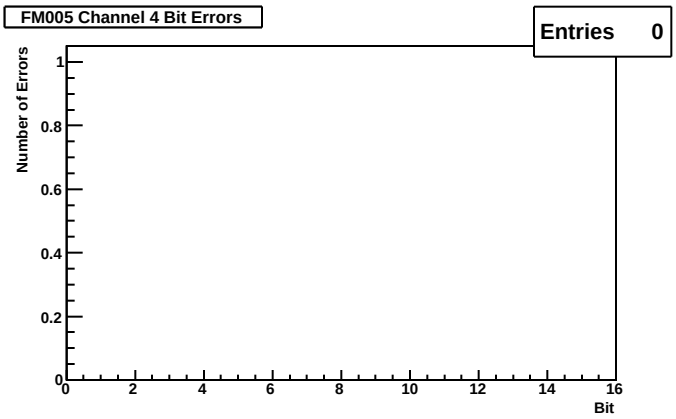

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

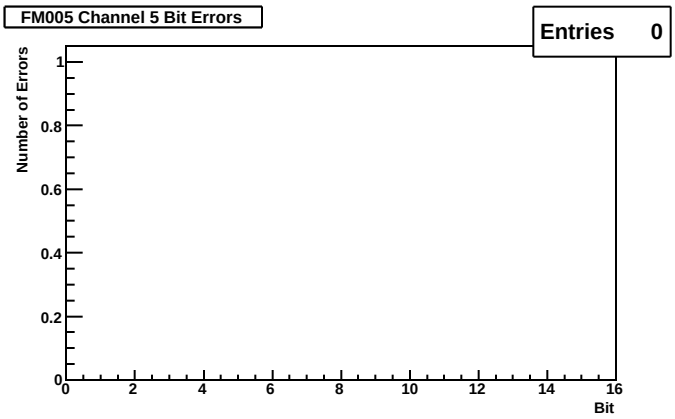

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

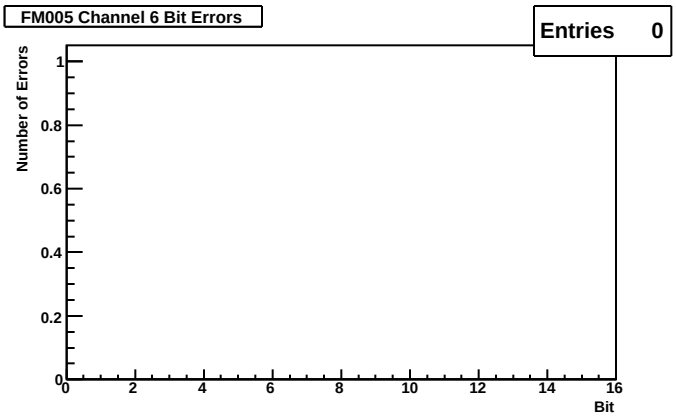

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

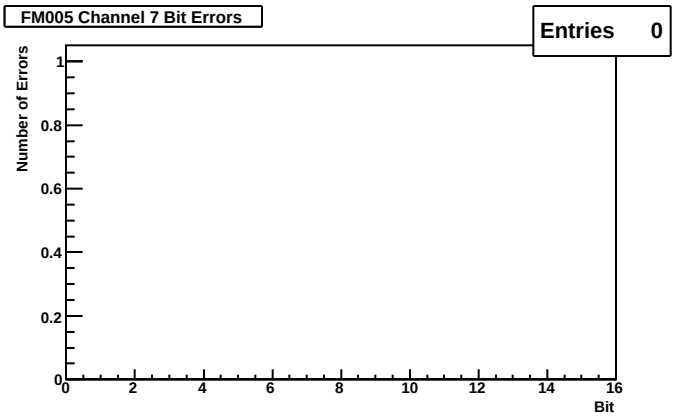

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

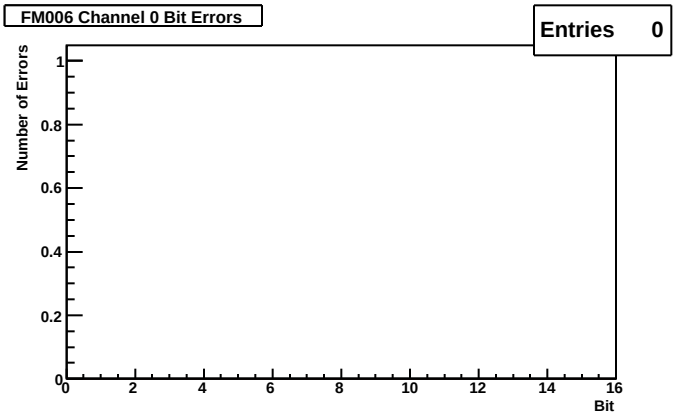

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

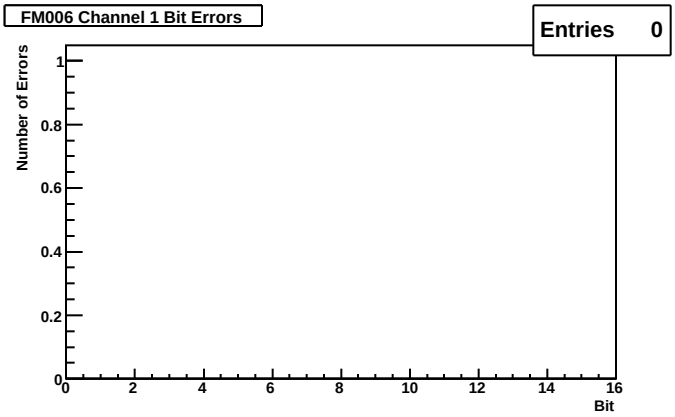

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

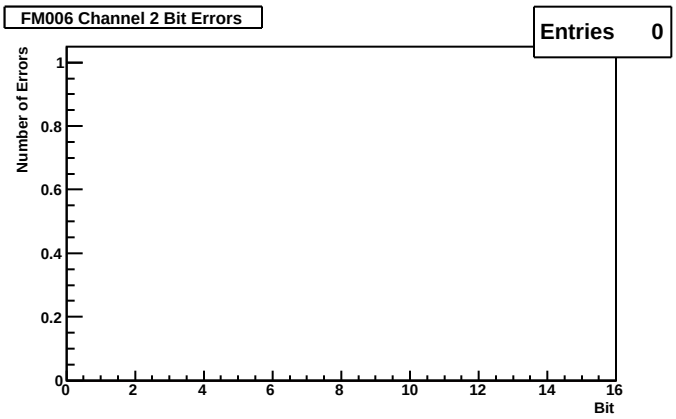

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

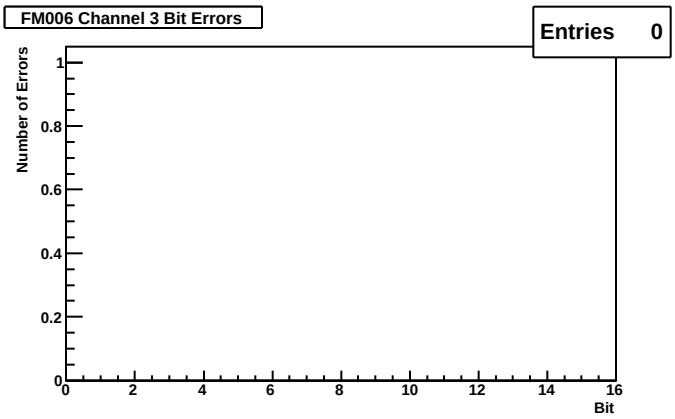

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

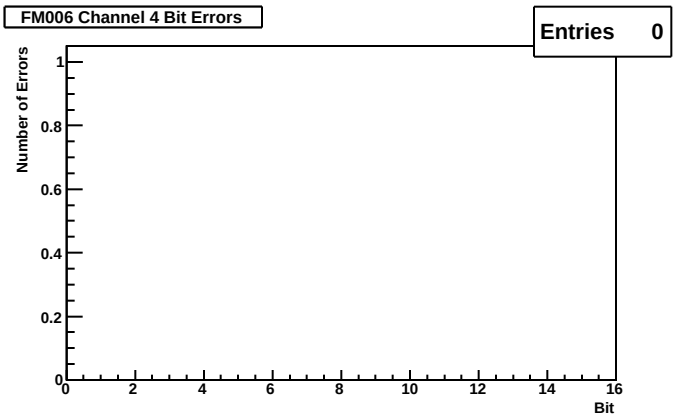

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

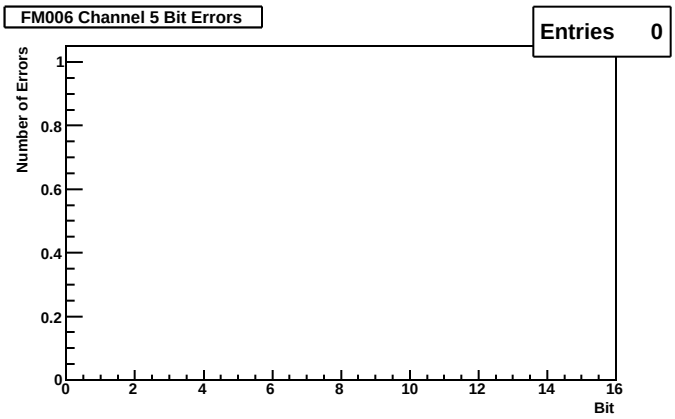

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

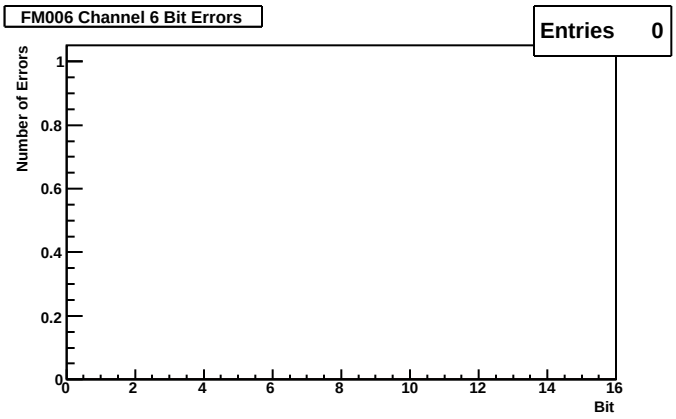

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

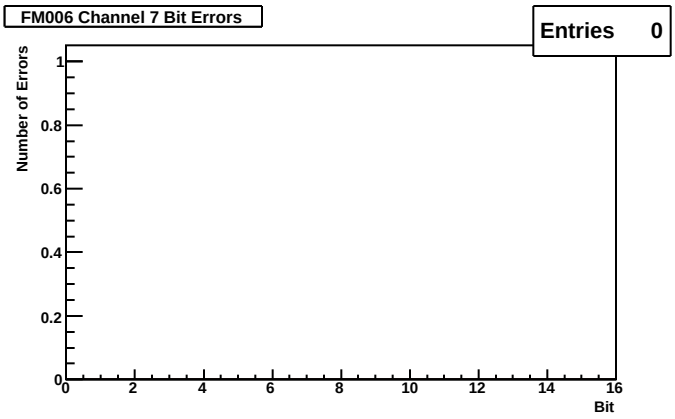

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

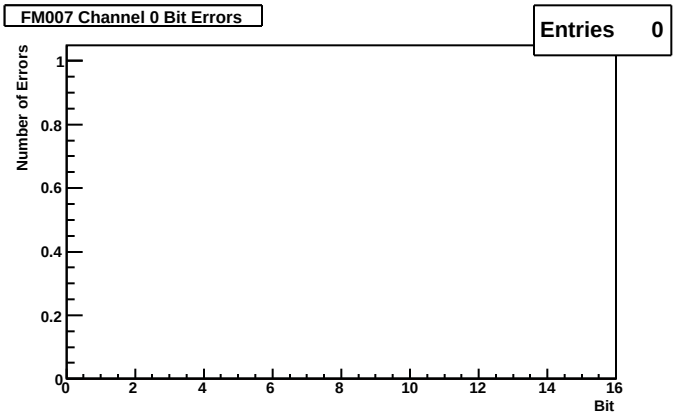

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

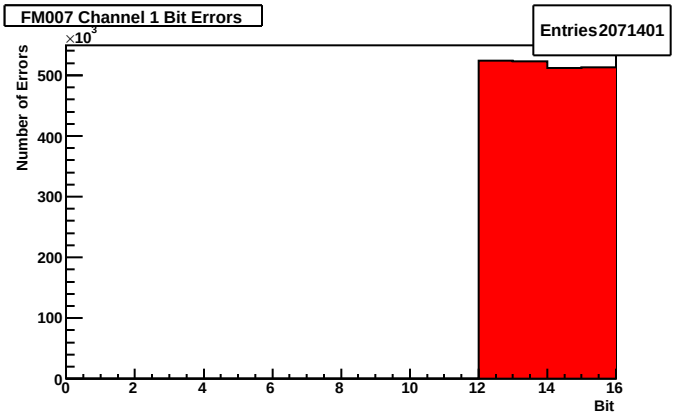

FM006 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

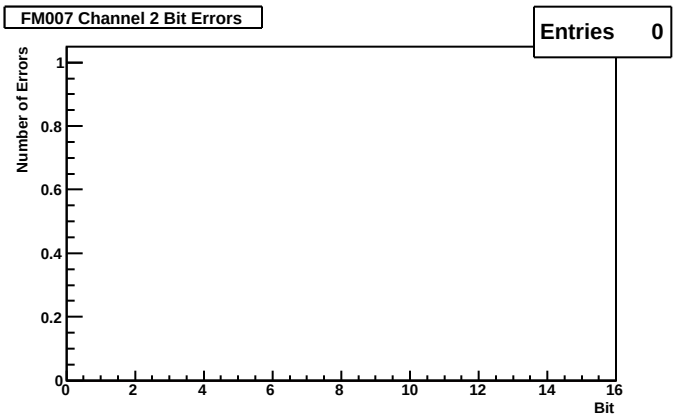

FM006 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

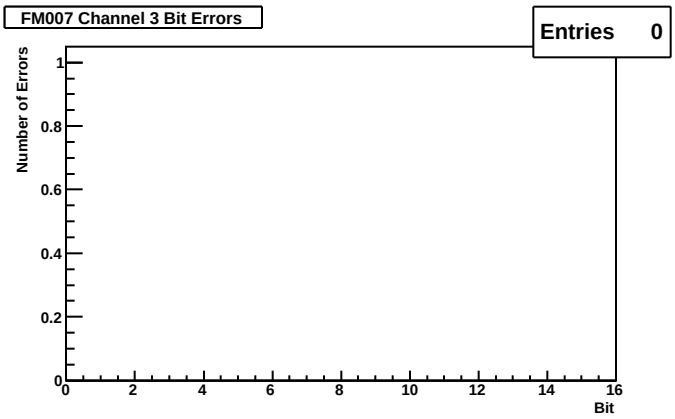

FM006 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

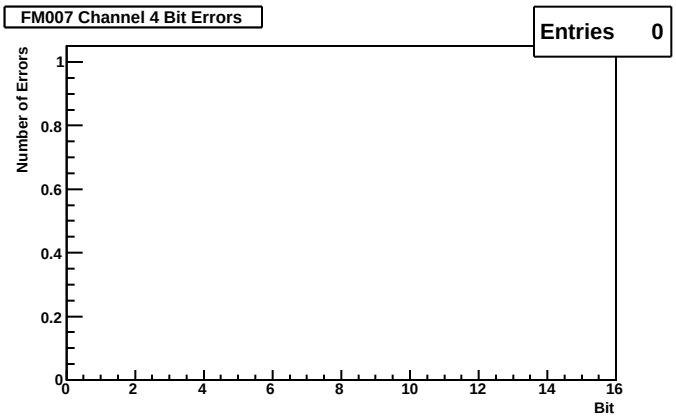

FM006 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

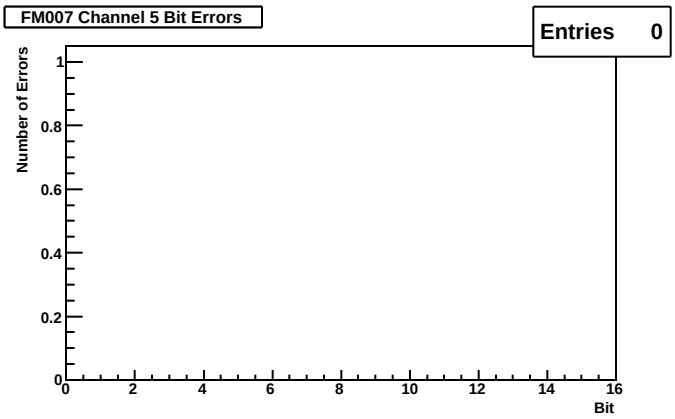

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

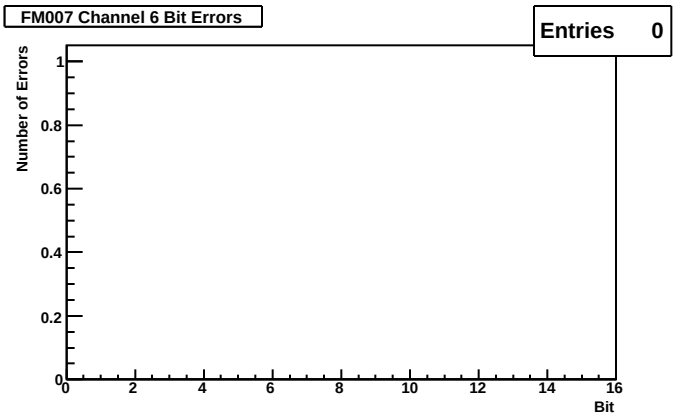

FM007 Channel 1 Bit Errors
Entries 2071401
×10³
Number of Errors
Bit

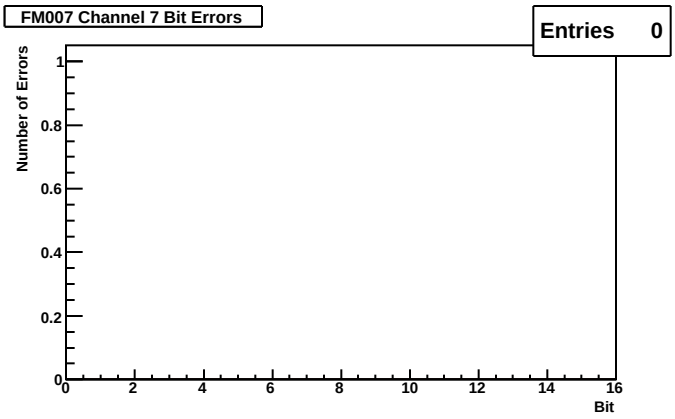

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

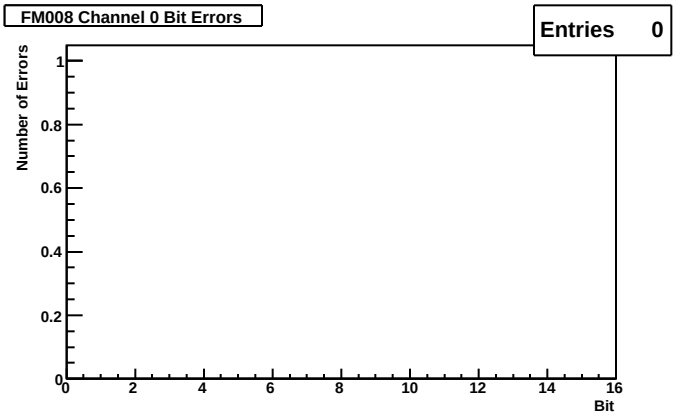

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

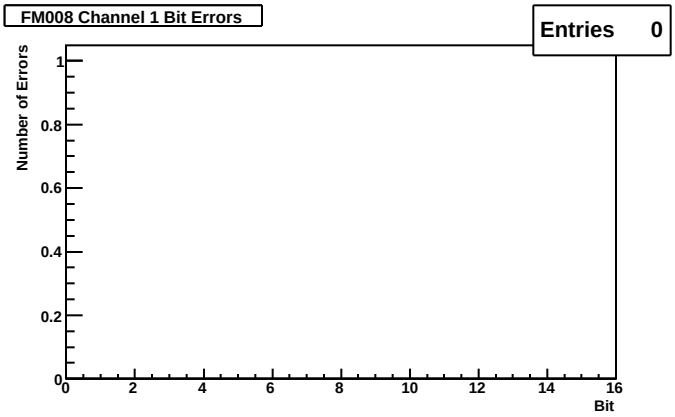

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

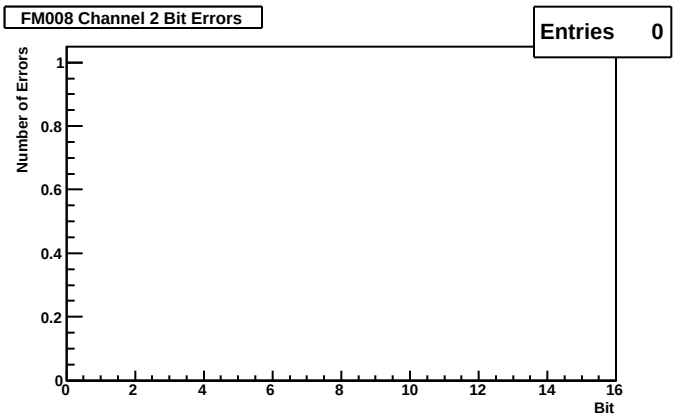

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

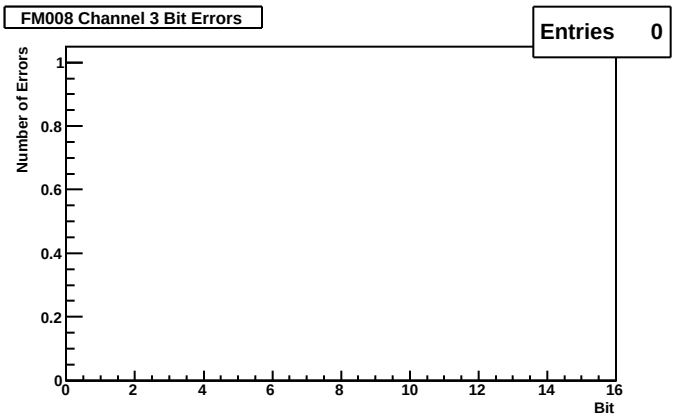

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

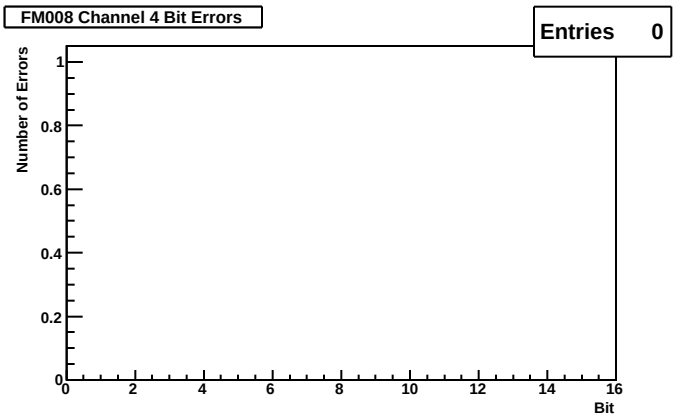

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

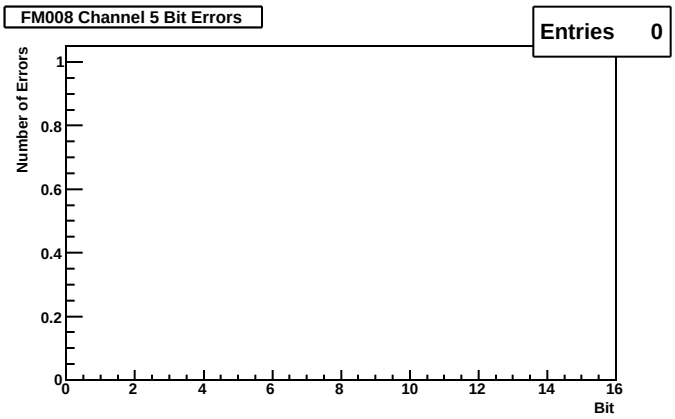

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

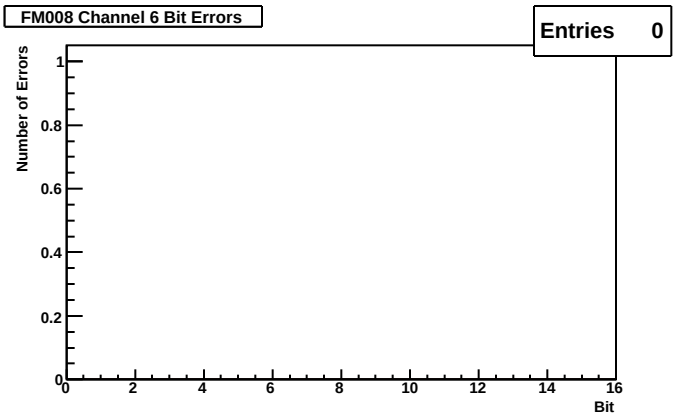

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

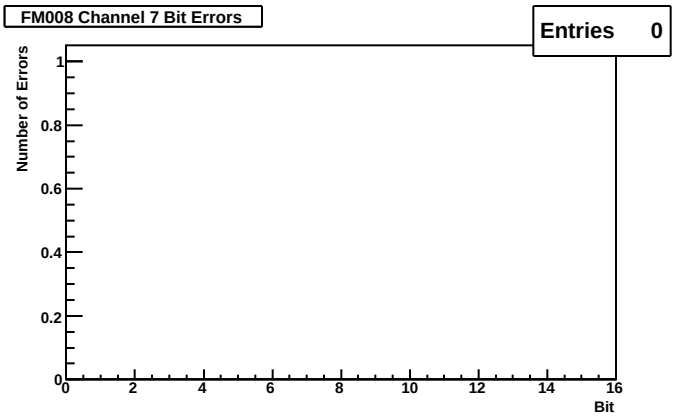

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

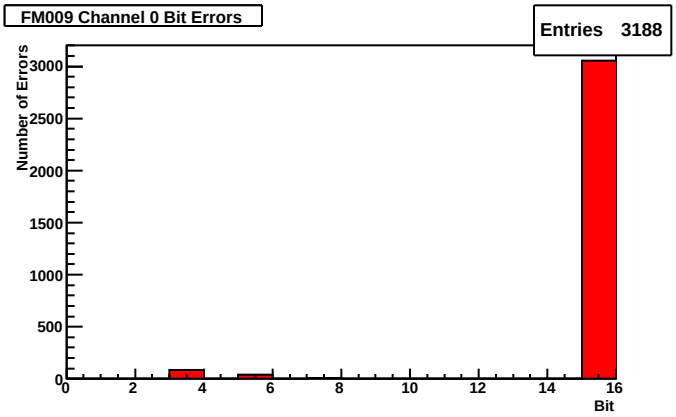

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

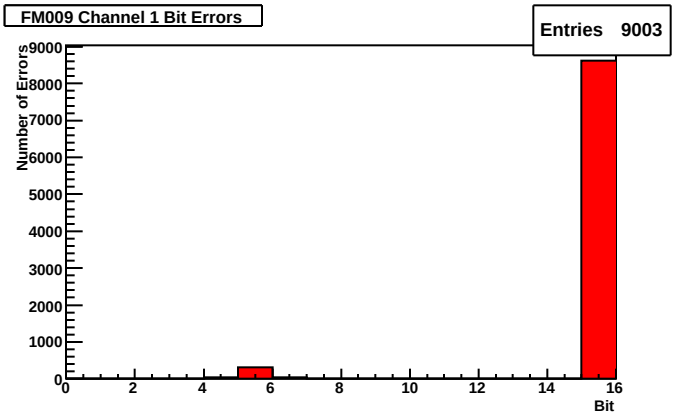

FM008 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

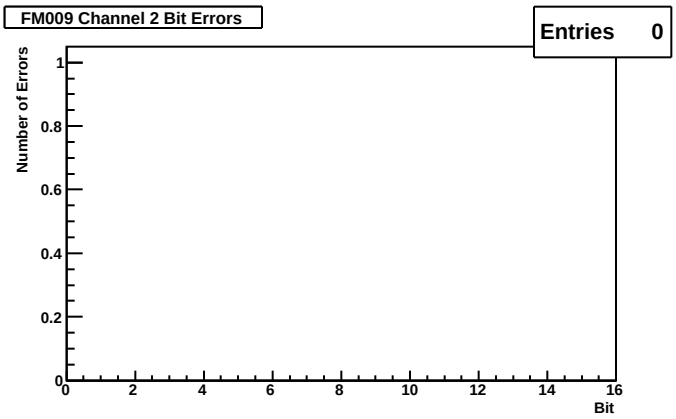

FM008 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

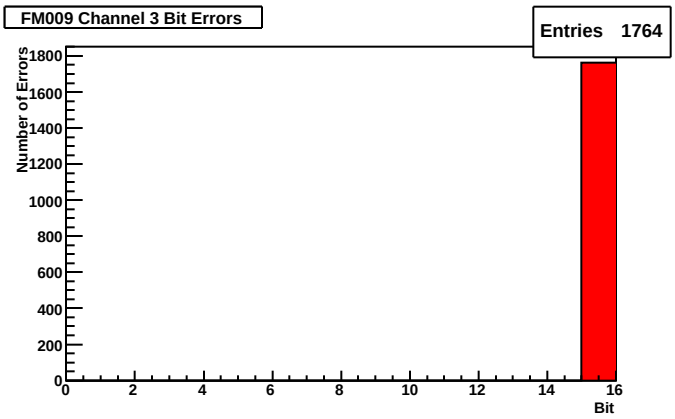

FM008 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

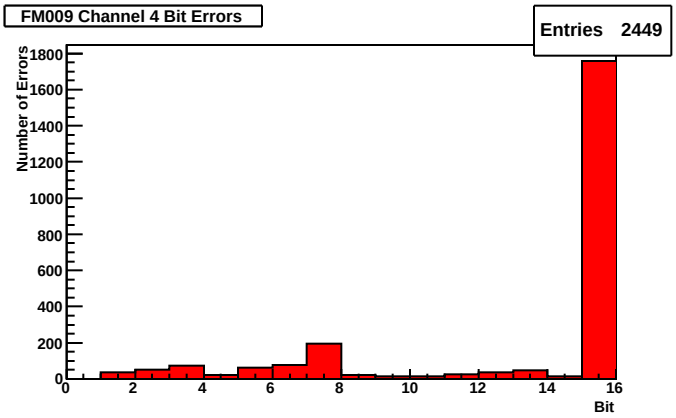

FM008 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

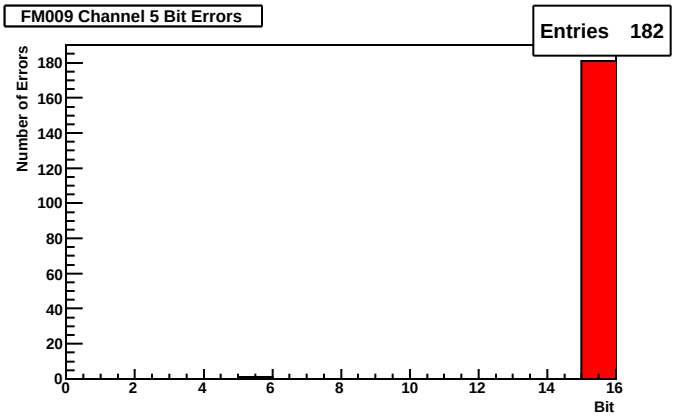

FM009 Channel 0 Bit Errors
Entries 3188
Number of Errors
Bit

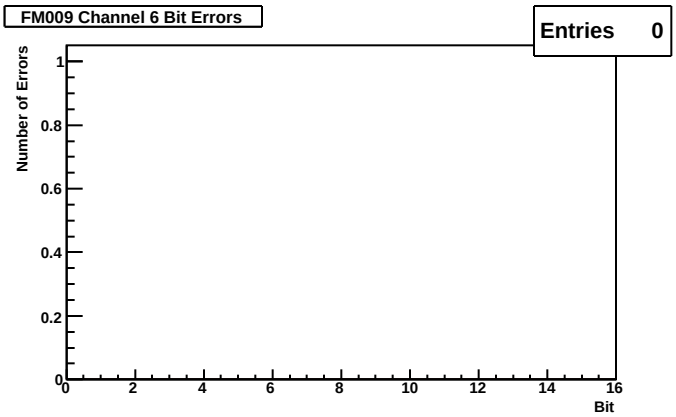

FM009 Channel 1 Bit Errors
Entries 9003
Number of Errors
Bit

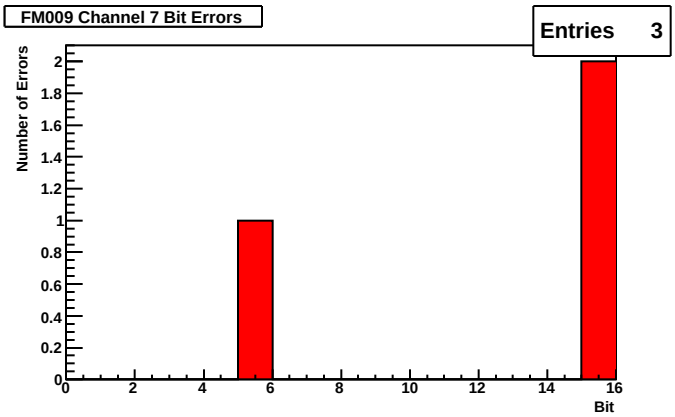

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

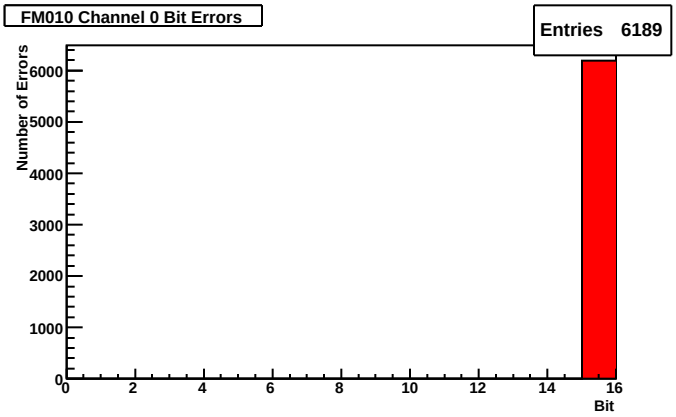

FM009 Channel 3 Bit Errors
Entries 1764
Number of Errors
Bit

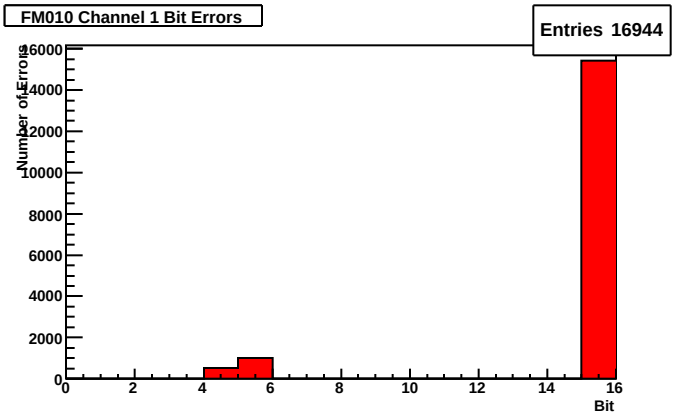

FM009 Channel 4 Bit Errors
Entries 2449
Number of Errors
Bit

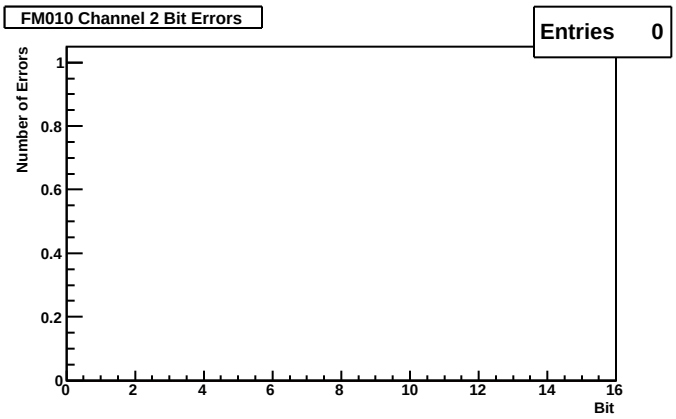

FM009 Channel 5 Bit Errors
Entries 182
Number of Errors
Bit

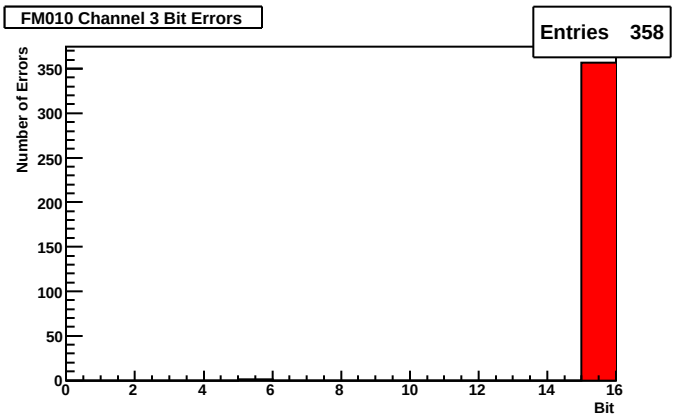

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM009 Channel 7 Bit Errors
Entries 3
Number of Errors
Bit


FM010 Channel 0 Bit Errors
Entries 6189
Number of Errors
Bit

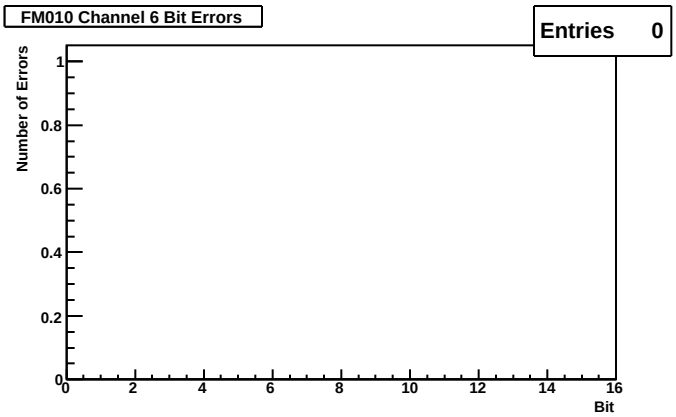

FM010 Channel 1 Bit Errors
Entries 16944
Number of Errors
Bit

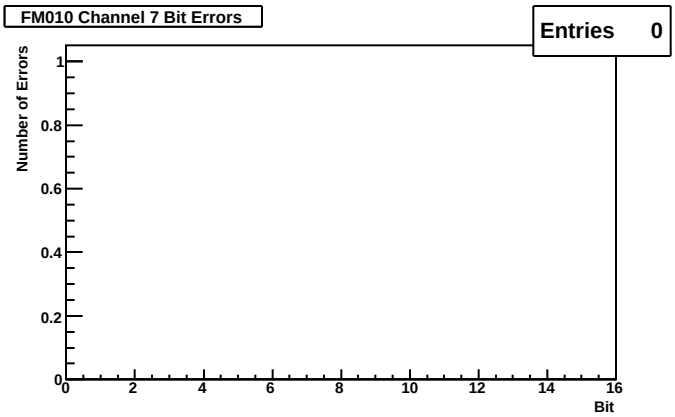

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

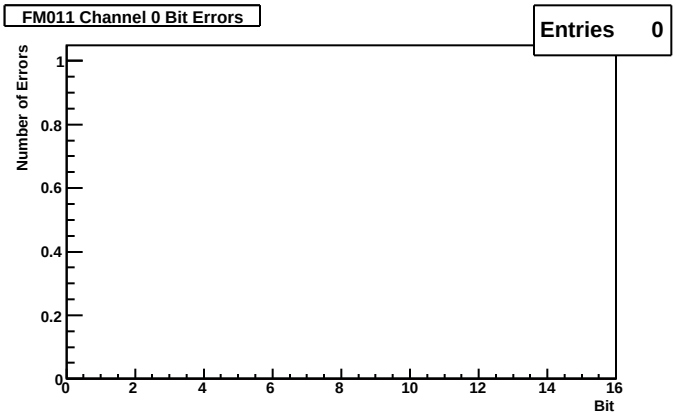

FM010 Channel 3 Bit Errors
Entries 358
Number of Errors
Bit

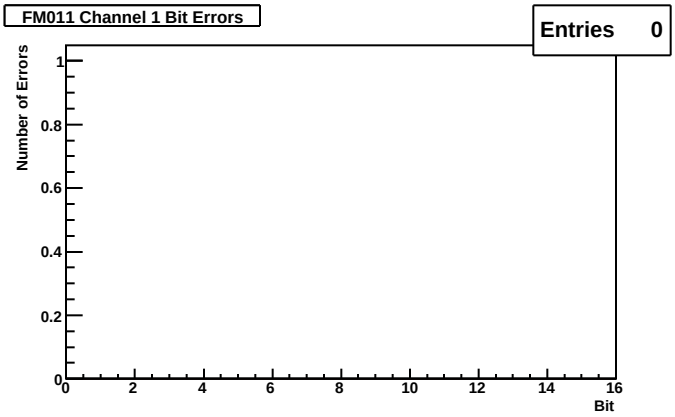
FM010 Channel 4 Bit Errors
Entries 1175537
Number of Errors
Bit

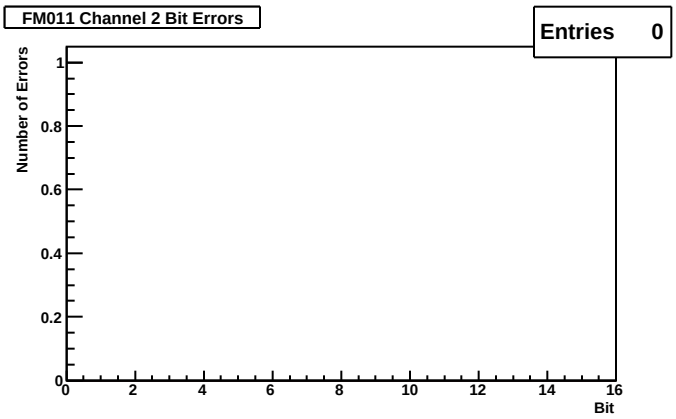
FM010 Channel 5 Bit Errors
Entries 868594
Number of Errors
Bit

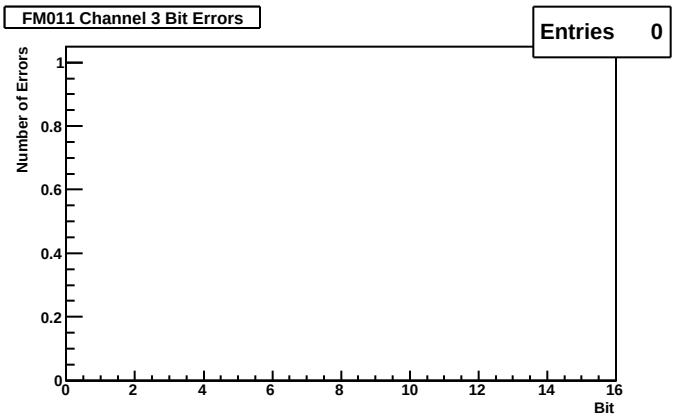

FM010 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

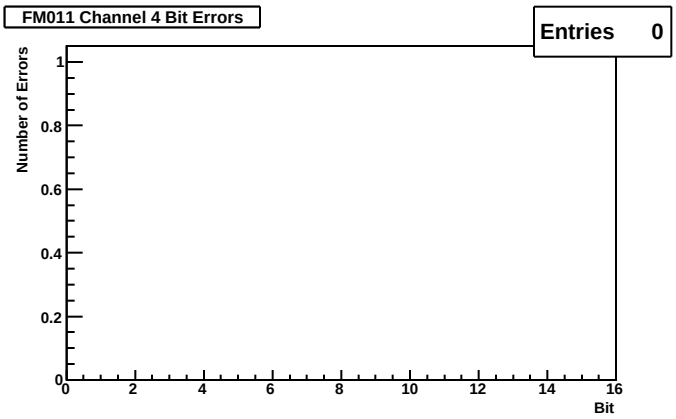

FM010 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

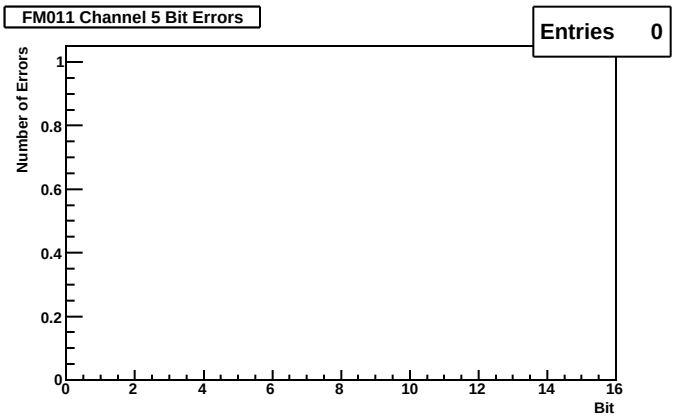

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

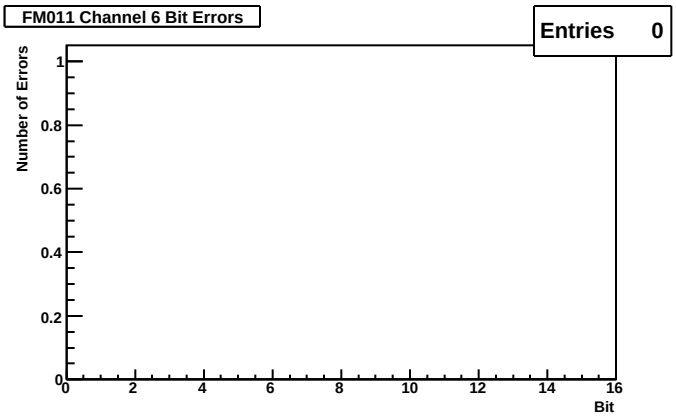

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

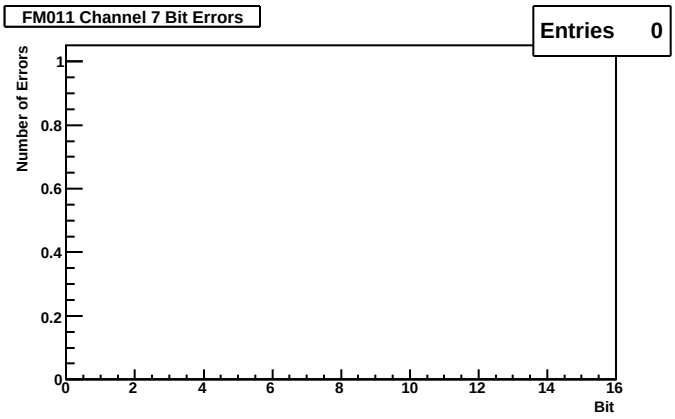

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

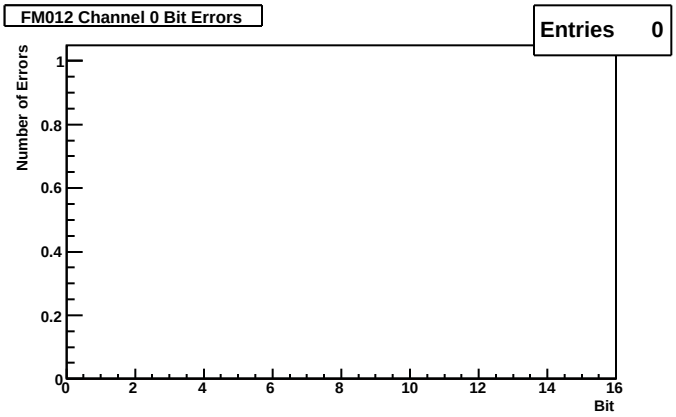

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

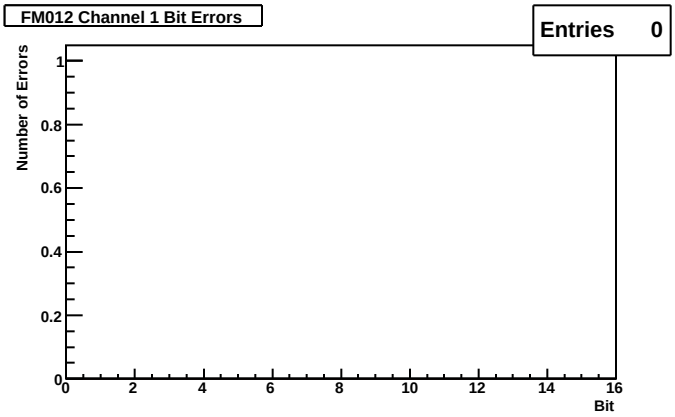

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

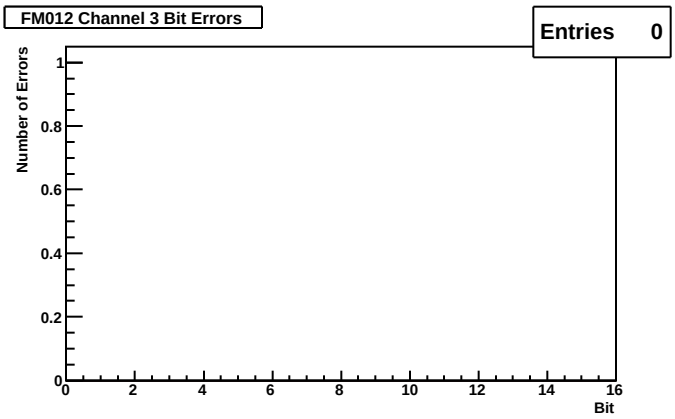

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

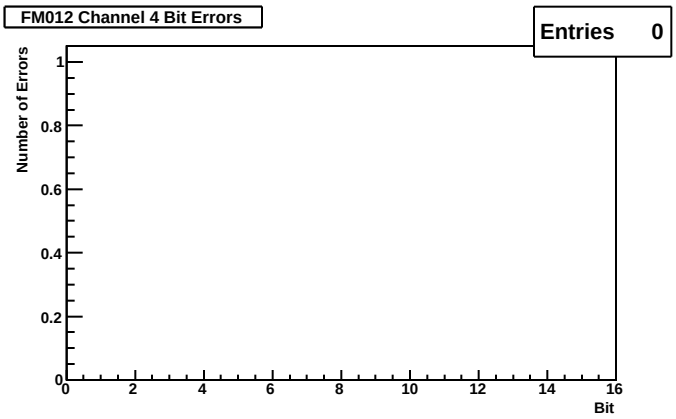

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

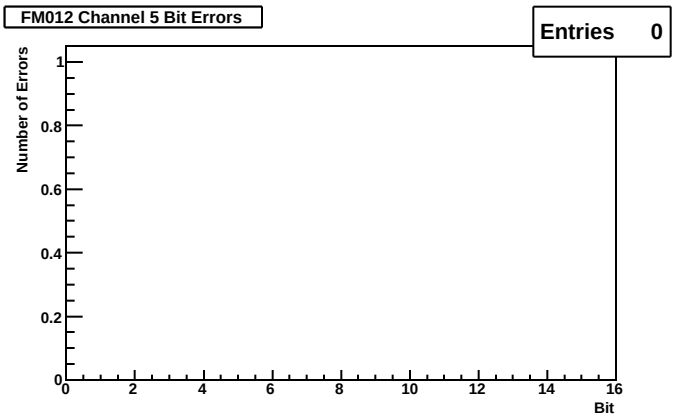

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

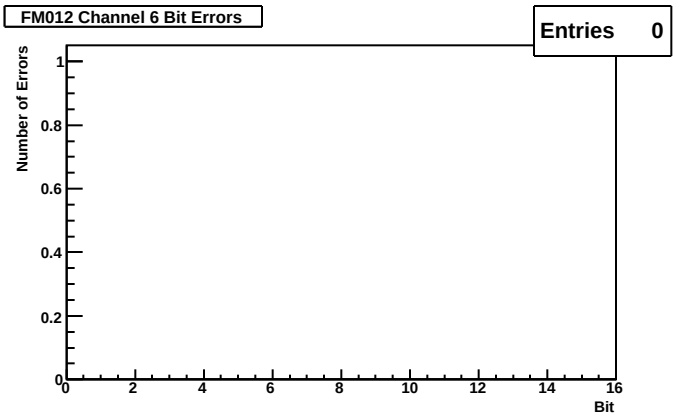

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

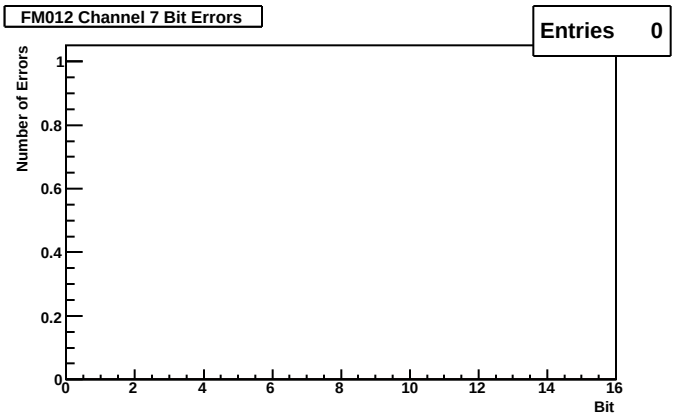
FM012 Channel 2 Bit Errors
Entries 1
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit