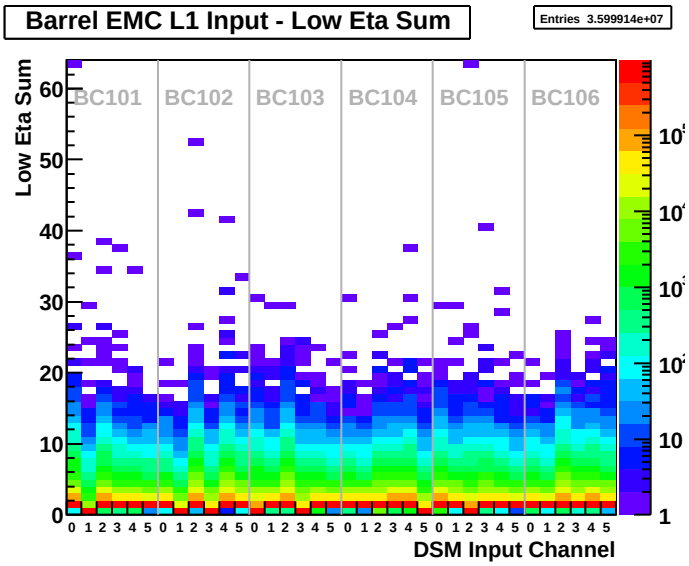
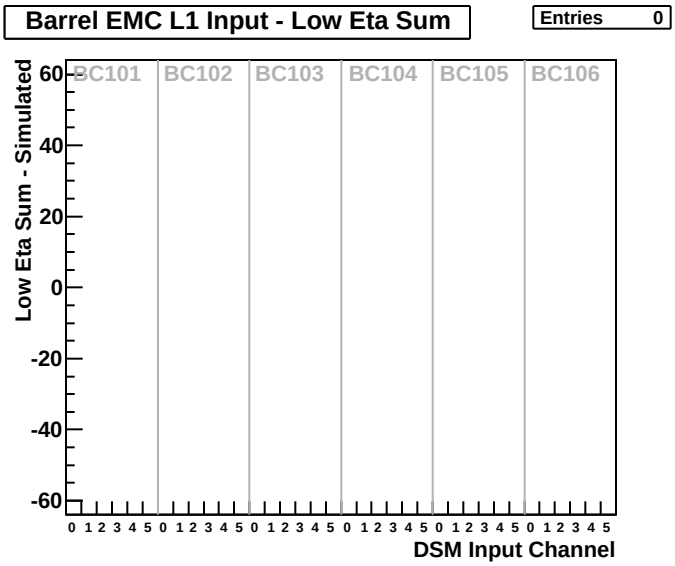
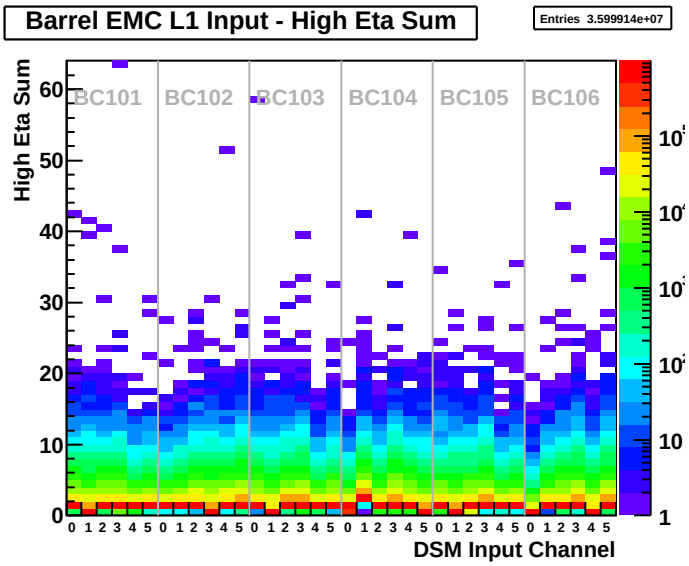
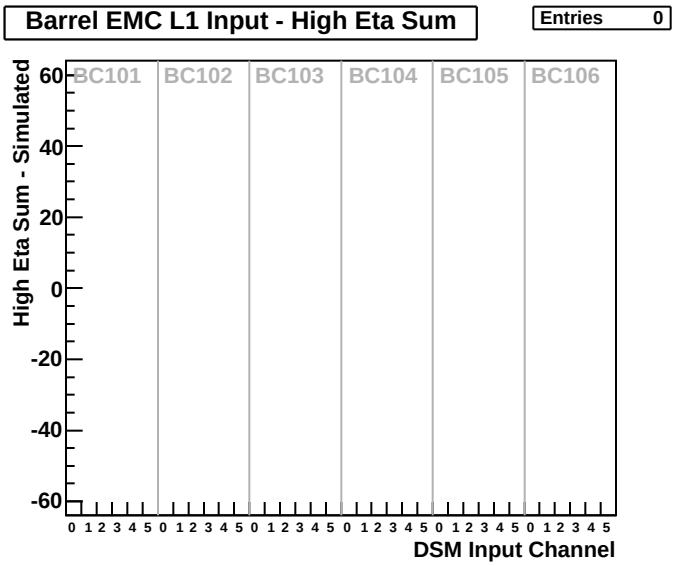
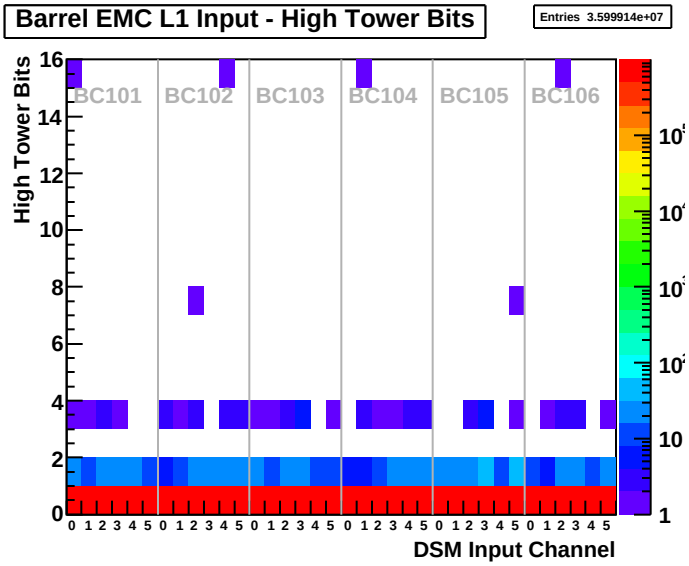
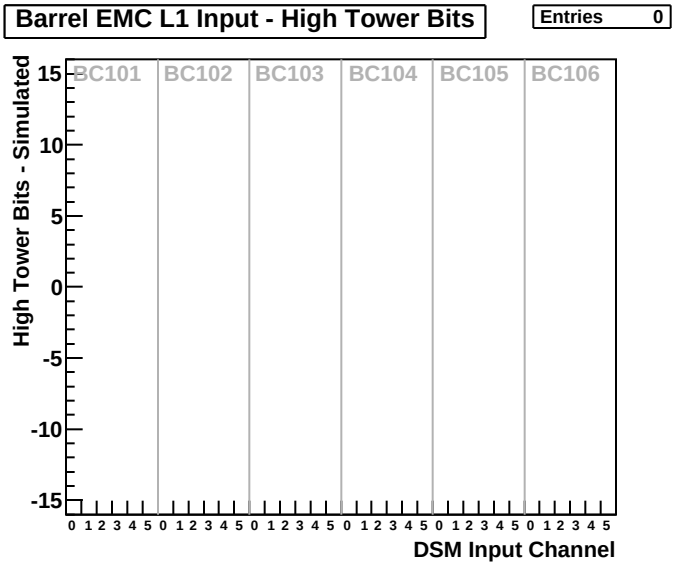

Barrel EMC L1 Input - Low Eta Sum
Entries 3.599914e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

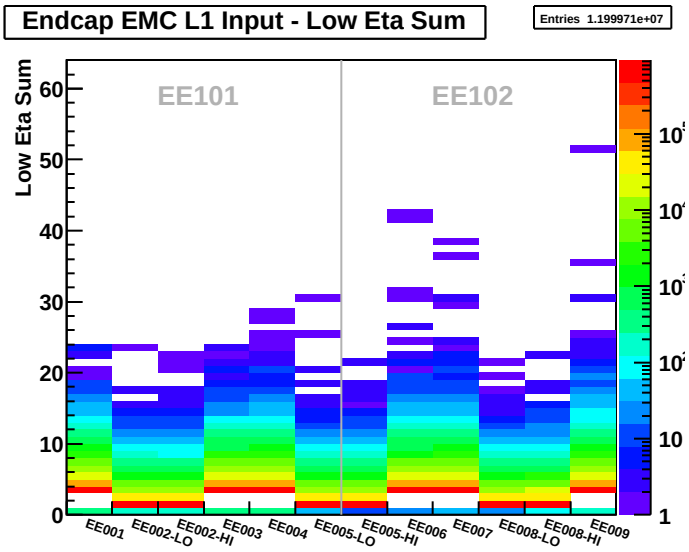

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

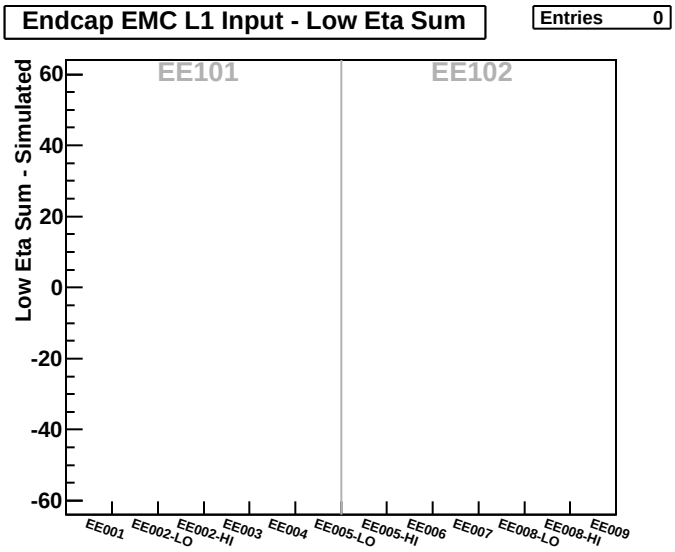

Barrel EMC L1 Input - High Eta Sum
Entries 3.599914e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

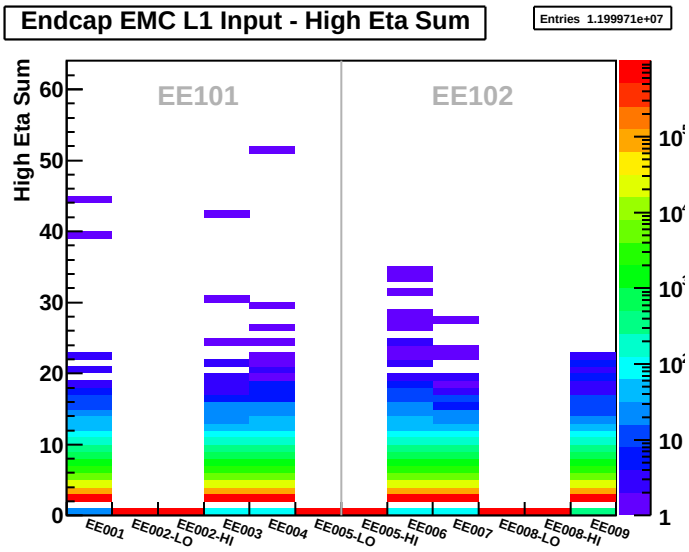

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

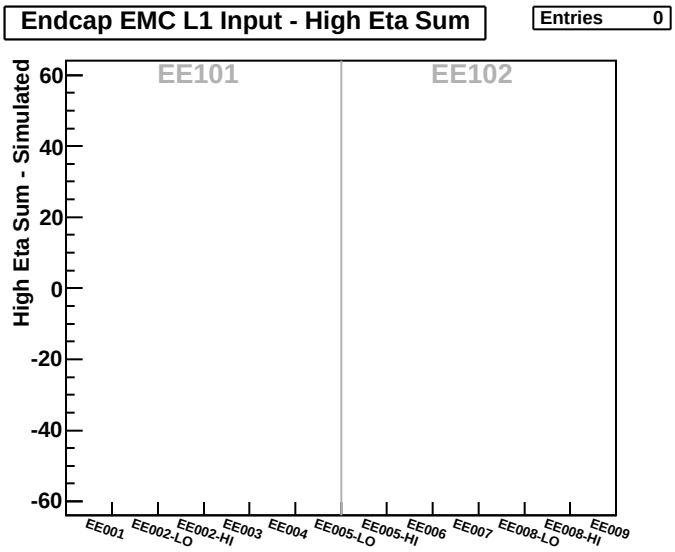

Barrel EMC L1 Input - High Tower Bits
Entries 3.599914e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

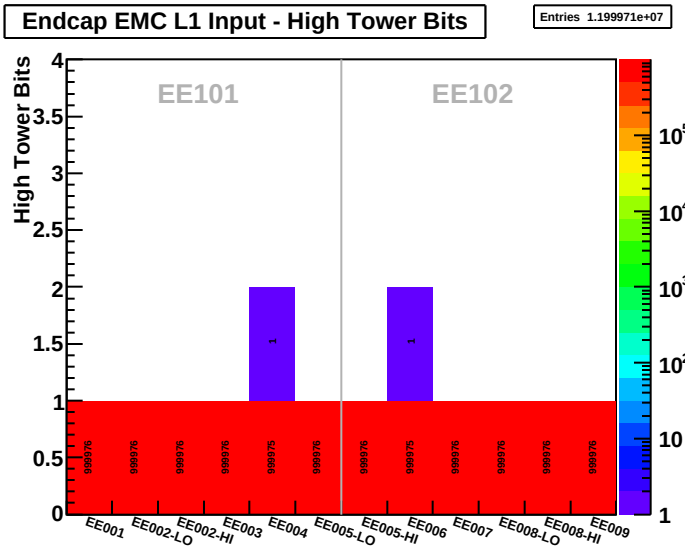

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

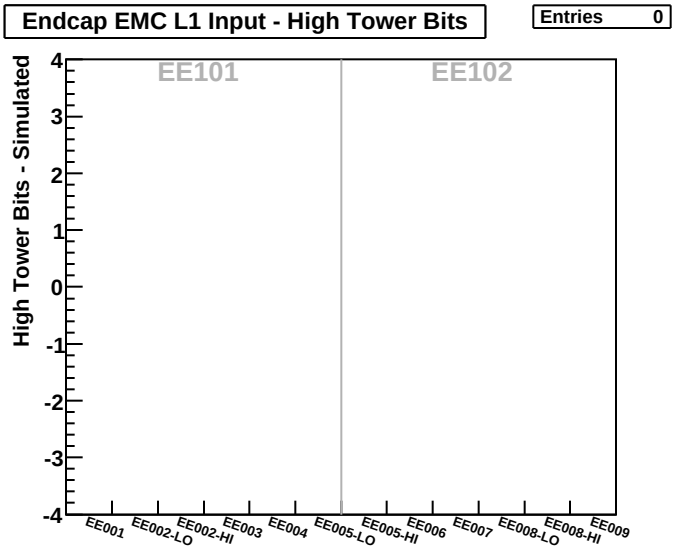

Endcap EMC L1 Input - Low Eta Sum
Entries 1.199971e+07
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

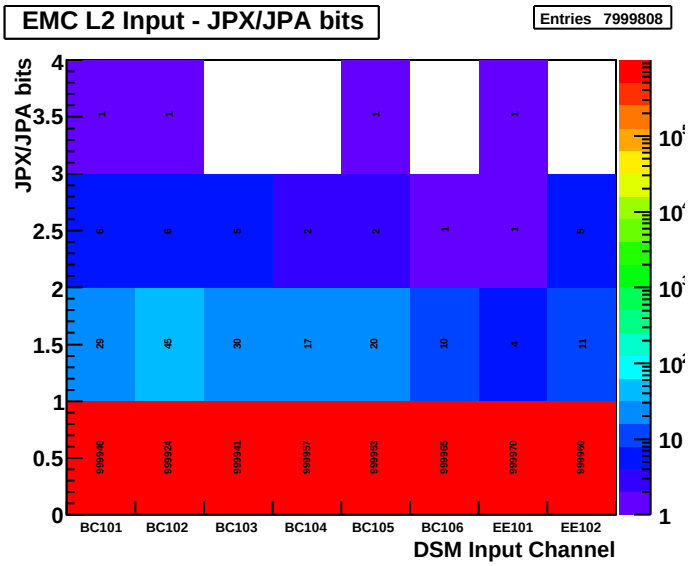

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

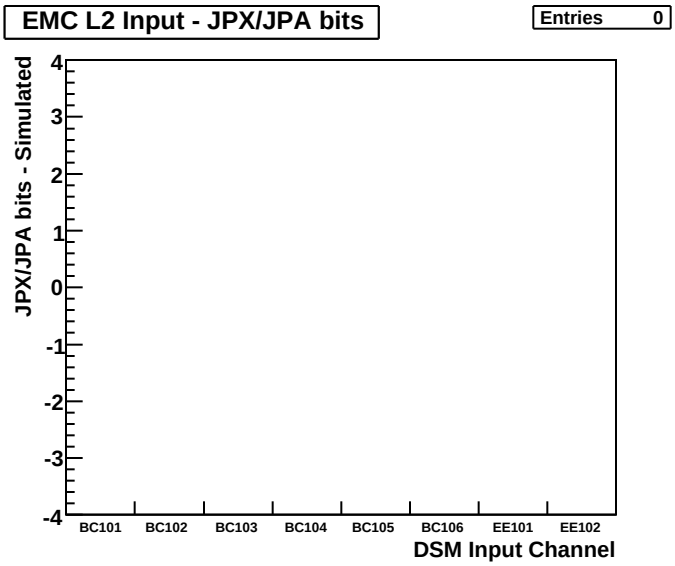

Endcap EMC L1 Input - High Eta Sum
Entries 1.199971e+07
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

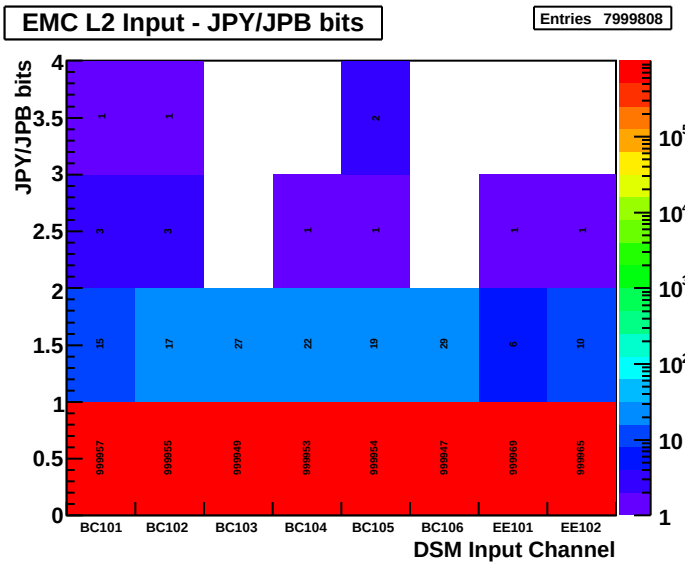

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

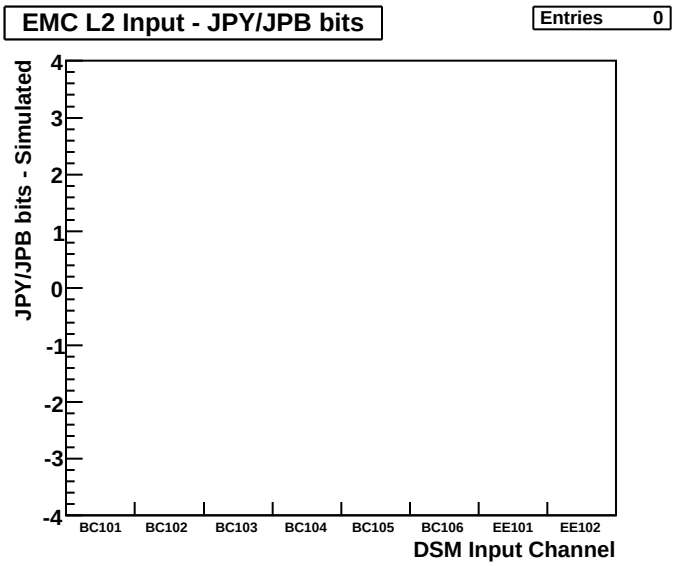

Endcap EMC L1 Input - High Tower Bits
Entries 1.199971e+07
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

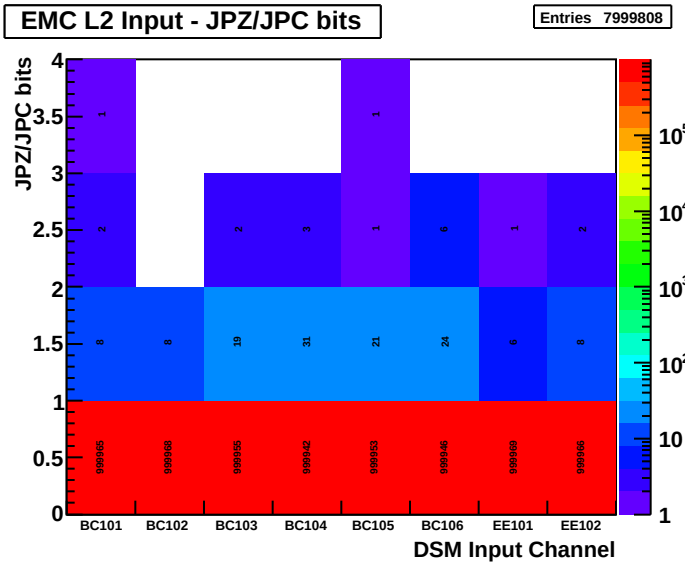

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

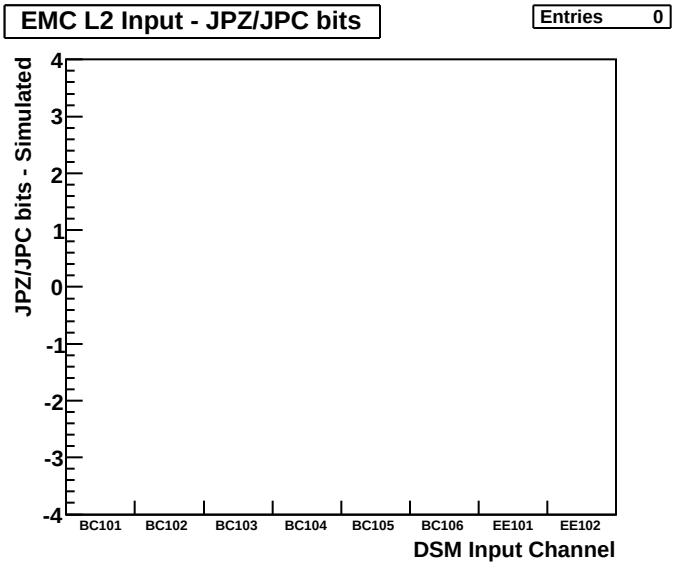

EMC L2 Input - JPX/JPA bits
Entries 7999808
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

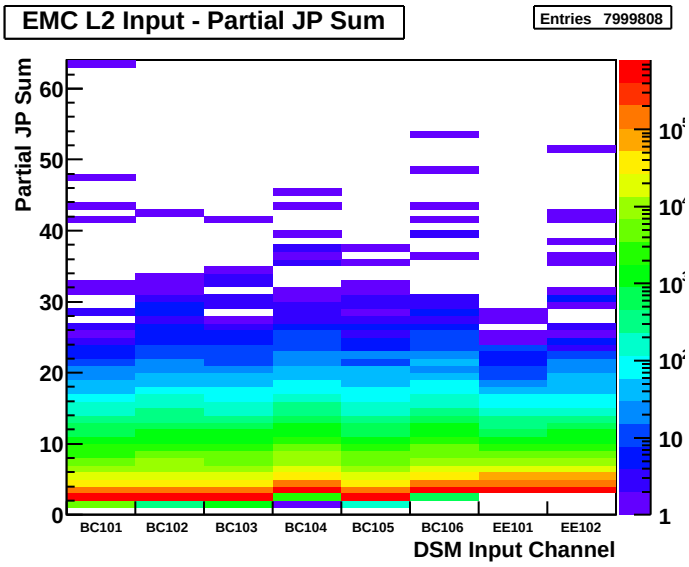

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

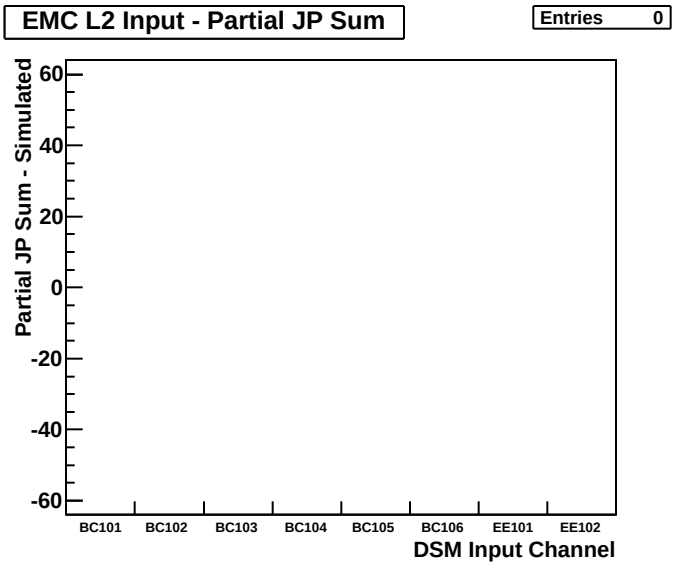

EMC L2 Input - JPY/JPB bits
Entries 7999808
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

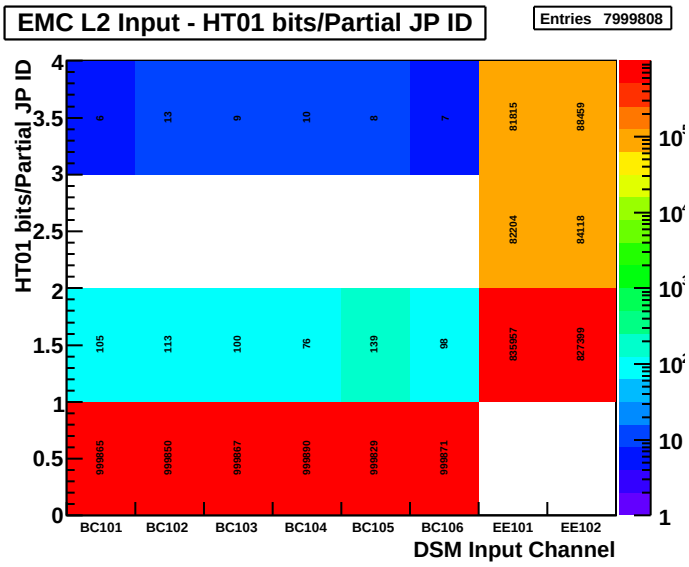

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

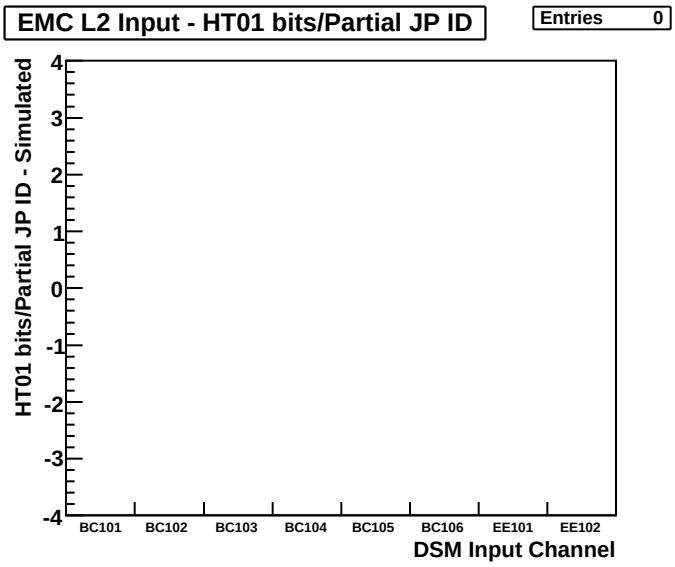

EMC L2 Input - JPZ/JPC bits
Entries 7999808
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

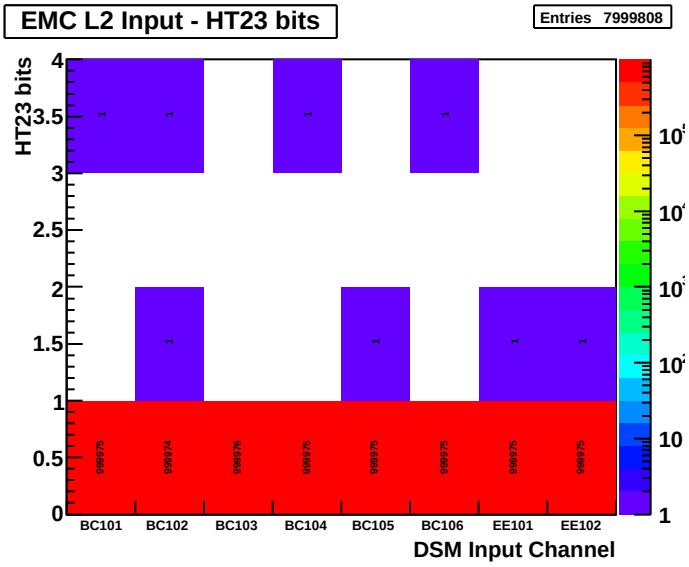

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

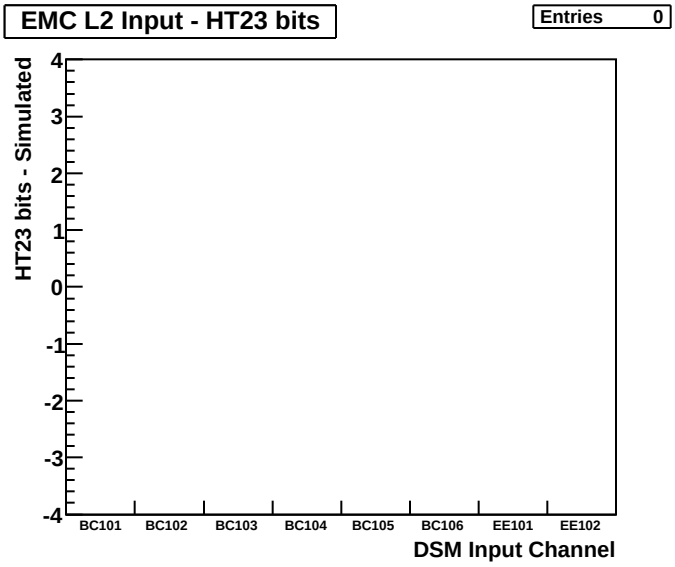

EMC L2 Input - Partial JP Sum
Entries 7999808
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

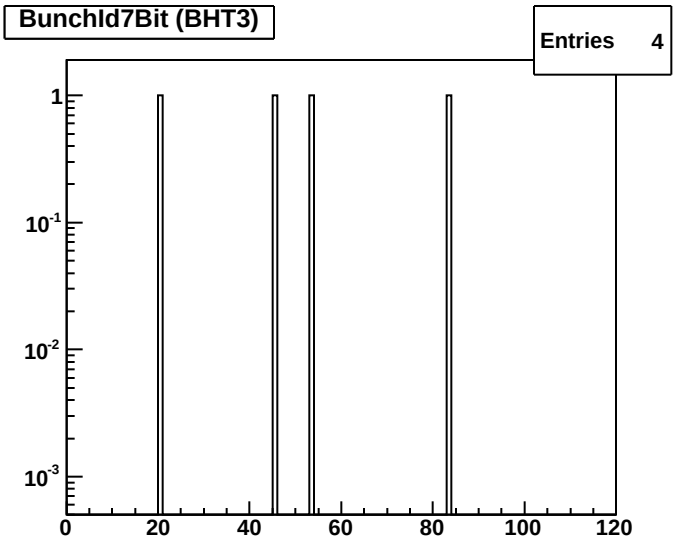

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

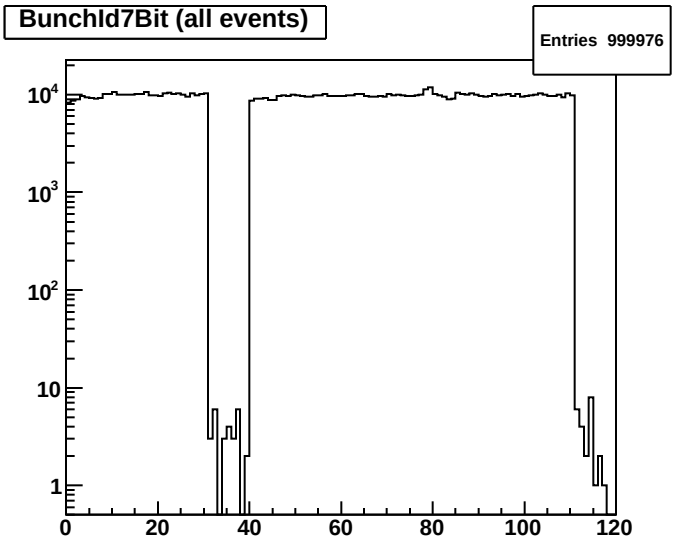

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7999808
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

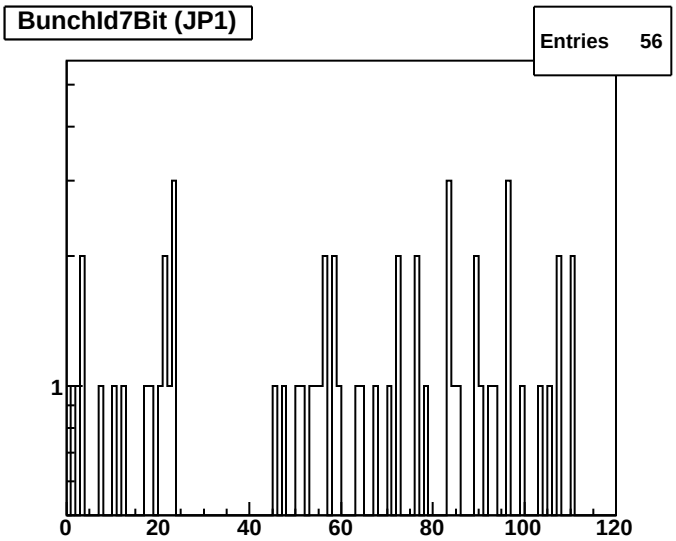

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

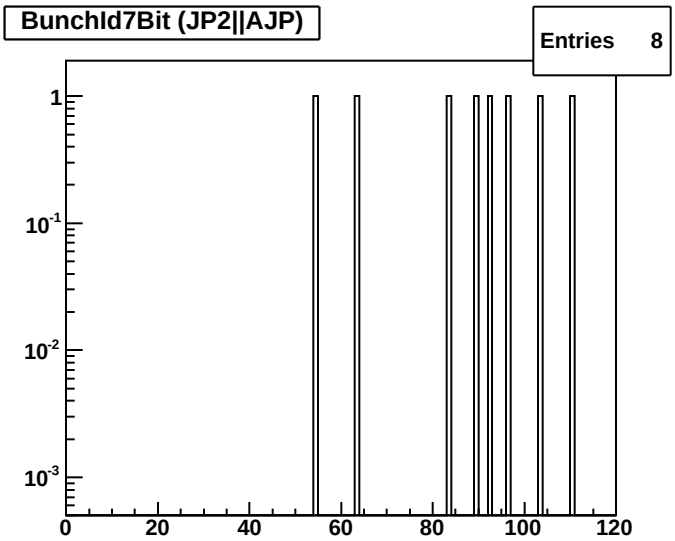

EMC L2 Input - HT23 bits
Entries 7999808
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

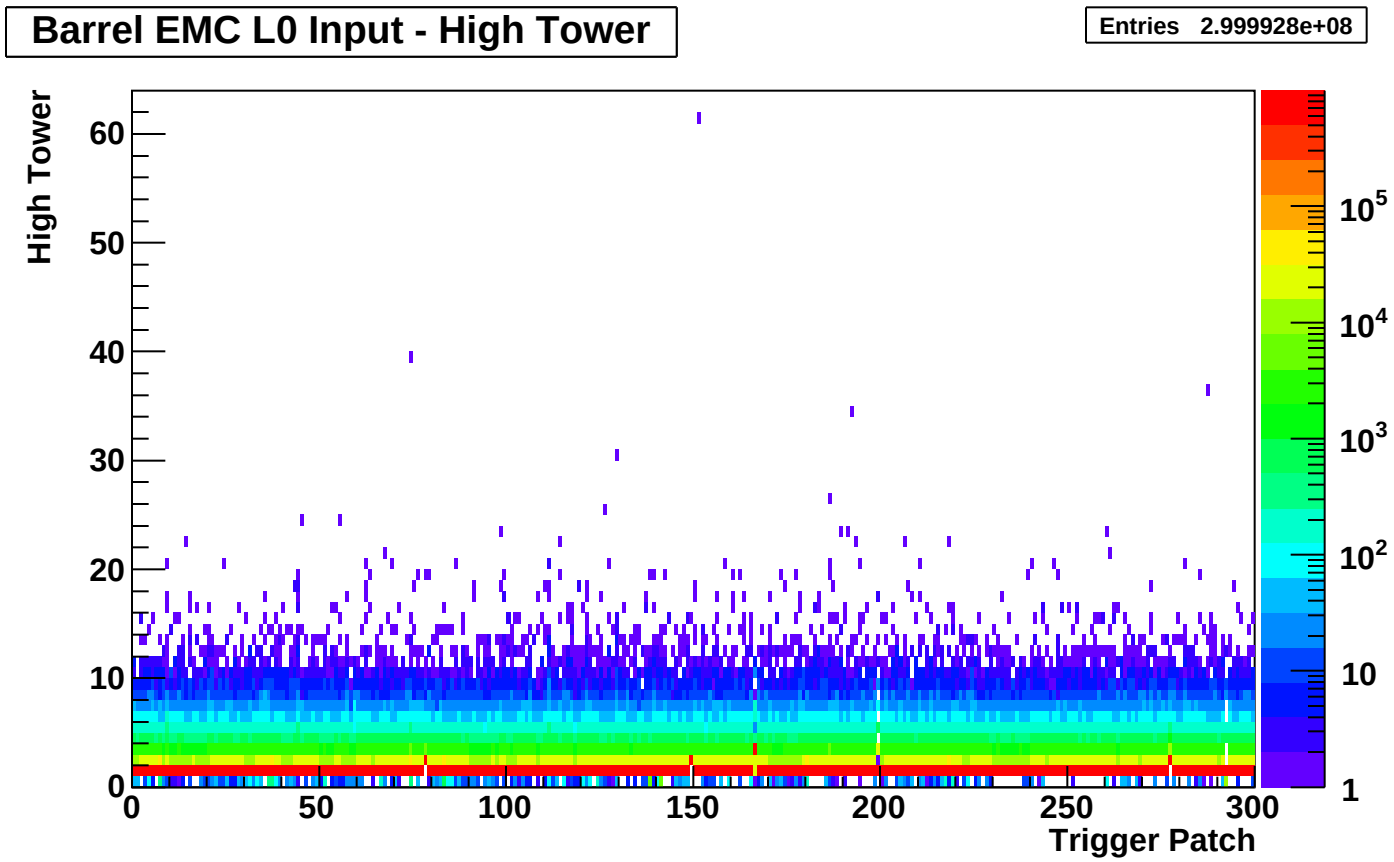

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

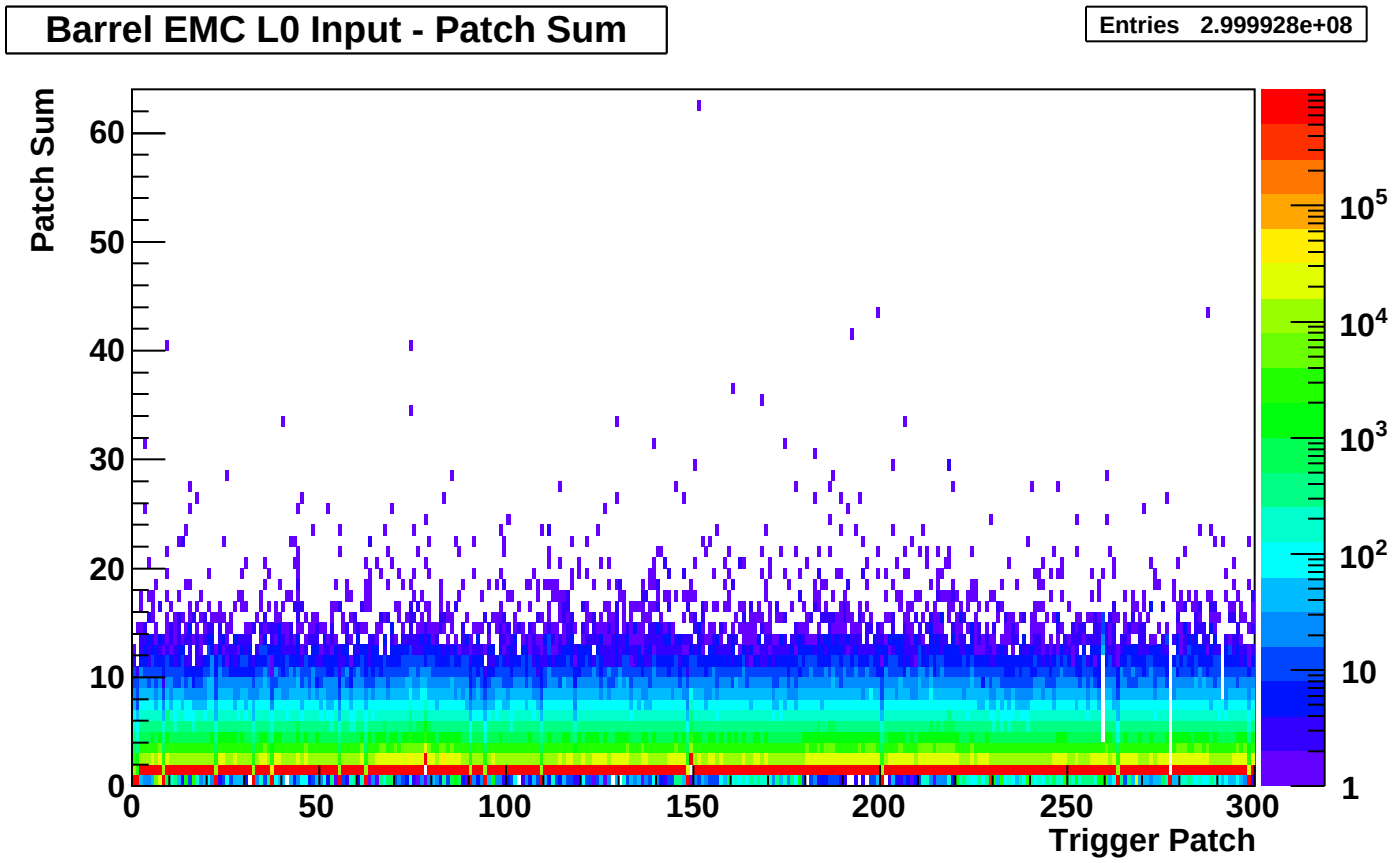

BunchId7Bit (BHT3)
Entries 4
1
10^-1
10^-2
10^-3
0 20 40 60 80 100 120

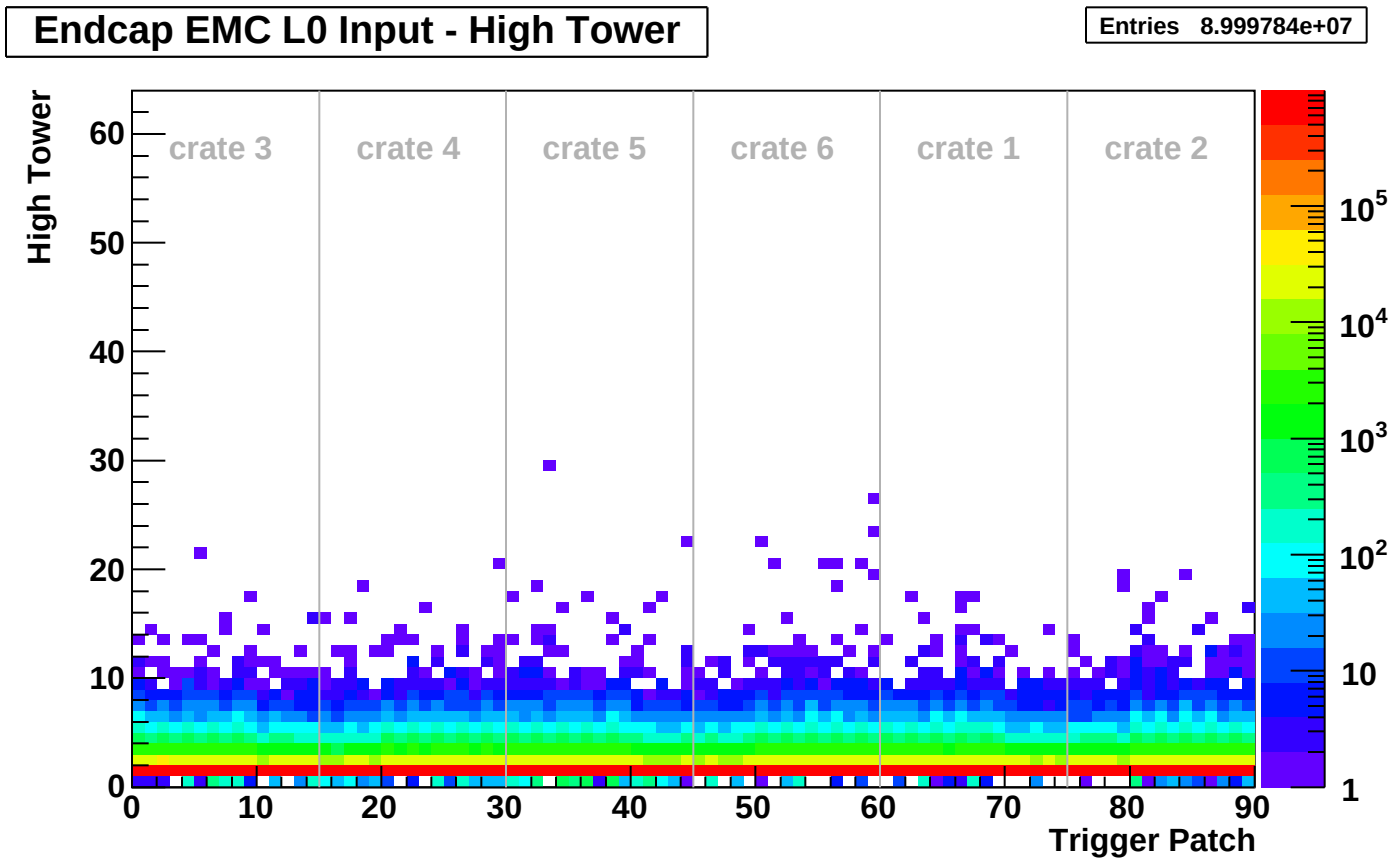

BunchId7Bit (all events)
Entries 999976
10^4
10^3
10^2
10
1
0 20 40 60 80 100 120

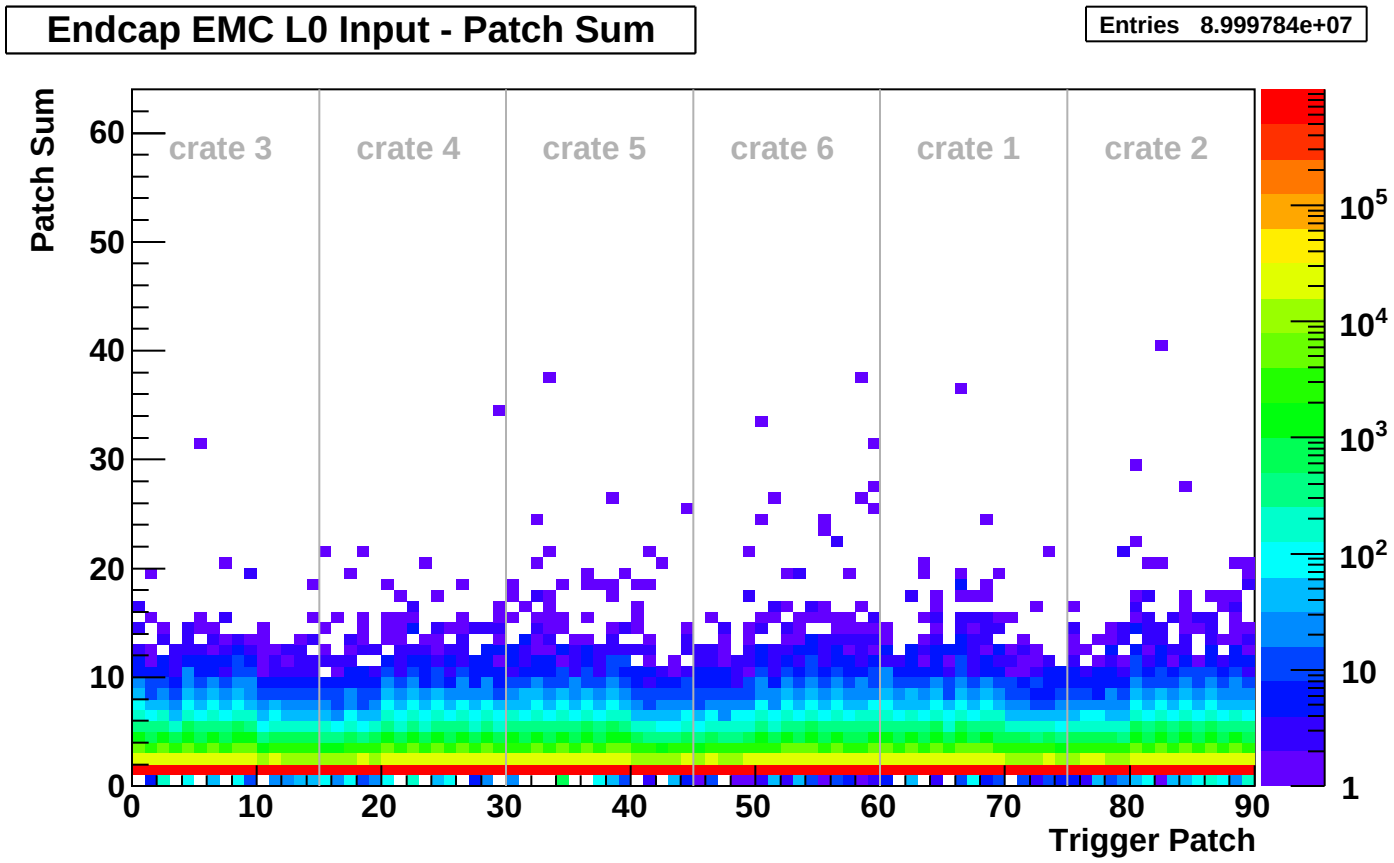

BunchId7Bit (JP1)
Entries 56
1
0 20 40 60 80 100 120

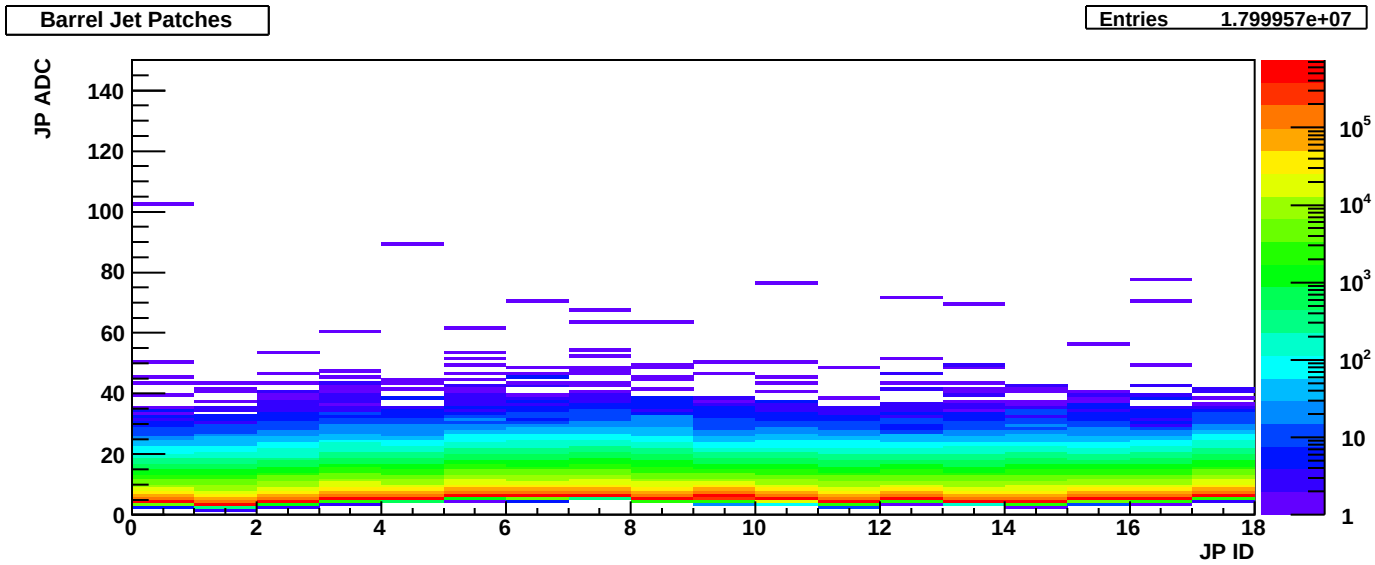

BunchId7Bit (JP2||AJP)
Entries 8
1
10^-1
10^-2
10^-3
0 20 40 60 80 100 120

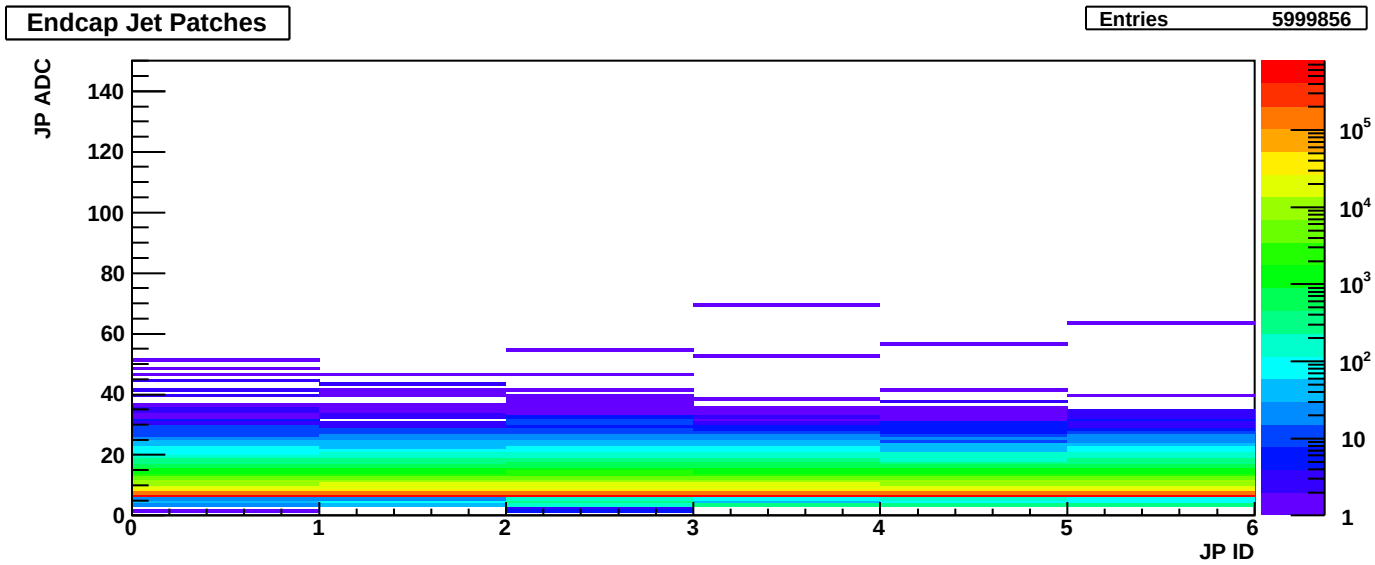

Barrel EMC L0 Input - High Tower
Entries 2.999928e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

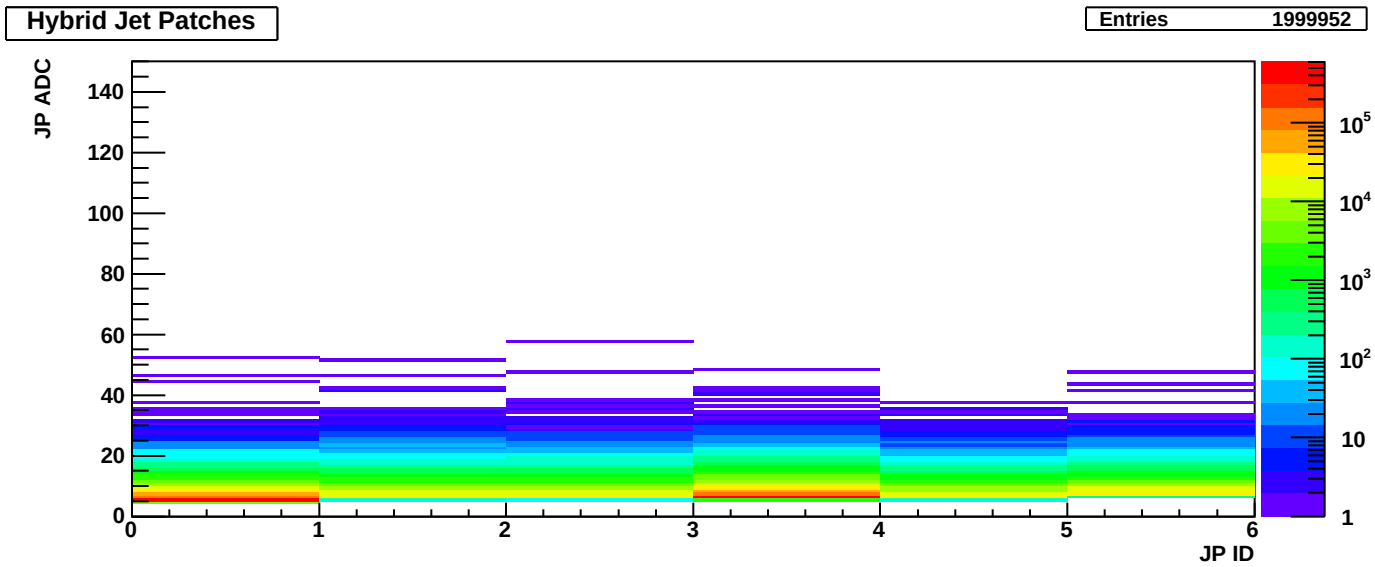

Barrel EMC L0 Input - Patch Sum
Entries 2.999928e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

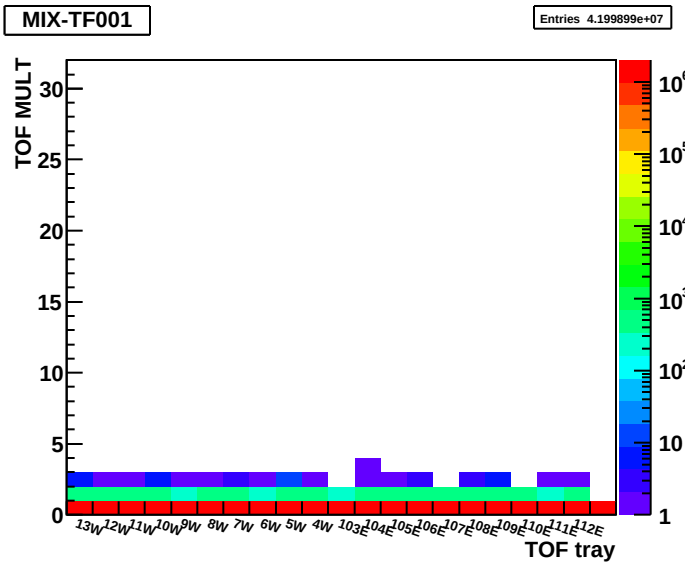

Endcap EMC L0 Input - High Tower
Entries 8.999784e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

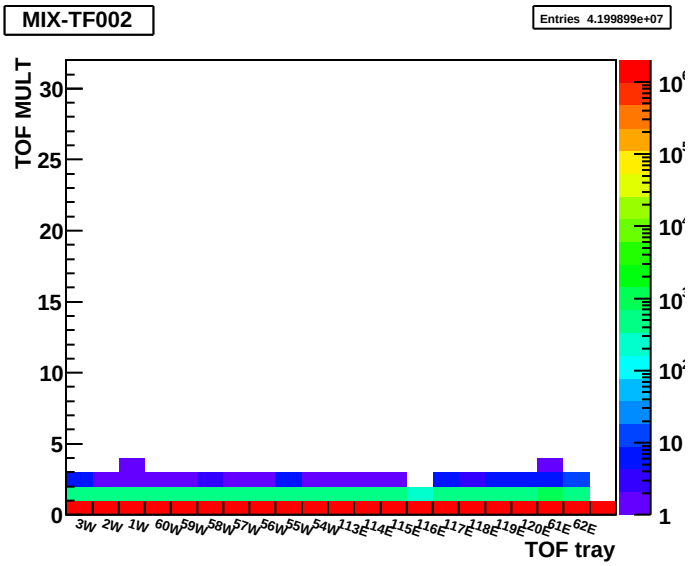

Endcap EMC L0 Input - Patch Sum
Entries 8.999784e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

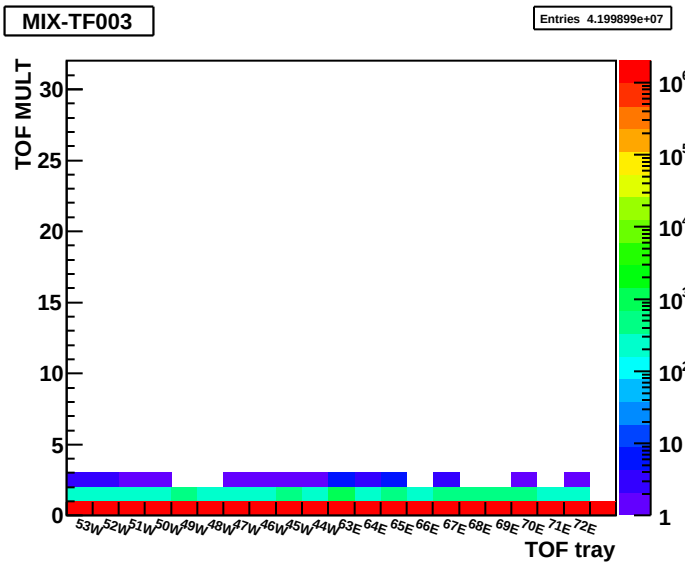

Barrel Jet Patches
Entries 1.799957e+07
JP ADC
JP ID

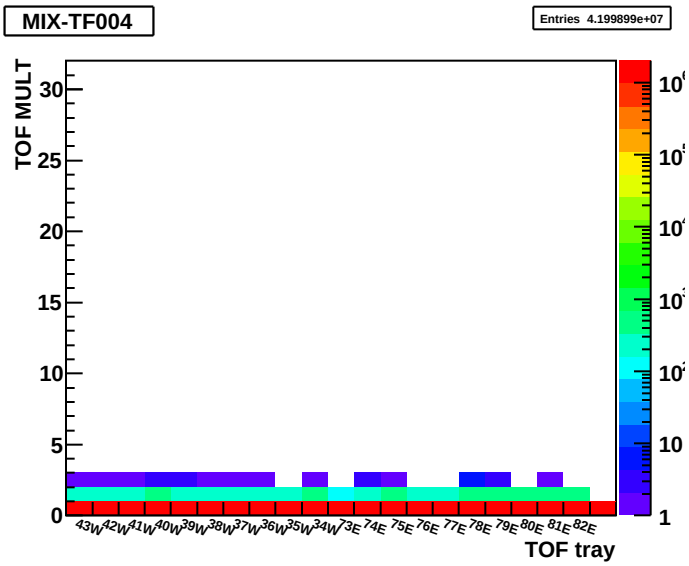

Endcap Jet Patches
Entries 5999856
JP ADC
JP ID

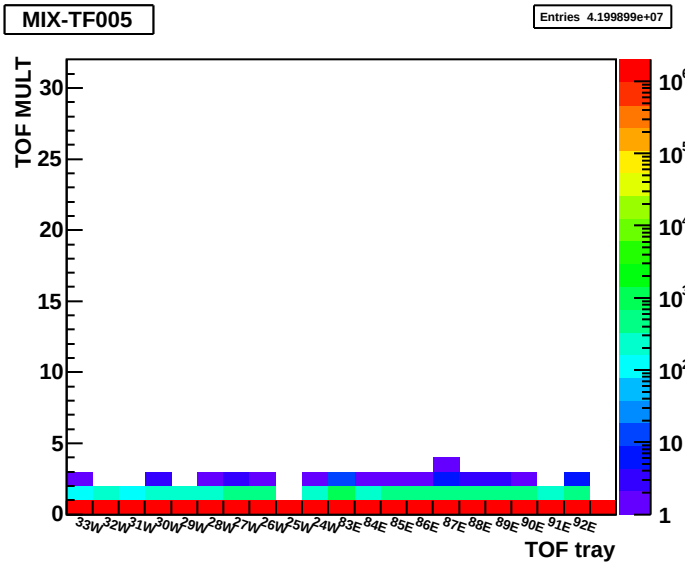

Hybrid Jet Patches
Entries 1999952
JP ADC
JP ID

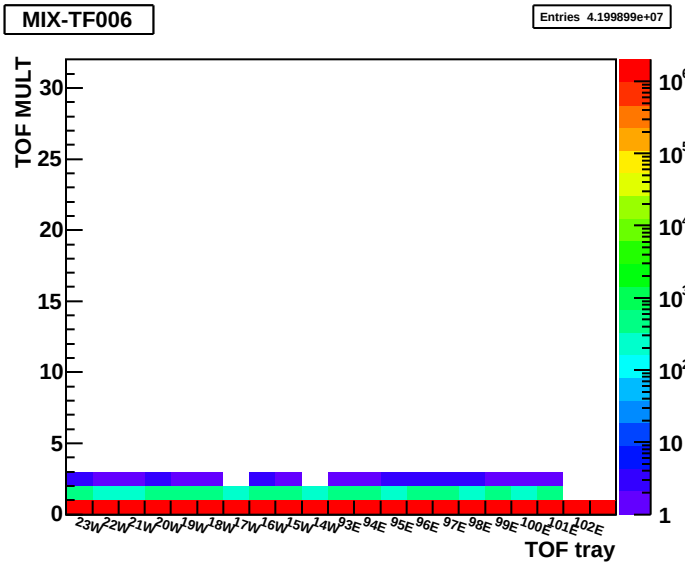

MIX-TF001
Entries 4.199899e+07
TOF MULT
TOF tray

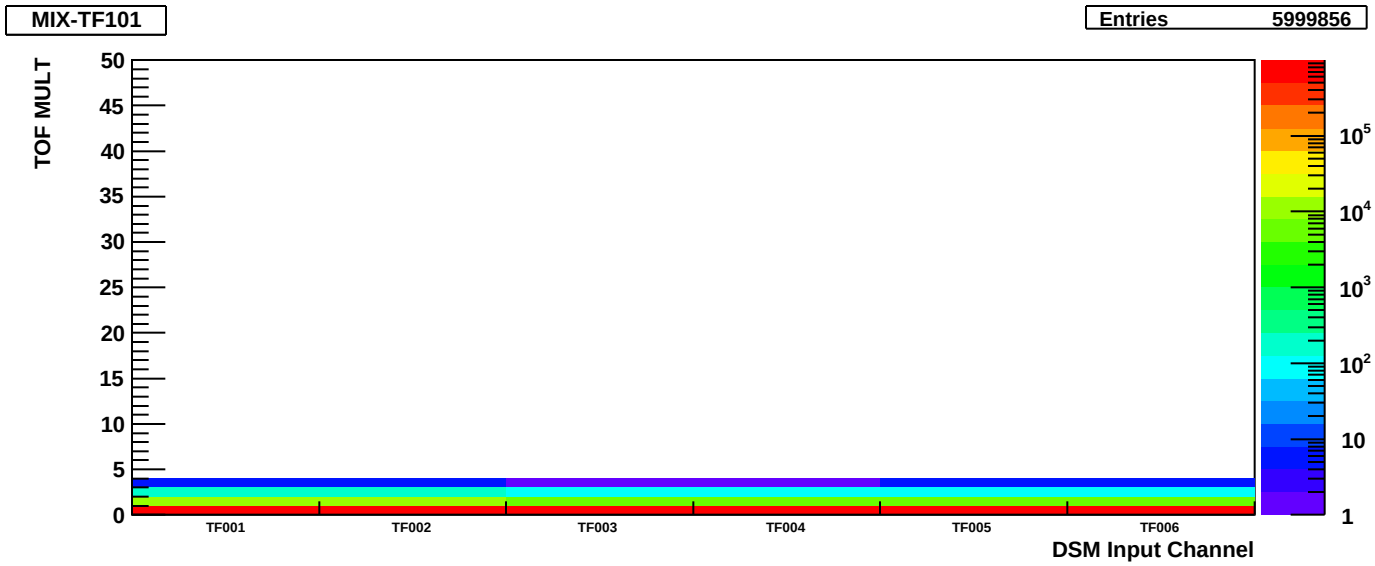

MIX-TF002
Entries 4.199899e+07
TOF MULT
TOF tray

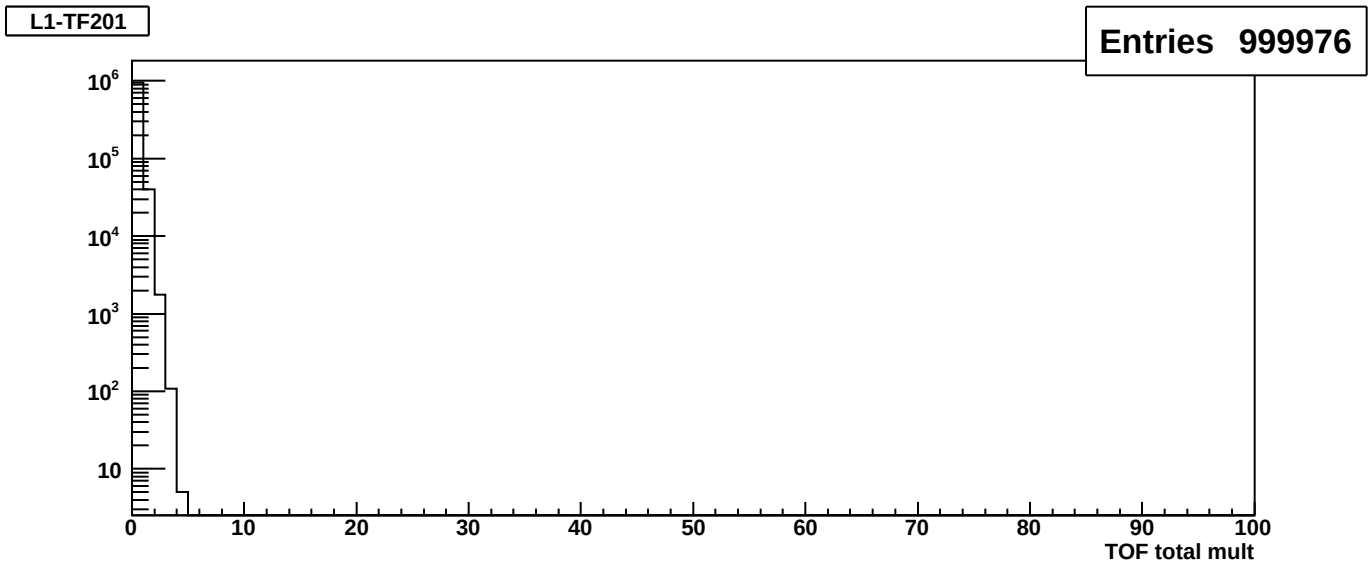

MIX-TF003
Entries 4.199899e+07
TOF MULT
TOF tray

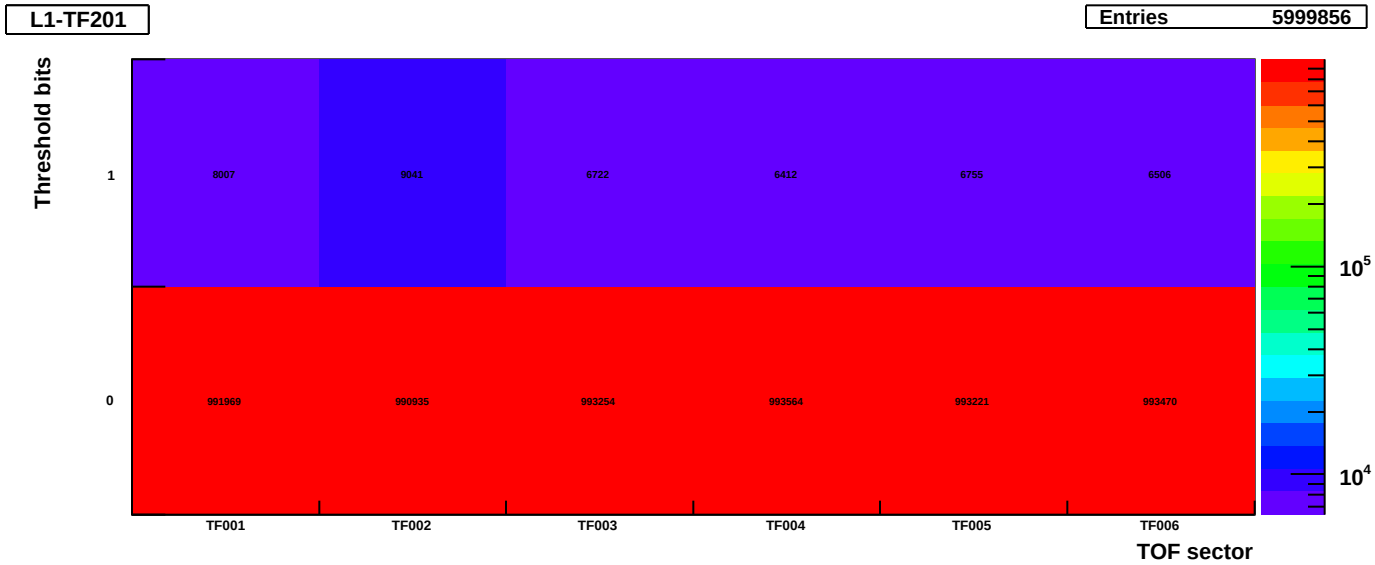

MIX-TF004
Entries 4.199899e+07
TOF MULT
TOF tray

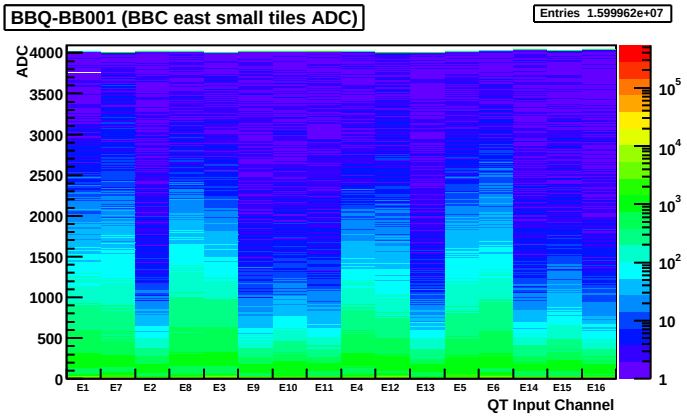

MIX-TF005
Entries 4.199899e+07
TOF MULT
TOF tray

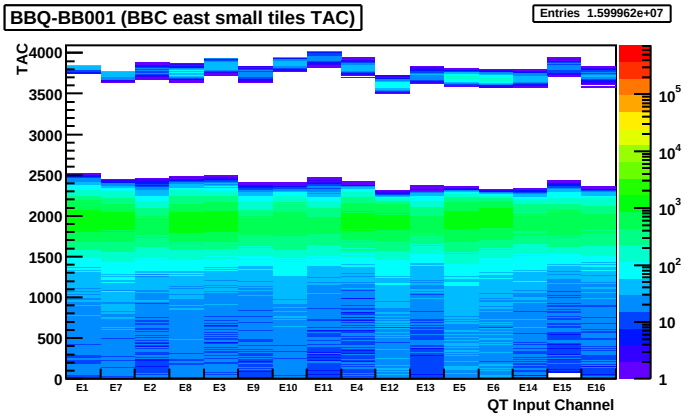

MIX-TF006
Entries 4.199899e+07
TOF MULT
TOF tray

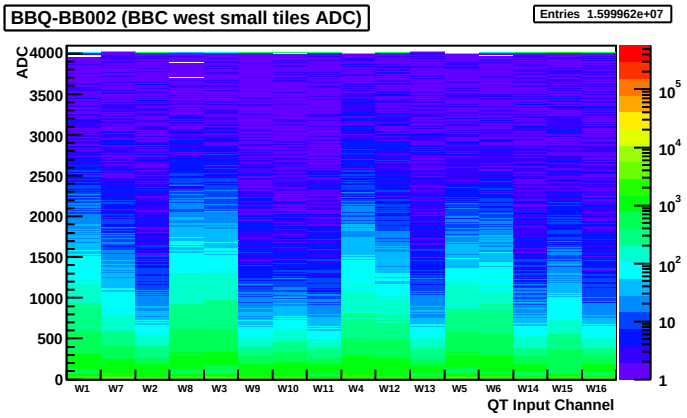

MIX-TF101
Entries 5999856
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

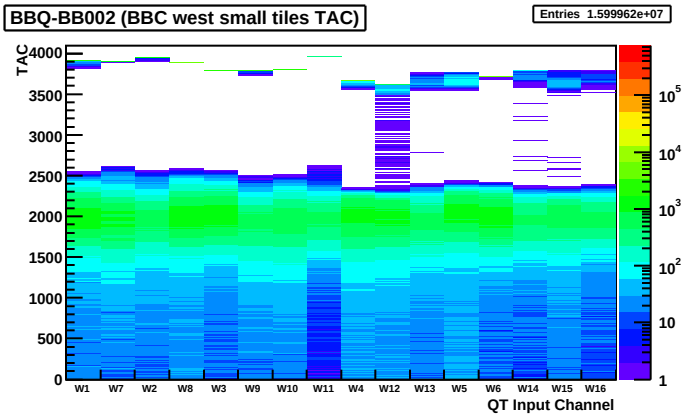

L1-TF201
Entries 999976
10^6
10^5
10^4
10^3
10^2
10
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

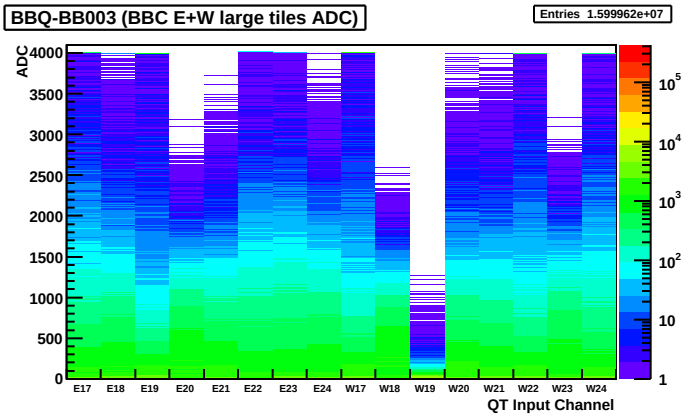

L1-TF201
Entries 5999856
Threshold bits
1
0
8007
9041
6722
6412
6755
6506
991969
990935
993254
993564
993221
993470
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

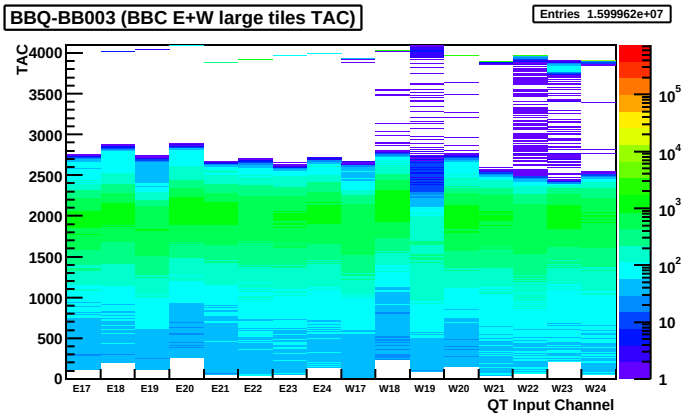

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.599962e+07
ADC
QT Input Channel

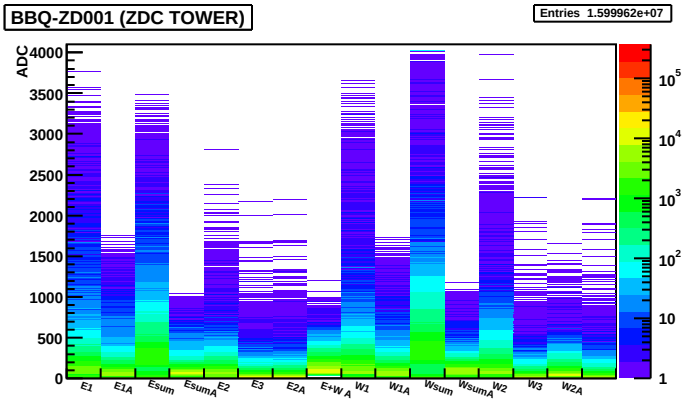

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.599962e+07
TAC
QT Input Channel

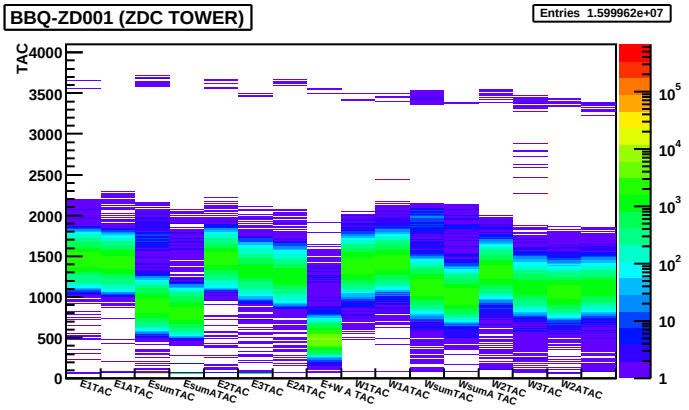

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.599962e+07
ADC
QT Input Channel

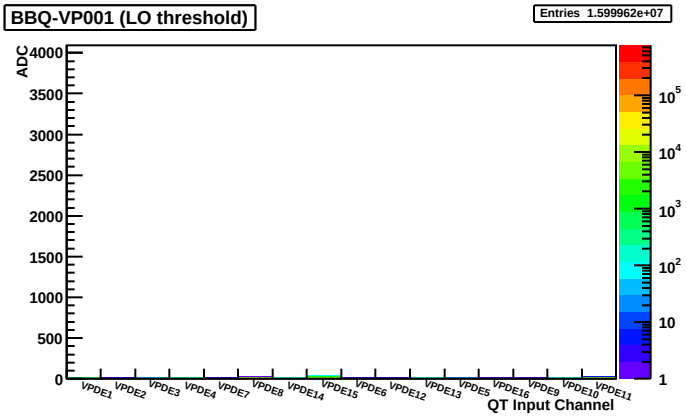

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.599962e+07
TAC
QT Input Channel

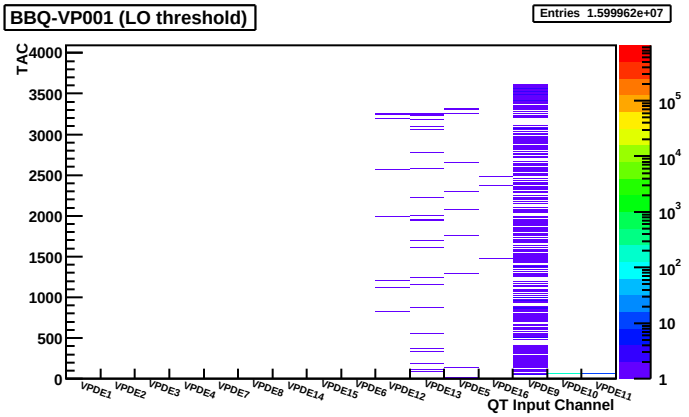

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.599962e+07
ADC
QT Input Channel

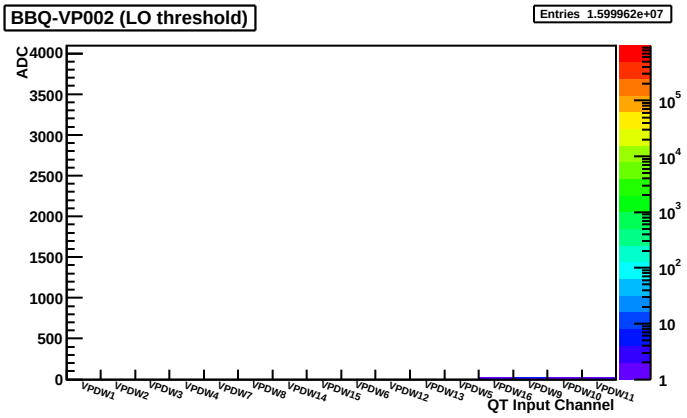

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.599962e+07
TAC
QT Input Channel

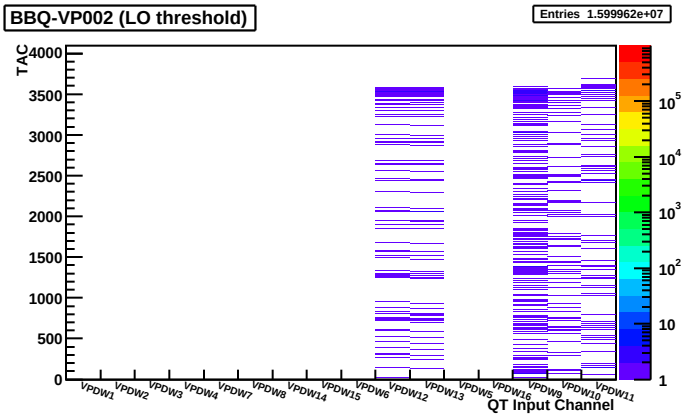

BBQ-ZD001 (ZDC TOWER)
Entries 1.599962e+07
ADC

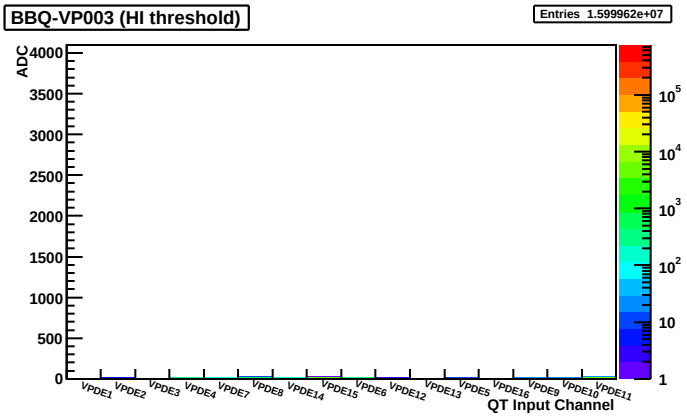

BBQ-ZD001 (ZDC TOWER)
Entries 1.599962e+07
TAC

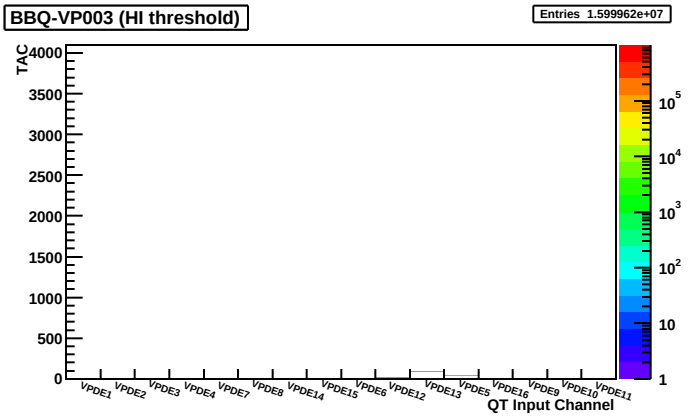

BBQ-VP001 (LO threshold)
Entries 1.599962e+07
ADC
QT Input Channel

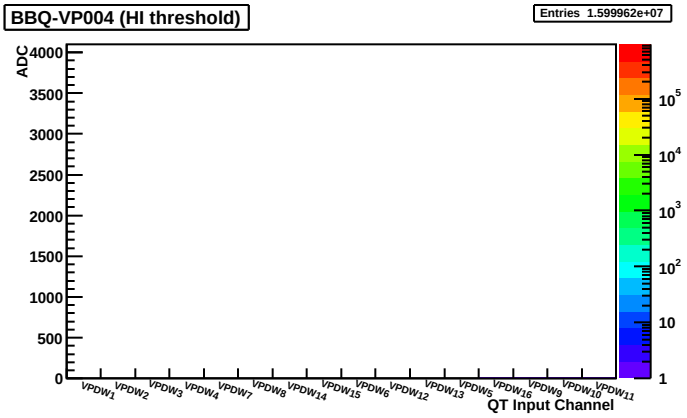

BBQ-VP001 (LO threshold)
Entries 1.599962e+07
TAC
QT Input Channel

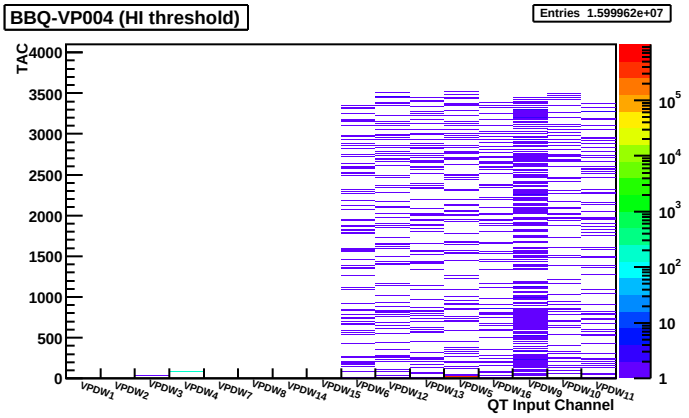

BBQ-VP002 (LO threshold)
Entries 1.599962e+07
ADC
QT Input Channel

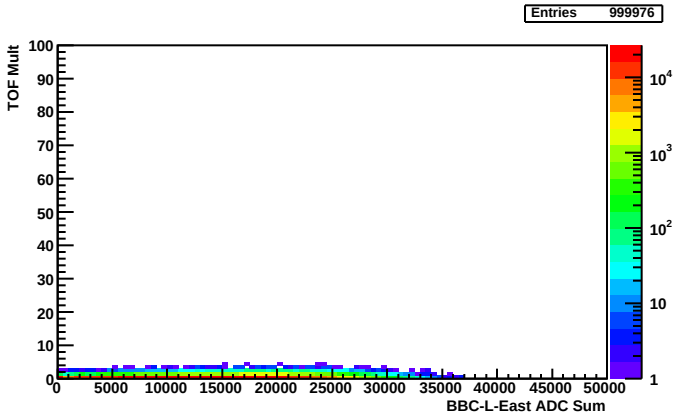

BBQ-VP002 (LO threshold)
Entries 1.599962e+07
TAC
QT Input Channel

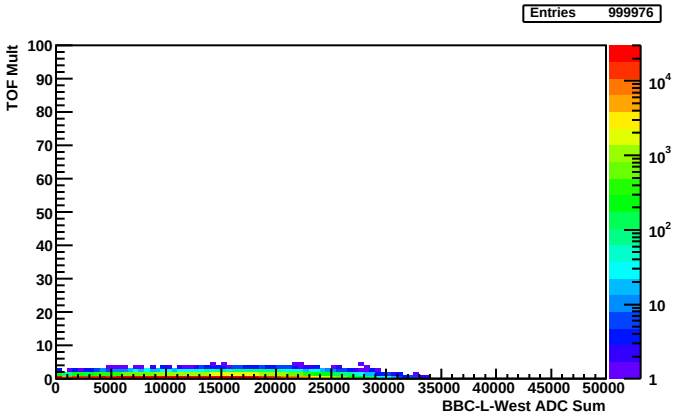

BBQ-VP003 (HI threshold)
Entries 1.599962e+07
ADC
QT Input Channel

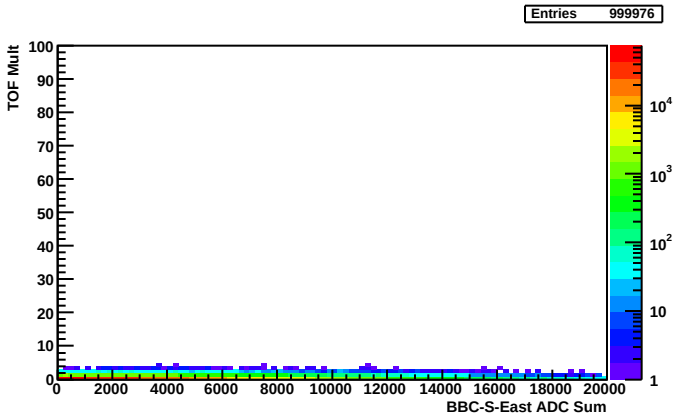

BBQ-VP003 (HI threshold)
Entries 1.599962e+07
TAC
QT Input Channel

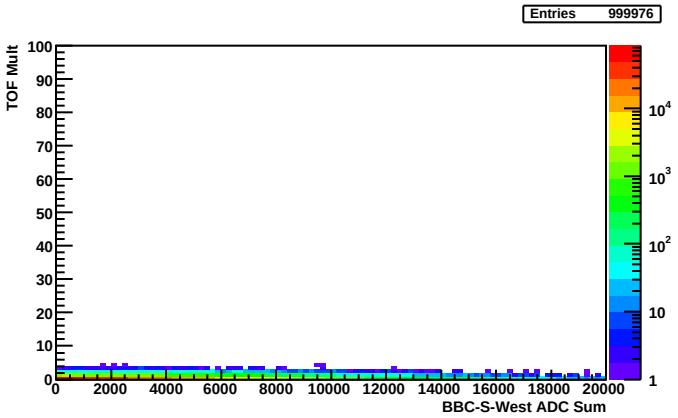

BBQ-VP004 (HI threshold)
Entries 1.599962e+07
ADC
QT Input Channel

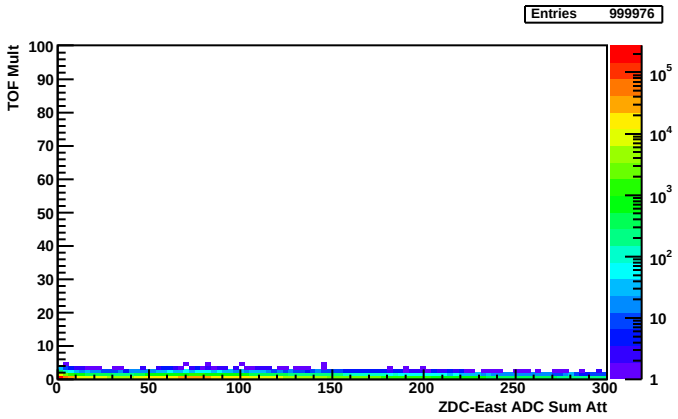

BBQ-VP004 (HI threshold)
Entries 1.599962e+07
TAC
QT Input Channel

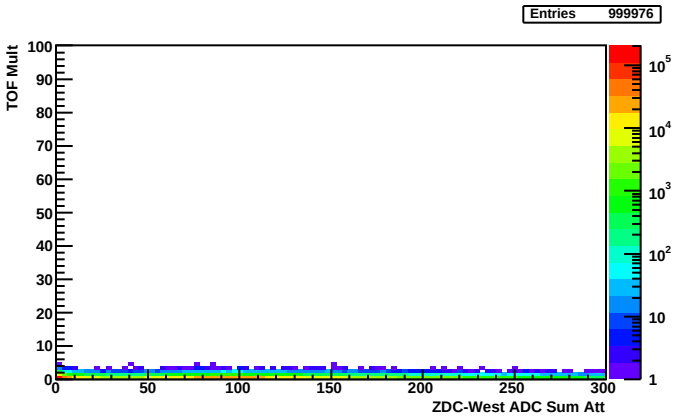

Entries 999976
TOF Mult
BBC-L-East ADC Sum

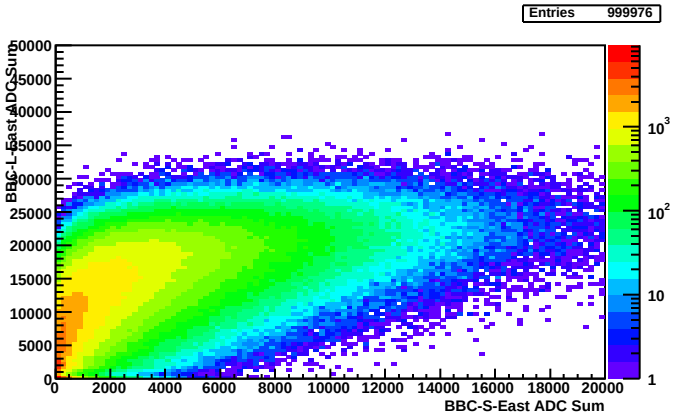

Entries 999976
TOF Mult
BBC-L-West ADC Sum

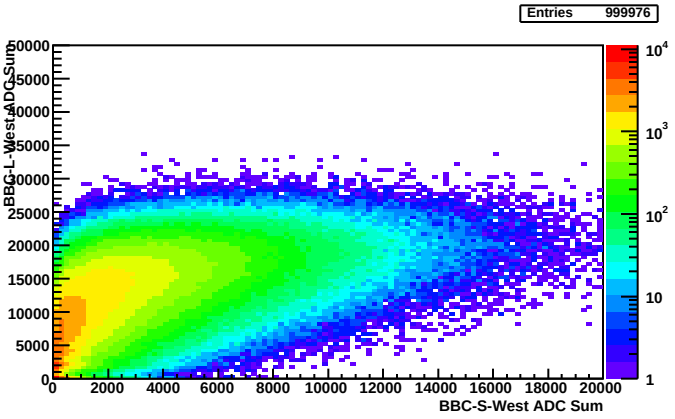

Entries 999976
TOF Mult
BBC-S-East ADC Sum

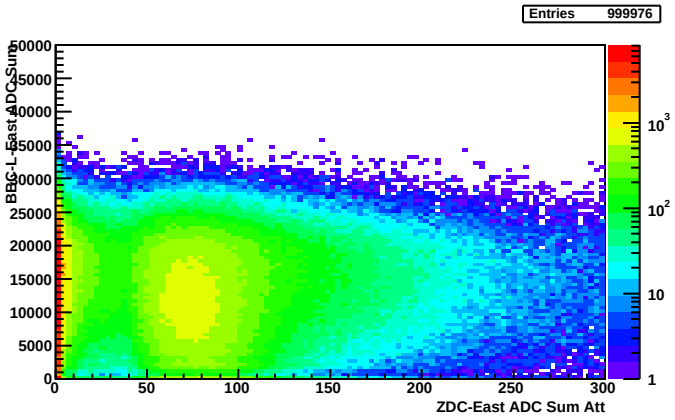

Entries 999976
TOF Mult
BBC-S-West ADC Sum

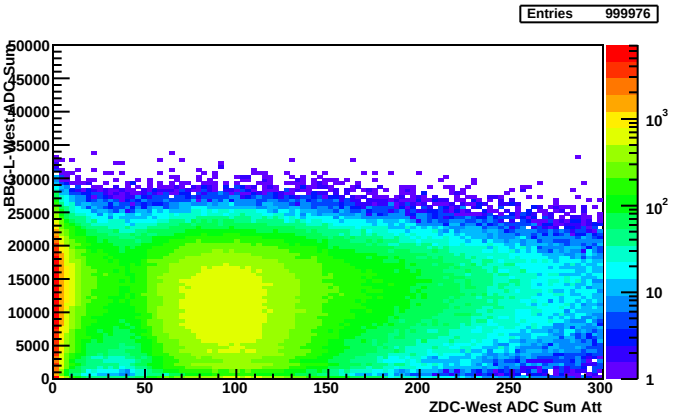

Entries 999976
TOF Mult
ZDC-East ADC Sum Att

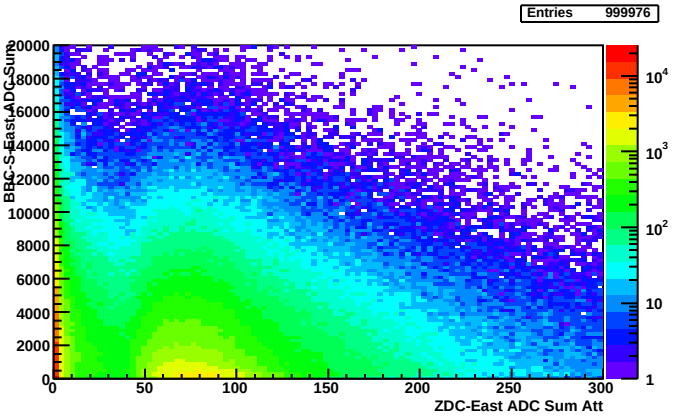

Entries 999976
TOF Mult
ZDC-West ADC Sum Att

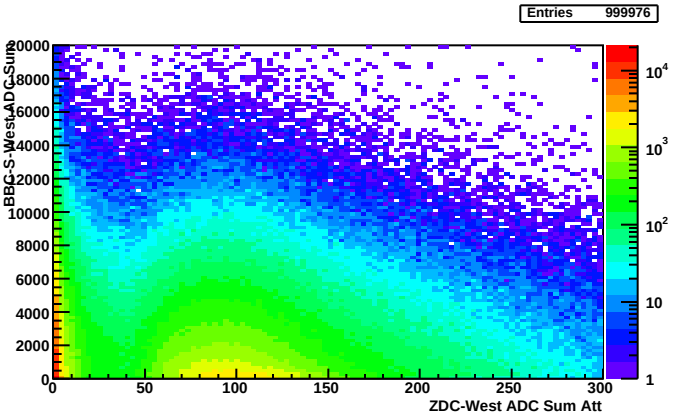

Entries 999976
BBC-L-East ADC Sum
BBC-S-East ADC Sum

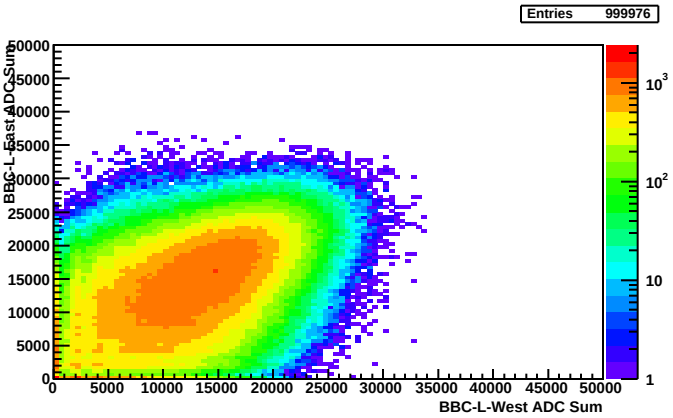

Entries 999976
BBC-L-West ADC Sum
BBC-S-West ADC Sum

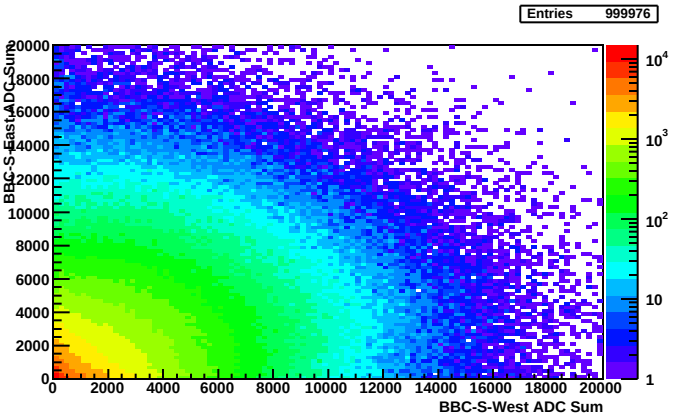

Entries 999976
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

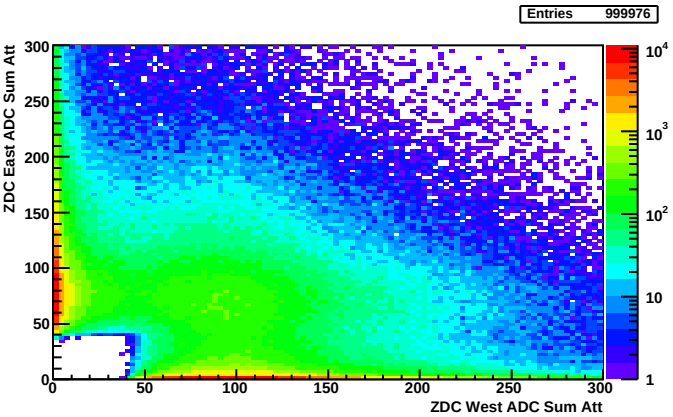

Entries 999976
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

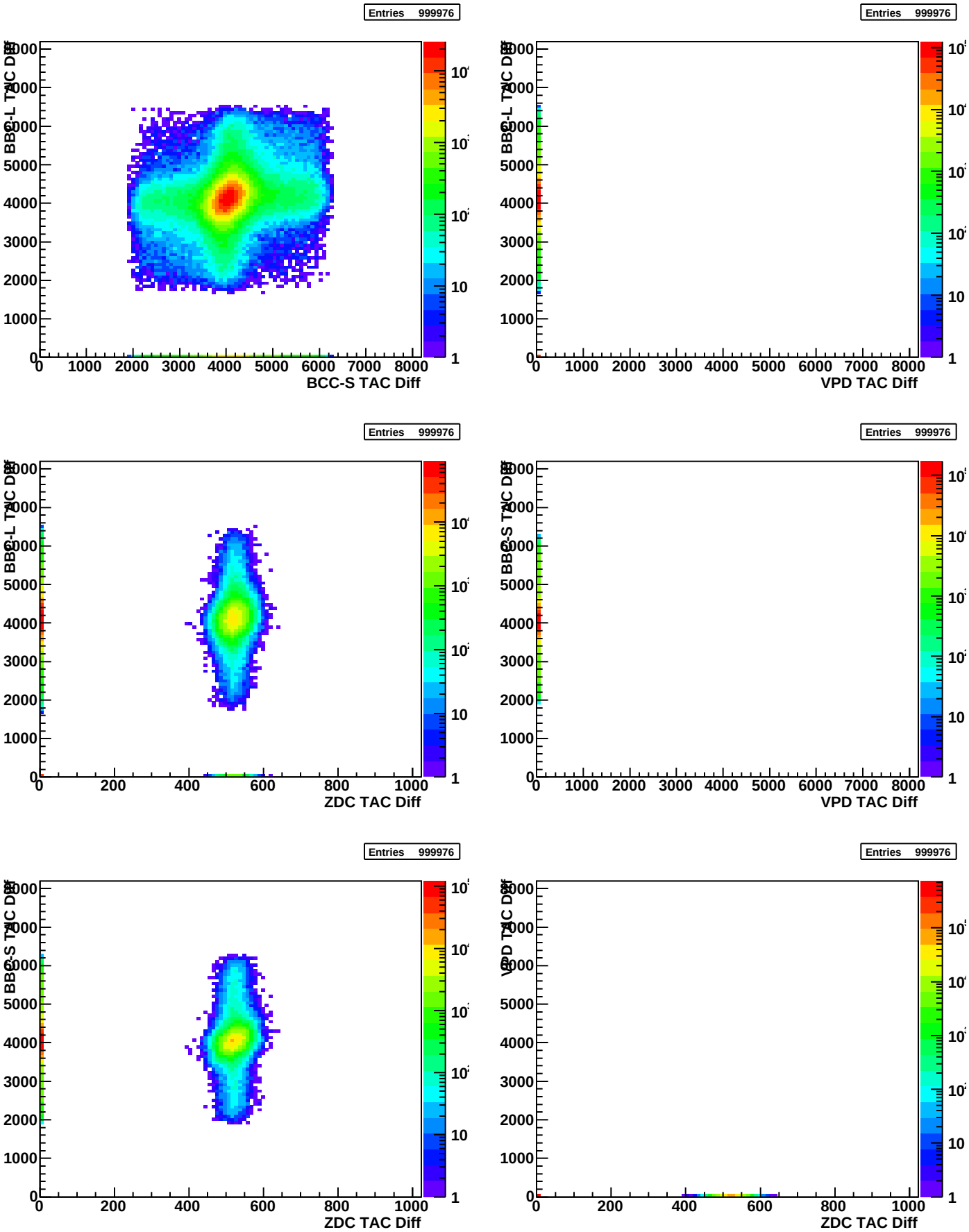

Entries 999976
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

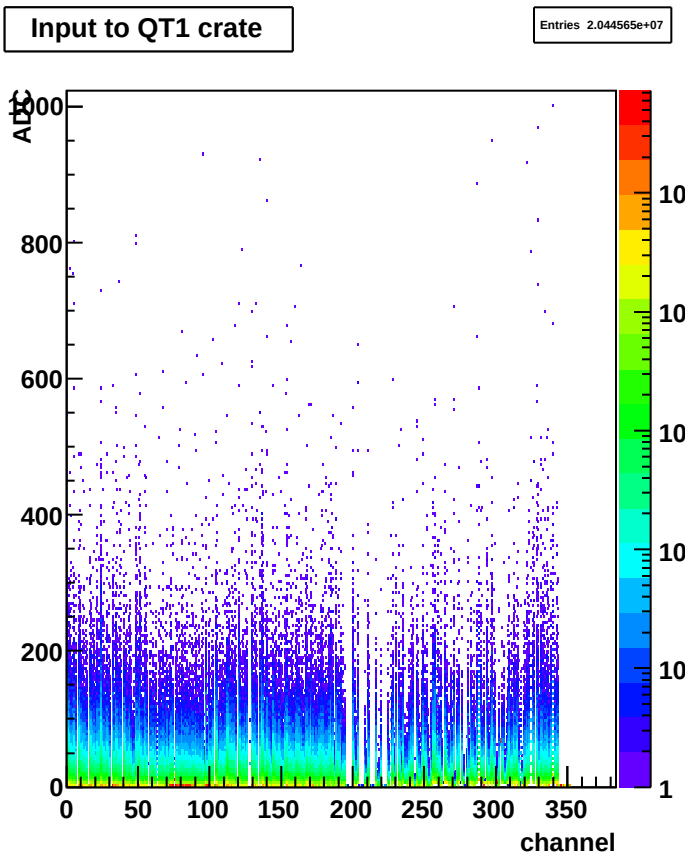

Entries 999976
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

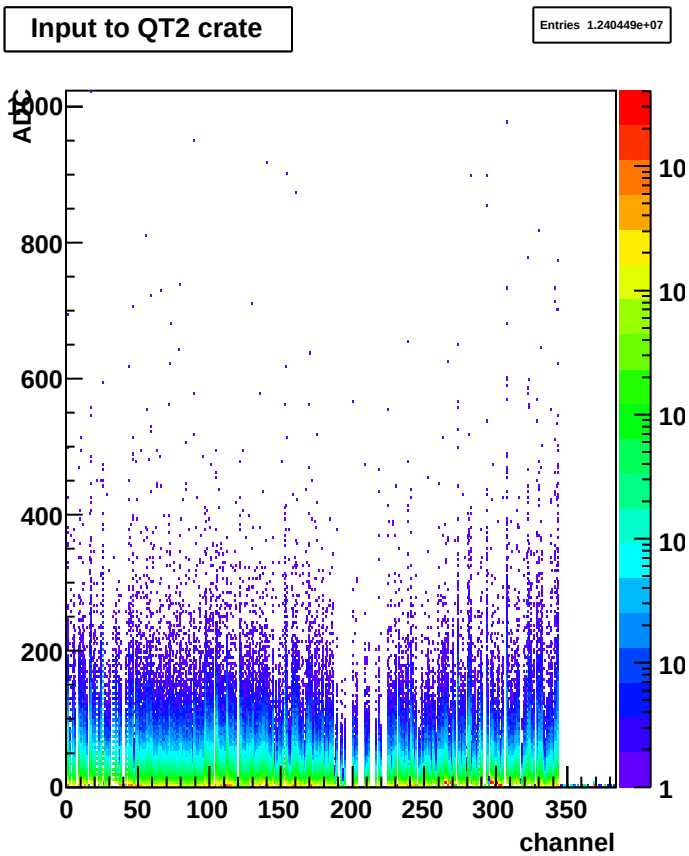

Entries 999976
BBC-L-East ADC Sum
BBC-L-West ADC Sum

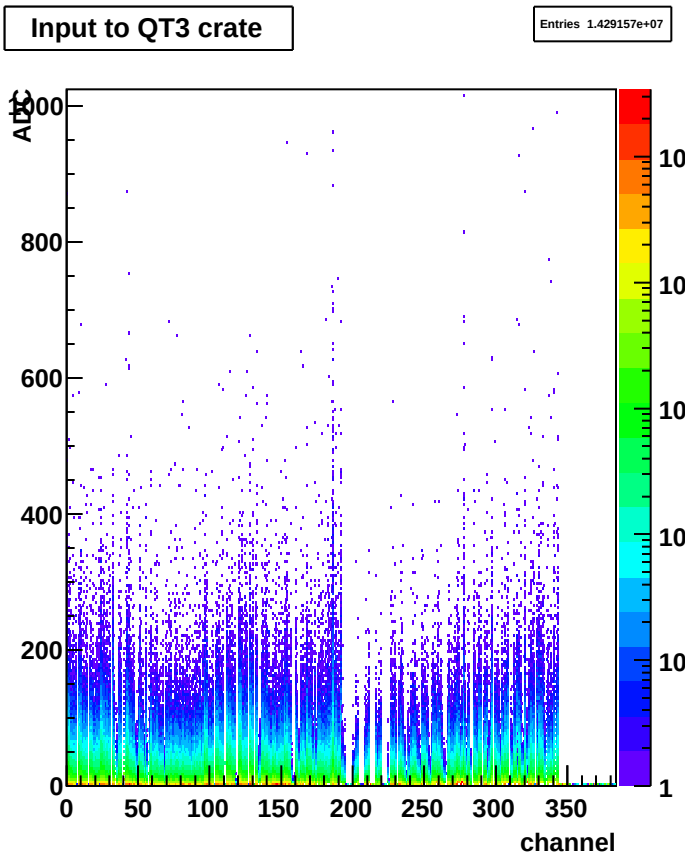

Entries 999976
BBC-S-East ADC Sum
BBC-S-West ADC Sum

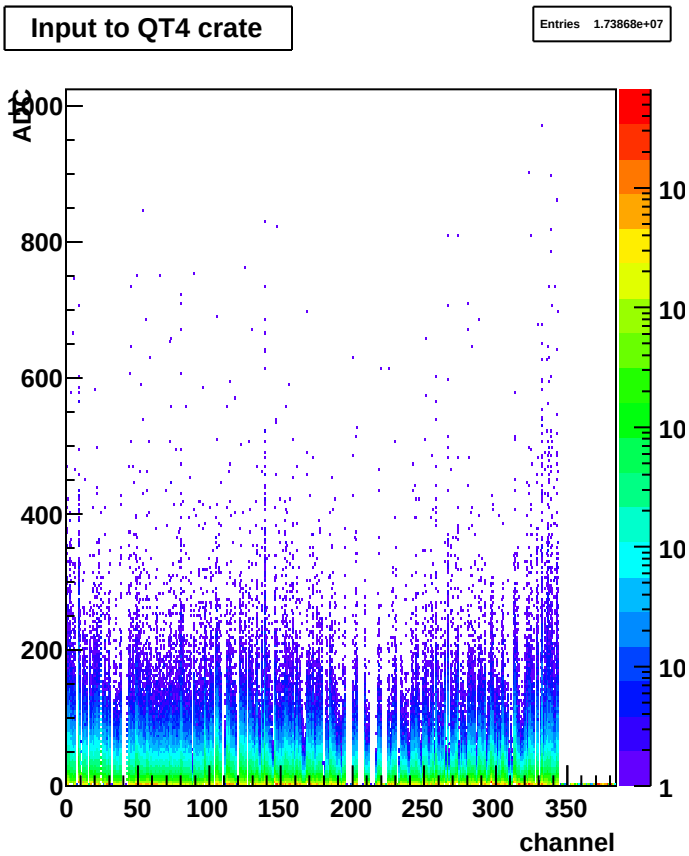

Entries 999976
ZDC East ADC Sum Att
ZDC West ADC Sum Att

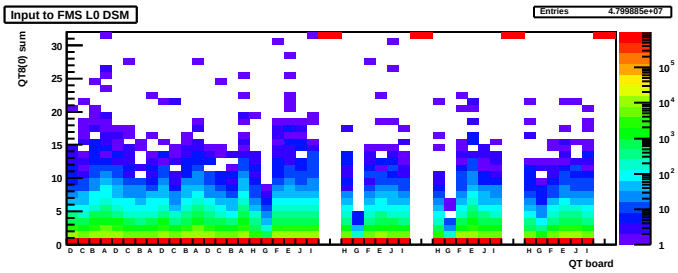

Entries 999976
BBC-L TAC Diff
BCC-S TAC Diff
Entries 999976
BBC-L TAC Diff
VPD TAC Diff
Entries 999976
BBC-L TAC Diff
ZDC TAC Diff
Entries 999976
BBC-S TAC Diff
VPD TAC Diff
Entries 999976
BBC-S TAC Diff
ZDC TAC Diff
Entries 999976
VPD TAC Diff
ZDC TAC Diff

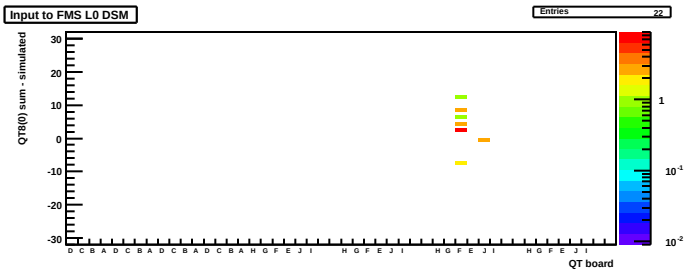

Input to QT1 crate
Entries 2.044565e+07
ADC
channel

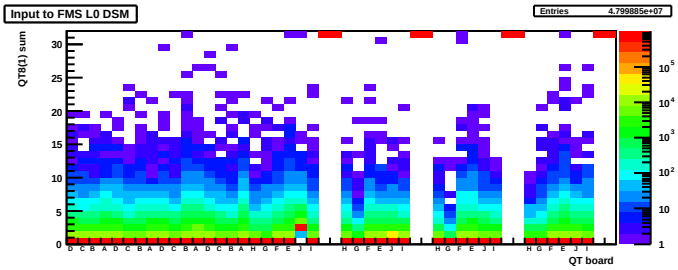

Input to QT2 crate
Entries 1.240449e+07
ADC
channel

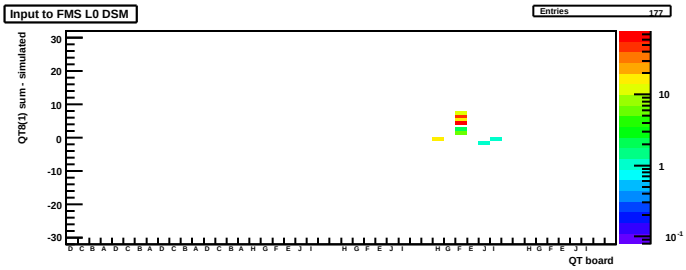

Input to QT3 crate
Entries 1.429157e+07
ADC
channel

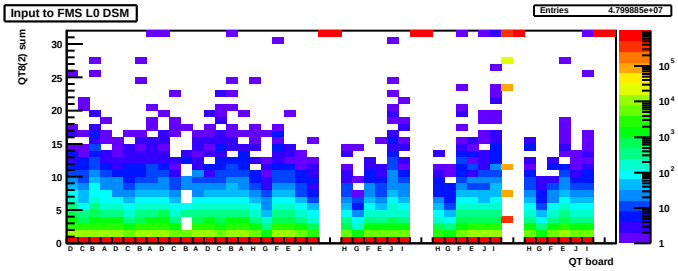

Input to QT4 crate
Entries 1.73868e+07
ADC
channel

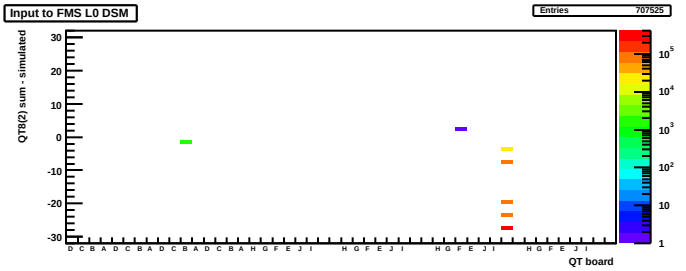

Input to FMS L0 DSM
Entries 4.799885e+07
QT8(0) sum
QT board

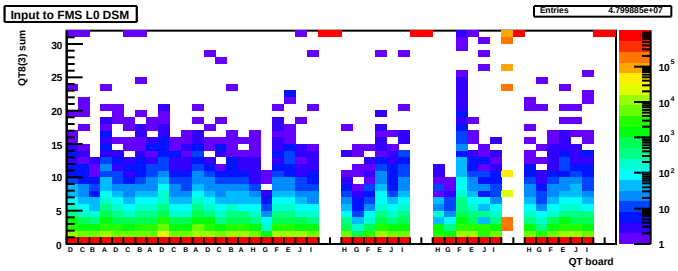

Input to FMS L0 DSM
Entries 22
QT8(0) sum - simulated
QT board

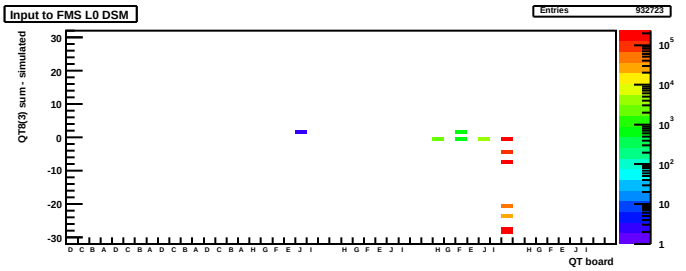

Input to FMS L0 DSM
Entries 4.799885e+07
QT8(1) sum
QT board

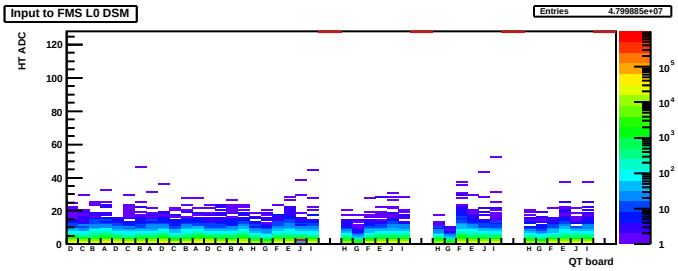

Input to FMS L0 DSM
Entries 177
QT8(1) sum - simulated
QT board

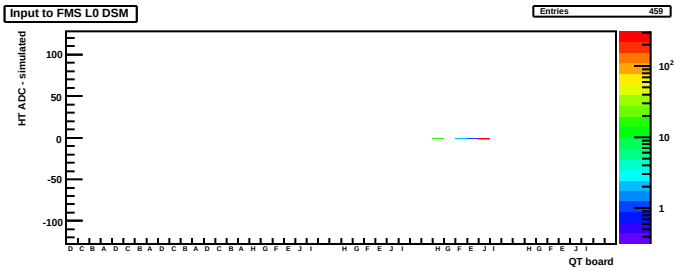

Input to FMS L0 DSM
Entries 4.799885e+07
QT8(2) sum
QT board

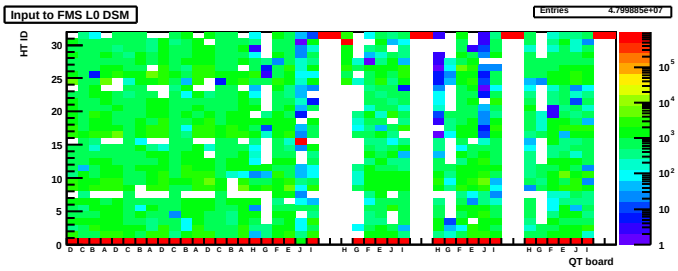

Input to FMS L0 DSM
Entries 707525
QT8(2) sum - simulated
QT board

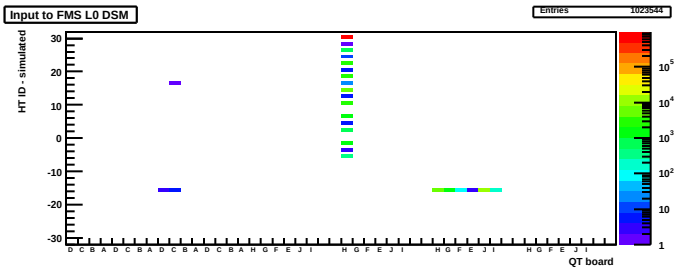

Input to FMS L0 DSM
Entries 4.799885e+07
QT8(3) sum
QT board

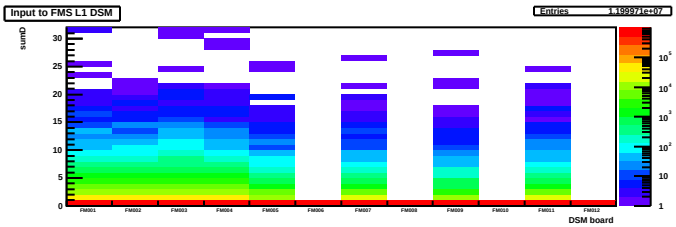

Input to FMS L0 DSM
Entries 932723
QT8(3) sum - simulated
QT board

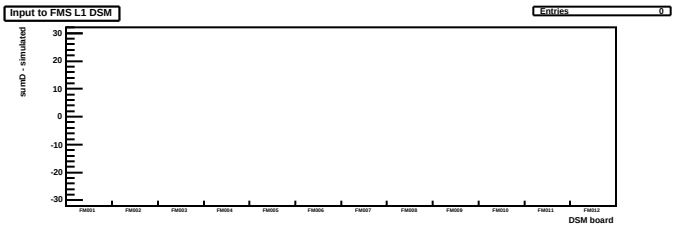

Input to FMS L0 DSM
Entries 4.799885e+07
HT ADC
QT board

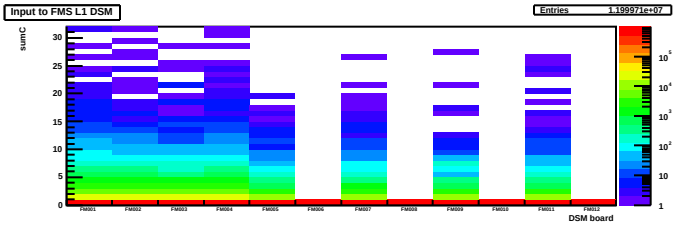

Input to FMS L0 DSM
Entries 459
HT ADC - simulated
QT board

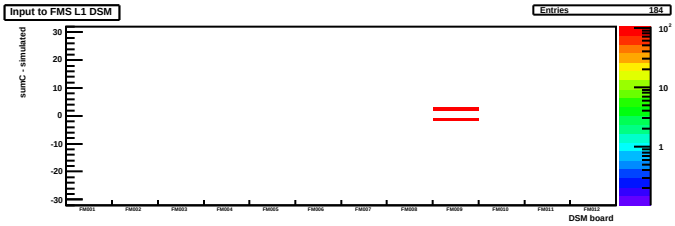

Input to FMS L0 DSM
Entries 4.799885e+07
HT ID
QT board

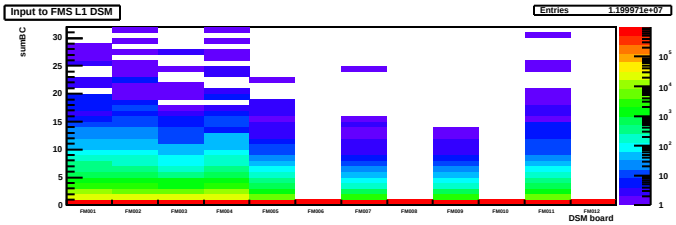

Input to FMS L0 DSM
Entries 1023544
HT ID - simulated
QT board

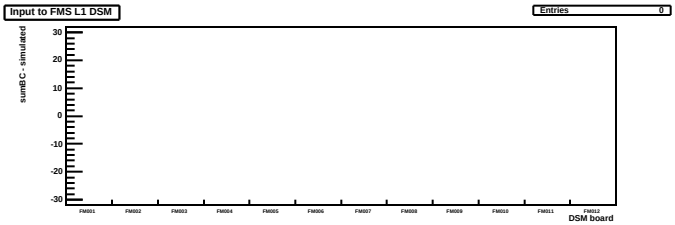

Input to FMS L1 DSM
Entries 1.199971e+07
sumD
DSM board

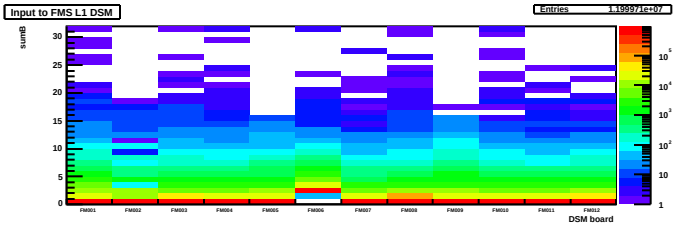

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

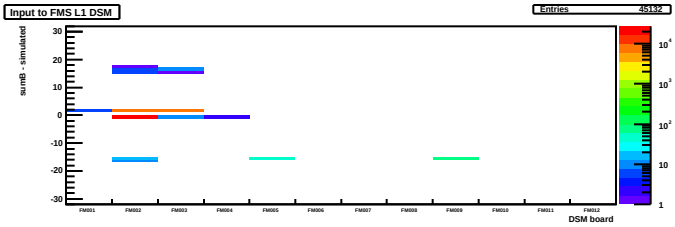

Input to FMS L1 DSM
Entries 1.199971e+07
sumC
DSM board

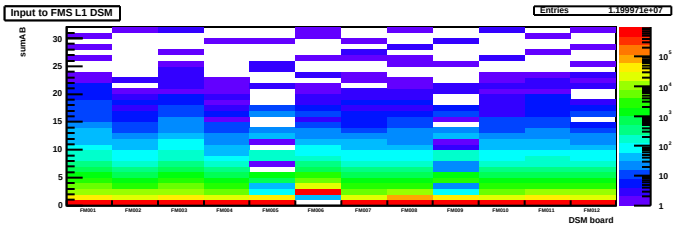

Input to FMS L1 DSM
Entries 184
sumC - simulated
DSM board

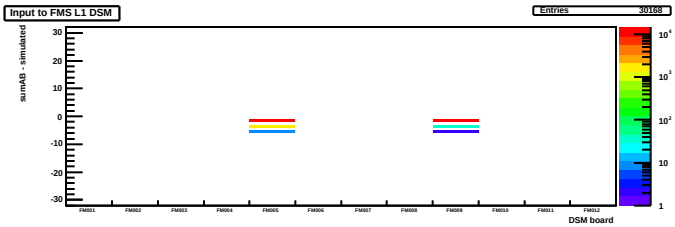

Input to FMS L1 DSM
Entries 1.199971e+07
sumBC
DSM board

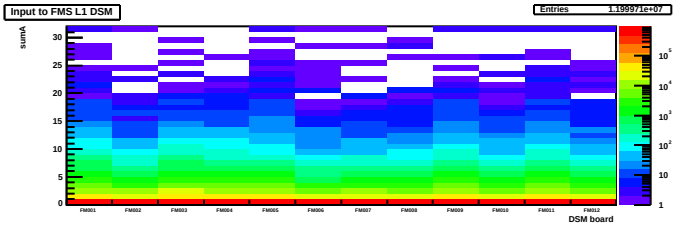

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

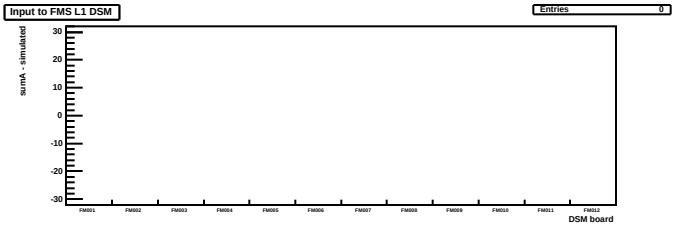

Input to FMS L1 DSM
Entries 1.199971e+07
sumB
DSM board

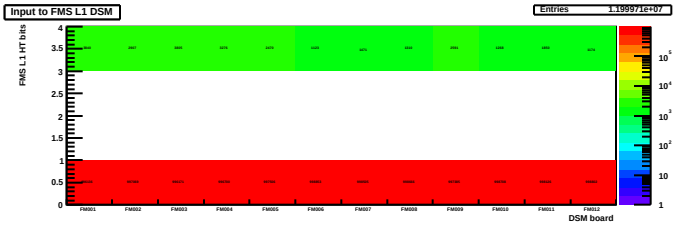

Input to FMS L1 DSM
Entries 45132
sumB - simulated
DSM board

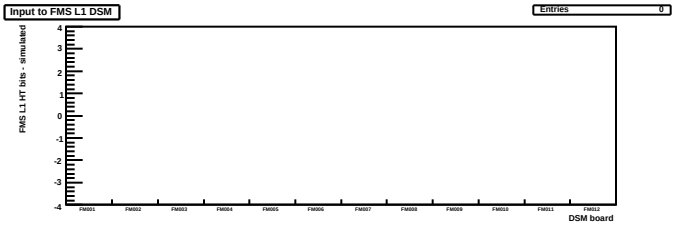

Input to FMS L1 DSM
Entries 1.199971e+07
sumAB
DSM board

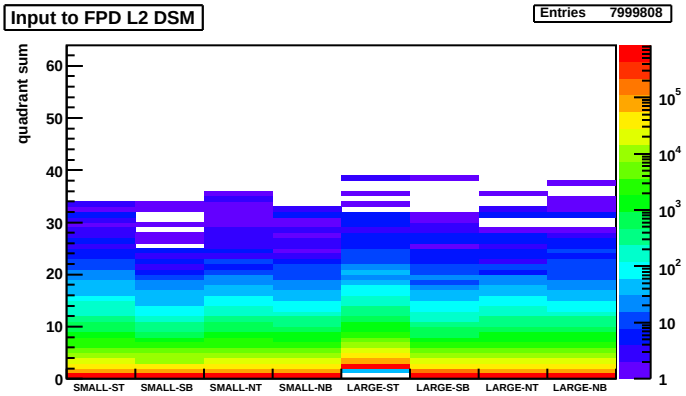

Input to FMS L1 DSM
Entries 30168
sumAB - simulated
DSM board

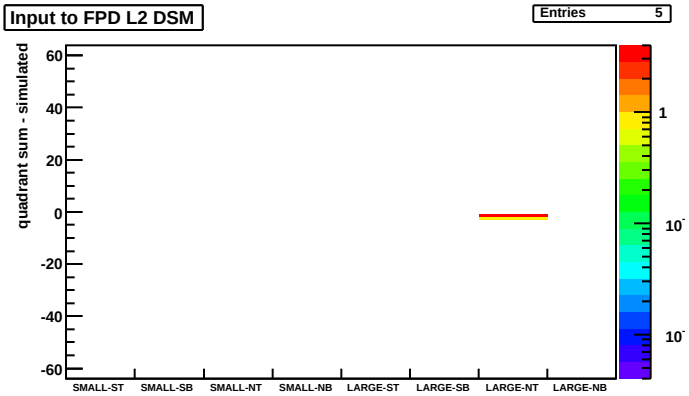

Input to FMS L1 DSM
Entries 1.199971e+07
sumA
DSM board

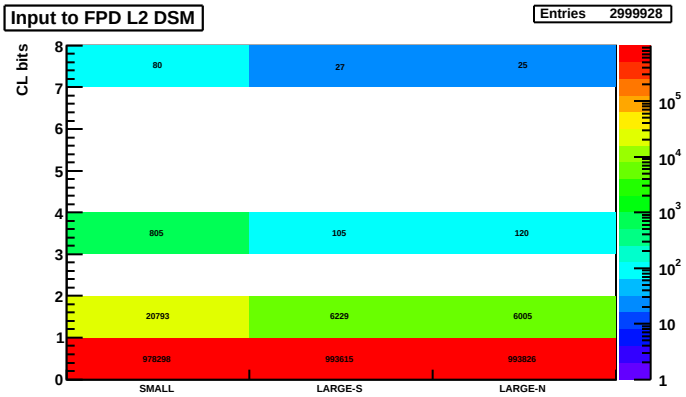

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

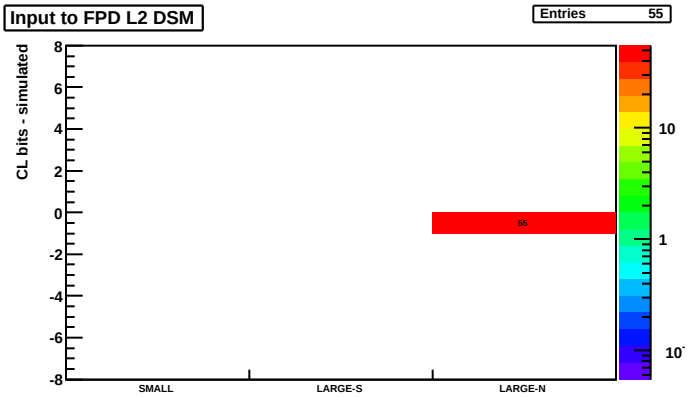

Input to FMS L1 DSM
Entries 1.199971e+07
FMS L1 HT bits
DSM board

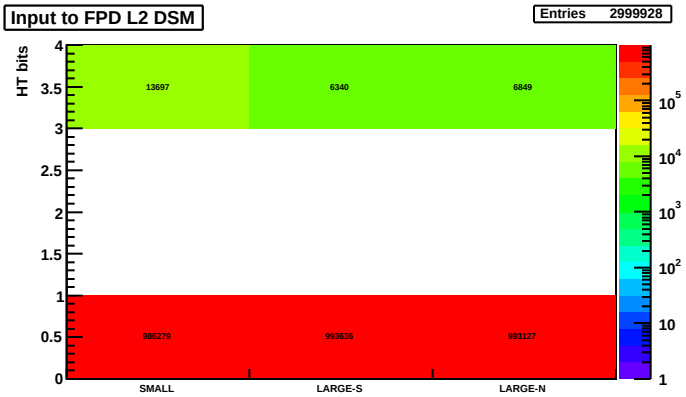

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

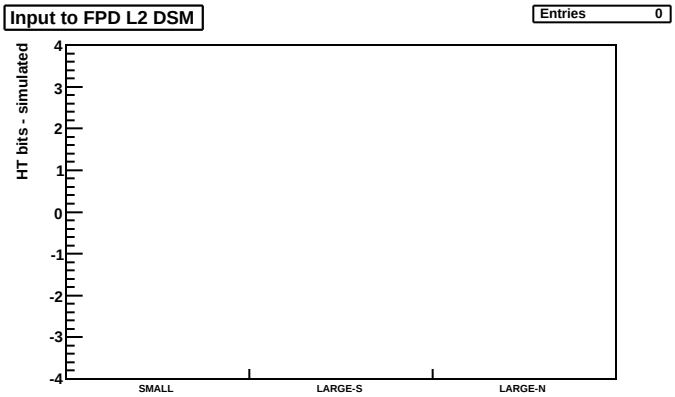

Input to FPD L2 DSM
Entries 7999808
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

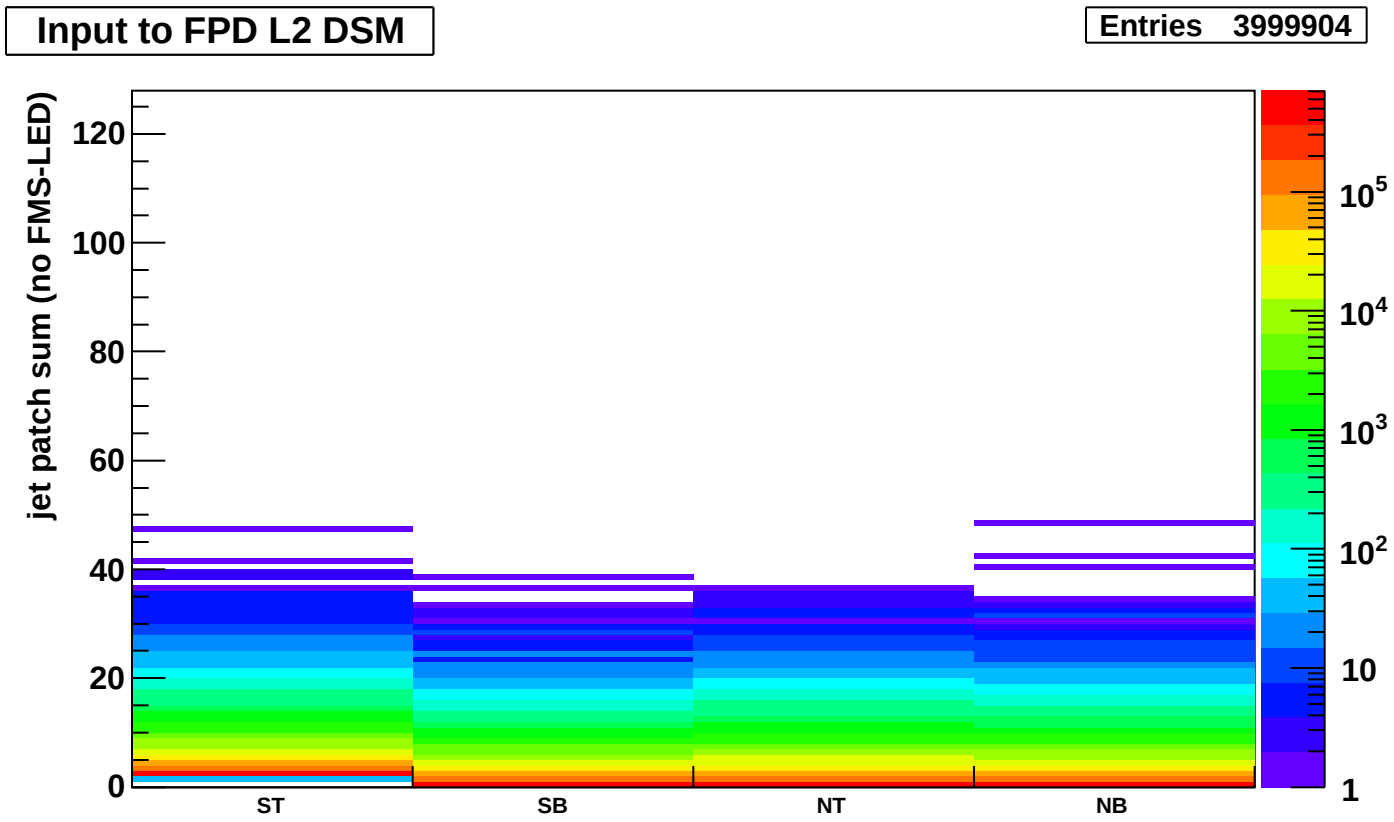

Input to FPD L2 DSM
Entries 5
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

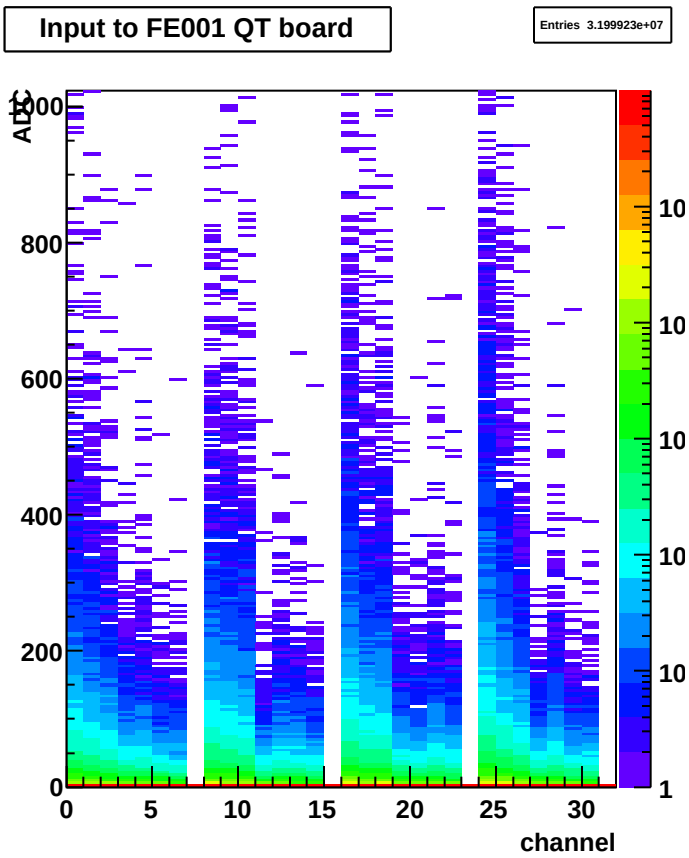

Input to FPD L2 DSM
Entries 2999928
CL bits
80
27
25
805
105
120
20793
6229
6005
978298
993615
993826
SMALL
LARGE-S
LARGE-N

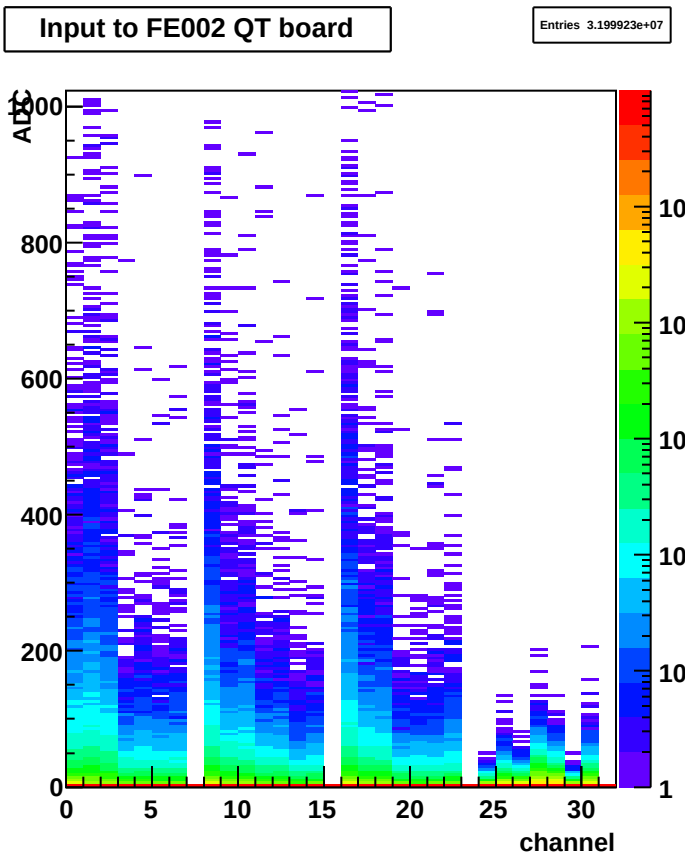

Input to FPD L2 DSM
Entries 55
CL bits - simulated
55
SMALL
LARGE-S
LARGE-N

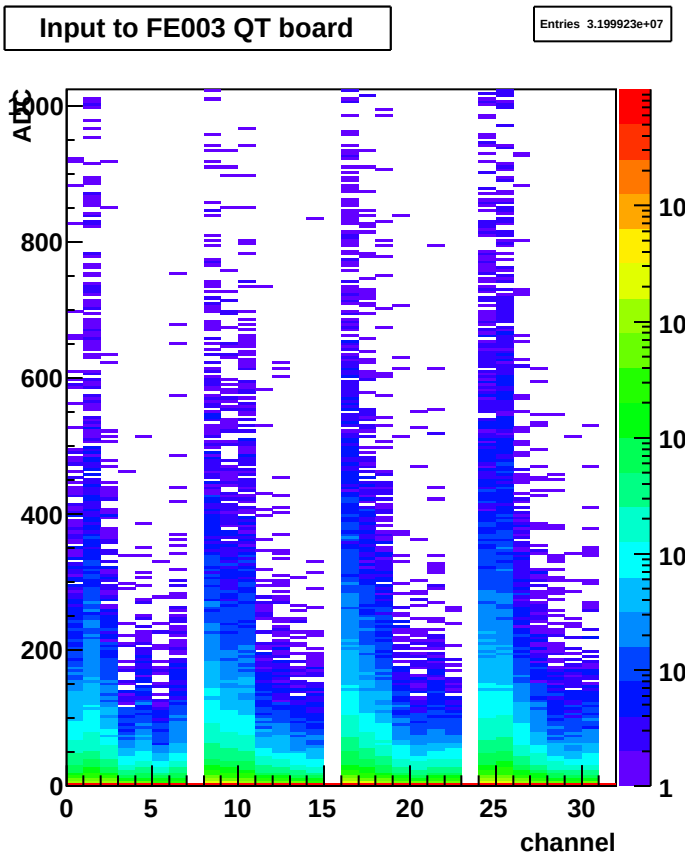

Input to FPD L2 DSM
Entries 2999928
HT bits
13697
6340
6849
986279
993636
993127
SMALL
LARGE-S
LARGE-N

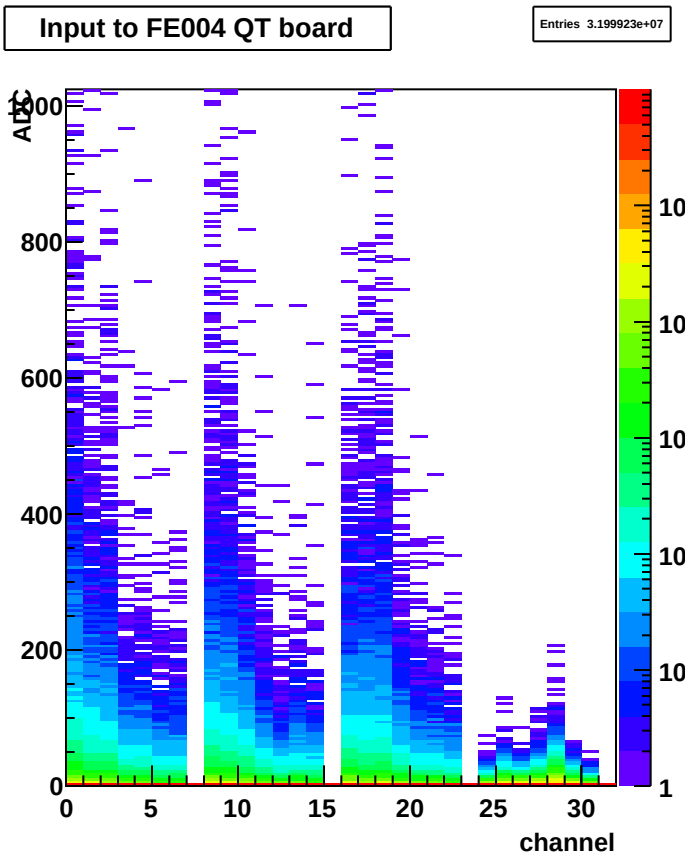

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

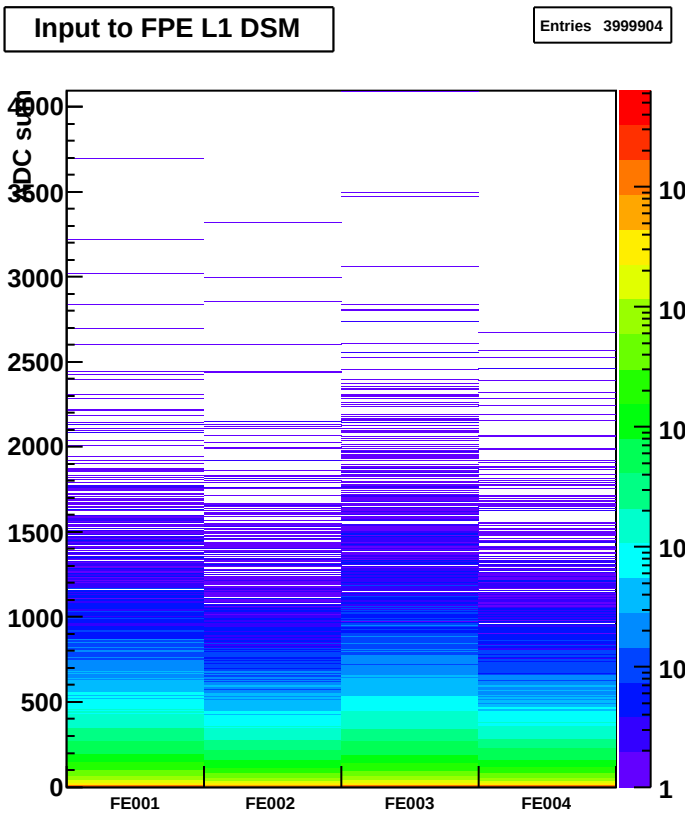

Input to FPD L2 DSM
Entries 3999904
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

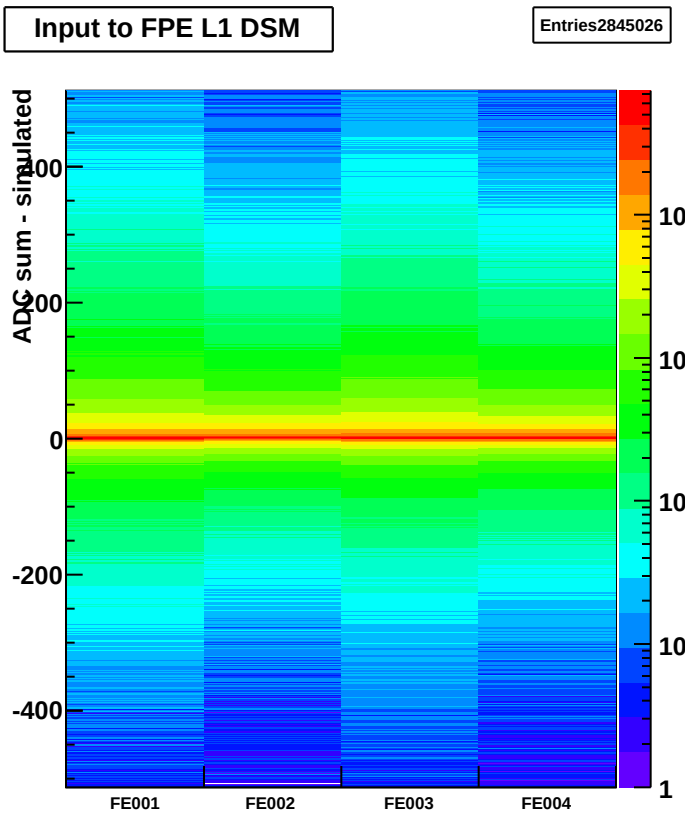

Input to FE001 QT board
Entries 3.199923e+07
ADC
channel

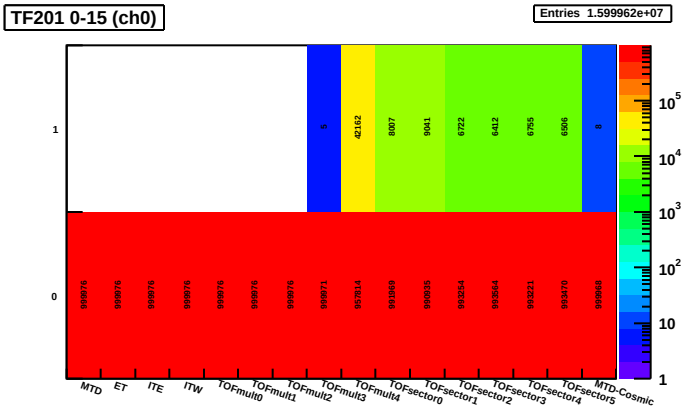

Input to FE002 QT board
Entries 3.199923e+07
ADC
channel

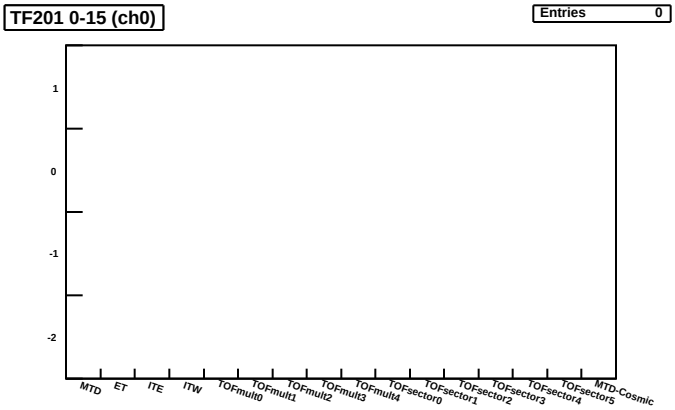

Input to FE003 QT board
Entries 3.199923e+07
ADC
channel

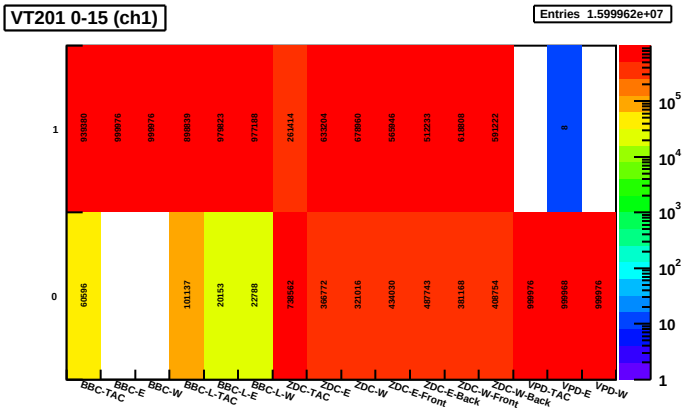

Input to FE004 QT board
Entries 3.199923e+07
ADC
channel

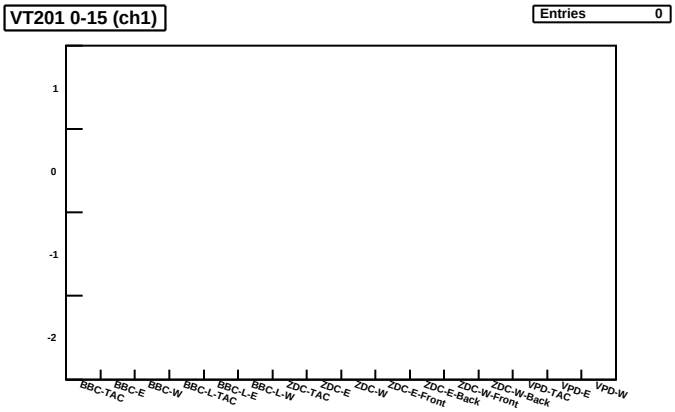

Input to FPE L1 DSM
Entries 3999904
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

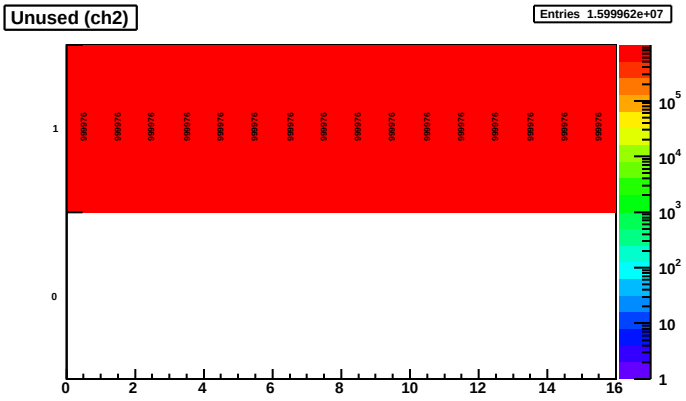

Input to FPE L1 DSM
Entries2845026
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1

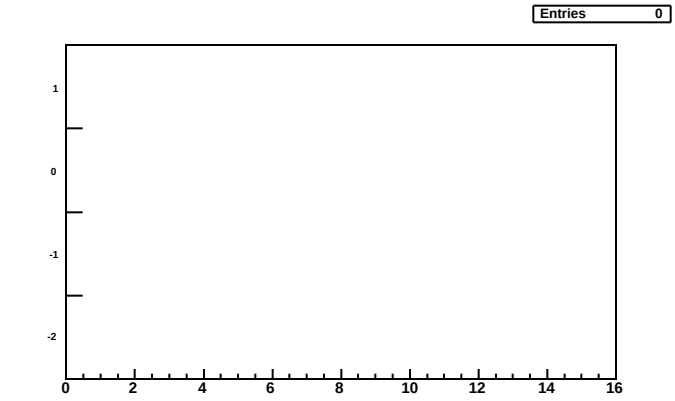

TF201 0-15 (ch0)
Entries 1.599962e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

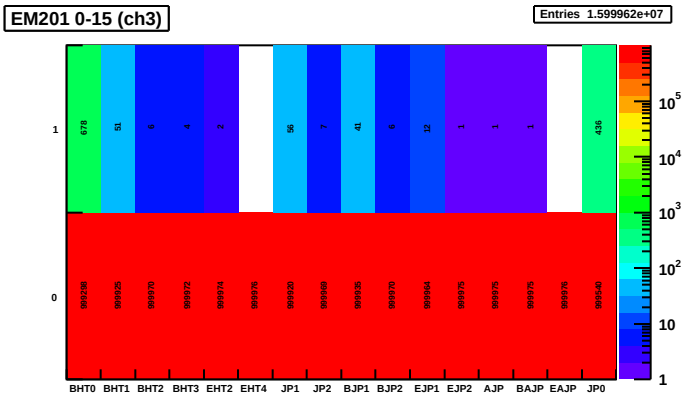

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

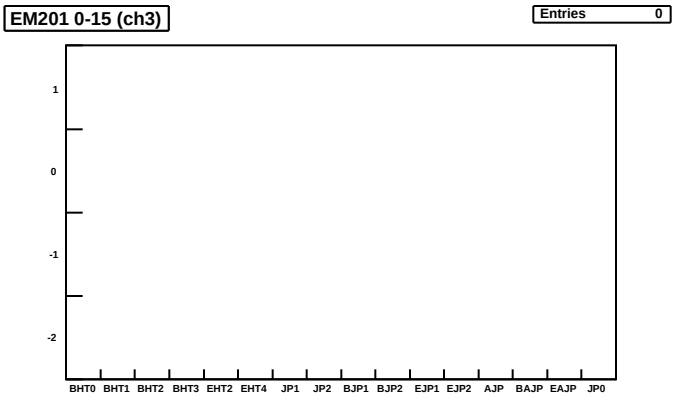

VT201 0-15 (ch1)
Entries 1.599962e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

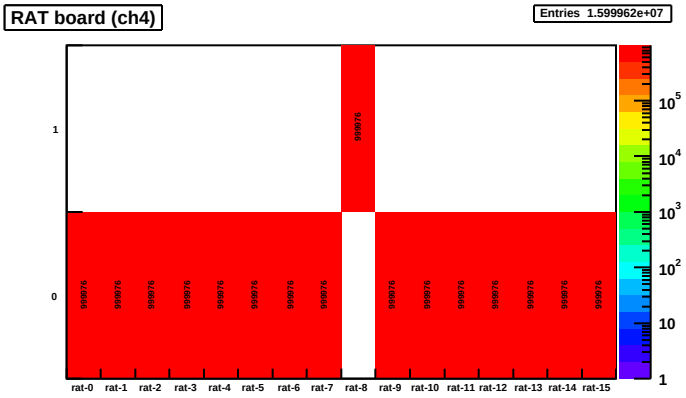

VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

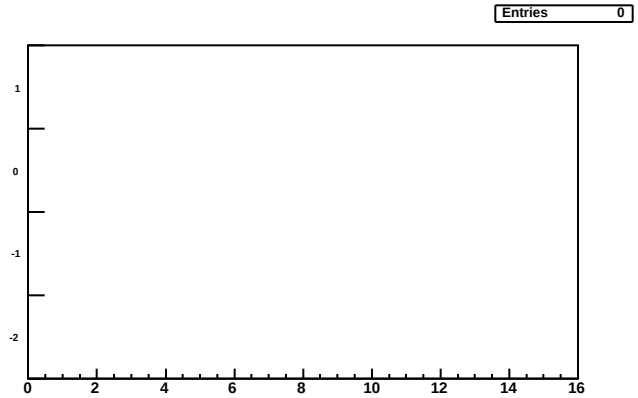

Unused (ch2)
Entries 1.599962e+07

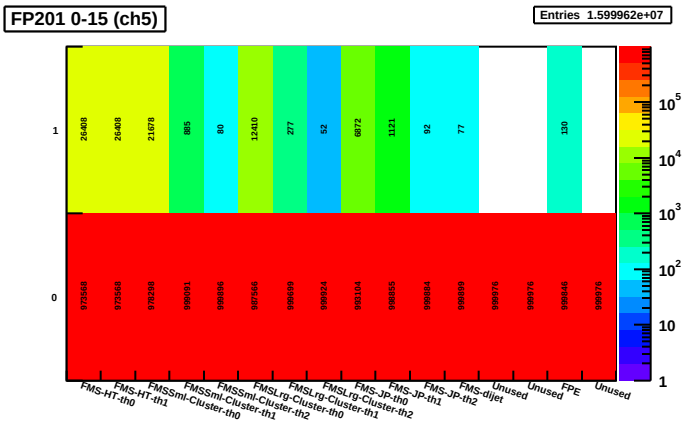

Entries 0

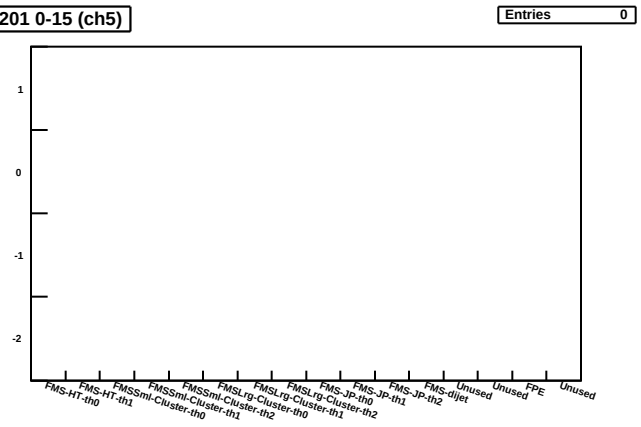

EM201 0-15 (ch3)
Entries 1.599962e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

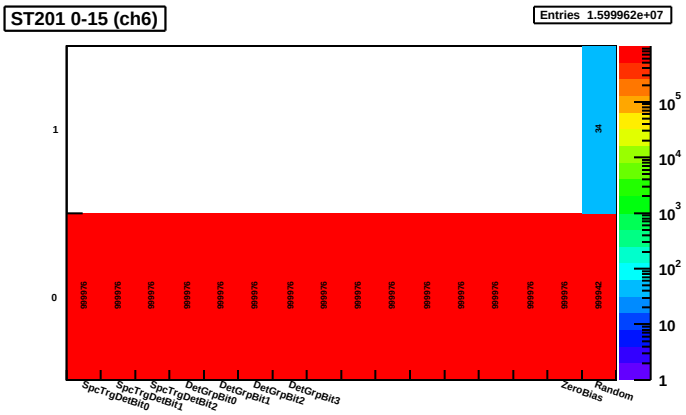

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

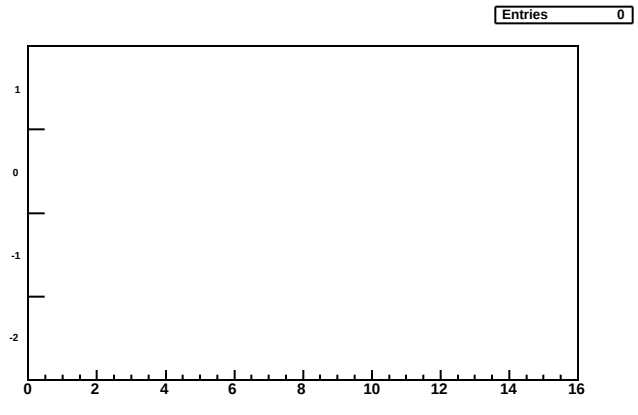

RAT board (ch4)
Entries 1.599962e+07
999976
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

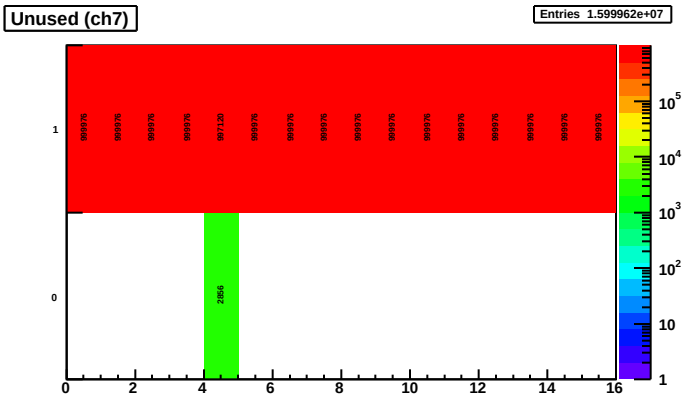

Entries 0

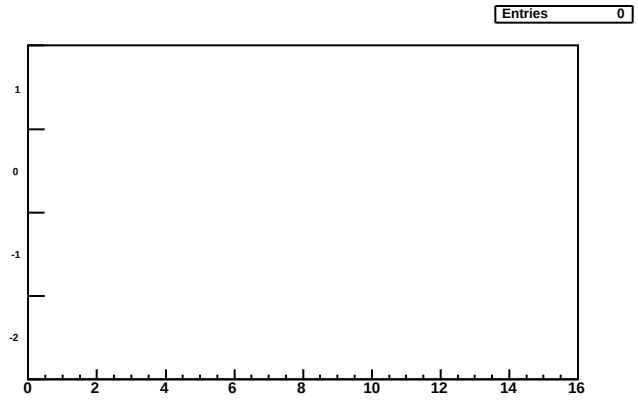

FP201 0-15 (ch5)
Entries 1.599962e+07
25408 25408 21678 885 80 12410 277 52 6872 1121 92 77 110
973568 973568 978298 990091 998896 997566 996699 999924 993104 998855 998884 998839 999976 999976 998946 999976
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused

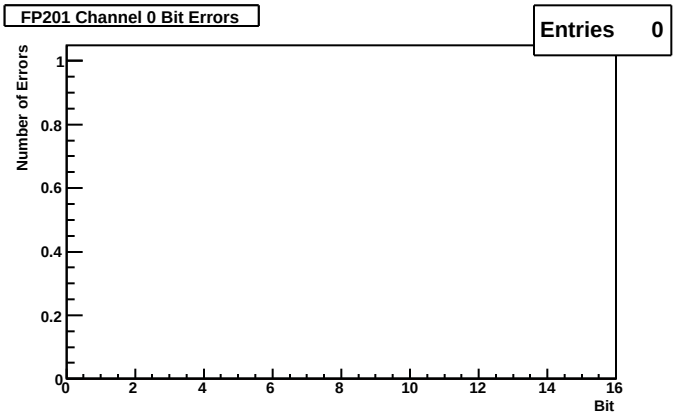

FP201 0-15 (ch5)
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused

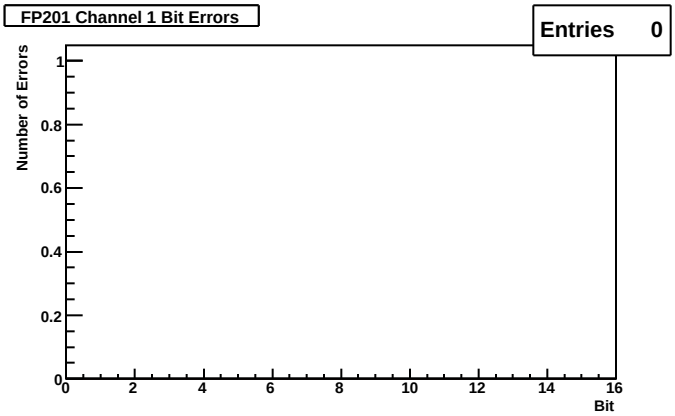

ST201 0-15 (ch6)
Entries 1.599962e+07
34
999976 999976 999976 999976 999976 999976 999976 999976 999976 999976 999976 999976 999976 999976 999942
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

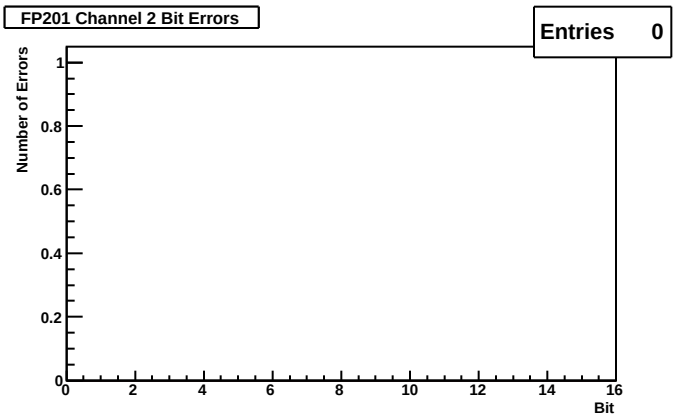

Entries 0

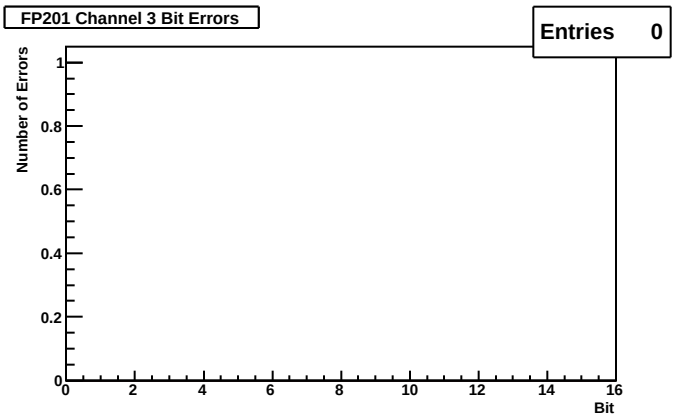

Unused (ch7)
Entries 1.599962e+07
999976 999976 999976 999976 997120 999976 999976 999976 999976 999976 999976 999976 999976 999976 999976
2856

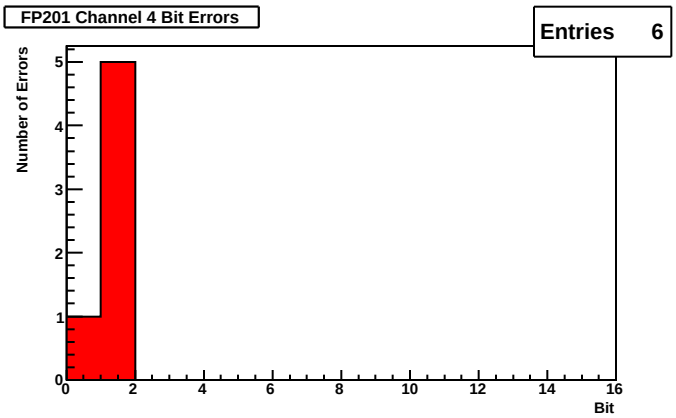

Entries 0

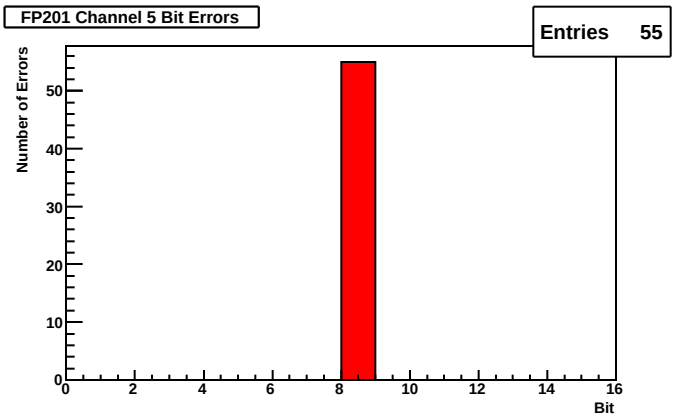

FP201 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

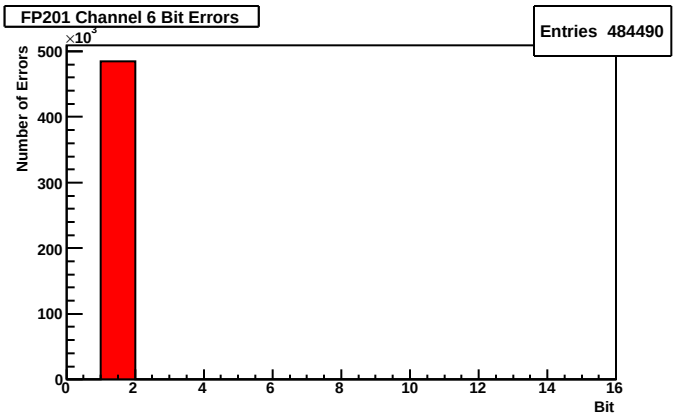

FP201 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

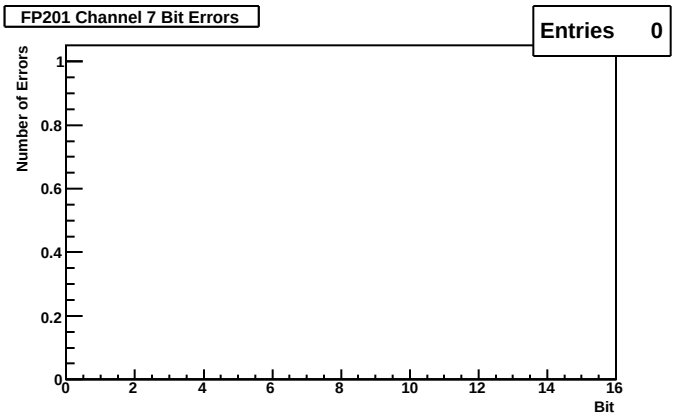

FP201 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

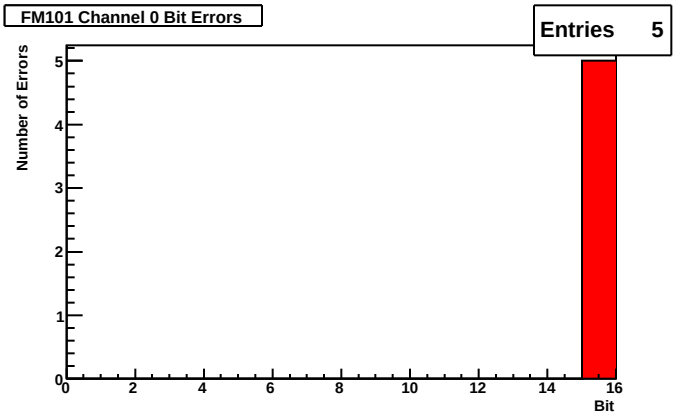

FP201 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

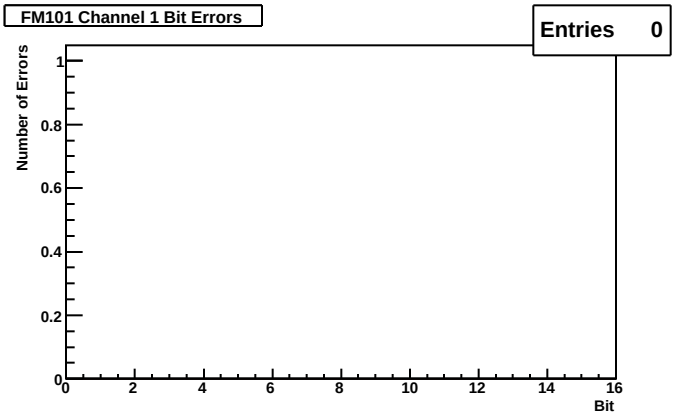

FP201 Channel 4 Bit Errors
Entries 6
Number of Errors
Bit

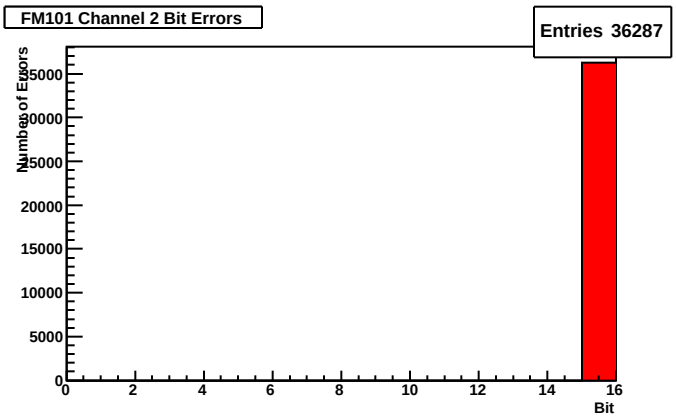

FP201 Channel 5 Bit Errors
Entries 55
Number of Errors
Bit

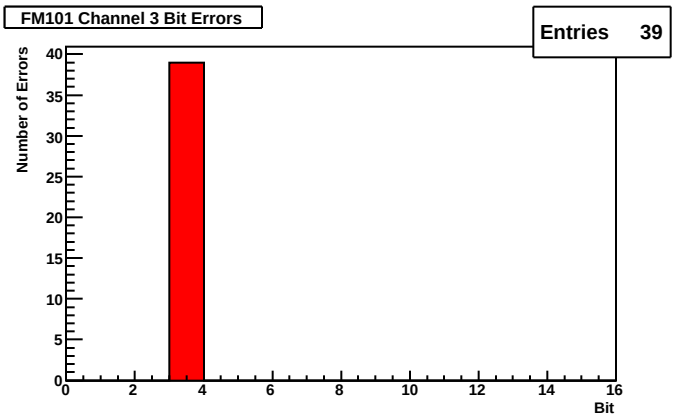

FP201 Channel 6 Bit Errors
Entries 484490
Number of Errors
Bit

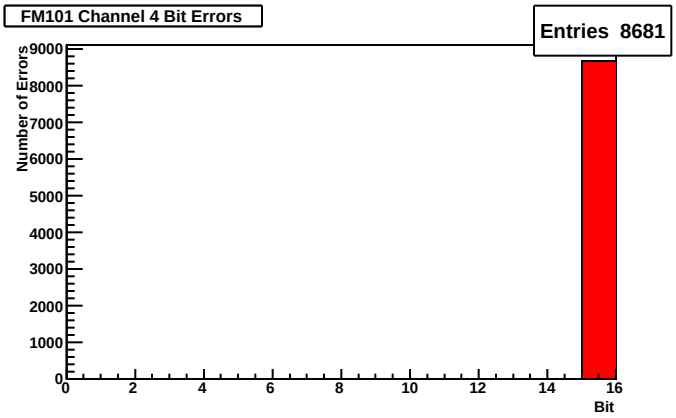

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

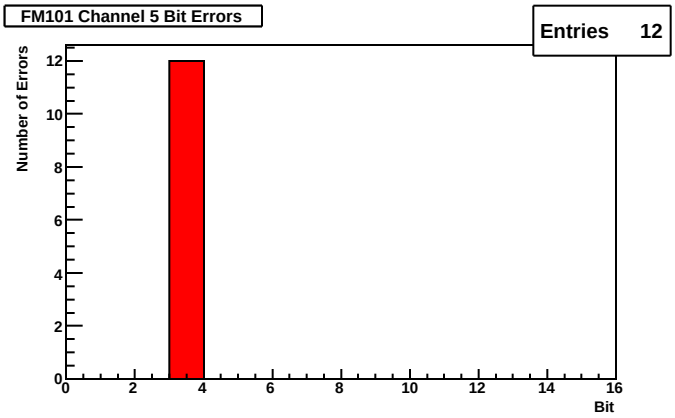

FM101 Channel 0 Bit Errors
Entries 5
Number of Errors
Bit

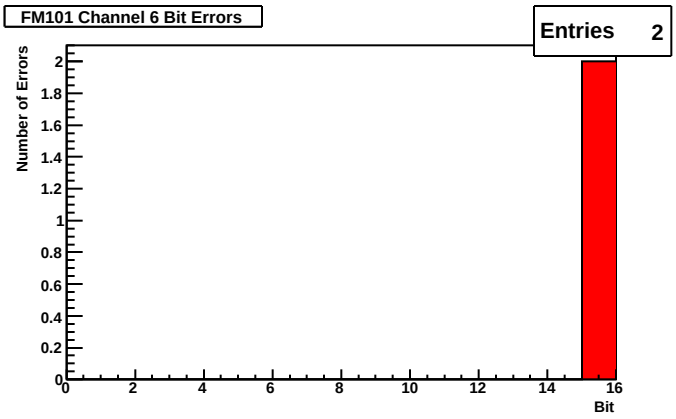

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

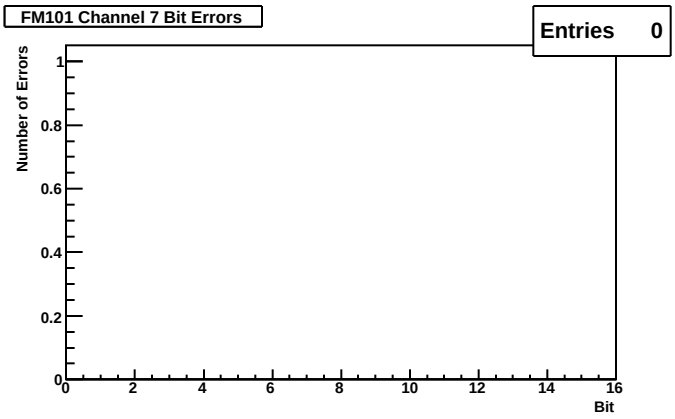

FM101 Channel 2 Bit Errors
Entries 36287
Number of Errors
Bit

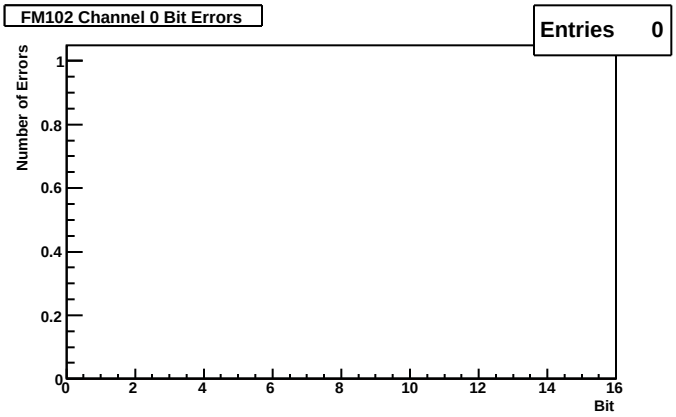

FM101 Channel 3 Bit Errors
Entries 39
Number of Errors
Bit

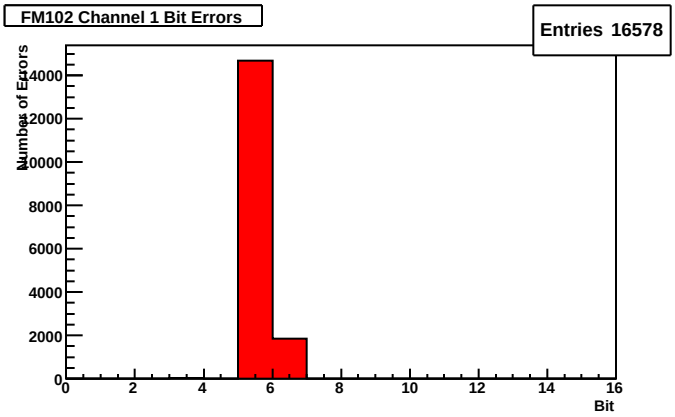

FM101 Channel 4 Bit Errors
Entries 8681
Number of Errors
Bit

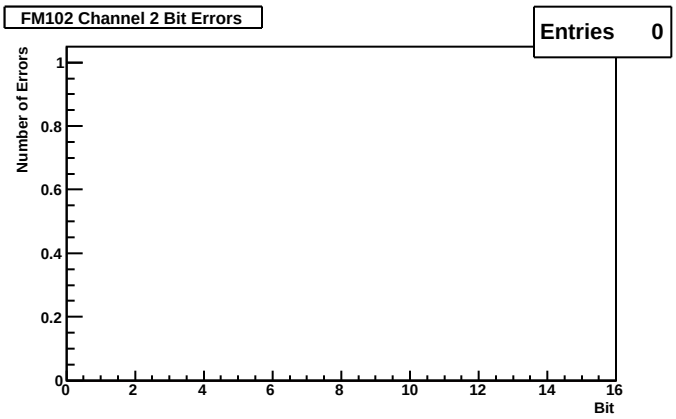

FM101 Channel 5 Bit Errors
Entries 12
Number of Errors
Bit

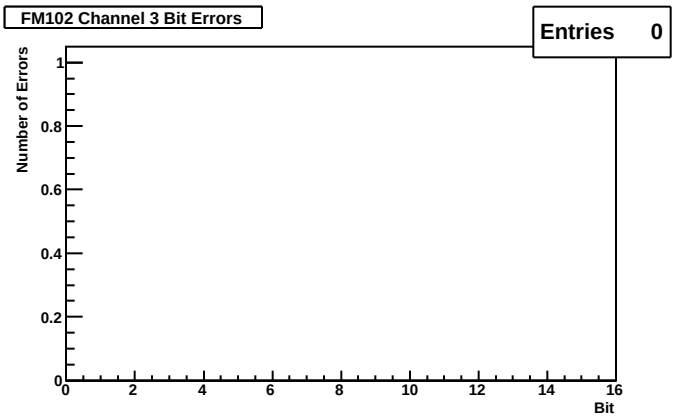

FM101 Channel 6 Bit Errors
Entries 2
Number of Errors
Bit

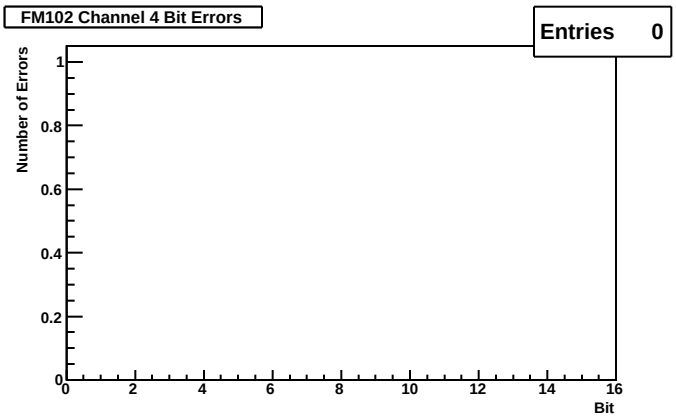

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

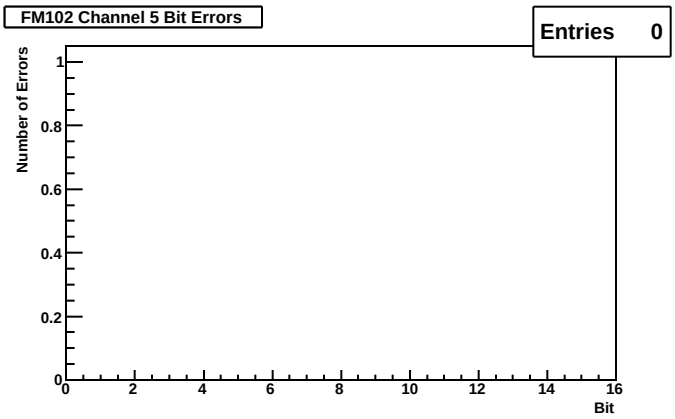

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

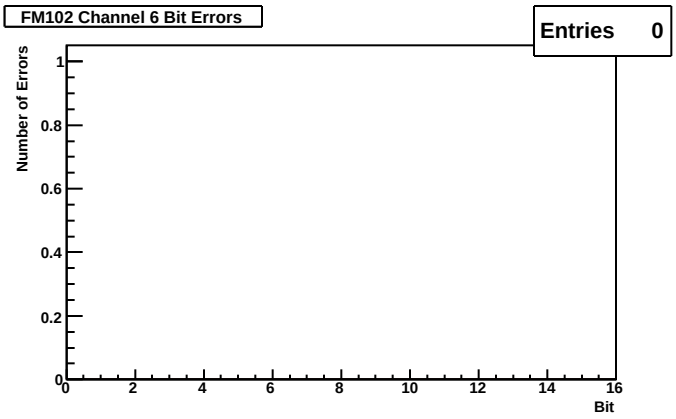

FM102 Channel 1 Bit Errors
Entries 16578
Number of Errors
Bit

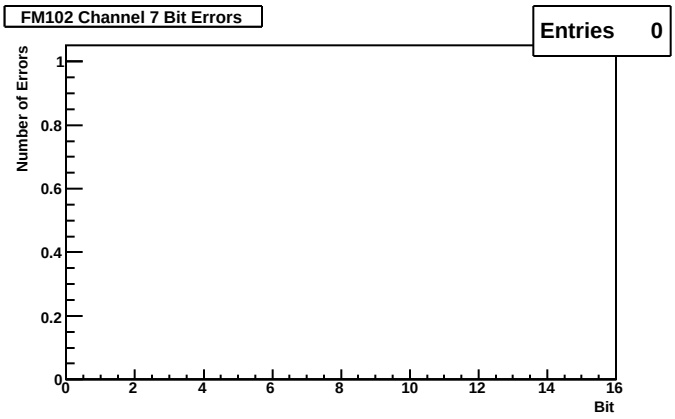

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

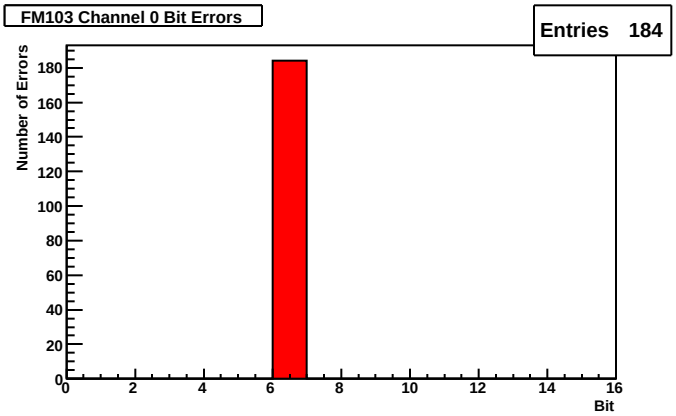

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

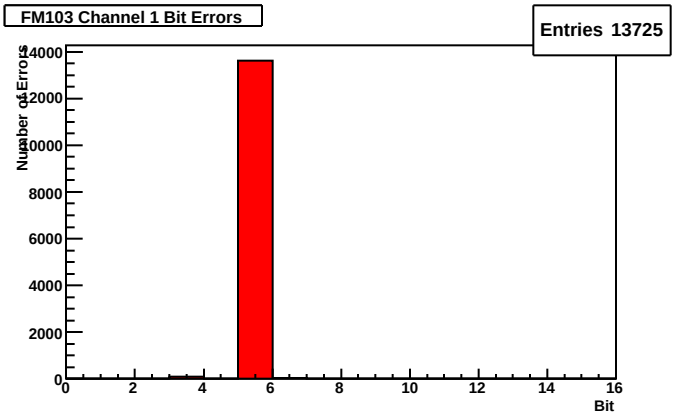

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

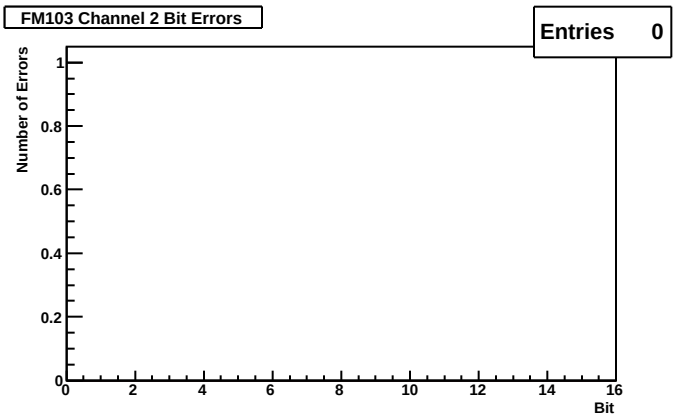

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

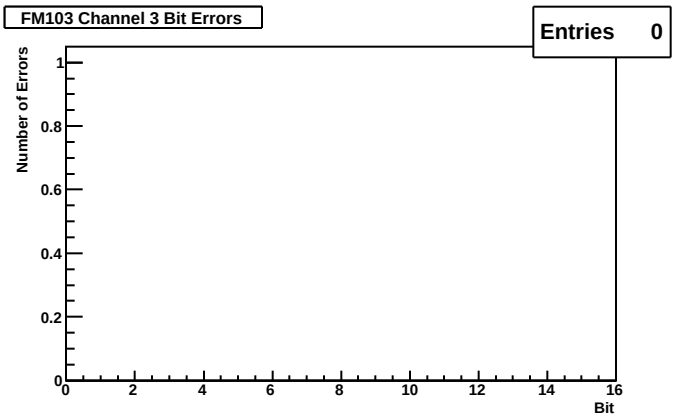

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

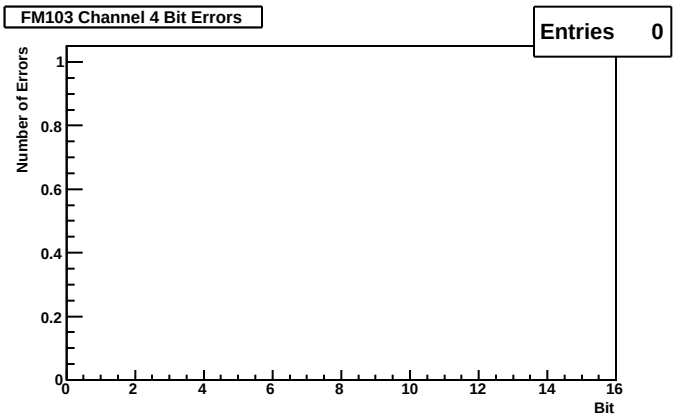

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

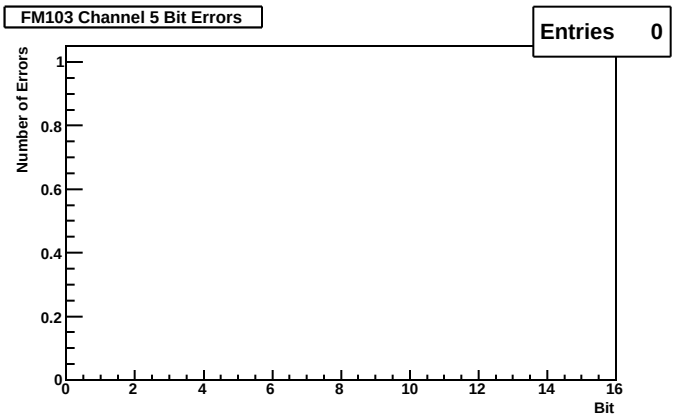

FM103 Channel 0 Bit Errors
Entries 184
Number of Errors
Bit

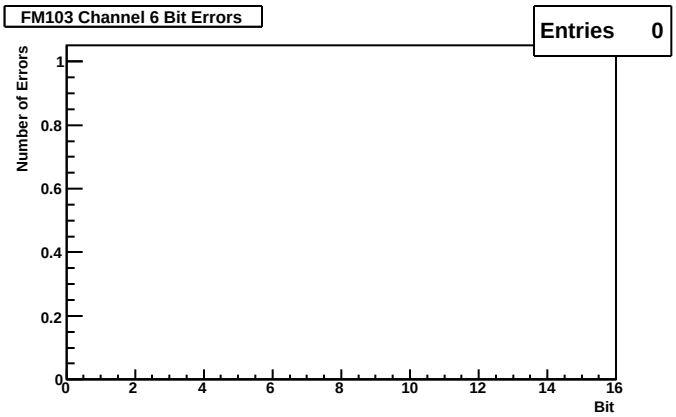

FM103 Channel 1 Bit Errors
Entries 13725
Number of Errors
Bit

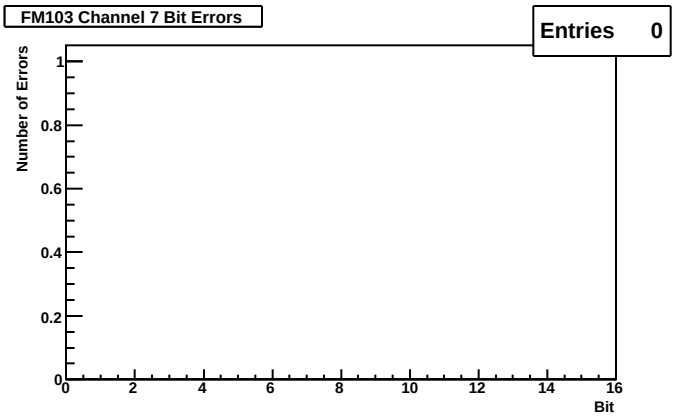

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

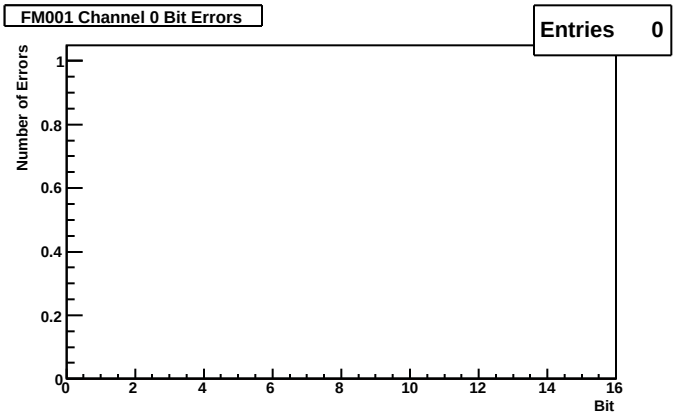

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

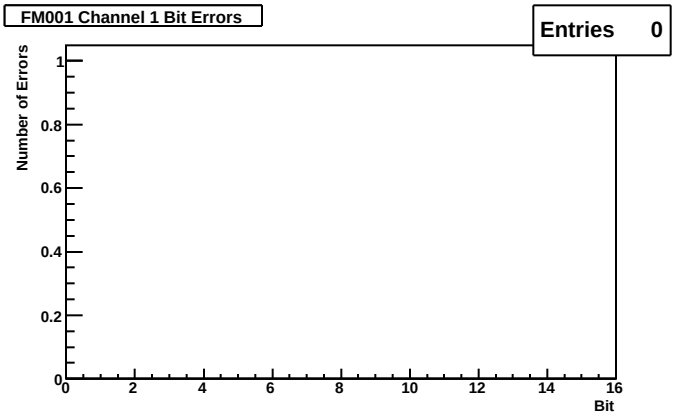

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

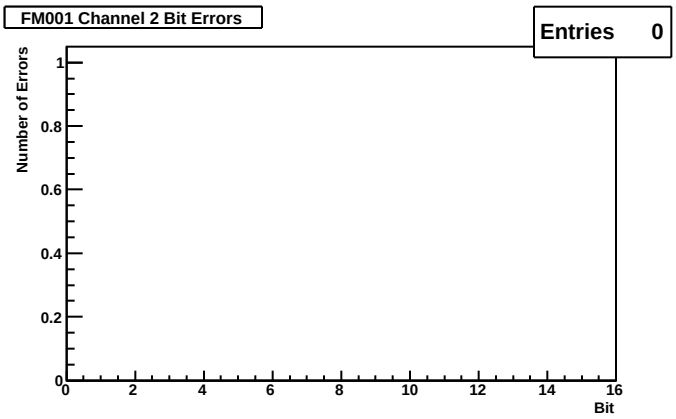

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

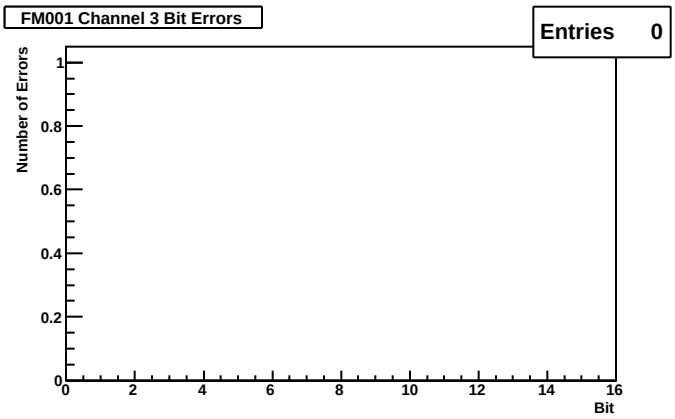

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

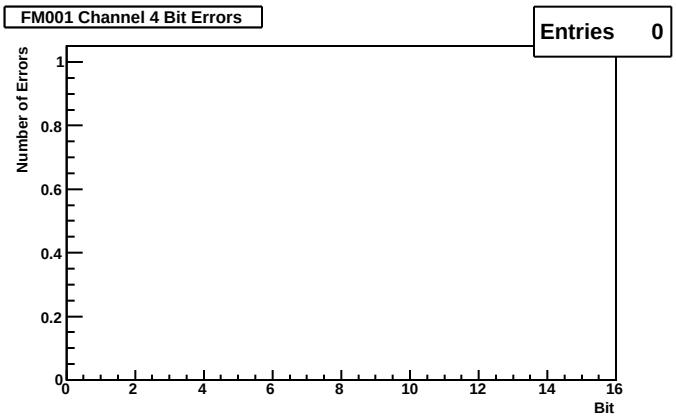

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

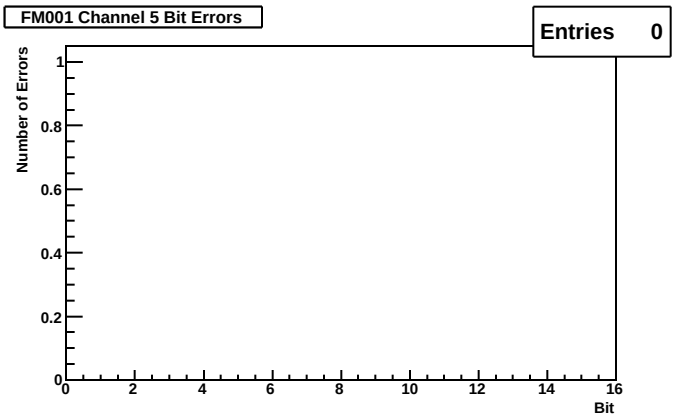

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

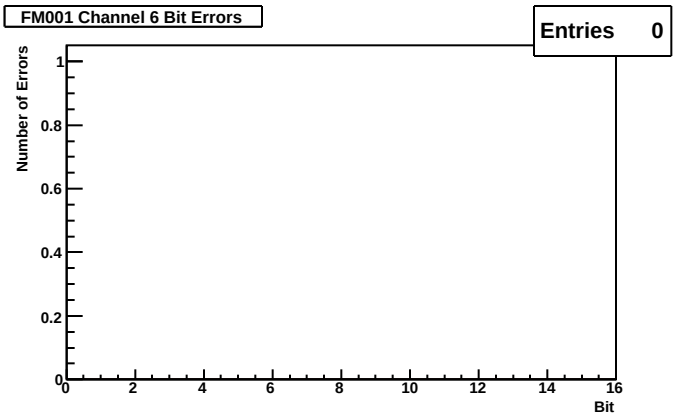

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

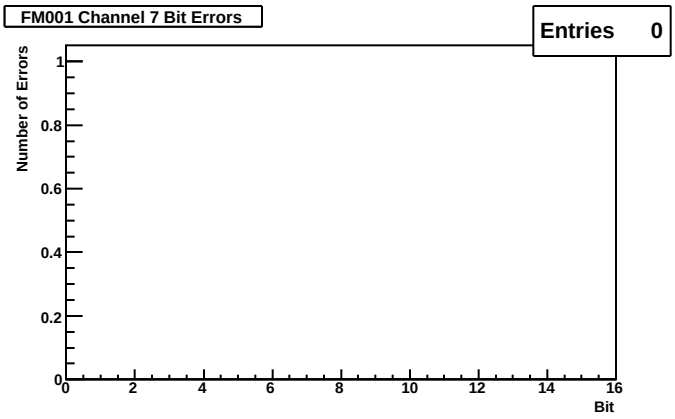

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

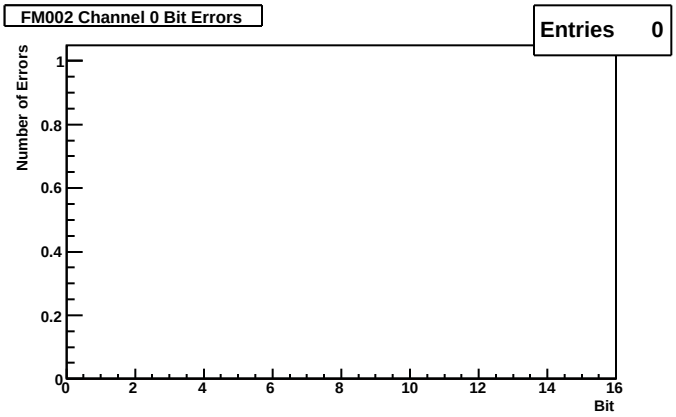

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

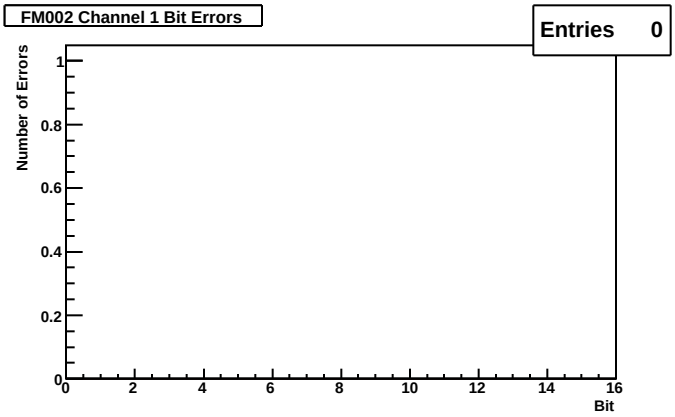

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

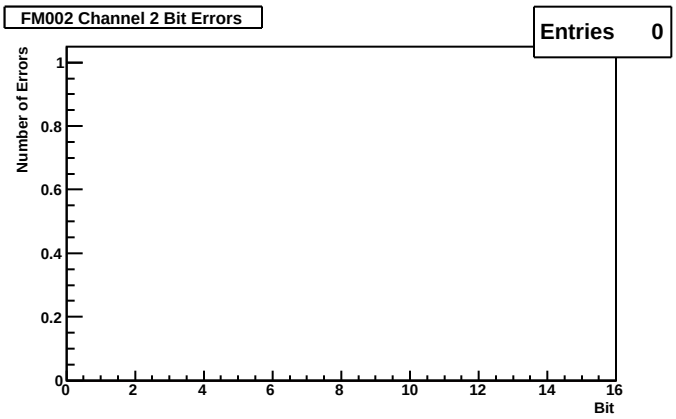

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

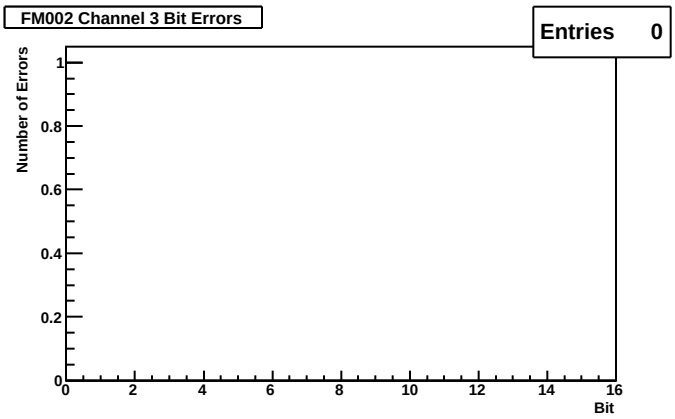

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

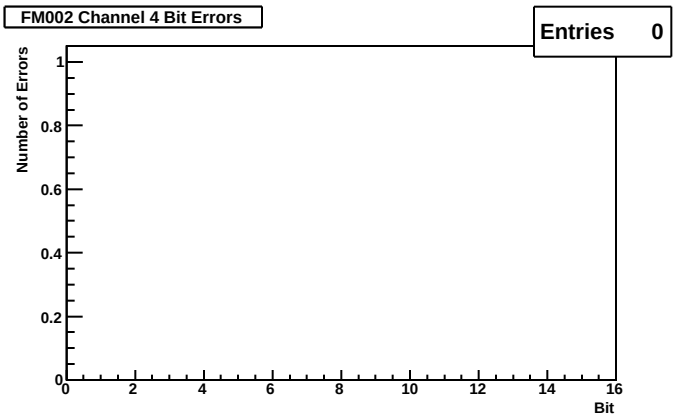

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

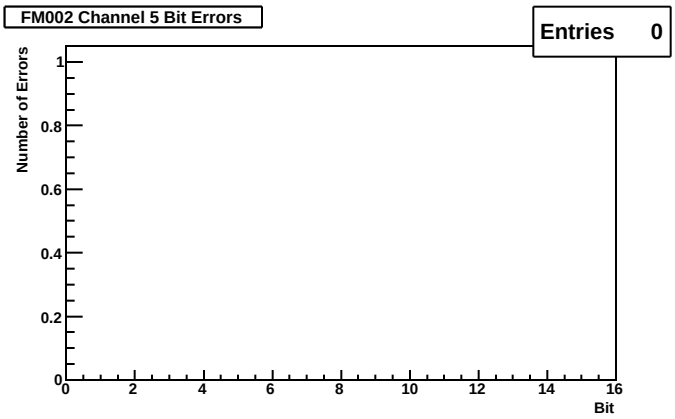

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

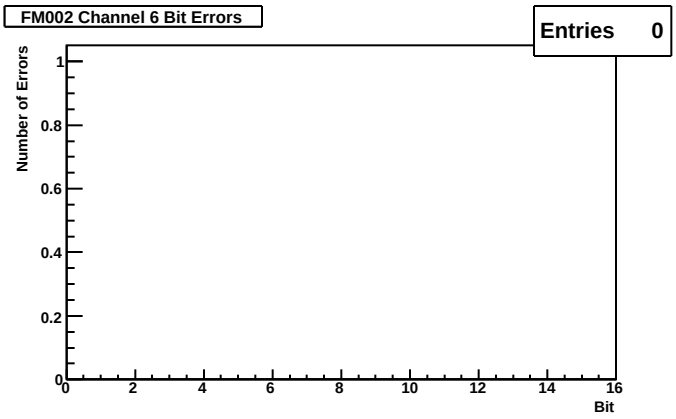

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

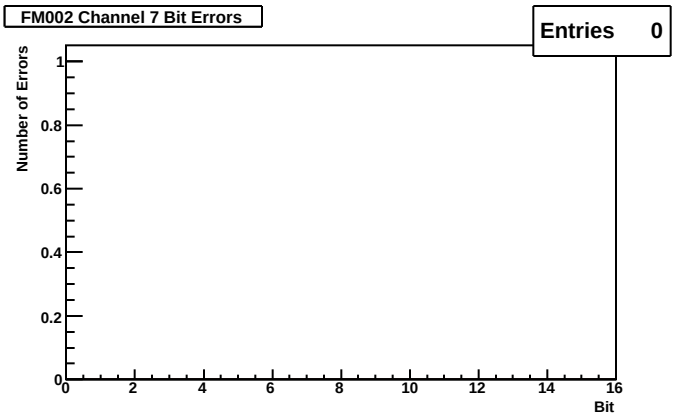

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

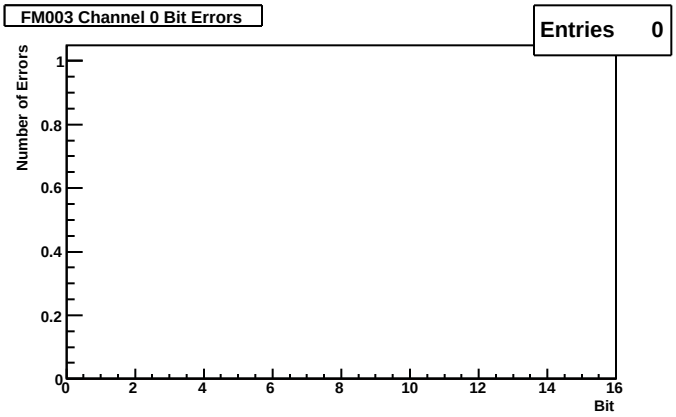

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

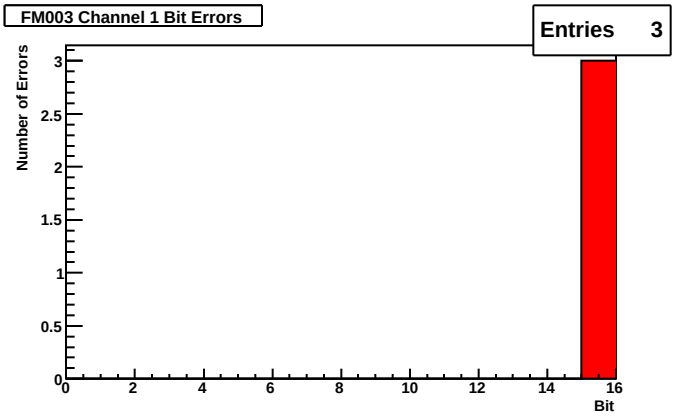

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

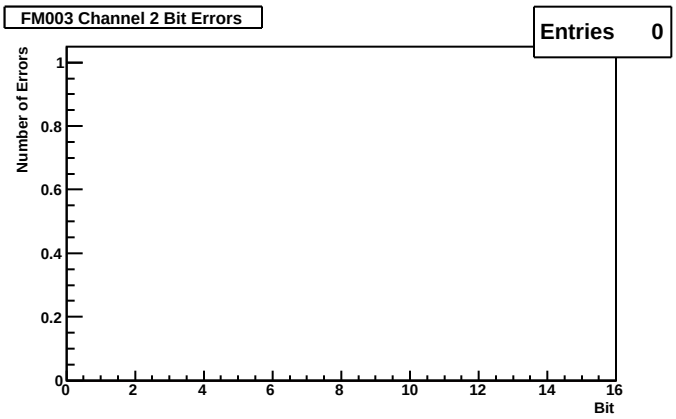

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

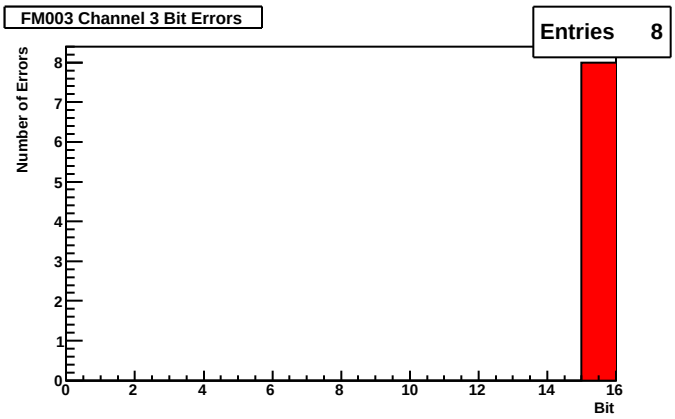

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

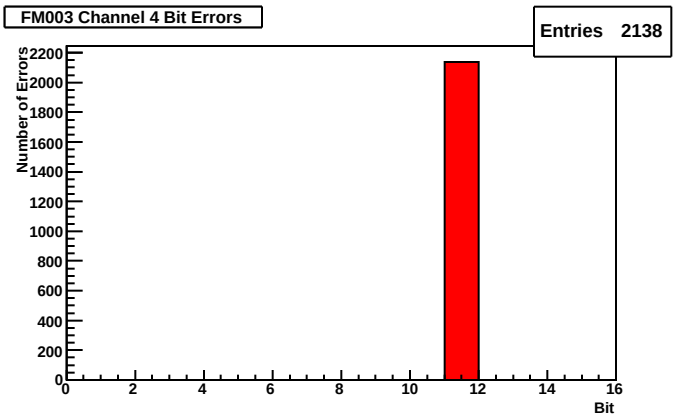

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

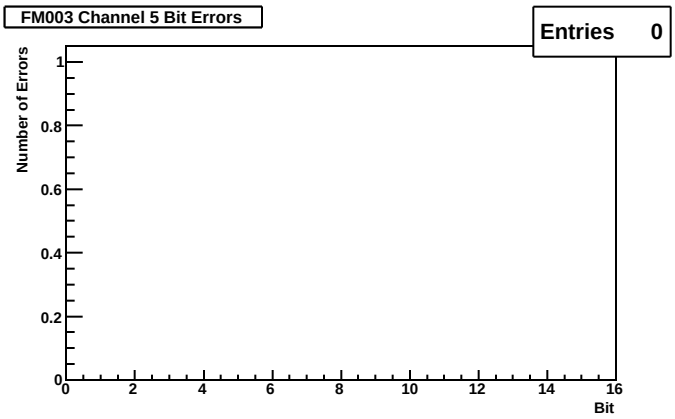

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

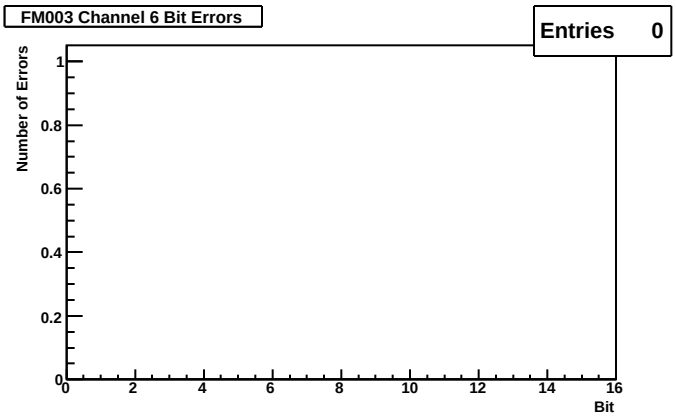

FM003 Channel 1 Bit Errors
Entries 3
Number of Errors
Bit

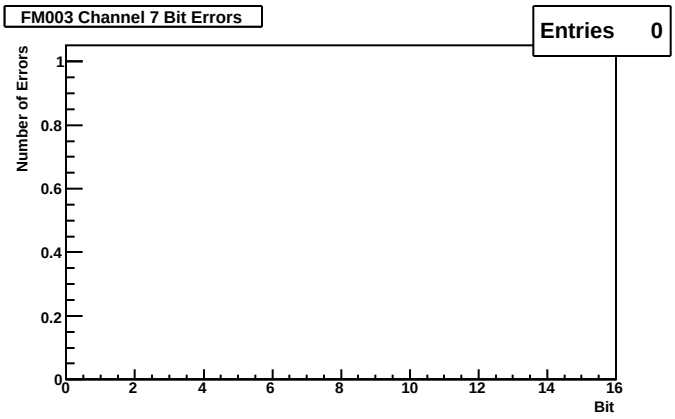

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

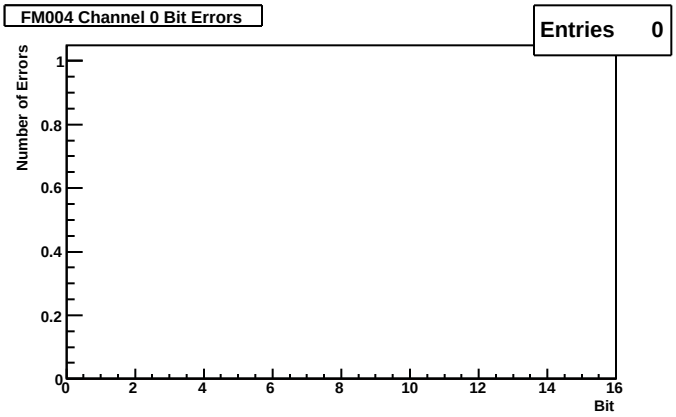

FM003 Channel 3 Bit Errors
Entries 8
Number of Errors
Bit

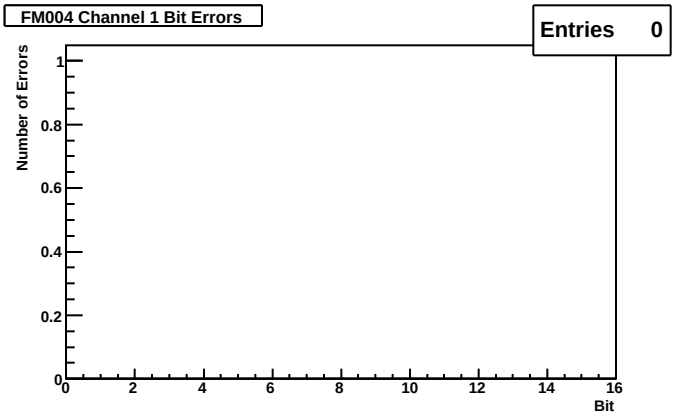

FM003 Channel 4 Bit Errors
Entries 2138
Number of Errors
Bit

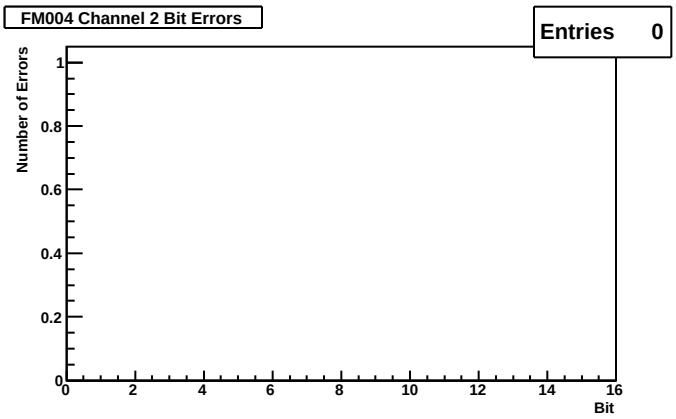

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

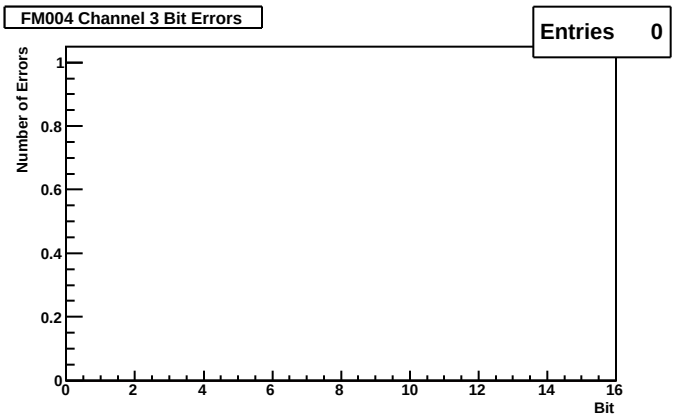

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

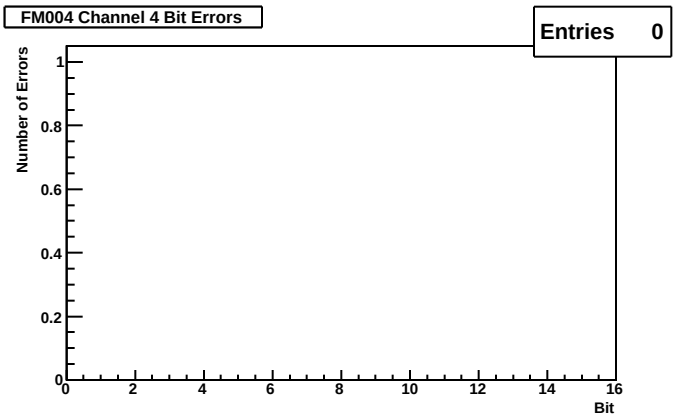

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

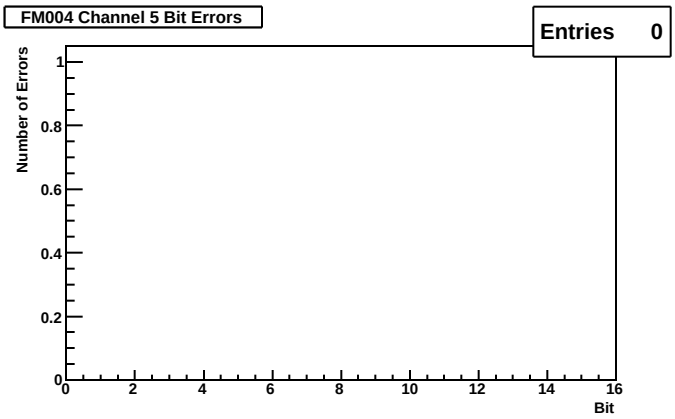

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

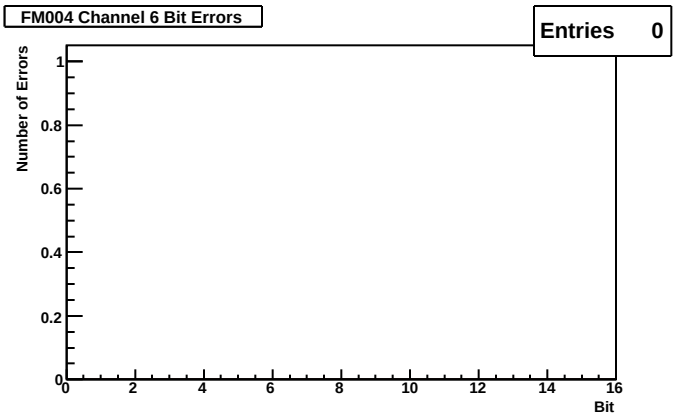

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

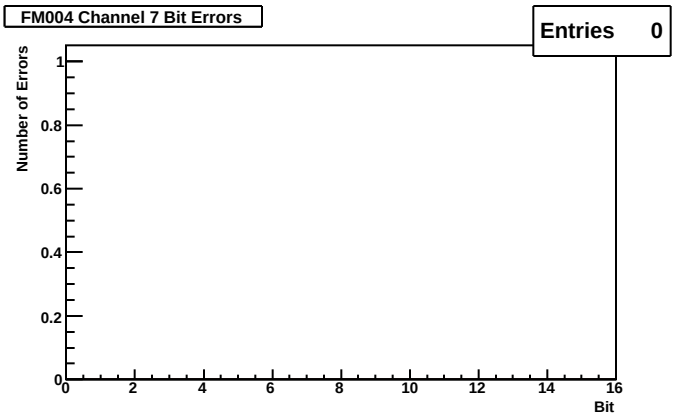

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

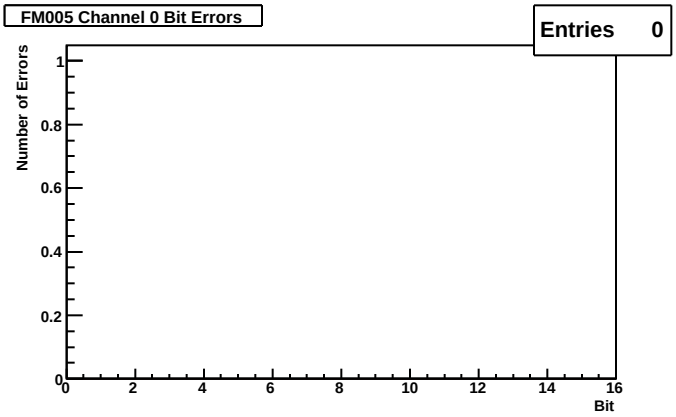

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

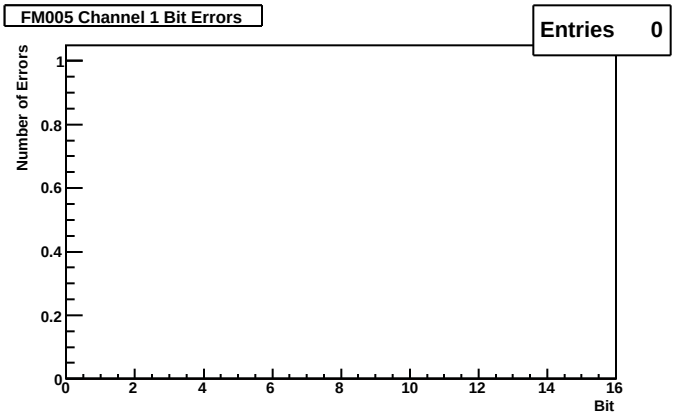

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

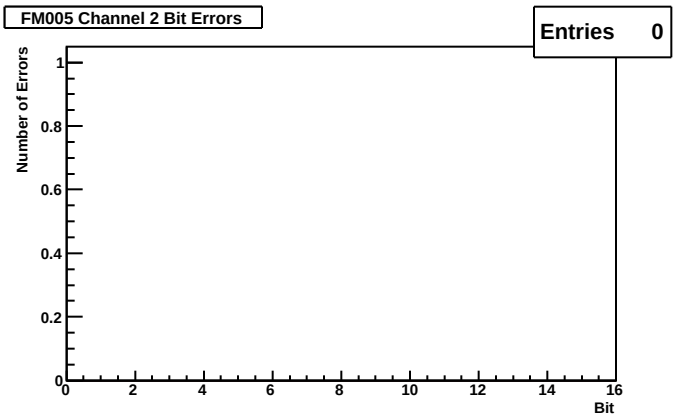

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

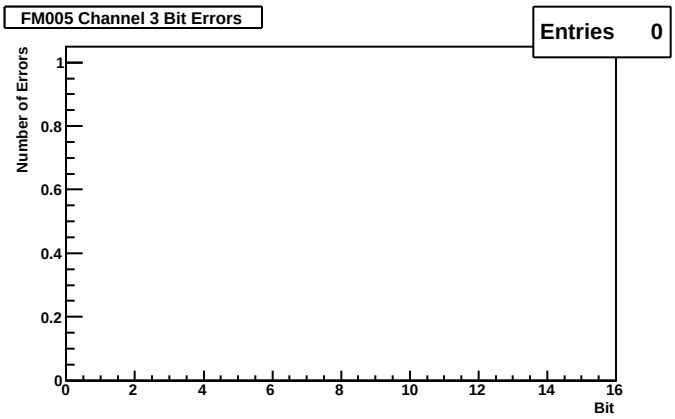

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

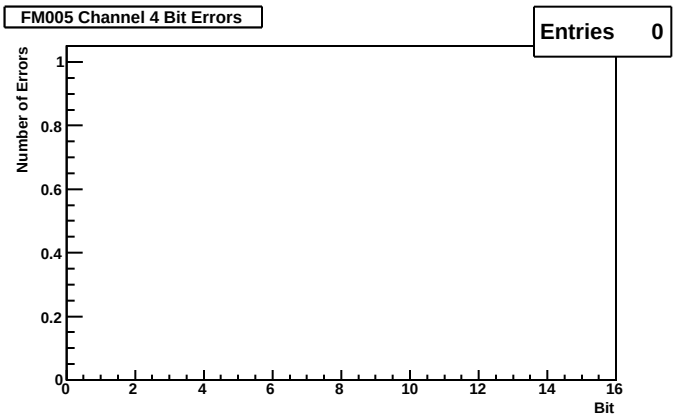

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

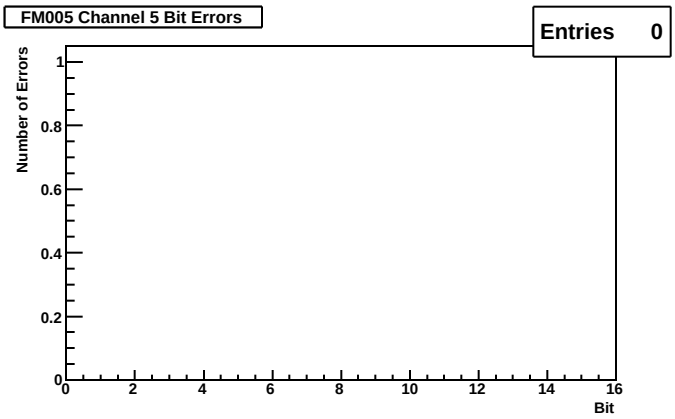

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

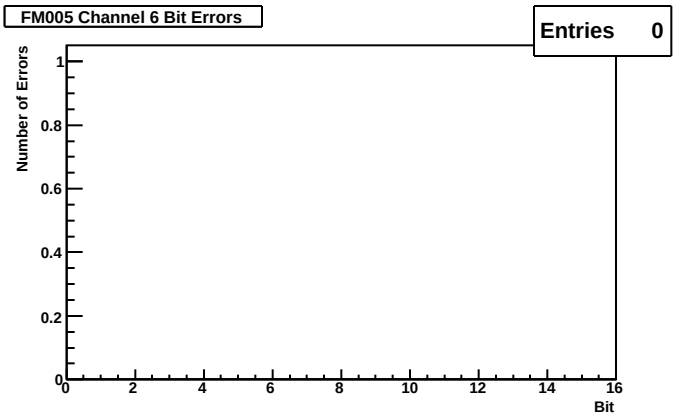

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

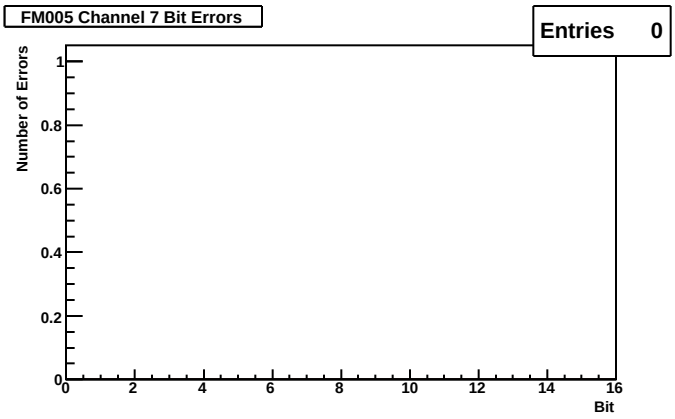

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

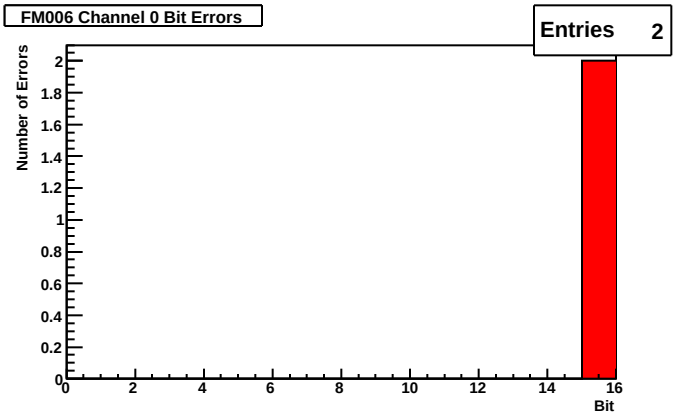

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

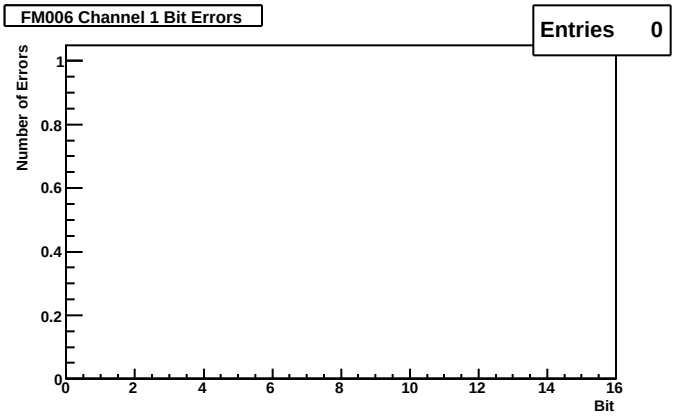

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

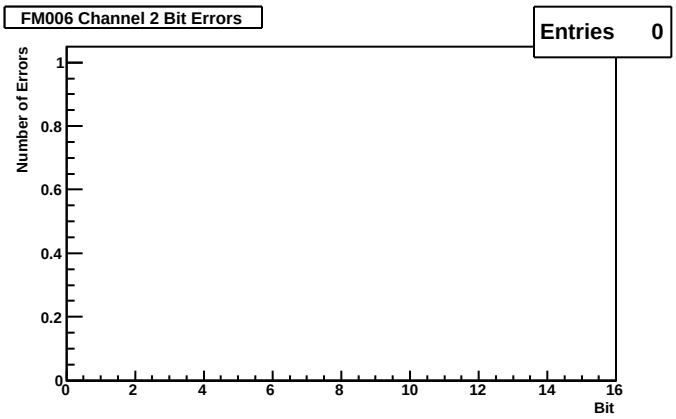

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

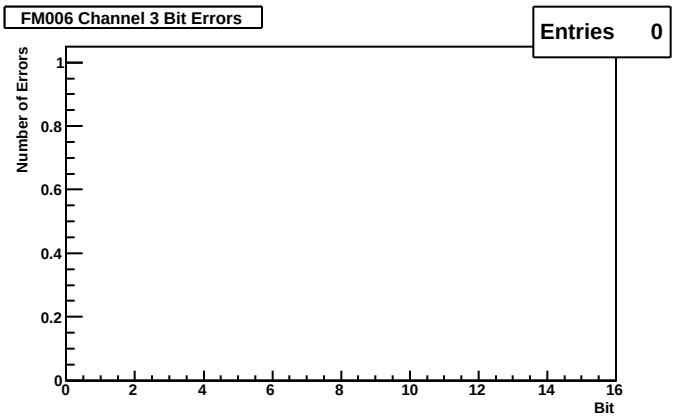

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

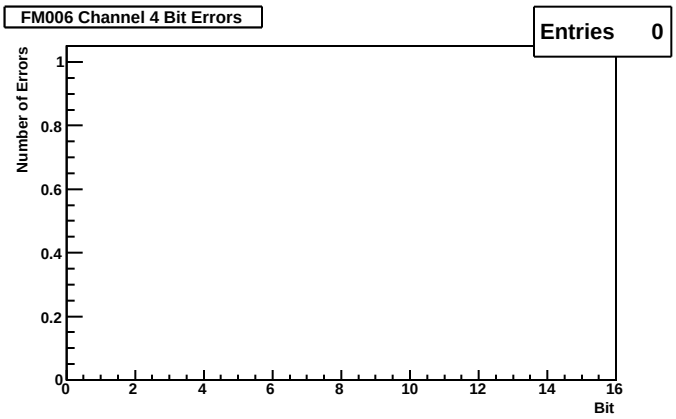

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

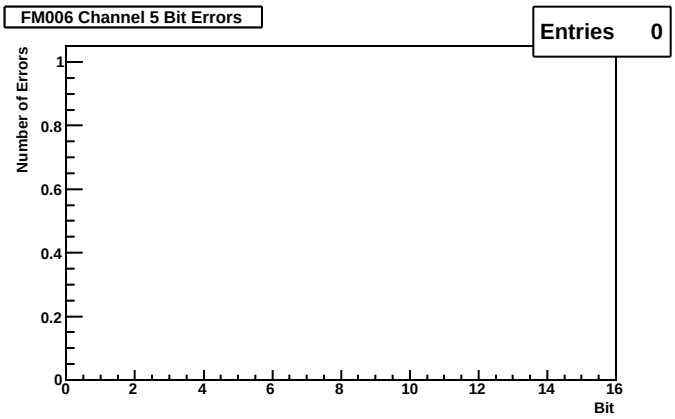

FM006 Channel 0 Bit Errors
Entries 2
Number of Errors
Bit

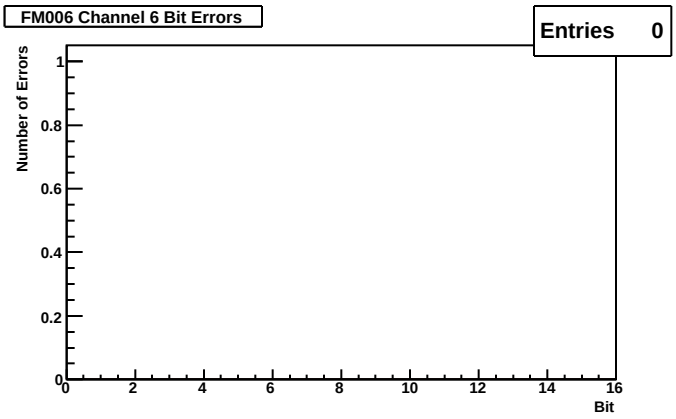

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

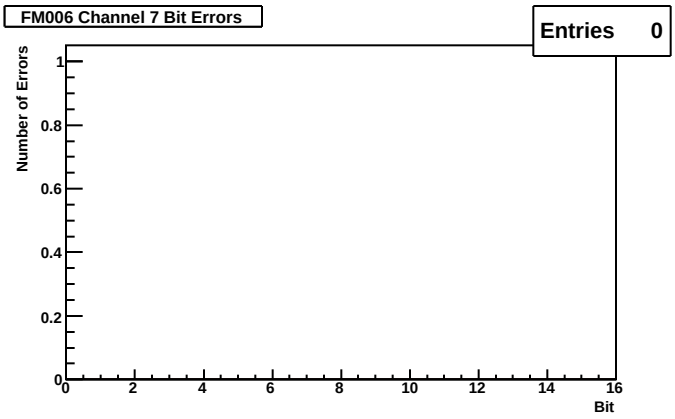

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

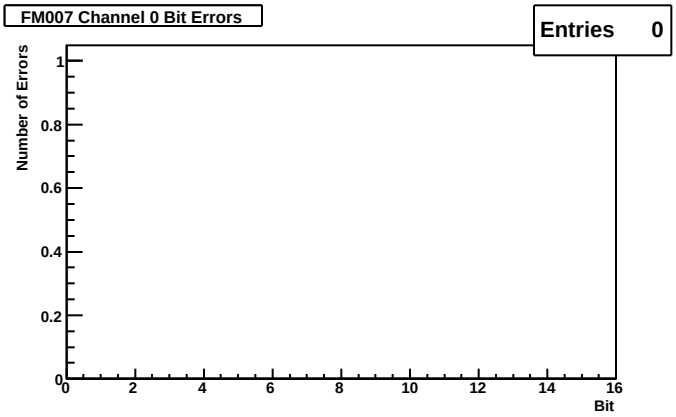

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

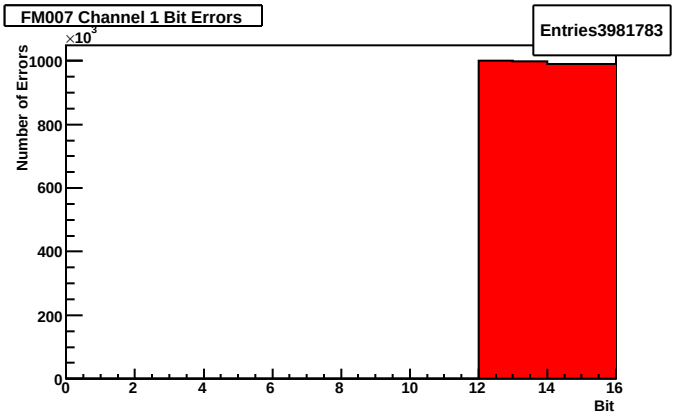

FM006 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

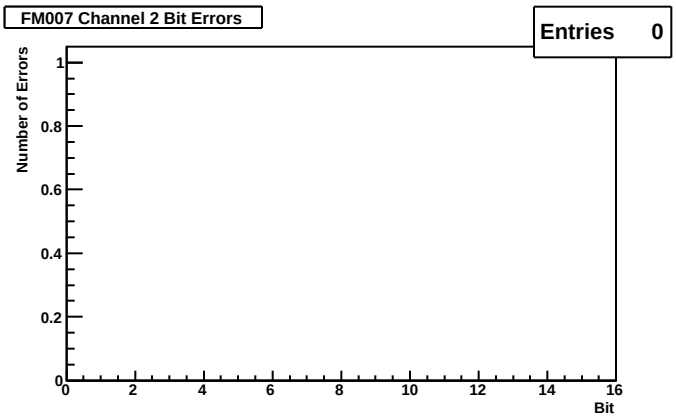

FM006 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

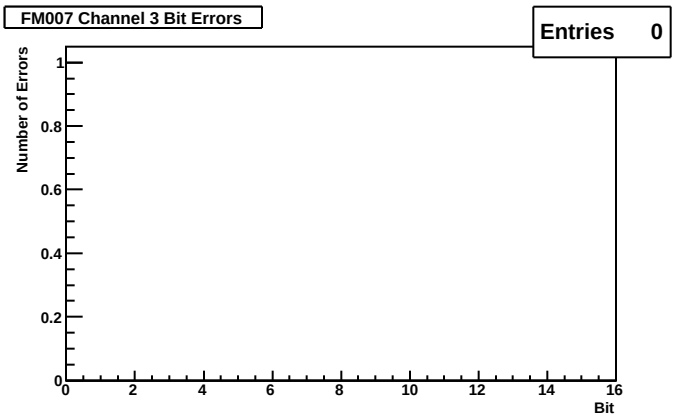

FM006 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

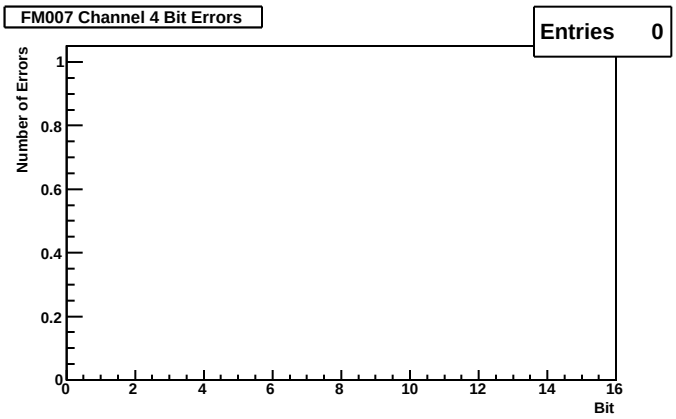

FM006 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

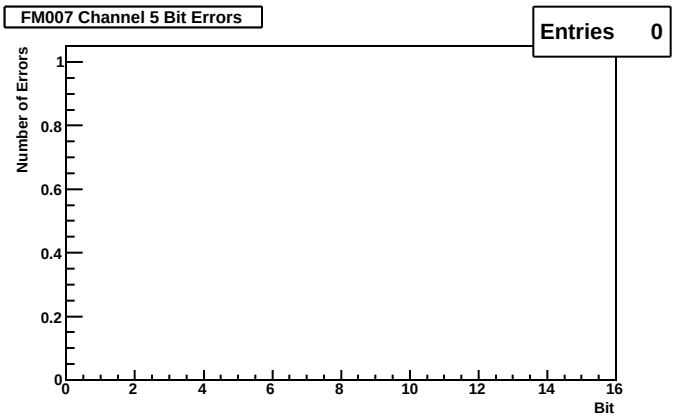

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

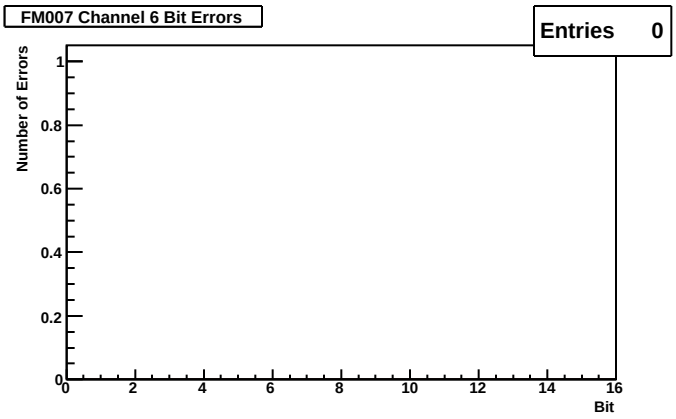

FM007 Channel 1 Bit Errors
Entries3981783
×10³
Number of Errors
Bit

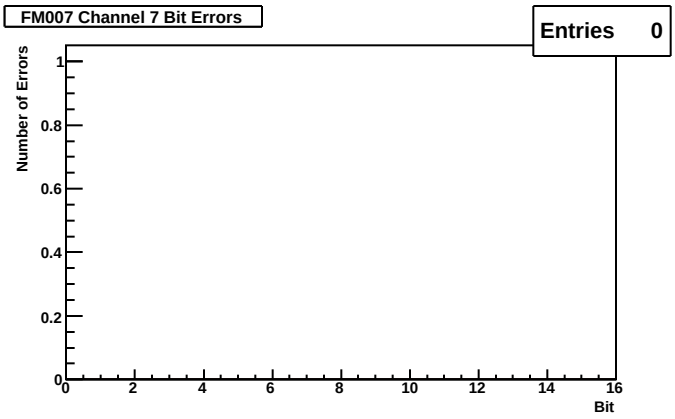

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

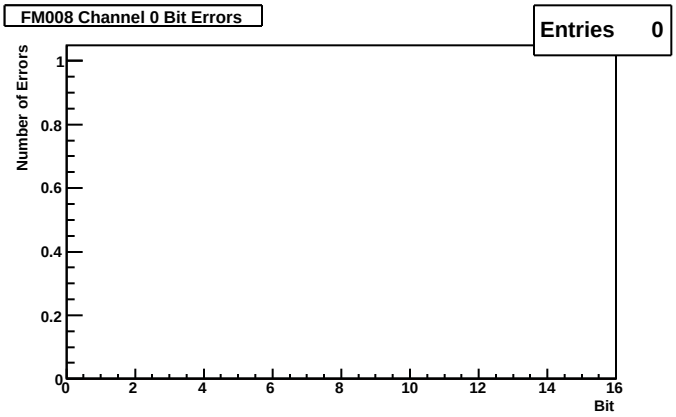

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

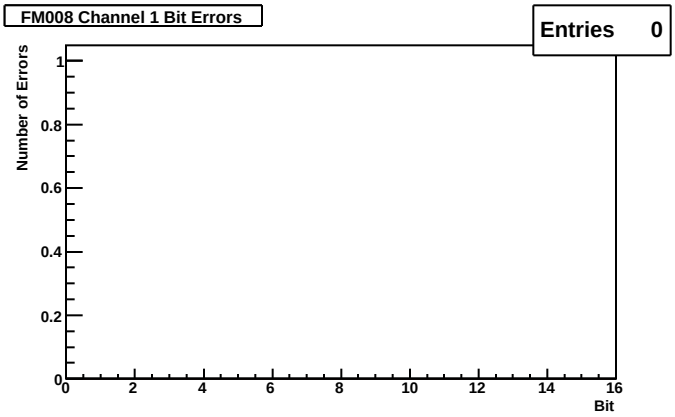

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

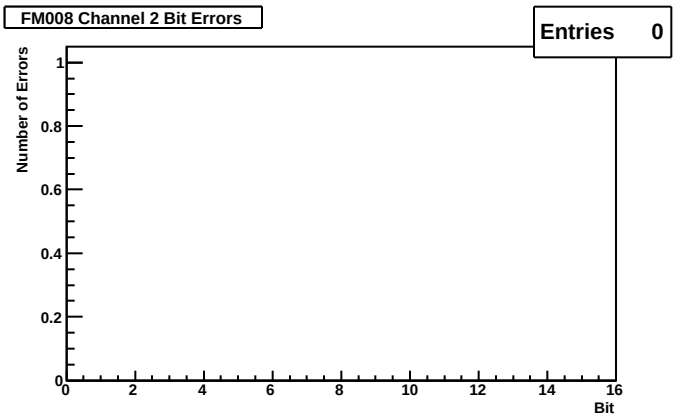

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

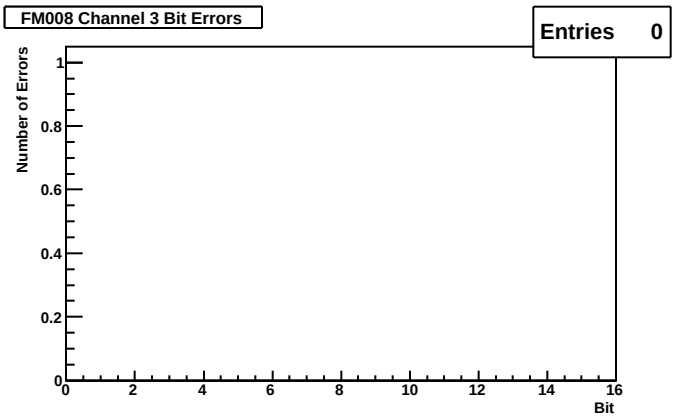

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

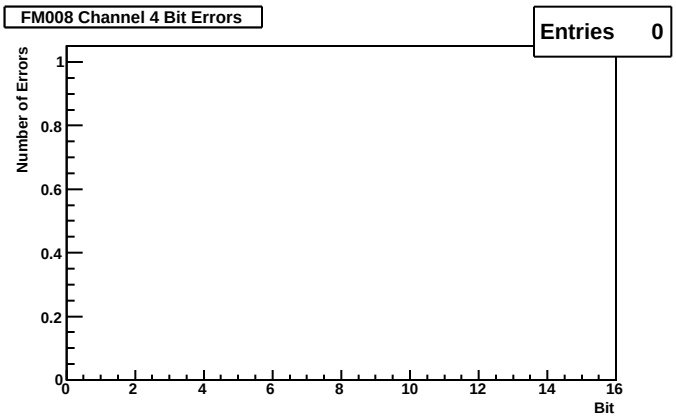

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

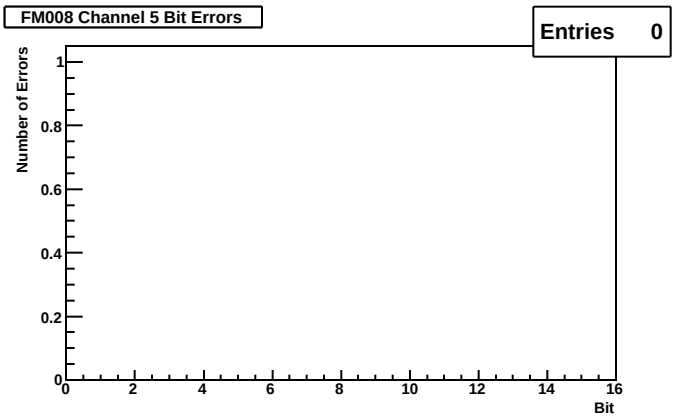

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

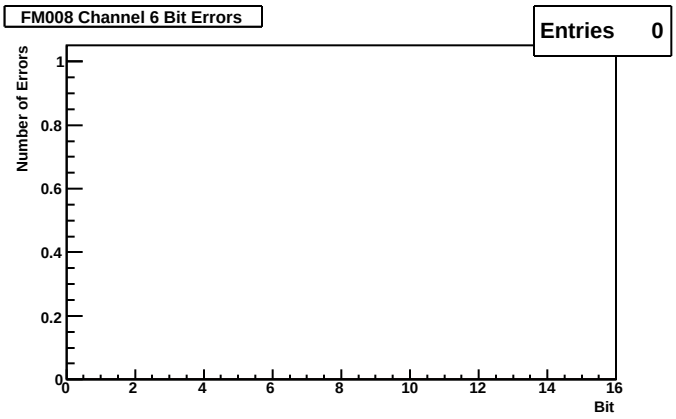

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

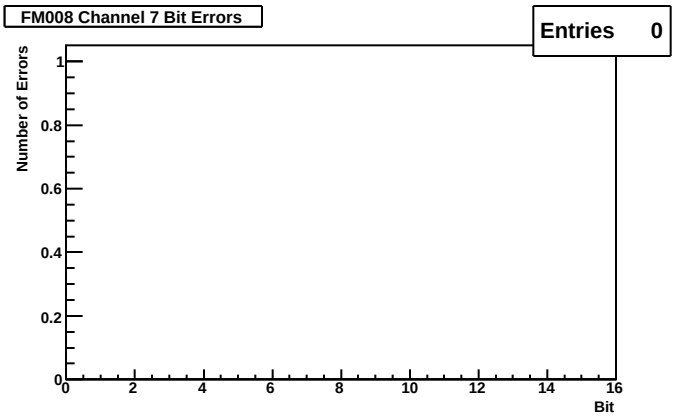

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

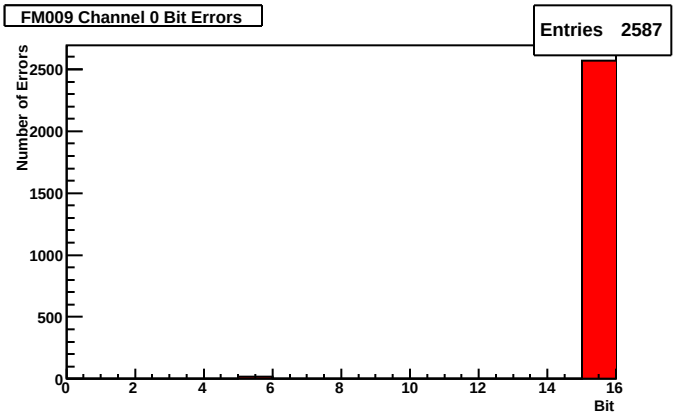

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

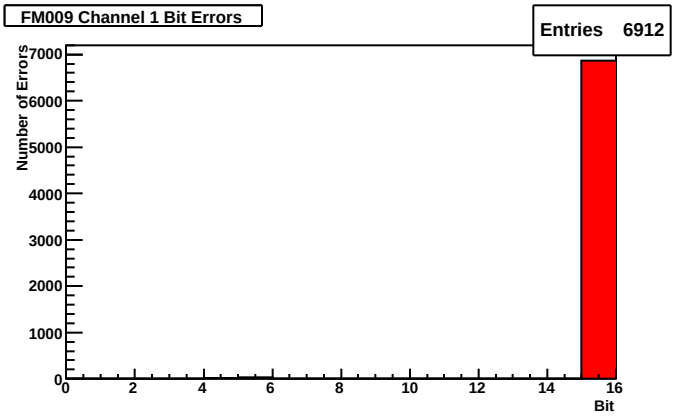

FM008 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

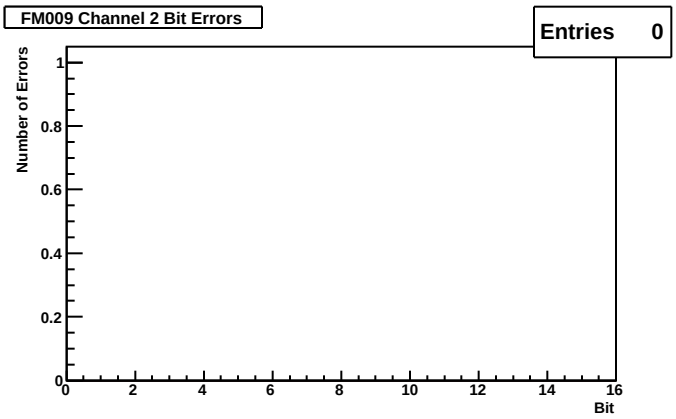

FM008 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

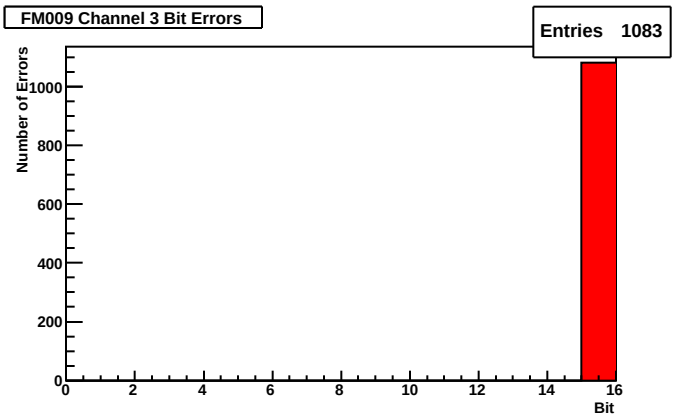

FM008 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

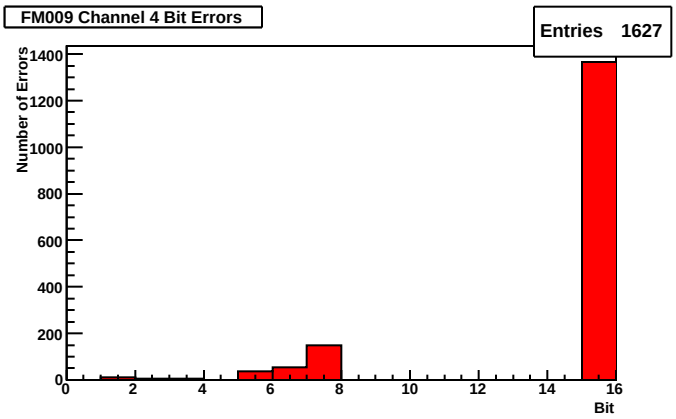

FM008 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

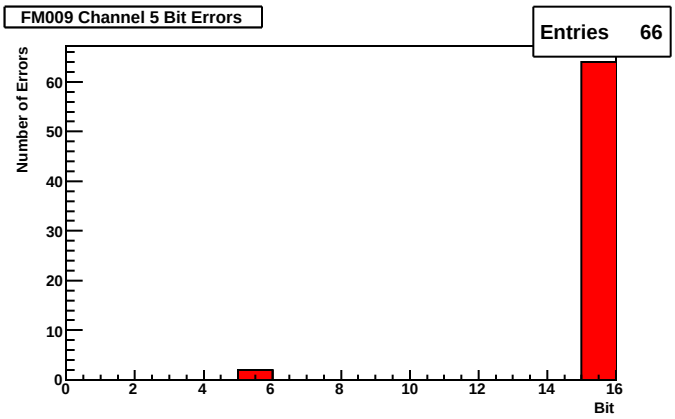

FM009 Channel 0 Bit Errors
Entries 2587
Number of Errors
Bit

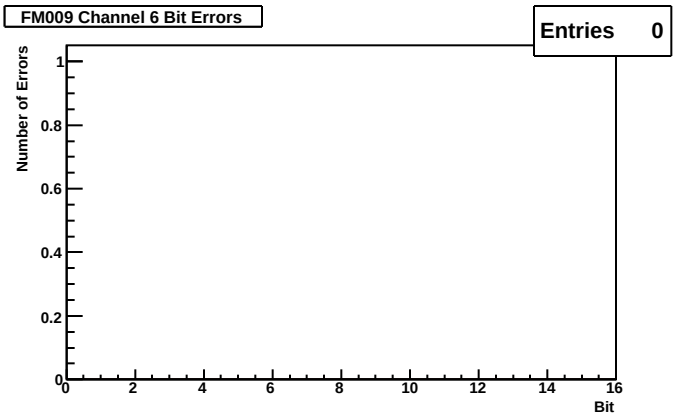

FM009 Channel 1 Bit Errors
Entries 6912
Number of Errors
Bit

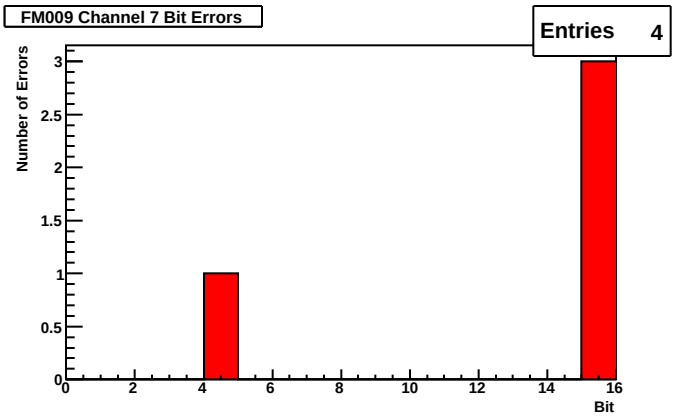

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

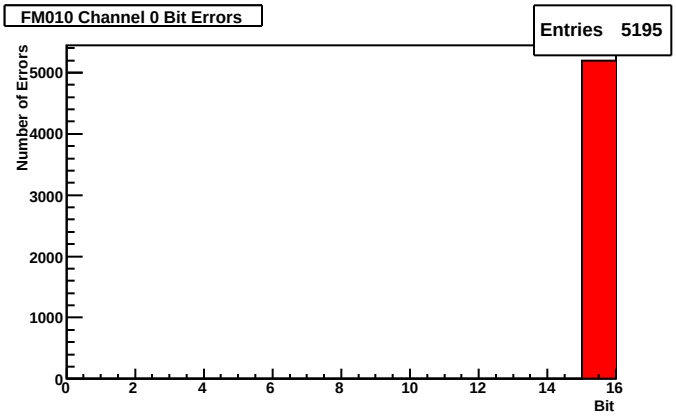

FM009 Channel 3 Bit Errors
Entries 1083
Number of Errors
Bit

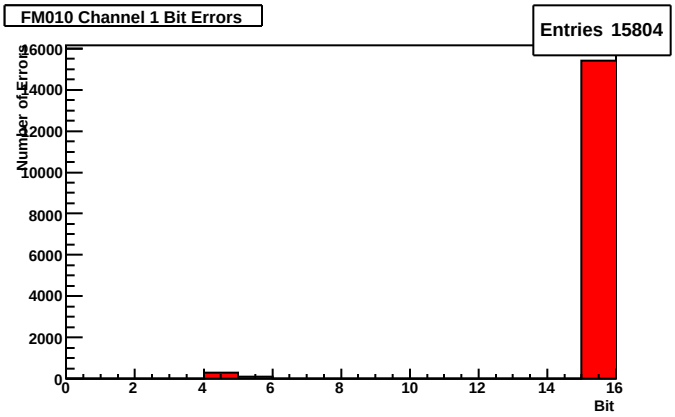

FM009 Channel 4 Bit Errors
Entries 1627
Number of Errors
Bit


FM009 Channel 5 Bit Errors
Entries 66
Number of Errors
Bit

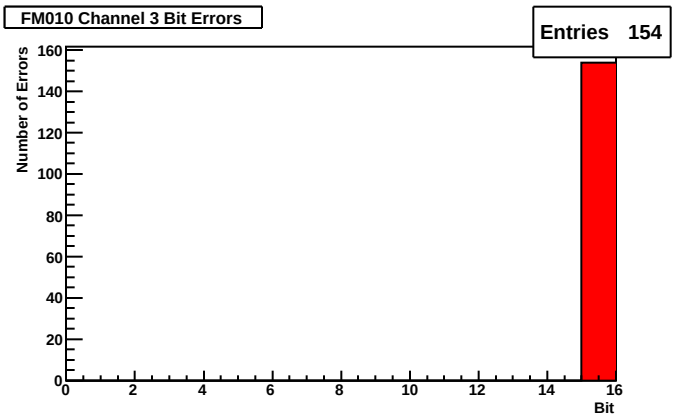

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM009 Channel 7 Bit Errors
Entries 4
Number of Errors
Bit


FM010 Channel 0 Bit Errors
Entries 5195
Number of Errors
Bit

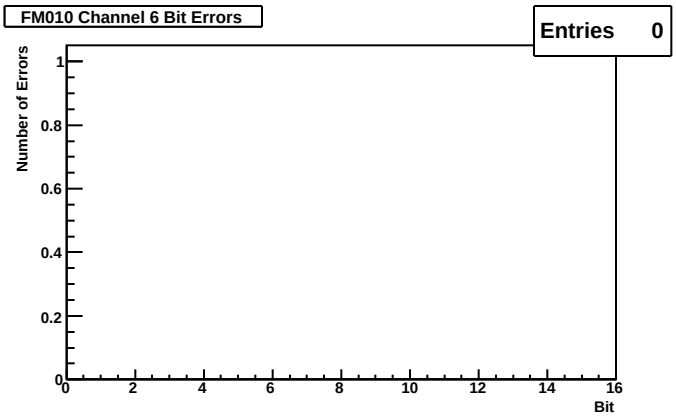

FM010 Channel 1 Bit Errors
Entries 15804
Number of Errors
Bit

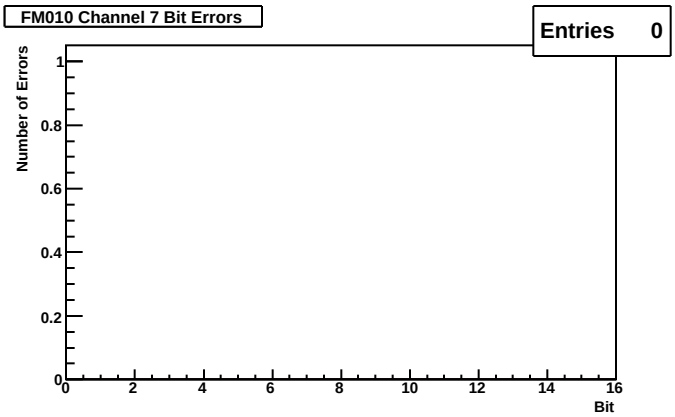
FM010 Channel 2 Bit Errors
Entries 1
Number of Errors
Bit

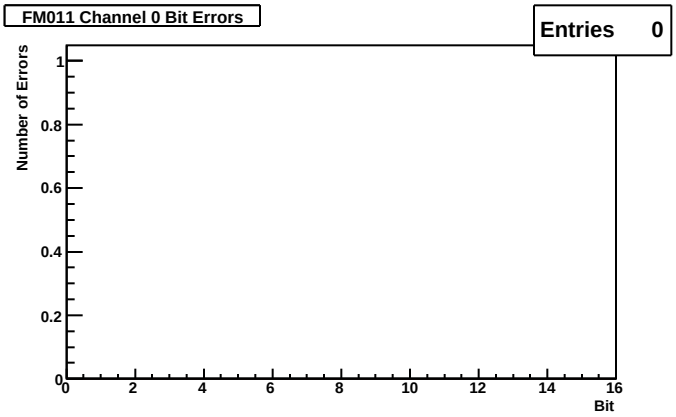

FM010 Channel 3 Bit Errors
Entries 154
Number of Errors
Bit

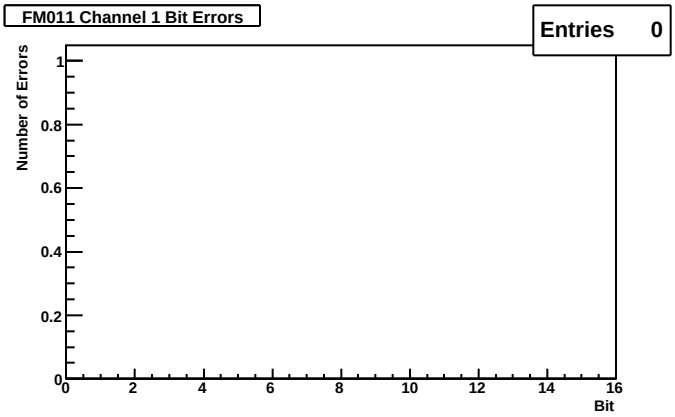
FM010 Channel 4 Bit Errors
Entries2247028
Number of Errors
Bit

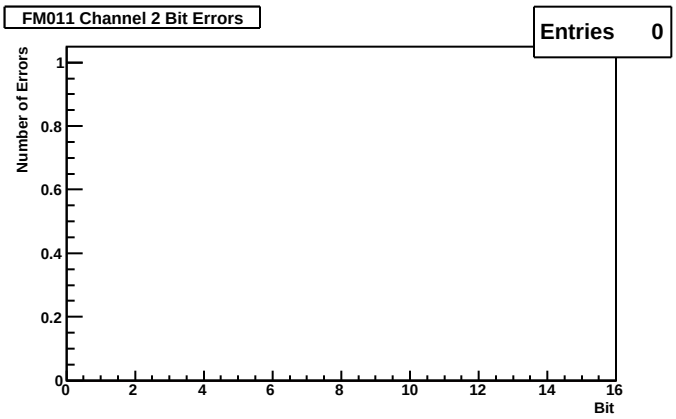
FM010 Channel 5 Bit Errors
Entries1660615
Number of Errors
Bit

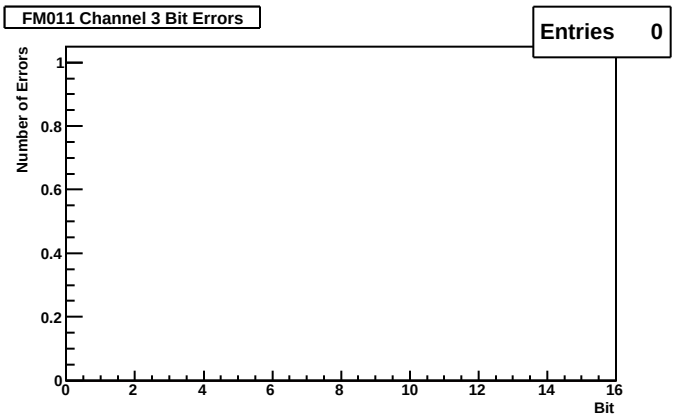

FM010 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

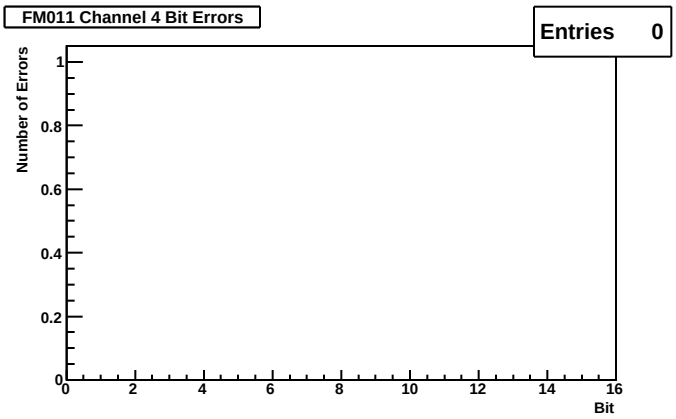

FM010 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

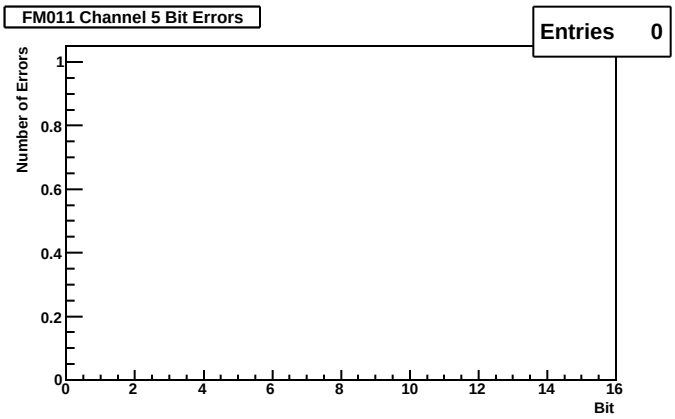

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

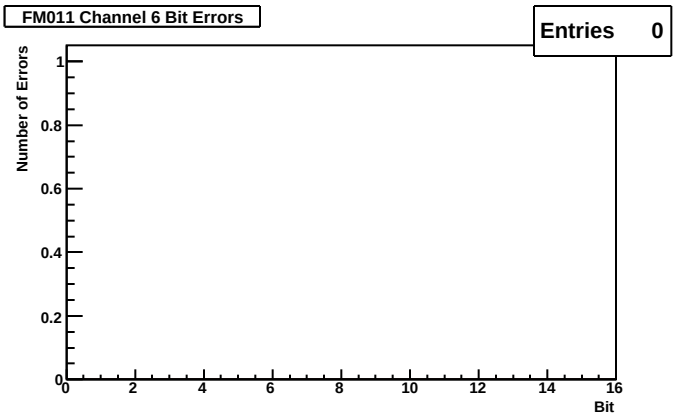

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

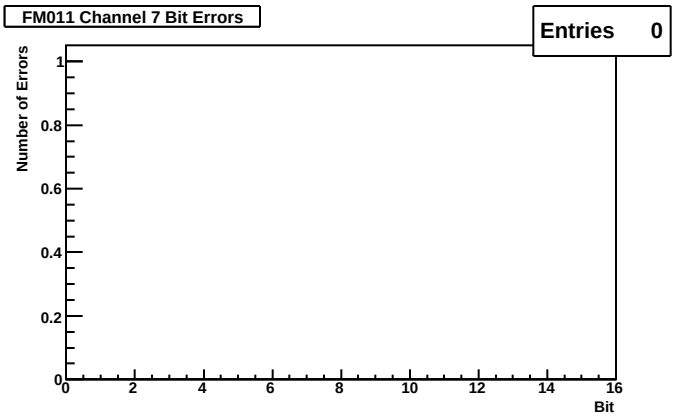

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

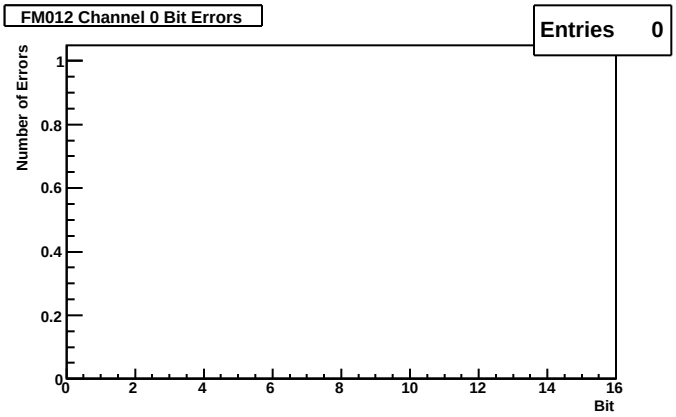

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

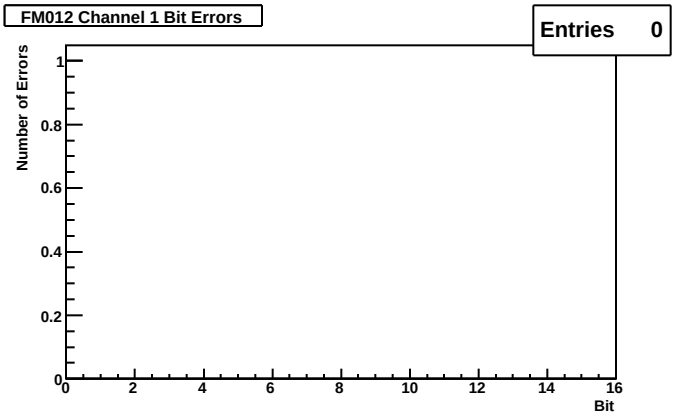

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

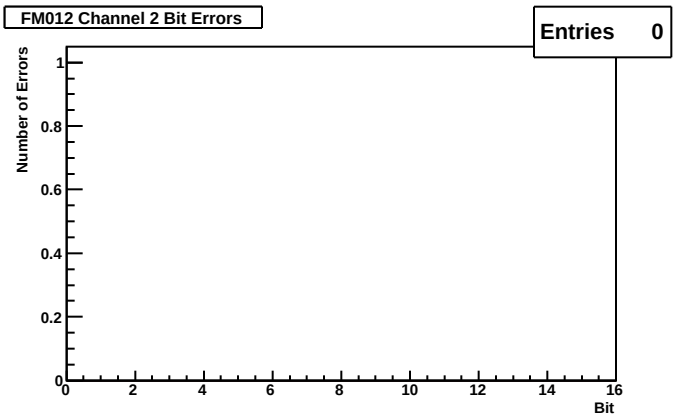

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

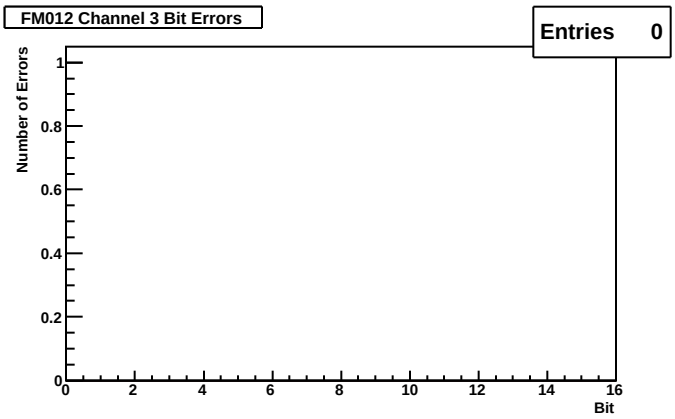

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

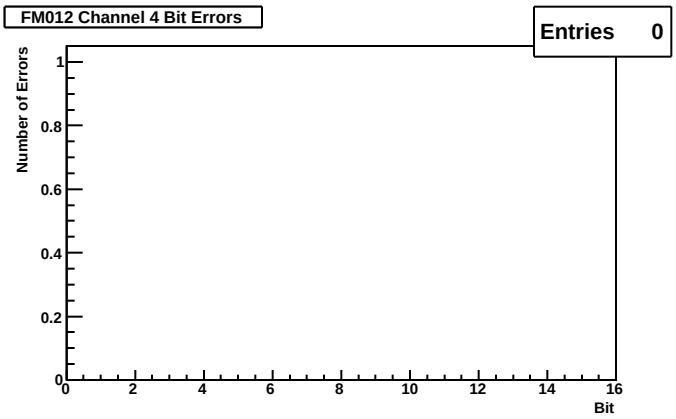

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

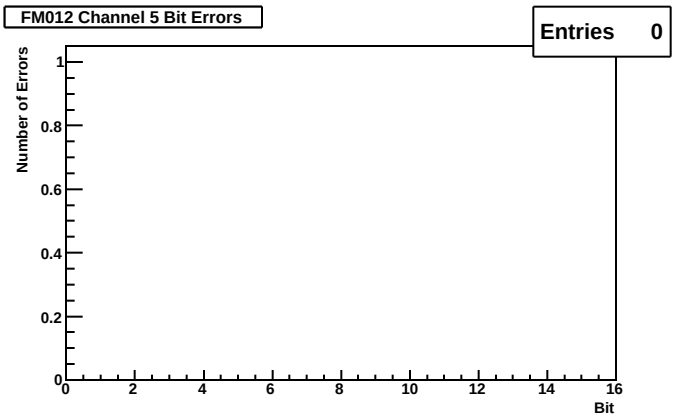

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

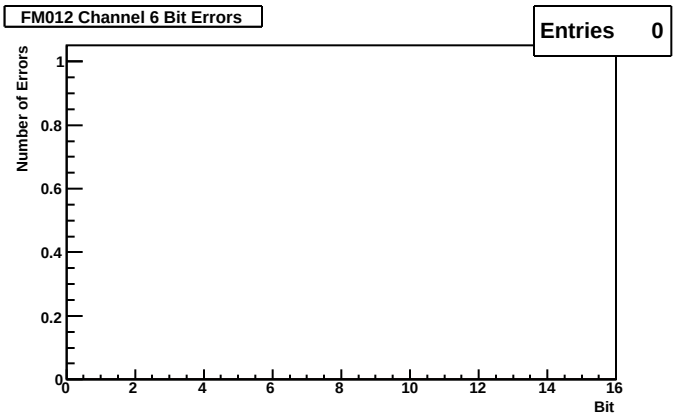

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

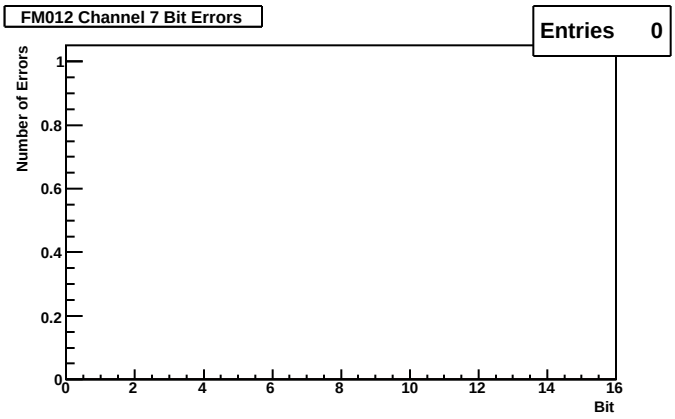

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit