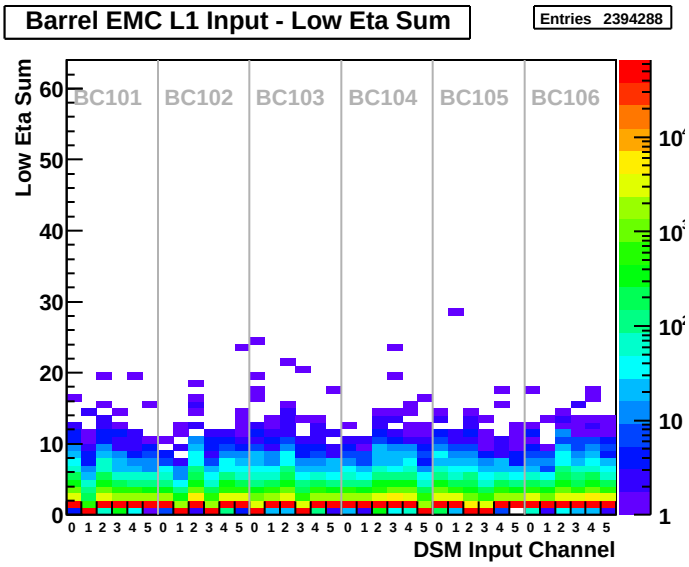
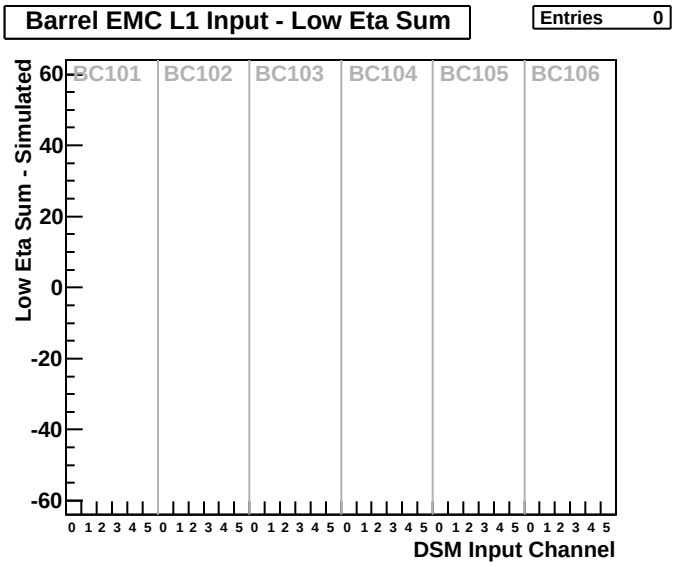
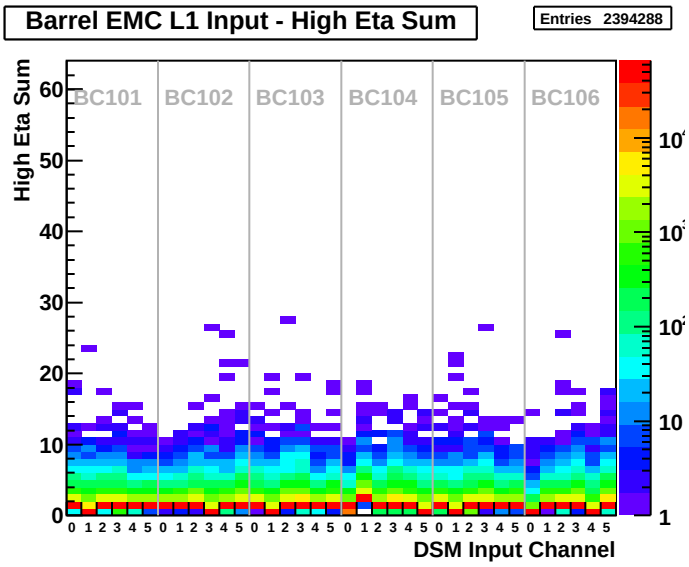
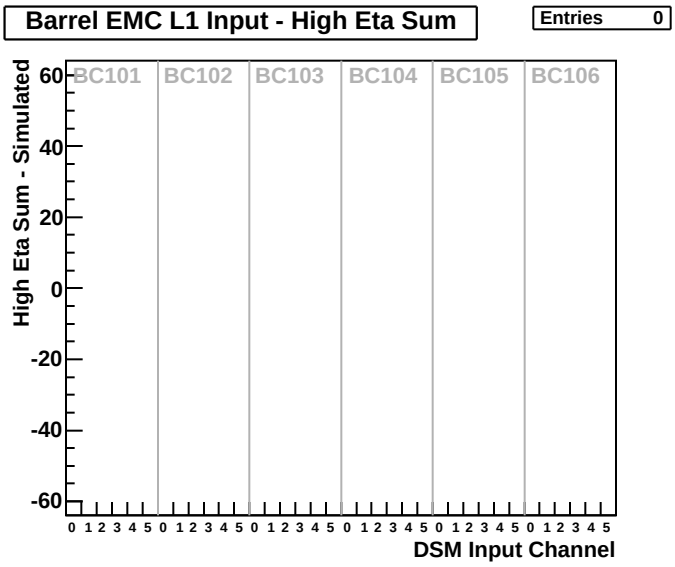
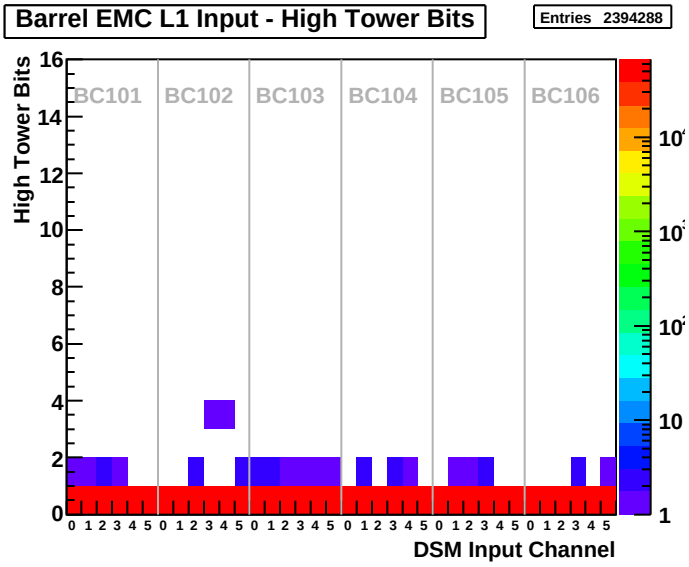
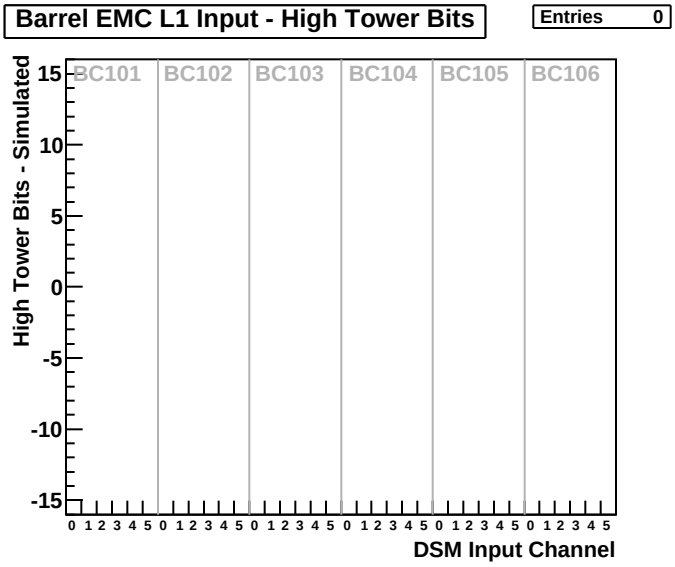

Barrel EMC L1 Input - Low Eta Sum
Entries 2394288
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

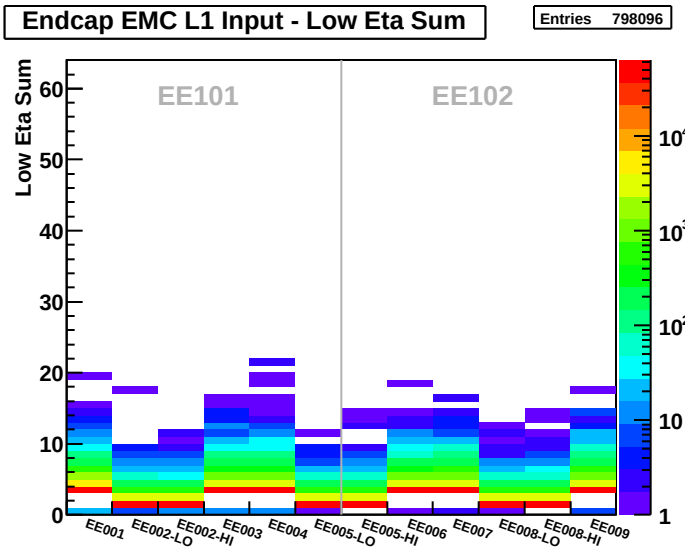

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

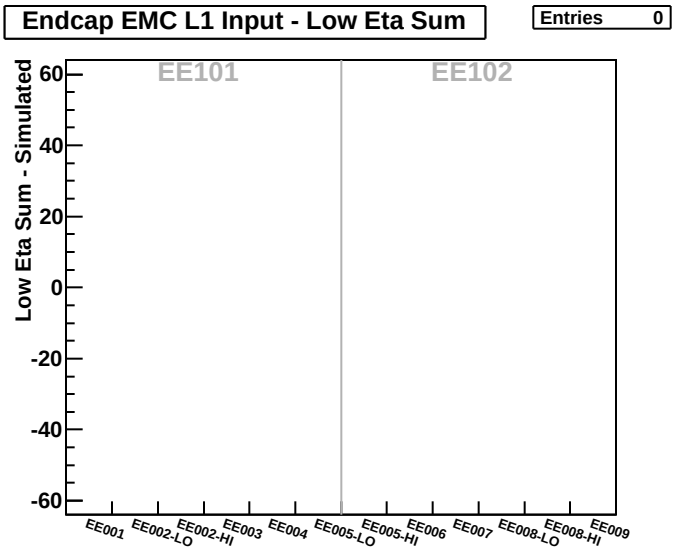

Barrel EMC L1 Input - High Eta Sum
Entries 2394288
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

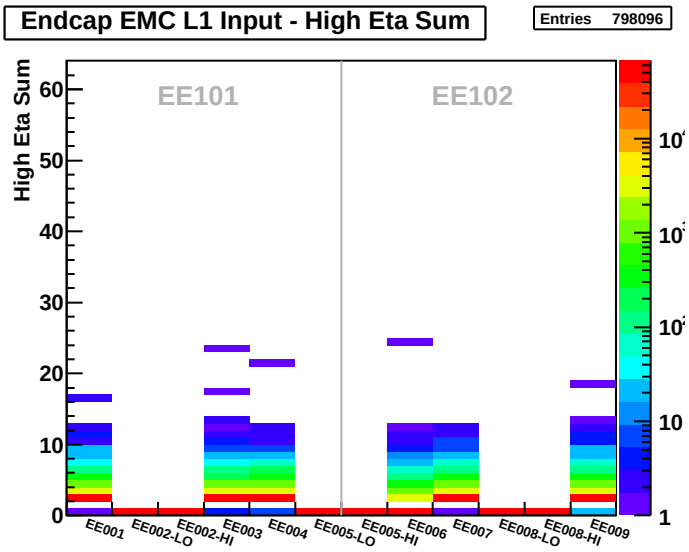

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

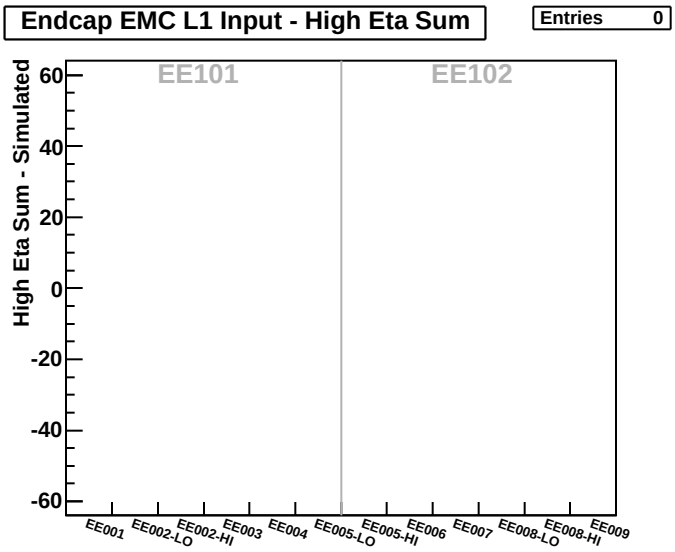

Barrel EMC L1 Input - High Tower Bits
Entries 2394288
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

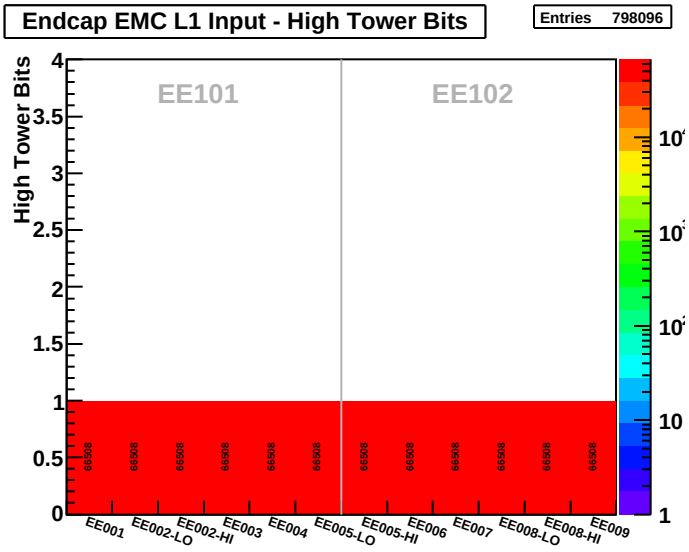

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

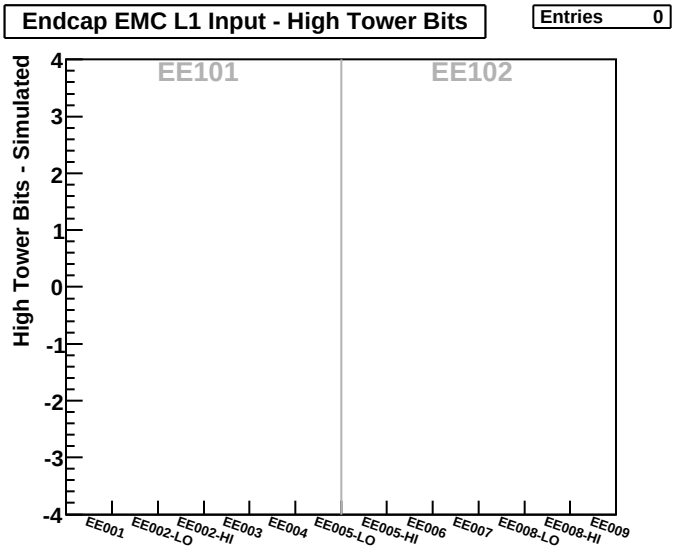

Endcap EMC L1 Input - Low Eta Sum
Entries 798096
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

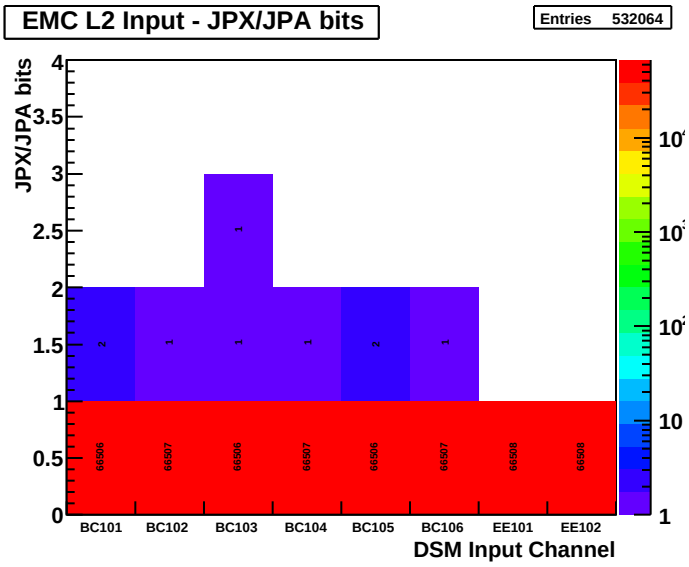

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

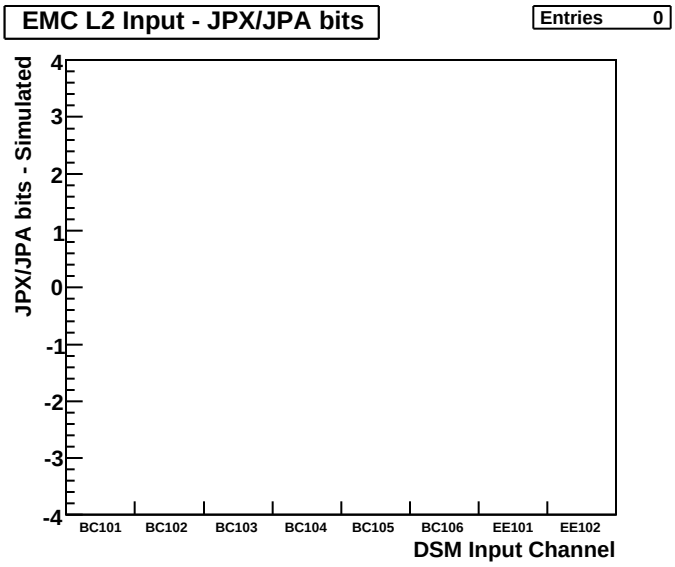

Endcap EMC L1 Input - High Eta Sum
Entries 798096
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

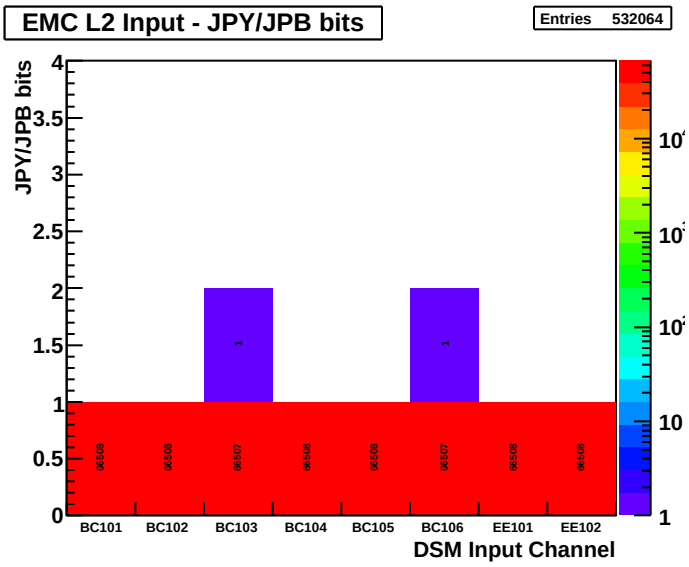

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

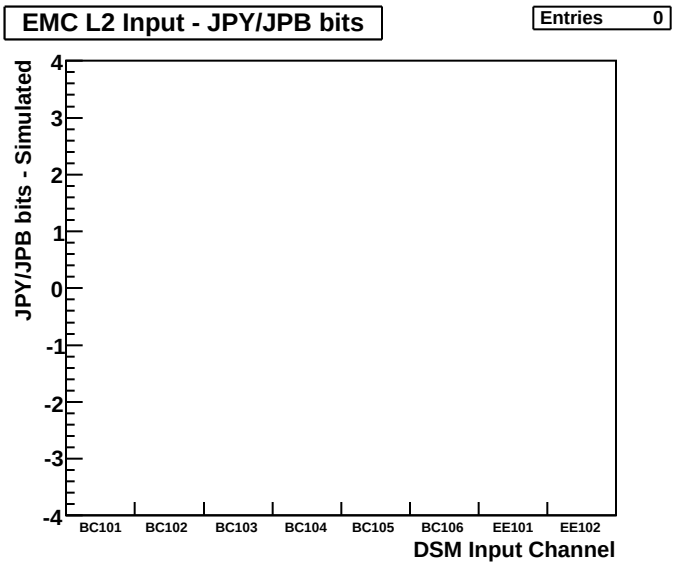

Endcap EMC L1 Input - High Tower Bits
Entries 798096
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

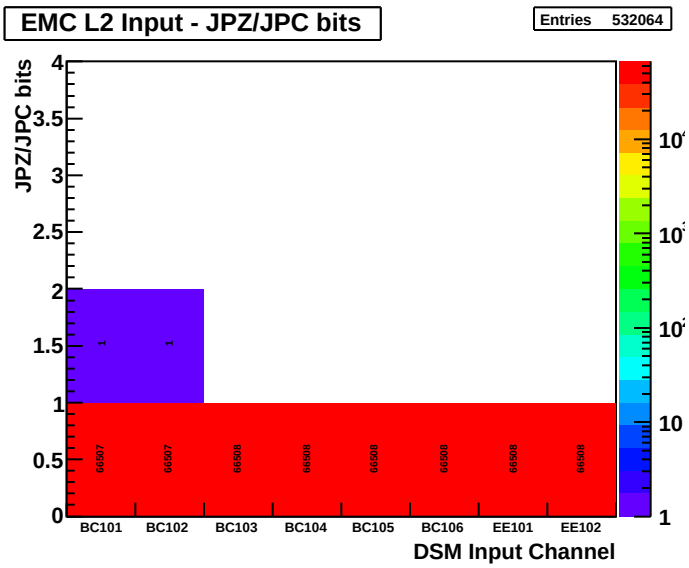

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

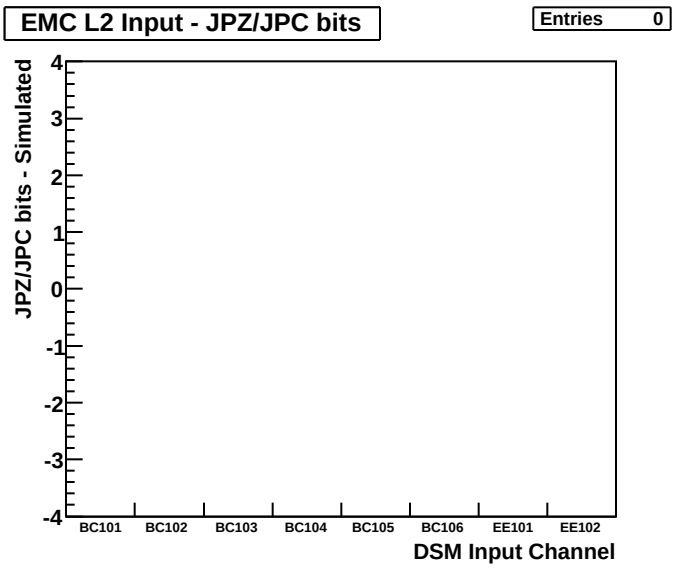

EMC L2 Input - JPX/JPA bits
Entries 532064
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

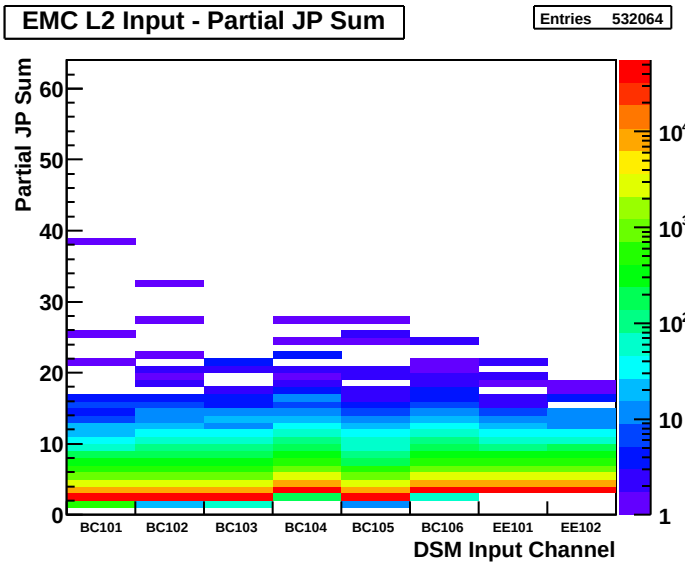

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

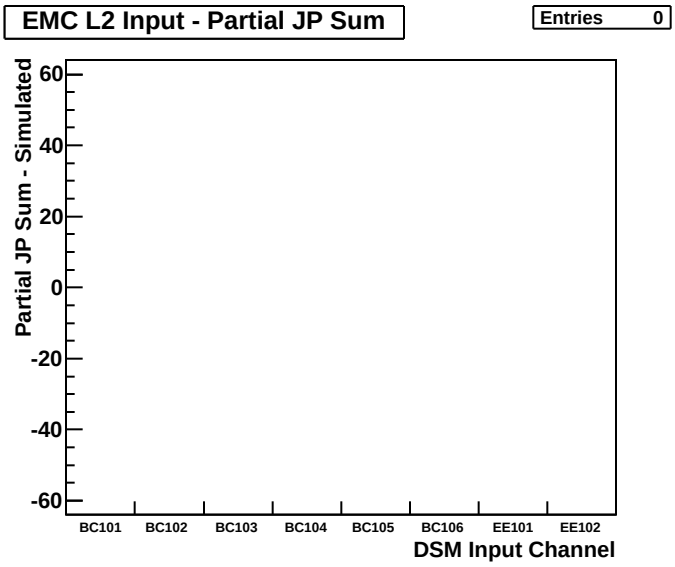

EMC L2 Input - JPY/JPB bits
Entries 532064
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

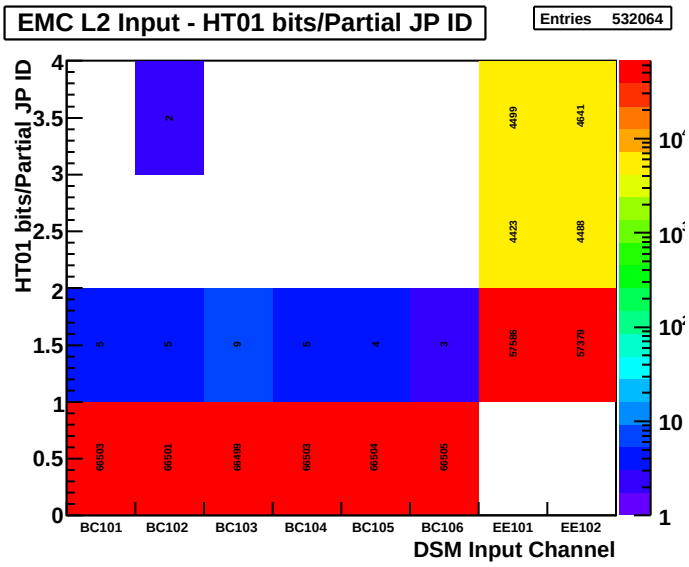

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

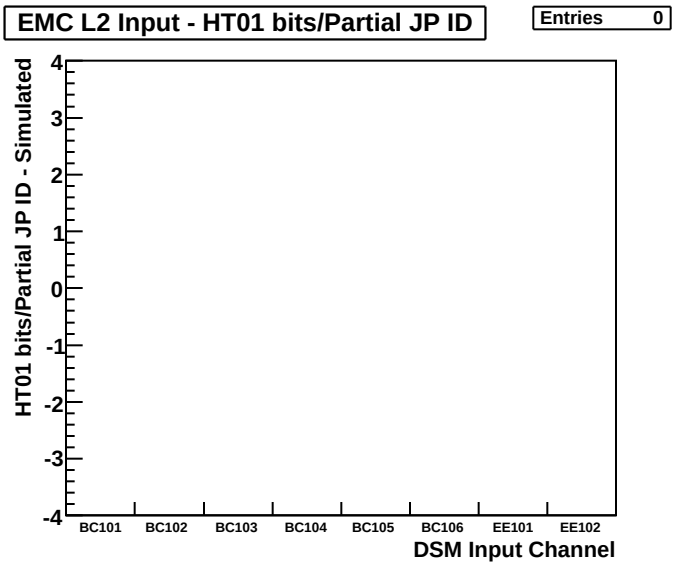

EMC L2 Input - JPZ/JPC bits
Entries 532064
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

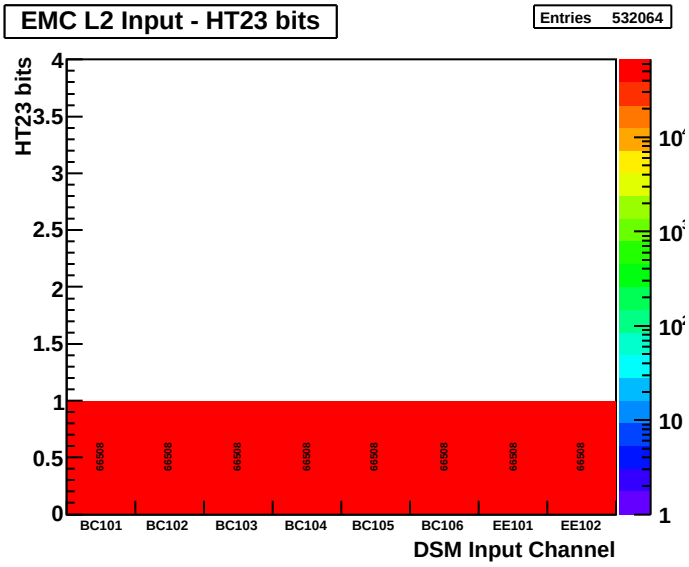

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

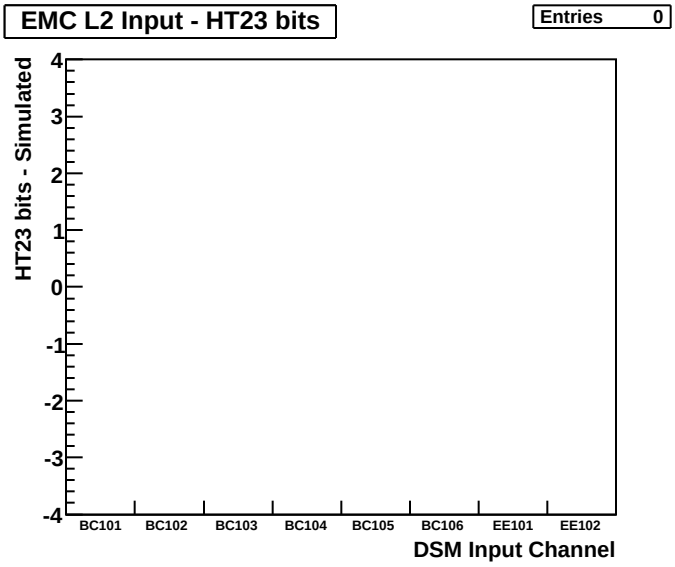

EMC L2 Input - Partial JP Sum
Entries 532064
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

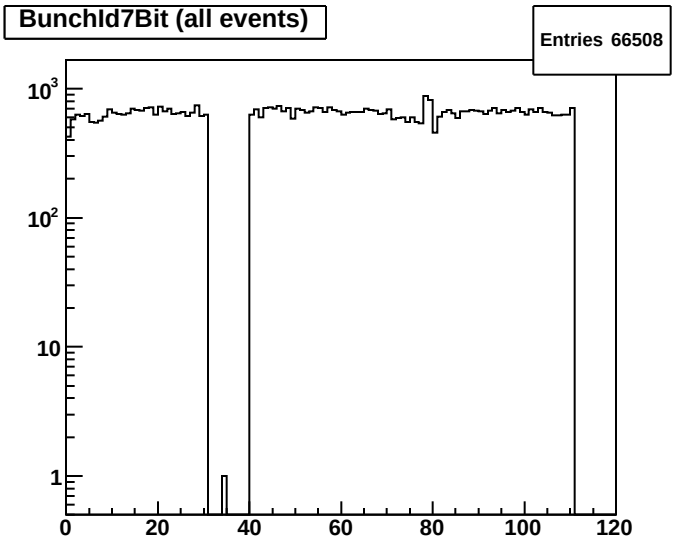

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

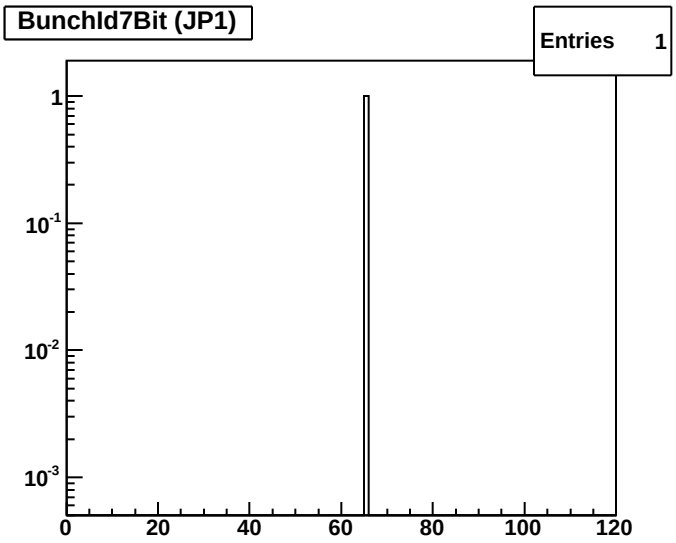

EMC L2 Input - HT01 bits/Partial JP ID
Entries 532064
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

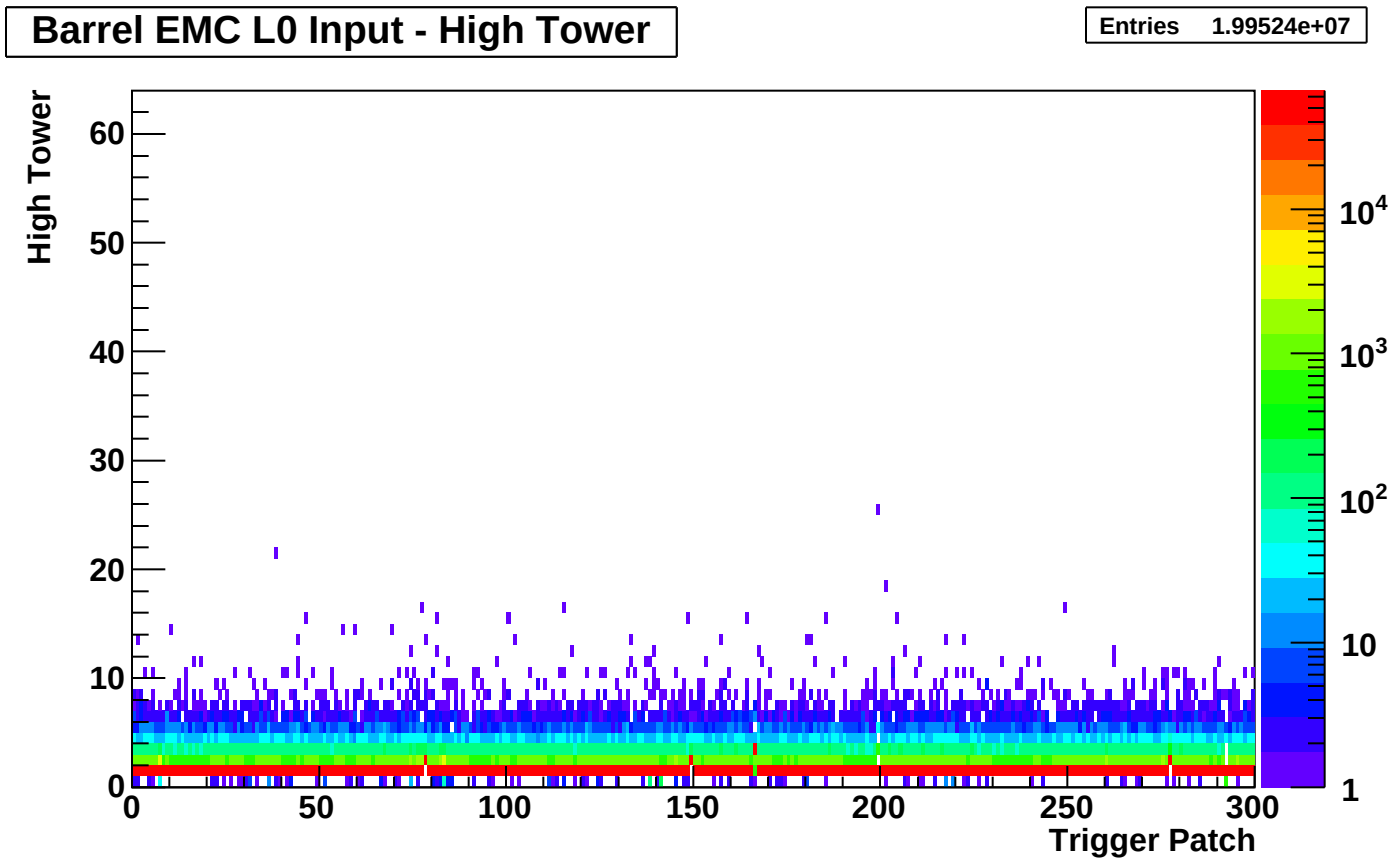

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

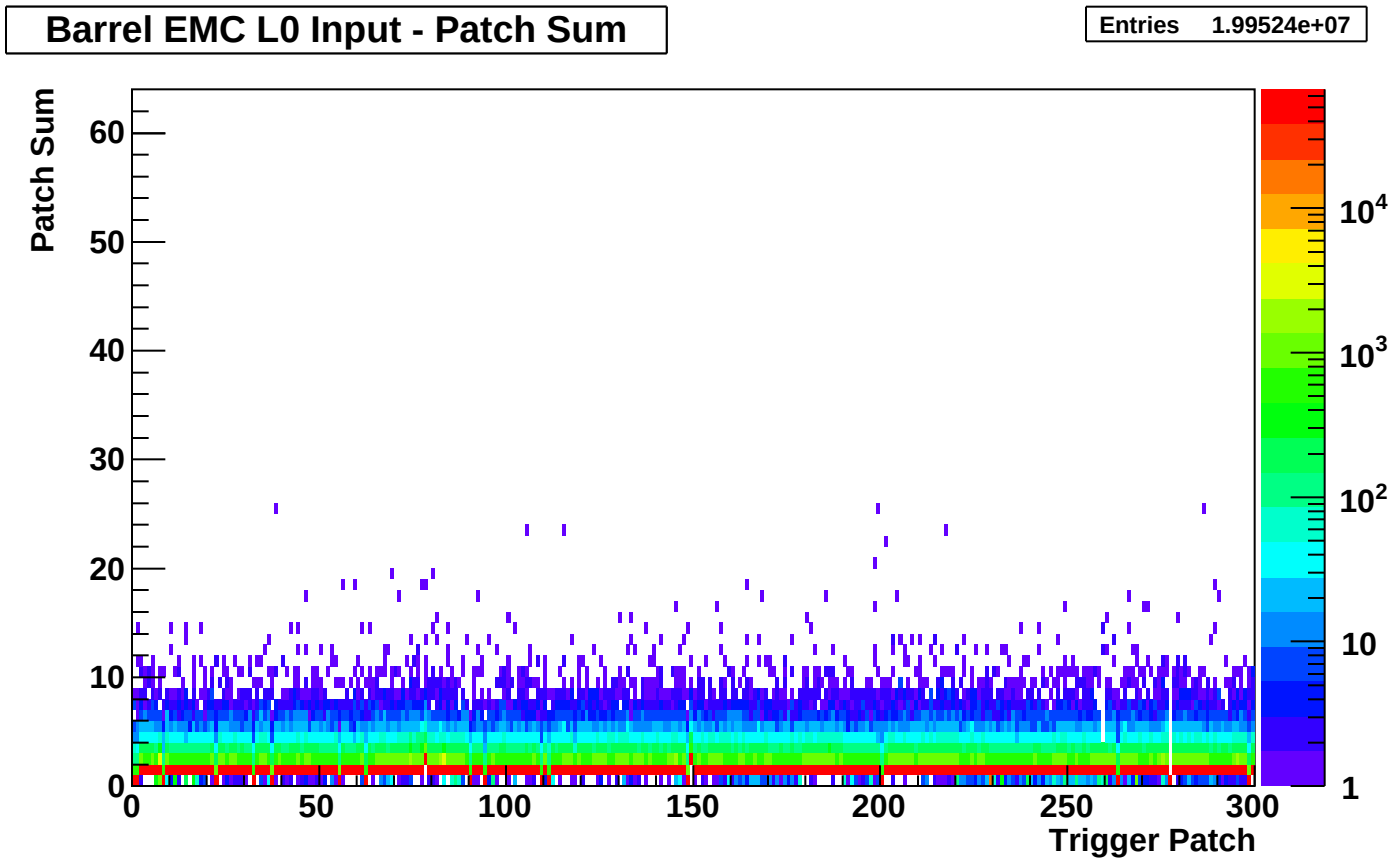

EMC L2 Input - HT23 bits
Entries 532064
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

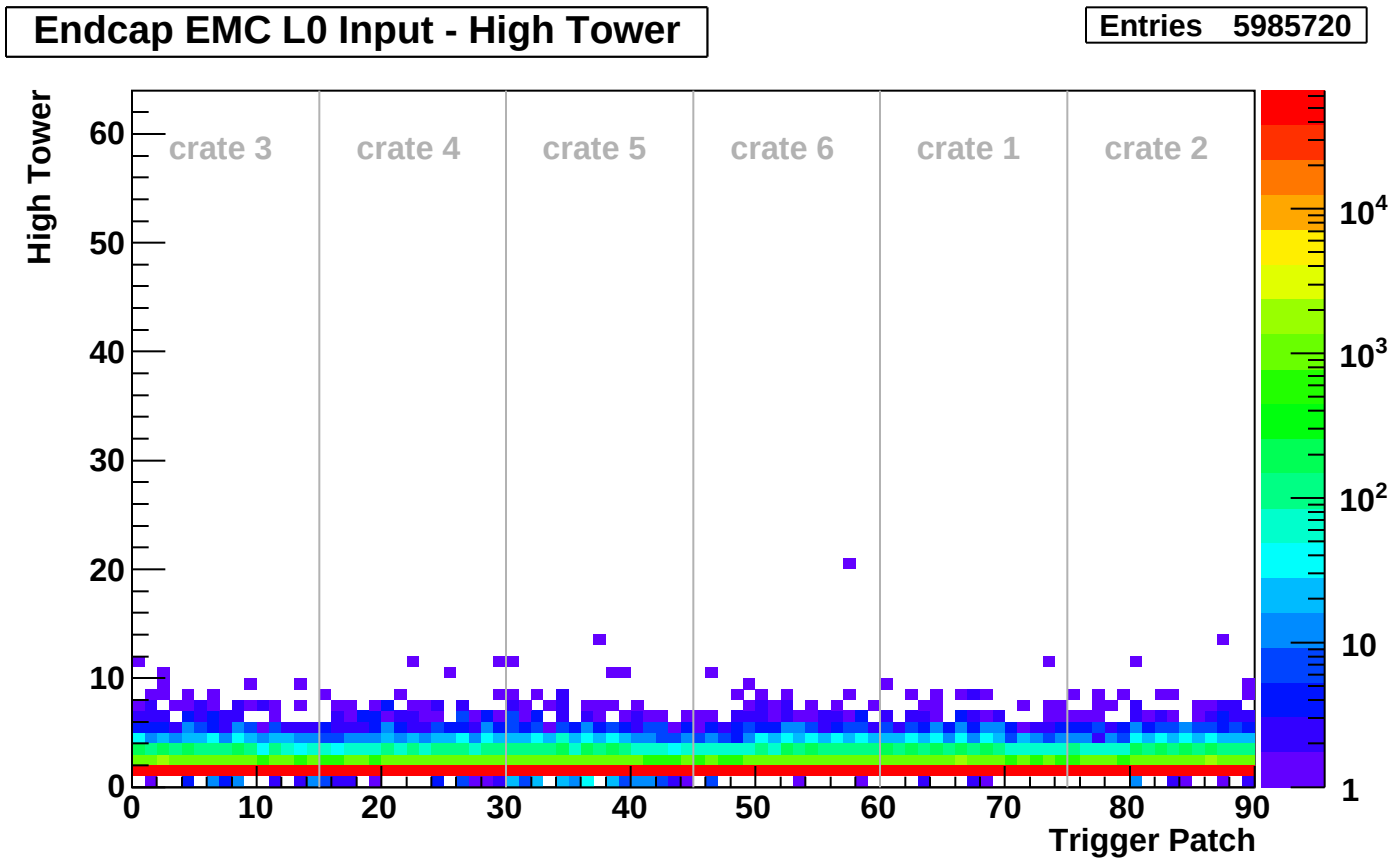

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

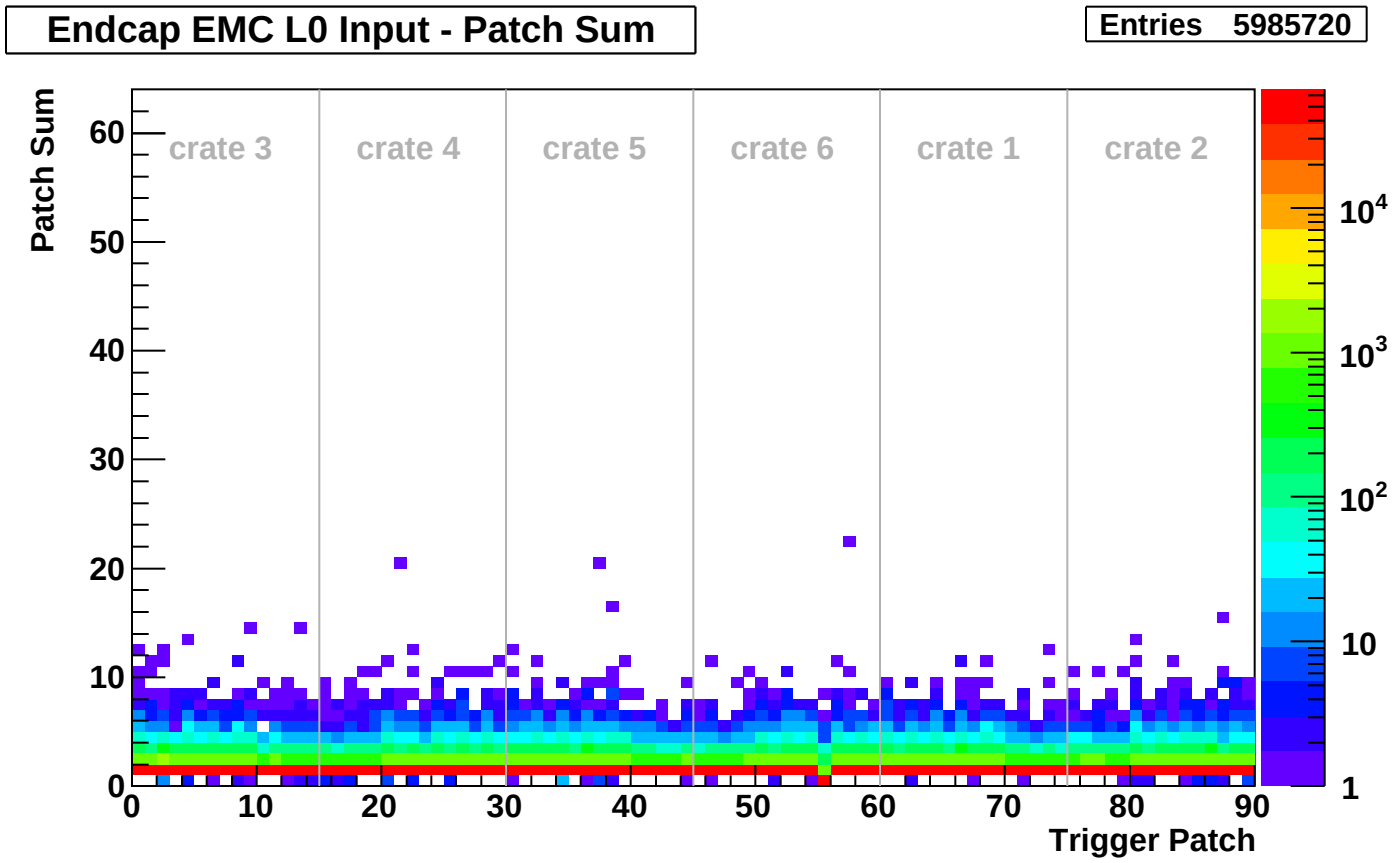

BunchId7Bit (all events)
Entries 66508
10^3
10^2
10
1
0
20
40
60
80
100
120

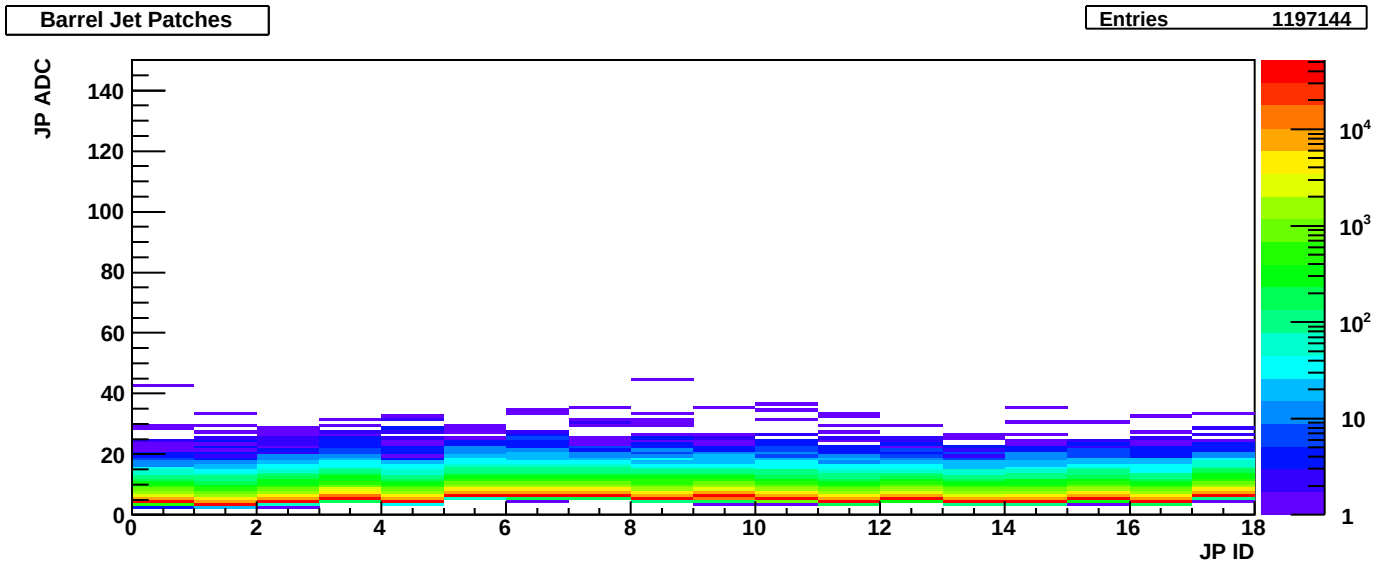

BunchId7Bit (JP1)
Entries 1
1
10^{-1}
10^{-2}
10^{-3}
0
20
40
60
80
100
120

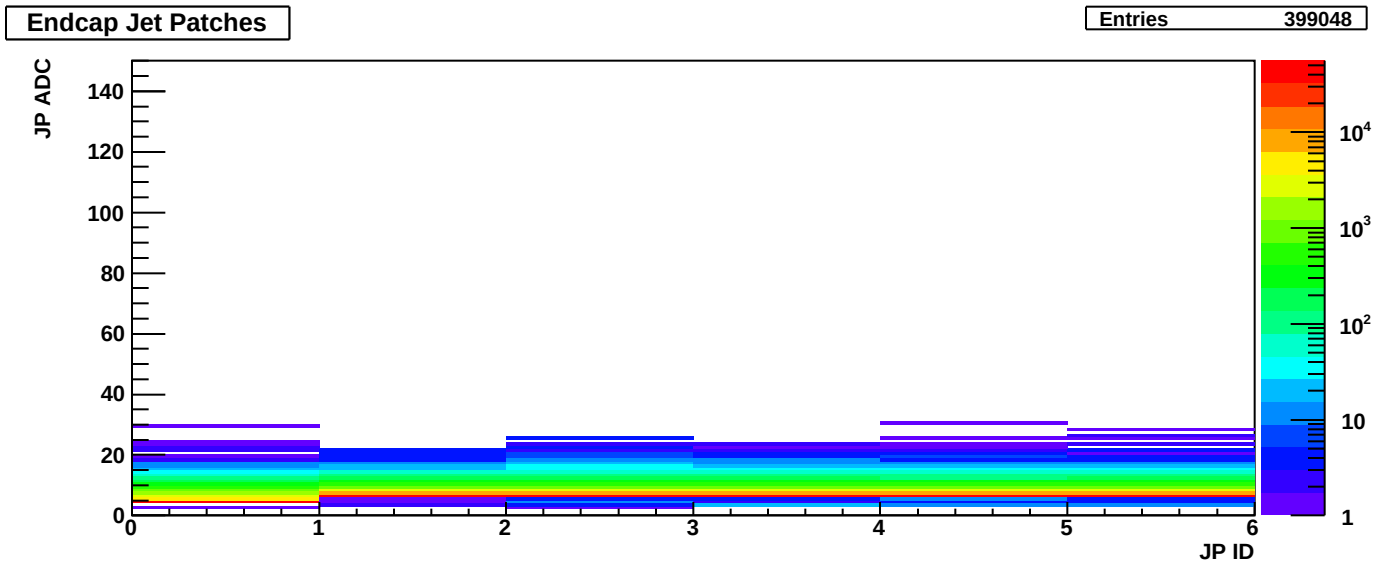

Barrel EMC L0 Input - High Tower
Entries 1.99524e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

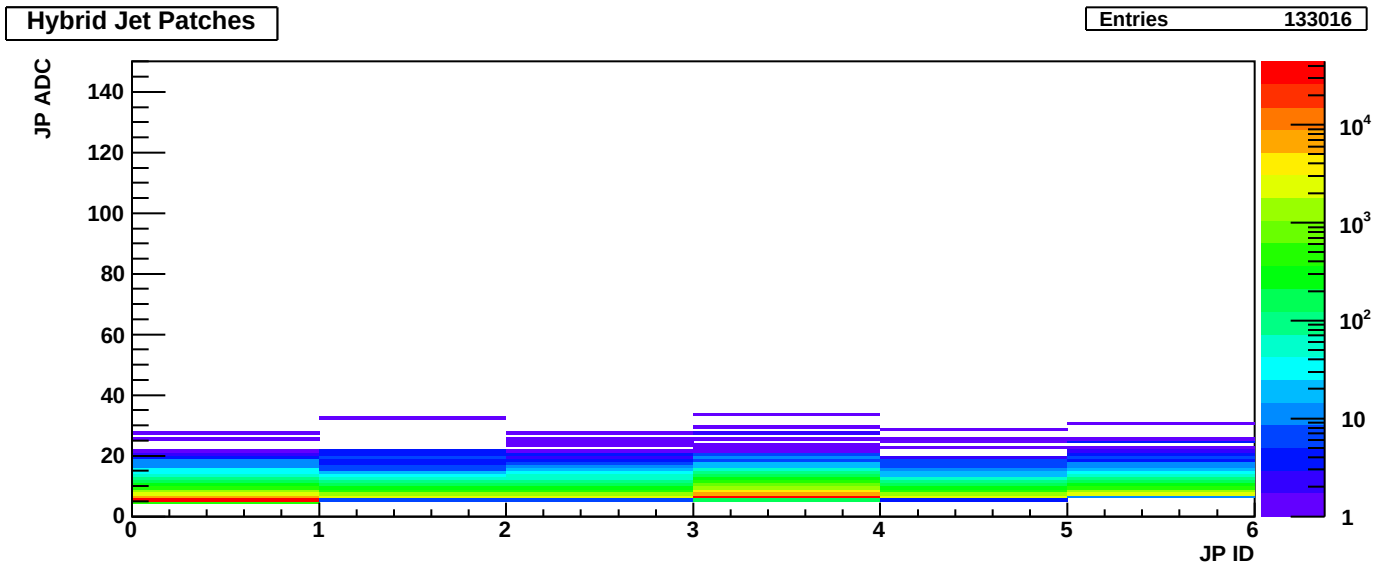

Barrel EMC L0 Input - Patch Sum
Entries 1.99524e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

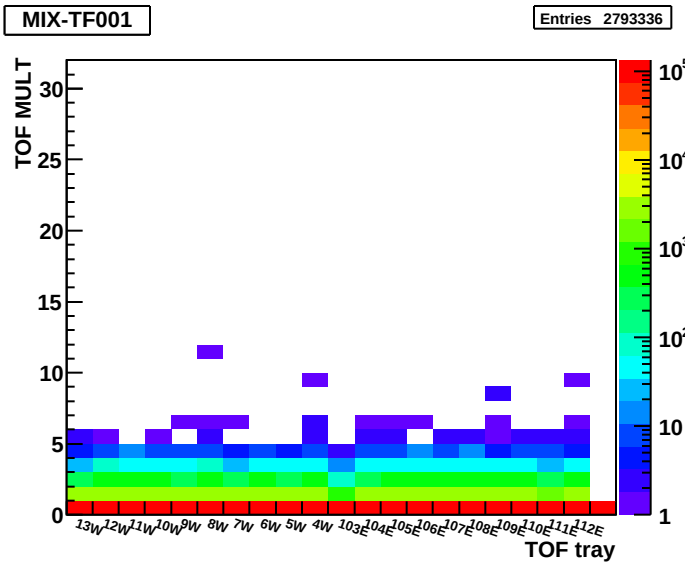

Endcap EMC L0 Input - High Tower
Entries 5985720
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

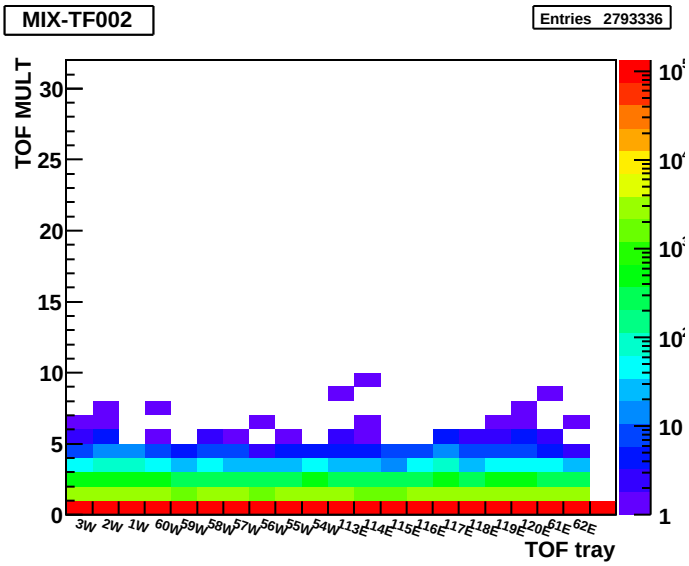

Endcap EMC L0 Input - Patch Sum
Entries 5985720
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

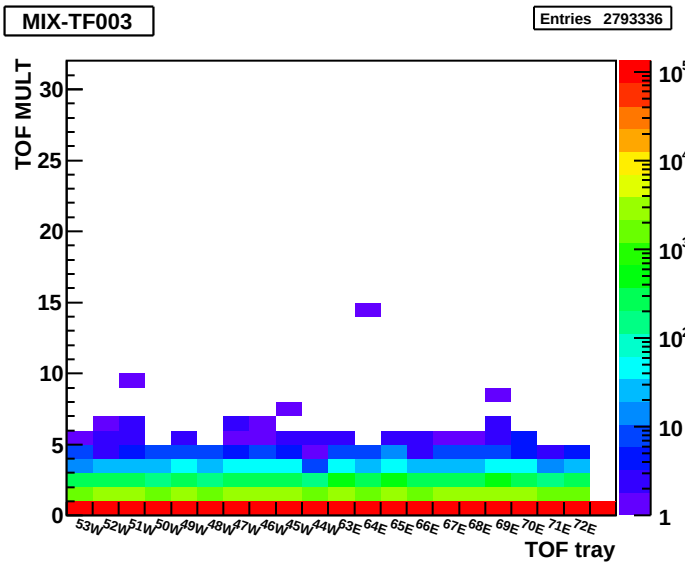

Barrel Jet Patches
Entries 1197144
JP ADC
JP ID

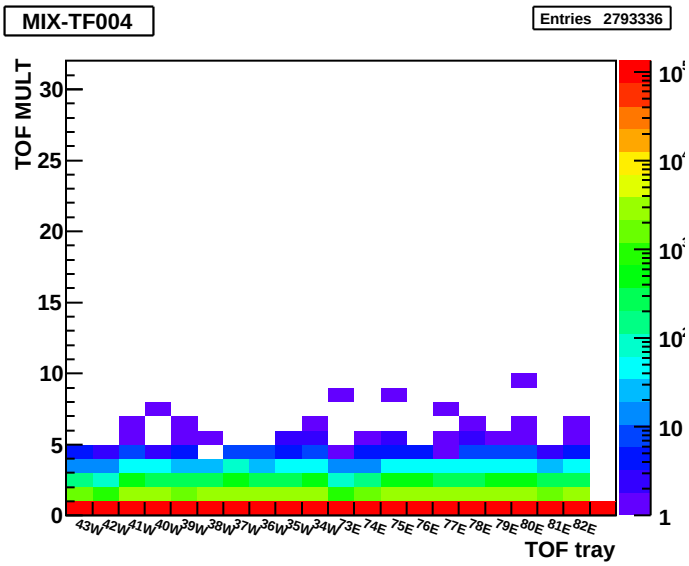

Endcap Jet Patches
Entries 399048
JP ADC
JP ID

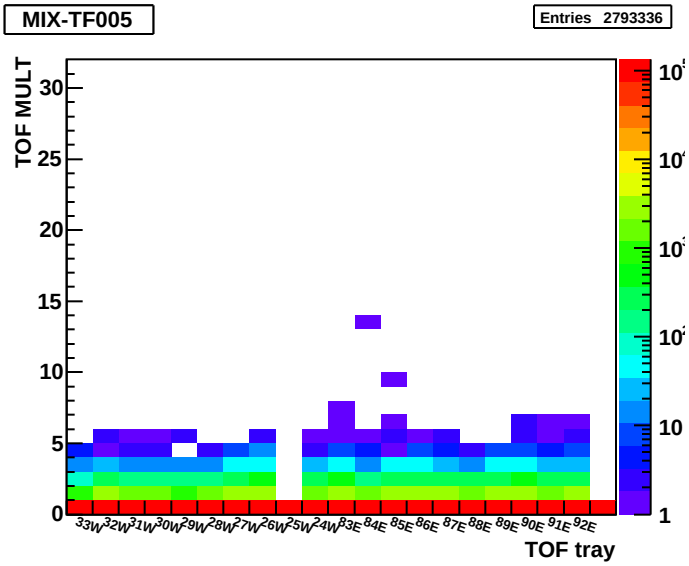

Hybrid Jet Patches
Entries 133016
JP ADC
JP ID

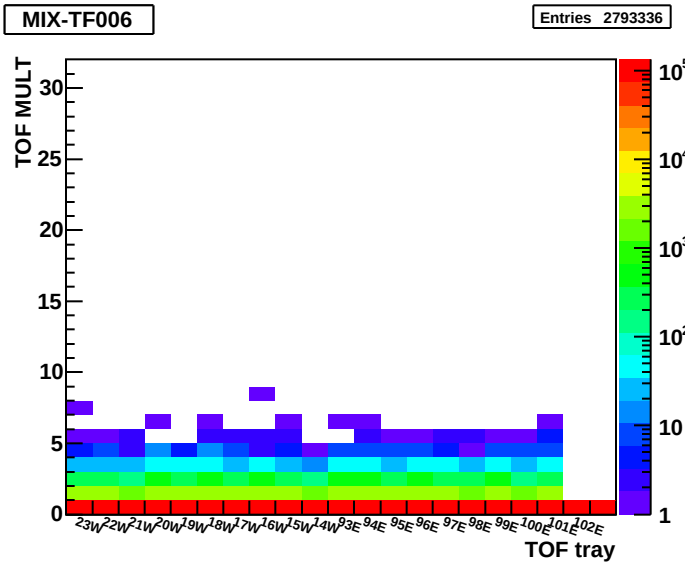

MIX-TF001
Entries 2793336
TOF MULT
TOF tray

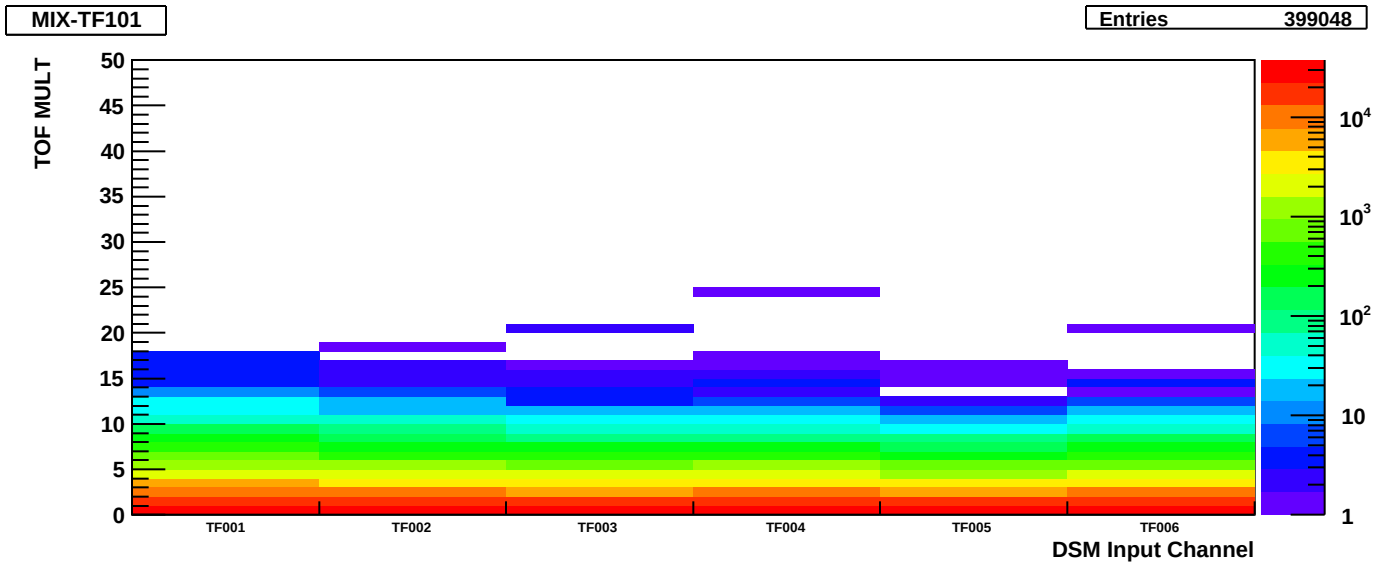

MIX-TF002
Entries 2793336
TOF MULT
TOF tray

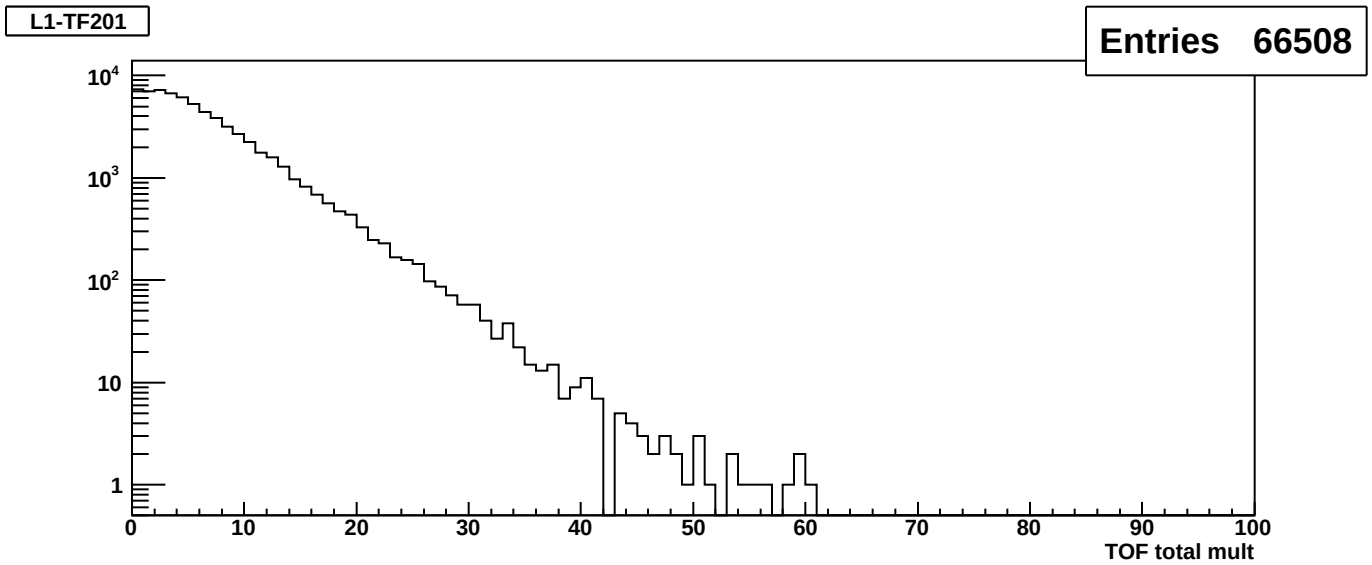

MIX-TF003
Entries 2793336
TOF MULT
TOF tray

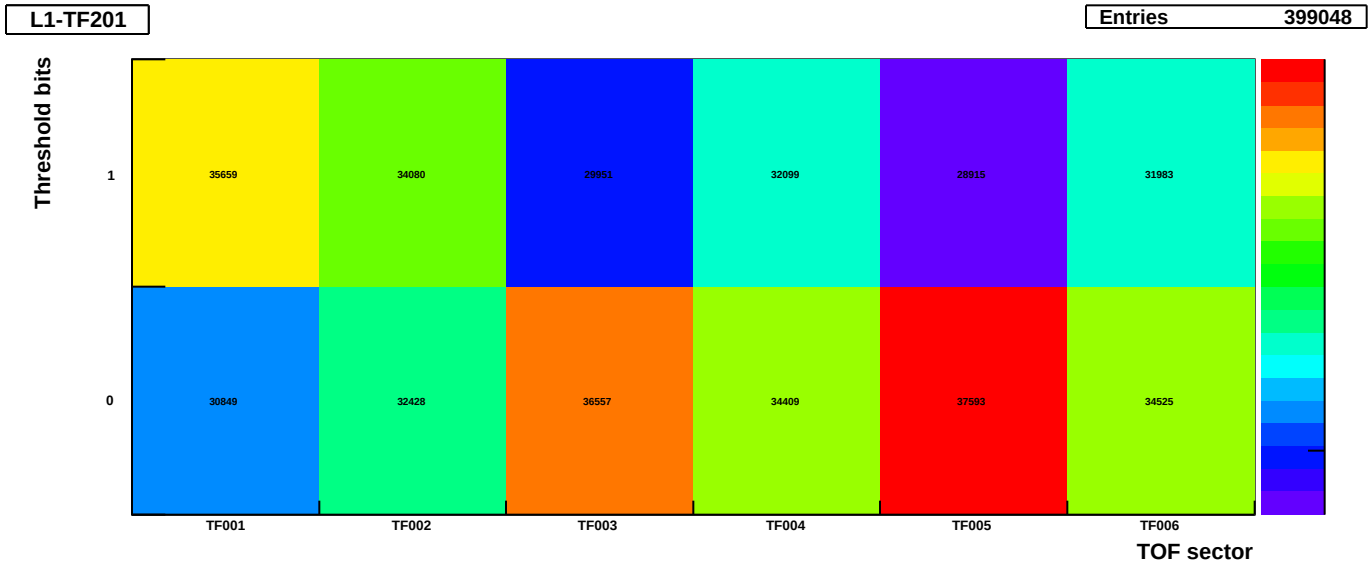

MIX-TF004
Entries 2793336
TOF MULT
TOF tray

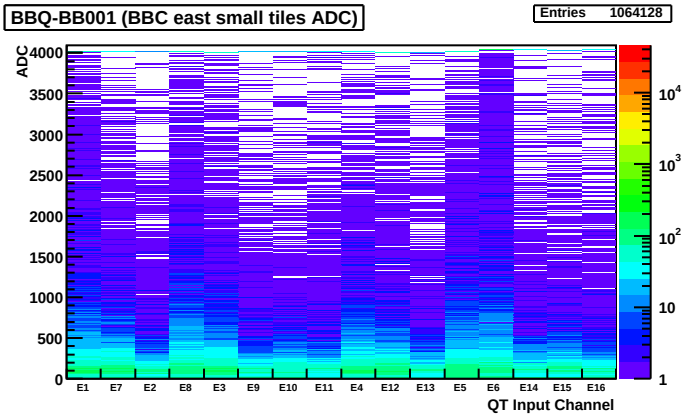

MIX-TF005
Entries 2793336
TOF MULT
TOF tray

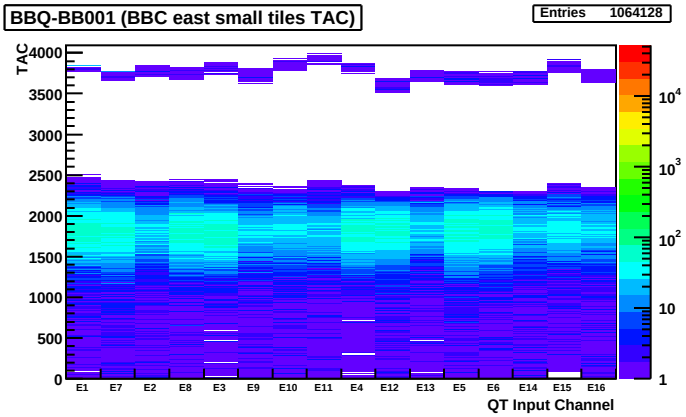

MIX-TF006
Entries 2793336
TOF MULT
TOF tray

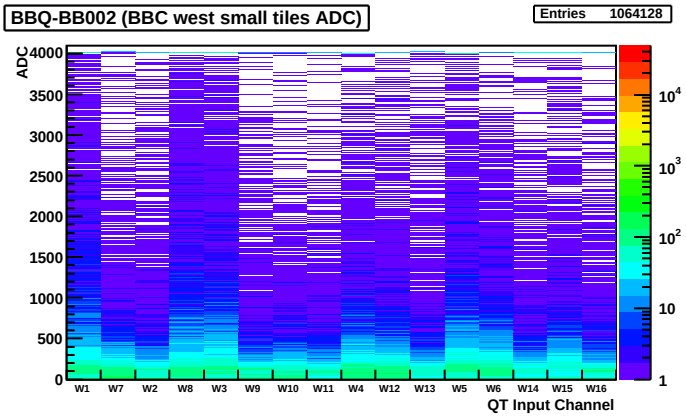

MIX-TF101
Entries 399048
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

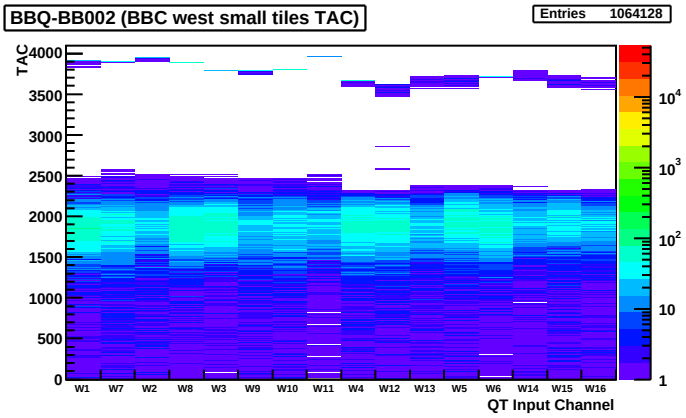

L1-TF201
Entries 66508
TOF total mult

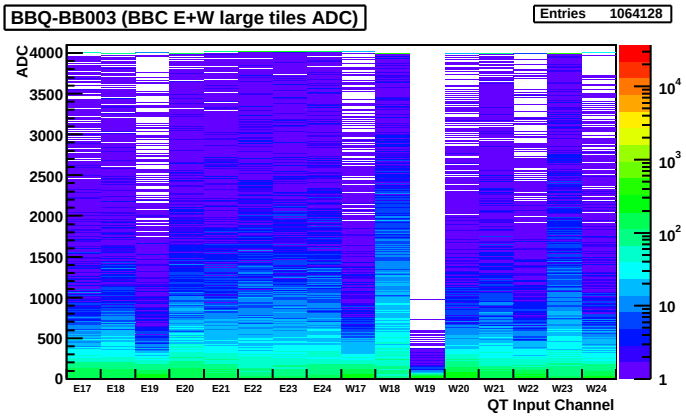

L1-TF201
Entries 399048
Threshold bits
35659
34080
29951
32099
28915
31983
30849
32428
36557
34409
37593
34525
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

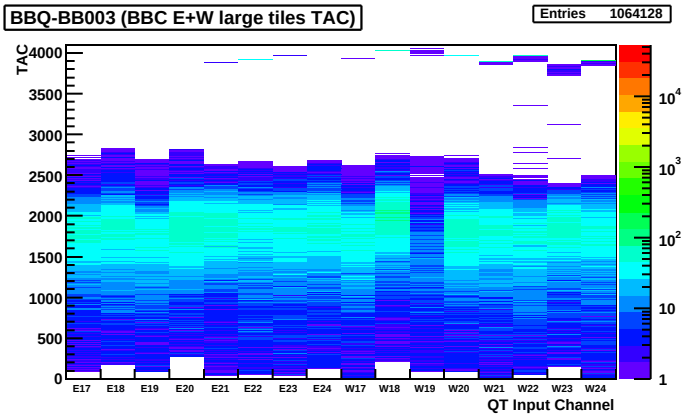

BBQ-BB001 (BBC east small tiles ADC)
Entries 1064128
ADC
QT Input Channel

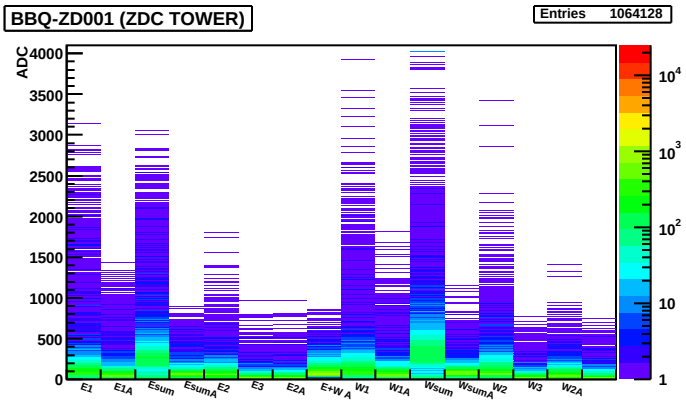

BBQ-BB001 (BBC east small tiles TAC)
Entries 1064128
TAC
QT Input Channel

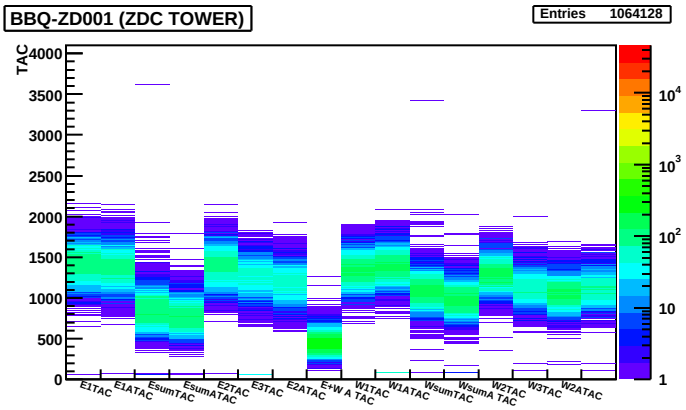

BBQ-BB002 (BBC west small tiles ADC)
Entries 1064128
ADC
QT Input Channel

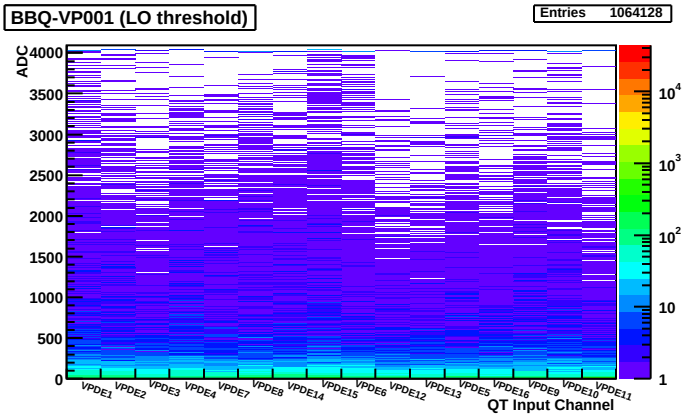

BBQ-BB002 (BBC west small tiles TAC)
Entries 1064128
TAC
QT Input Channel

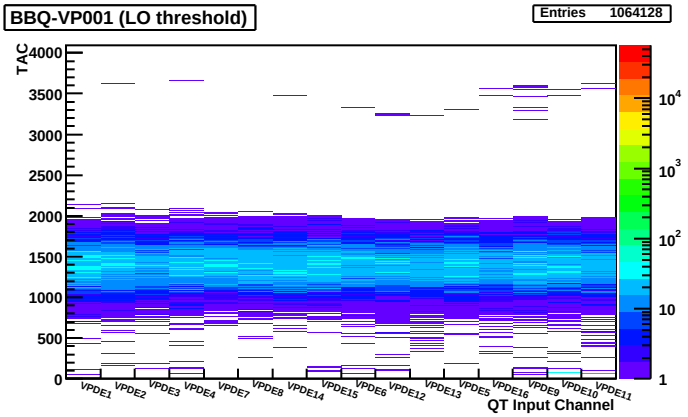

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1064128
ADC
QT Input Channel

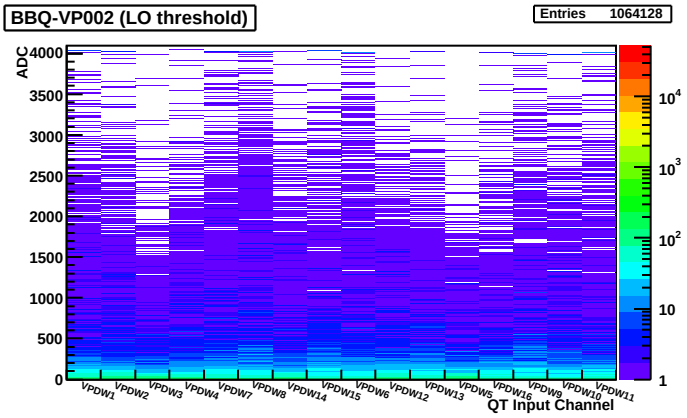

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1064128
TAC
QT Input Channel

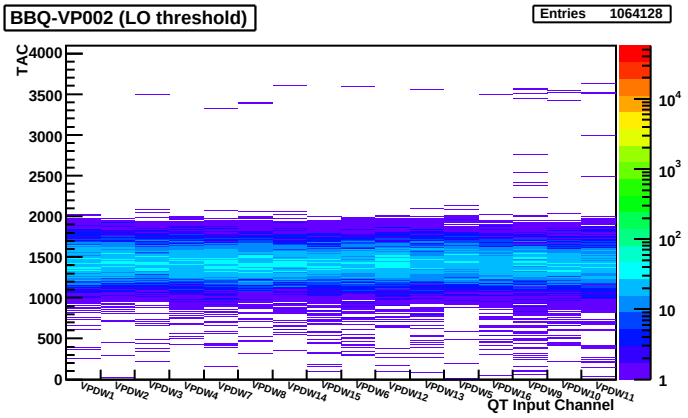

BBQ-ZD001 (ZDC TOWER)
Entries 1064128
ADC

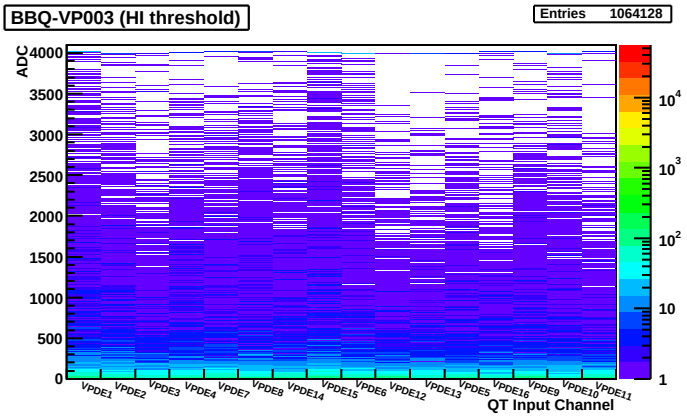

BBQ-ZD001 (ZDC TOWER)
Entries 1064128
TAC

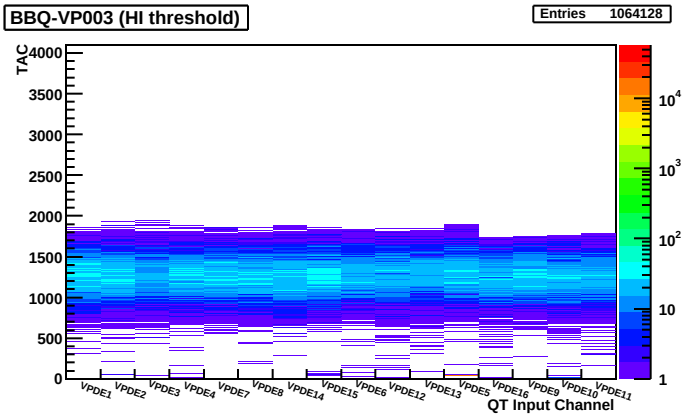

BBQ-VP001 (LO threshold)
Entries 1064128
ADC
QT Input Channel

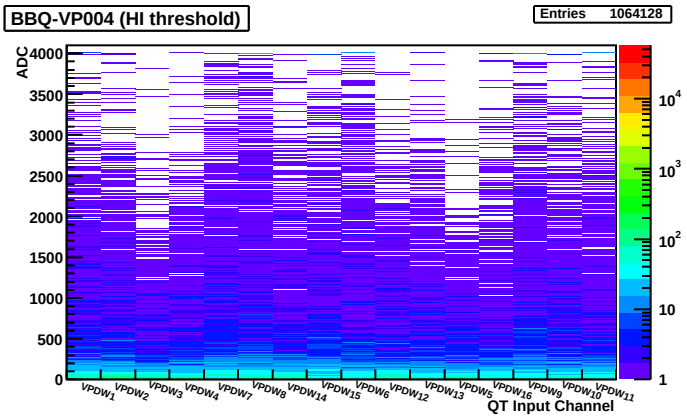

BBQ-VP001 (LO threshold)
Entries 1064128
TAC
QT Input Channel

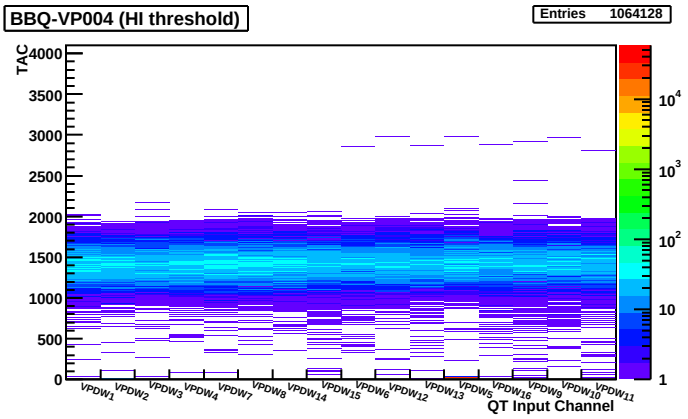

BBQ-VP002 (LO threshold)
Entries 1064128
ADC
QT Input Channel

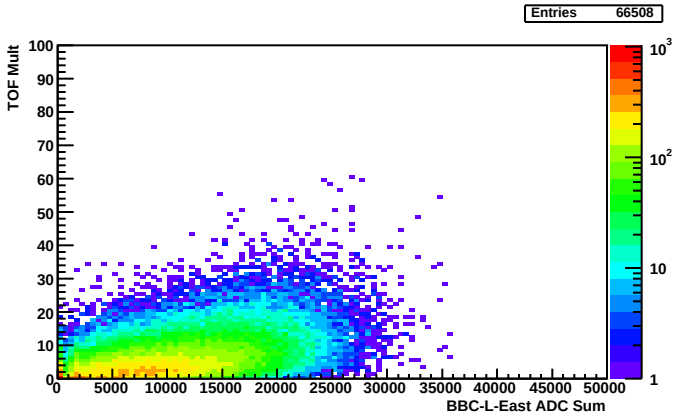

BBQ-VP002 (LO threshold)
Entries 1064128
TAC
QT Input Channel

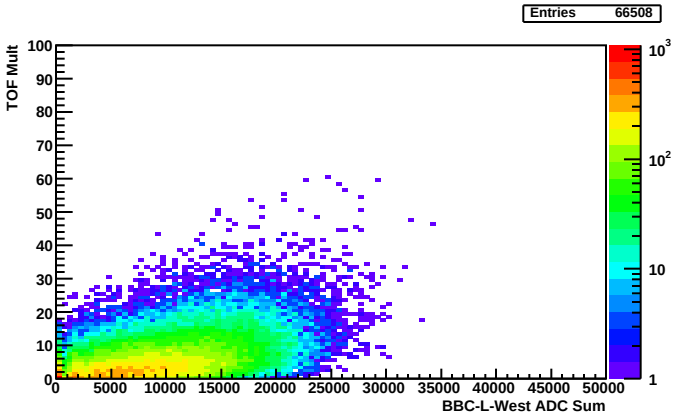

BBQ-VP003 (HI threshold)
Entries 1064128
ADC
QT Input Channel

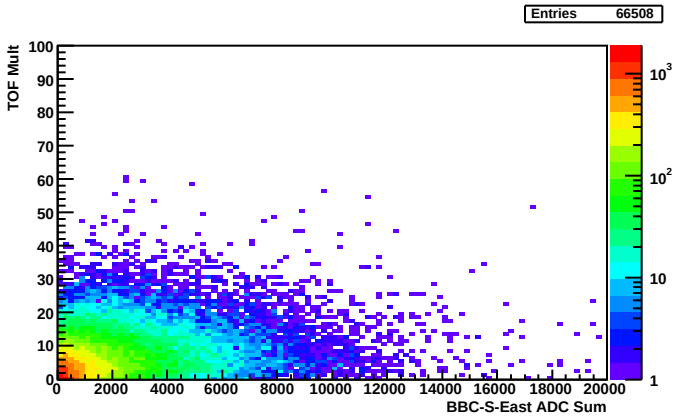

BBQ-VP003 (HI threshold)
Entries 1064128
TAC
QT Input Channel

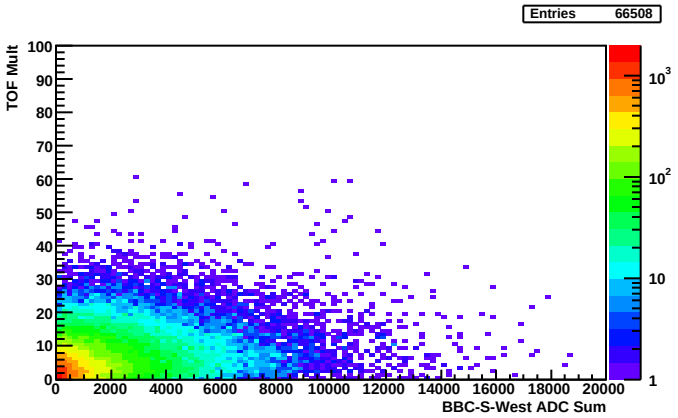

BBQ-VP004 (HI threshold)
Entries 1064128
ADC
QT Input Channel

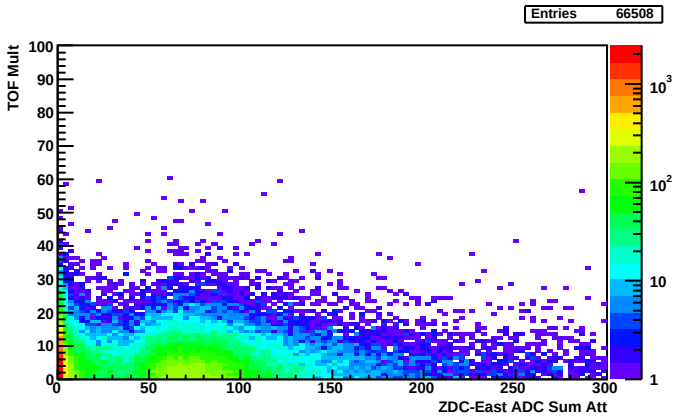

BBQ-VP004 (HI threshold)
Entries 1064128
TAC
QT Input Channel

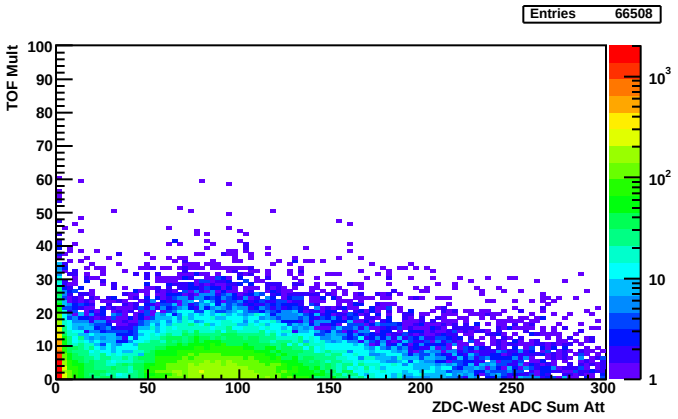

Entries 66508
TOF Mult
BBC-L-East ADC Sum

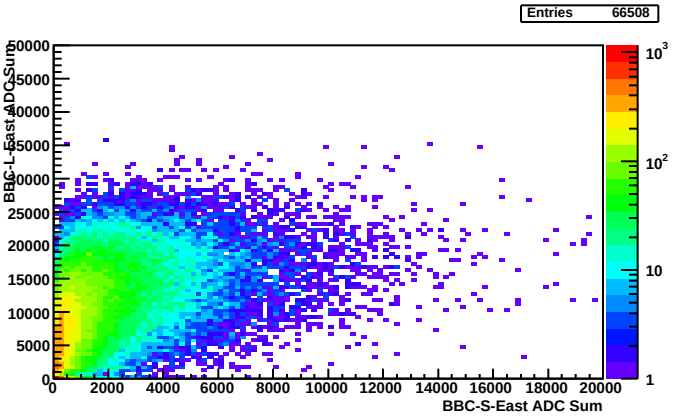

Entries 66508
TOF Mult
BBC-L-West ADC Sum

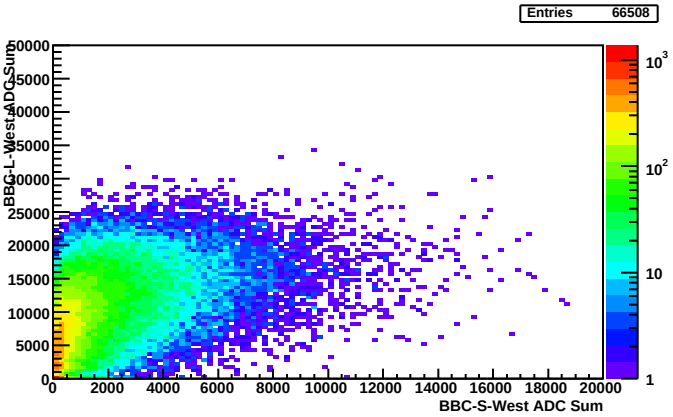

Entries 66508
TOF Mult
BBC-S-East ADC Sum

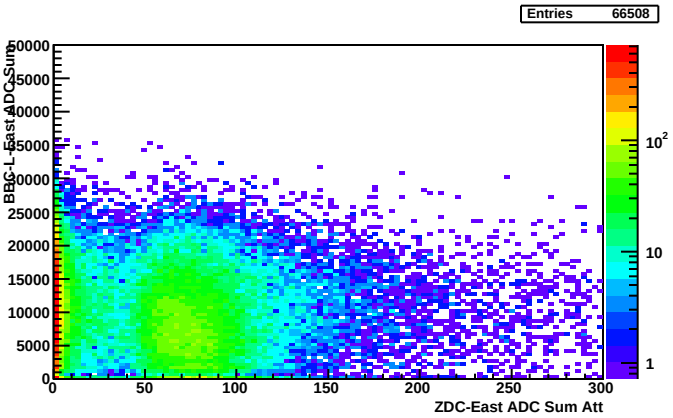

Entries 66508
TOF Mult
BBC-S-West ADC Sum

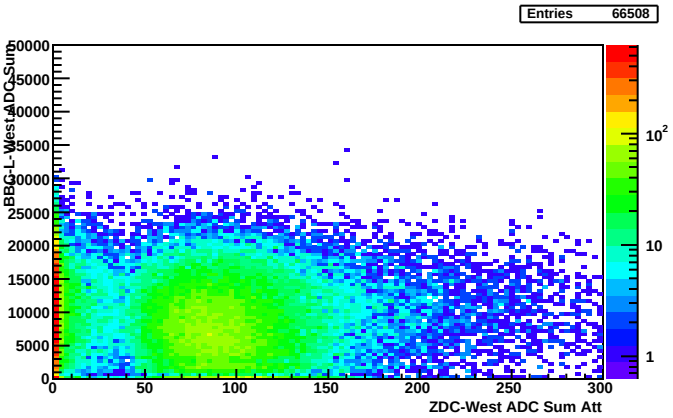

Entries 66508
TOF Mult
ZDC-East ADC Sum Att

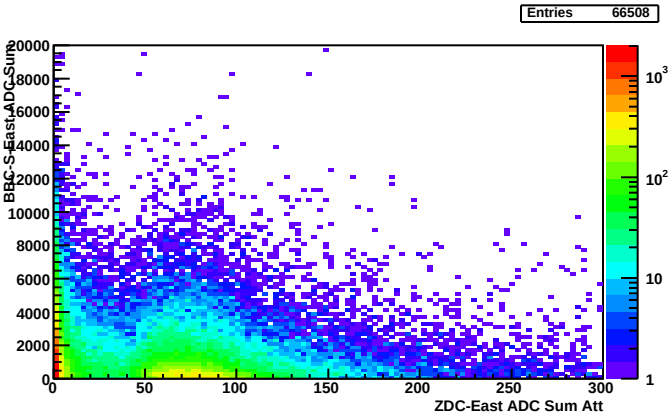

Entries 66508
TOF Mult
ZDC-West ADC Sum Att

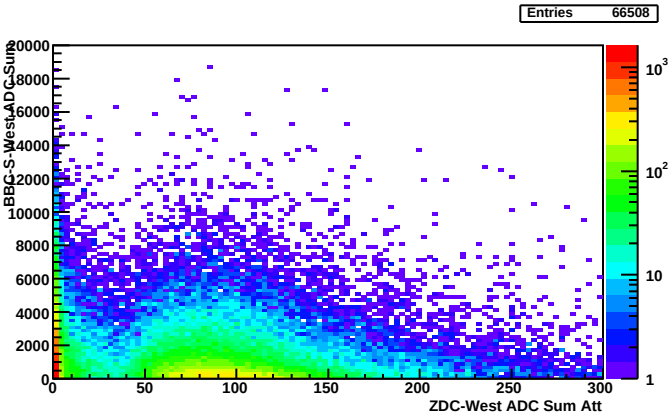

Entries 66508
BBC-L-East ADC Sum
BBC-S-East ADC Sum

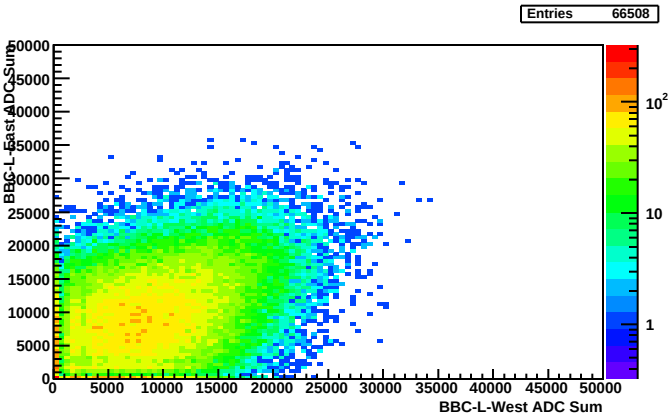

Entries 66508
BBC-L-West ADC Sum
BBC-S-West ADC Sum

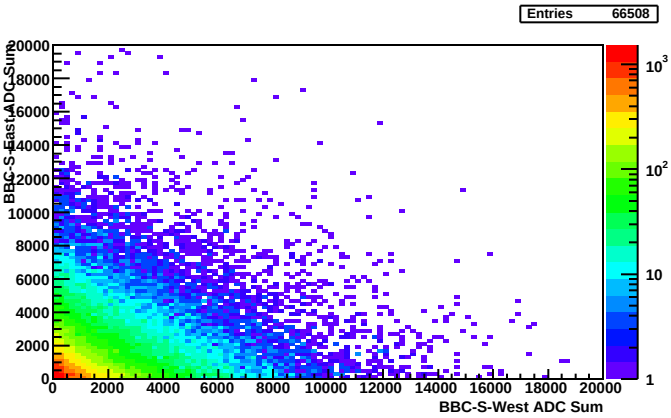

Entries 66508
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 66508
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 66508
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 66508
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 66508
BBC-L-East ADC Sum
BBC-L-West ADC Sum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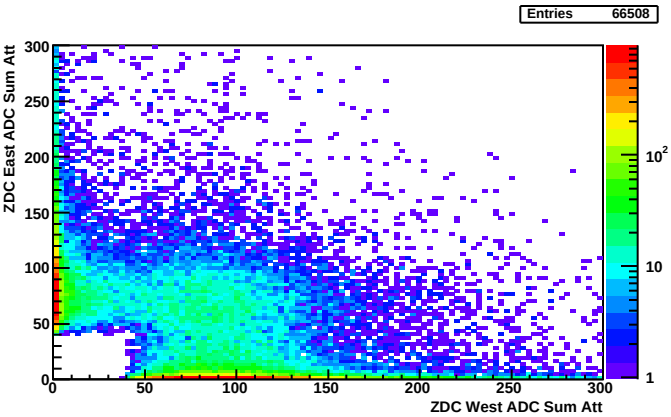
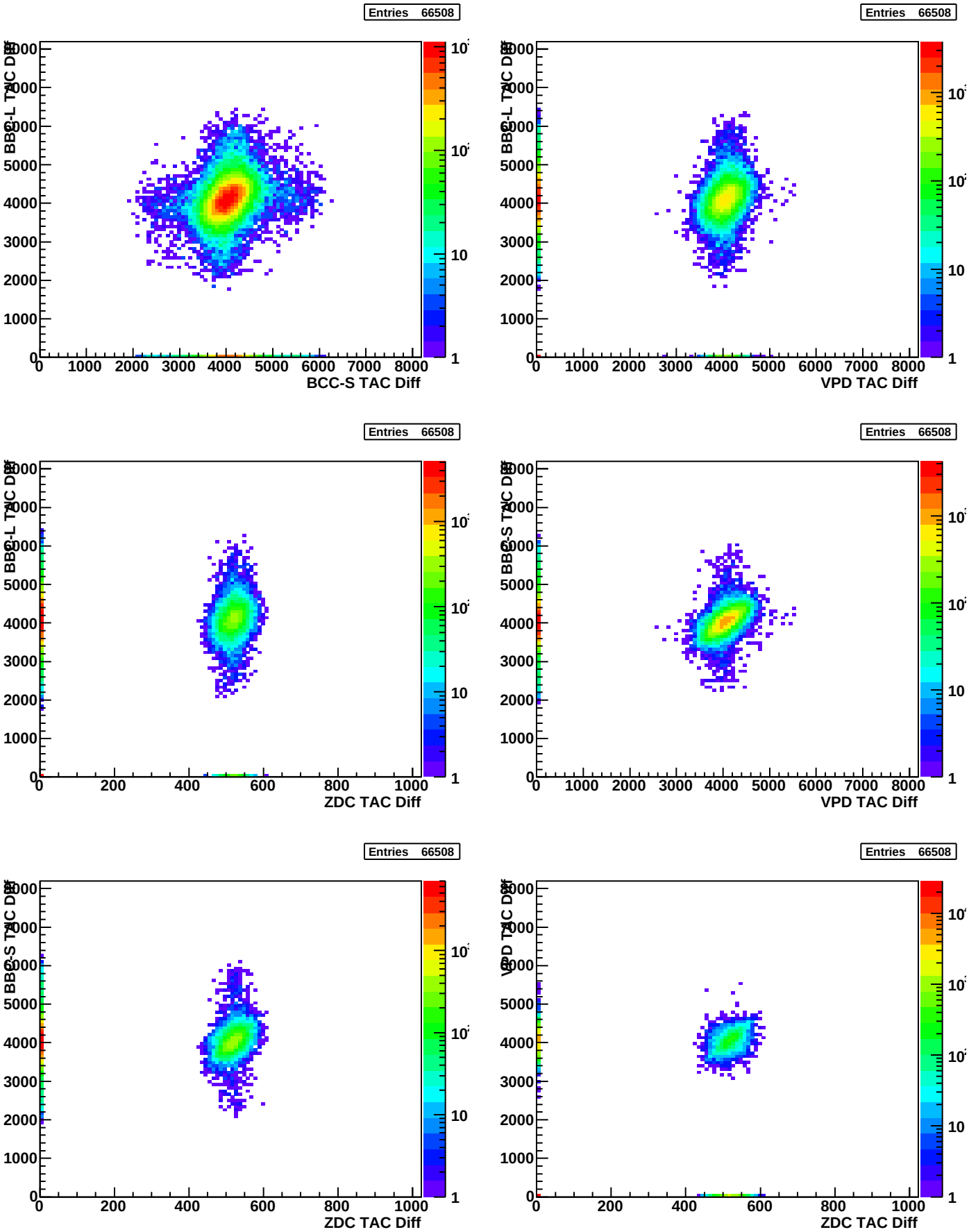
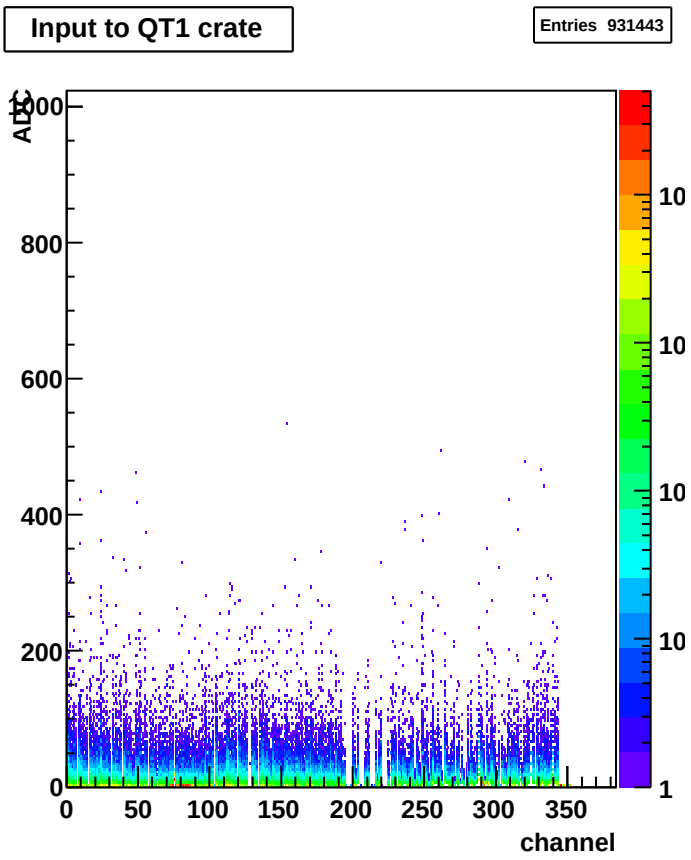
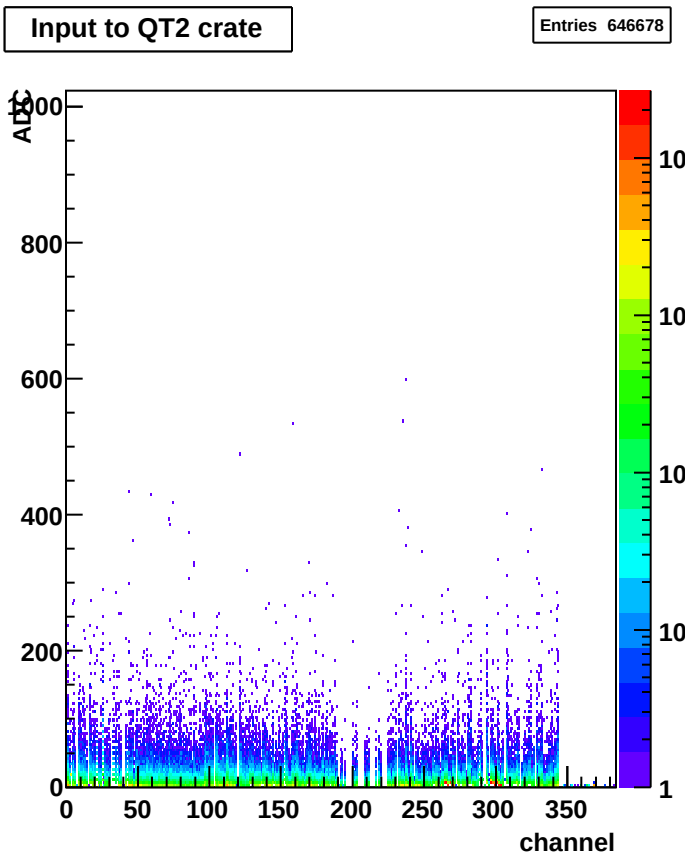
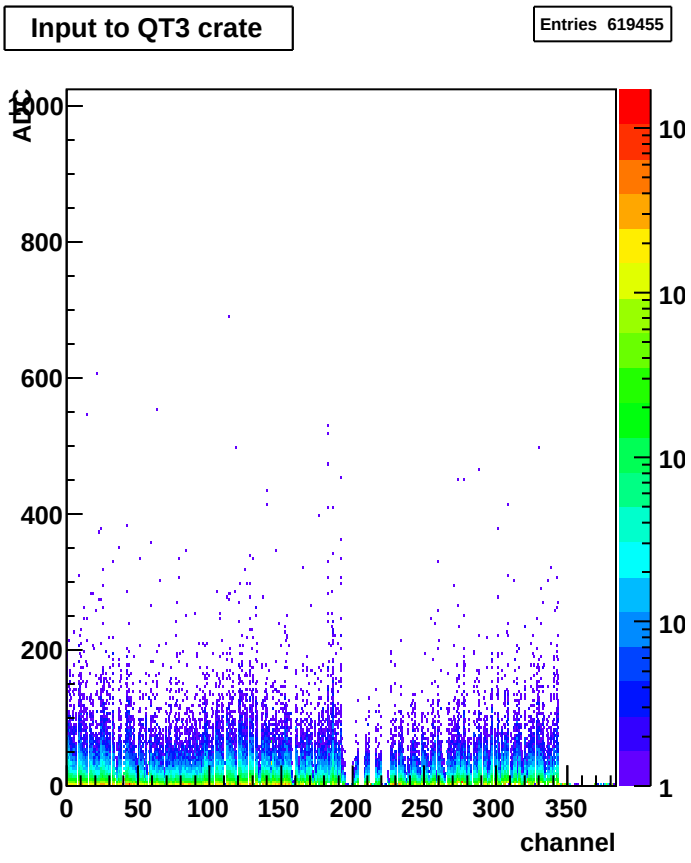
BBC-S-East ADC Sum
BBC-S-West ADC Sum

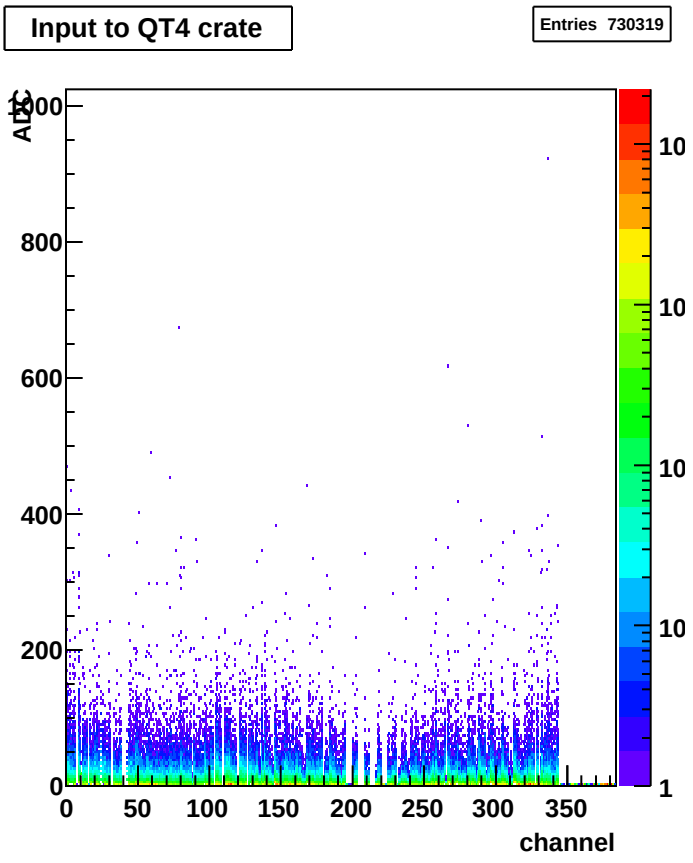

Entries 66508
ZDC East ADC Sum Att
ZDC West ADC Sum Att

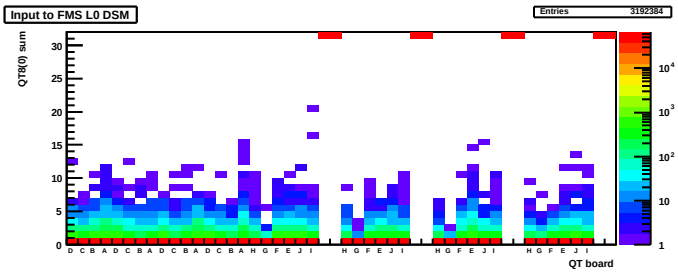

Entries 66508
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff
VPD TAC Diff

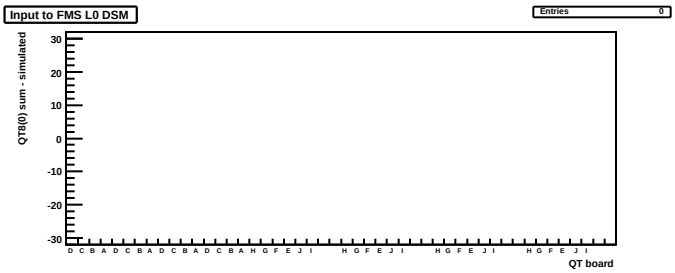

Input to QT1 crate
Entries 931443
ADC
channel

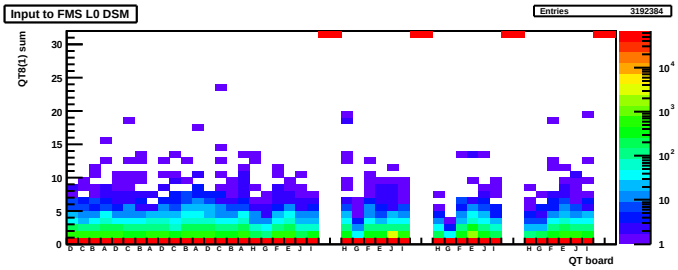

Input to QT2 crate
Entries 646678
ADC
channel

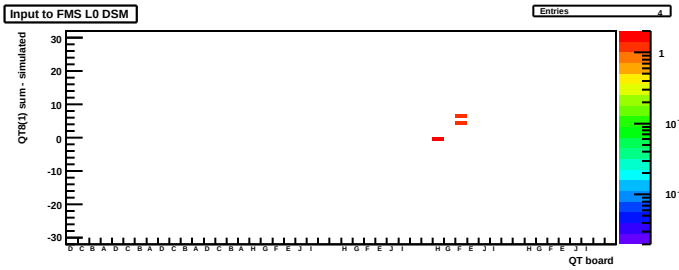

Input to QT3 crate
Entries 619455
ADC
channel

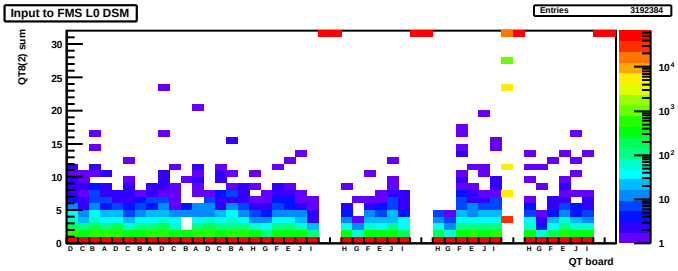

Input to QT4 crate
Entries 730319
ADC
channel

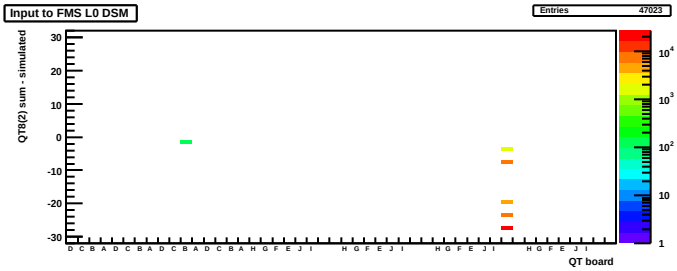

Input to FMS L0 DSM
Entries 3192384
QT8(0) sum
QT board

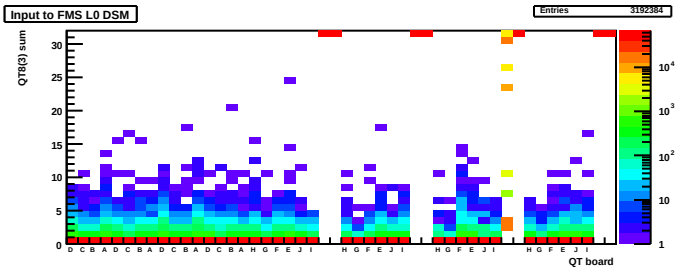

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

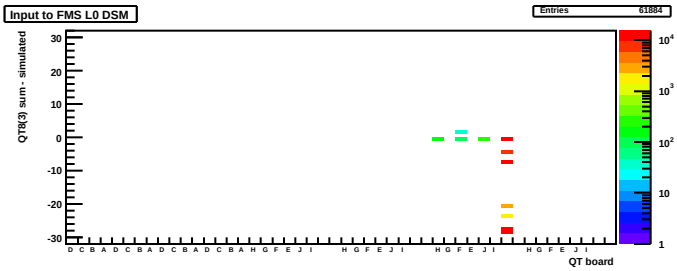

Input to FMS L0 DSM
Entries 3192384
QT8(1) sum
QT board

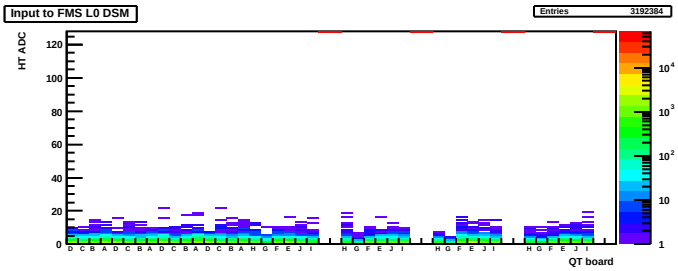

Input to FMS L0 DSM
Entries 4
QT8(1) sum - simulated
QT board

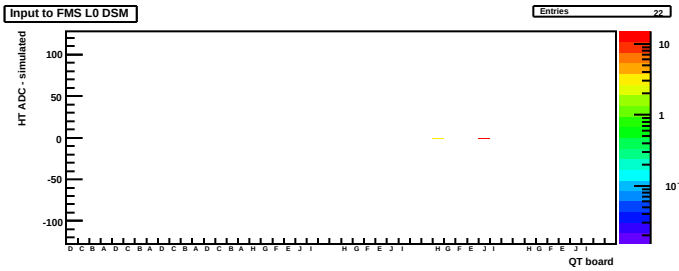

Input to FMS L0 DSM
Entries 3192384
QT8(2) sum
QT board

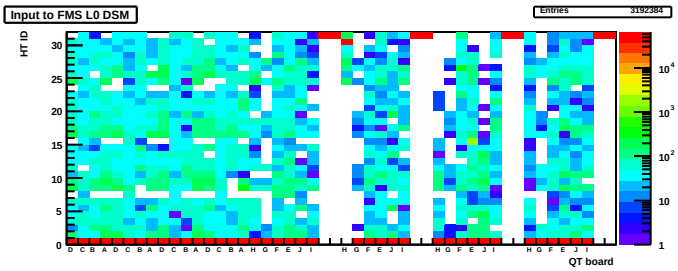

Input to FMS L0 DSM
Entries 47023
QT8(2) sum - simulated
QT board

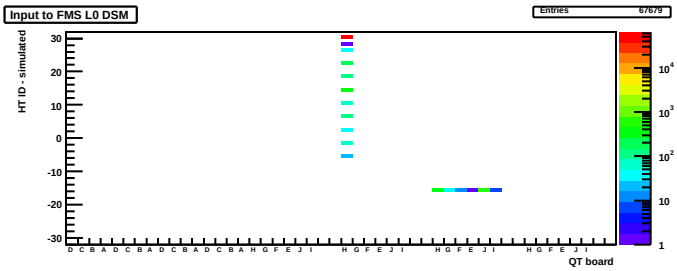

Input to FMS L0 DSM
Entries 3192384
QT8(3) sum
QT board

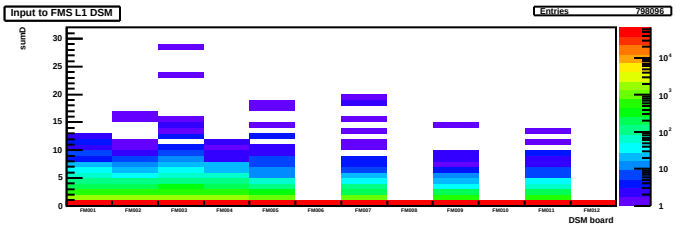

Input to FMS L0 DSM
Entries 61884
QT8(3) sum - simulated
QT board

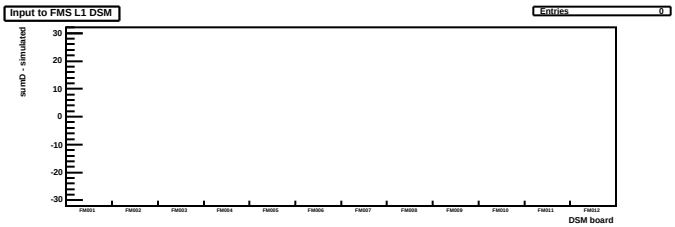

Input to FMS L0 DSM
Entries 3192384
HT ADC
QT board

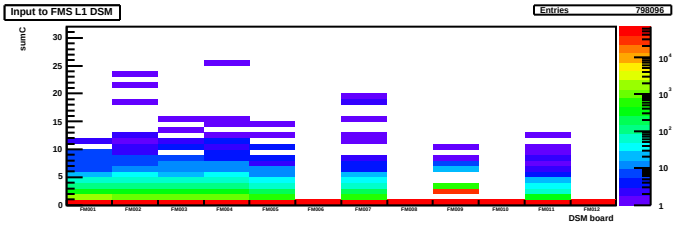

Input to FMS L0 DSM
Entries 22
HT ADC - simulated
QT board

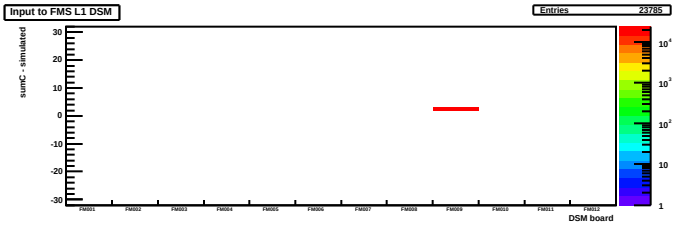

Input to FMS L0 DSM
Entries 3192384
HT ID
QT board

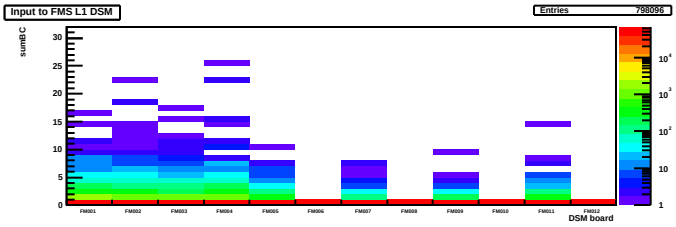

Input to FMS L0 DSM
Entries 67679
HT ID - simulated
QT board

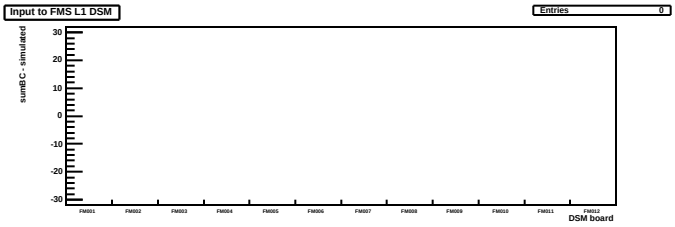

Input to FMS L1 DSM
Entries 798096
sumD
DSM board

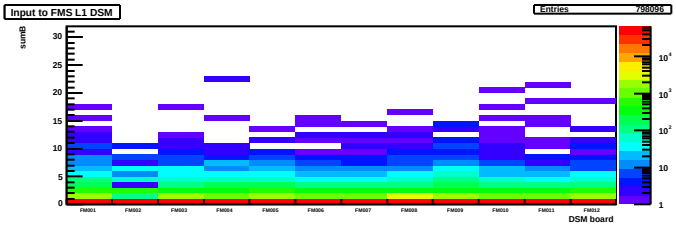

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

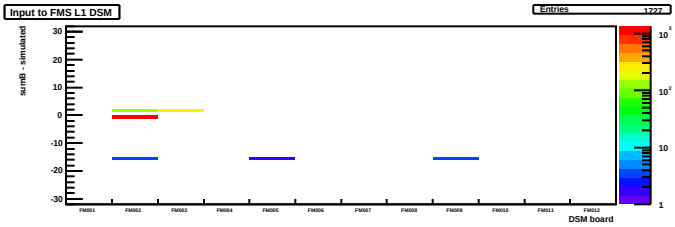

Input to FMS L1 DSM
Entries 798096
sumC
DSM board

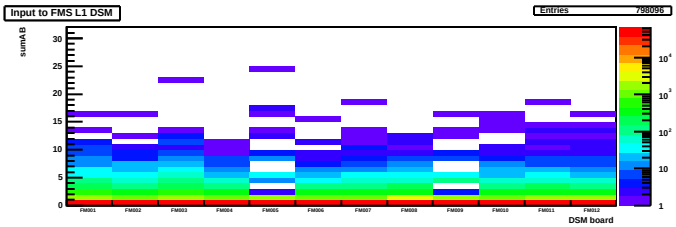

Input to FMS L1 DSM
Entries 23785
sumC - simulated
DSM board

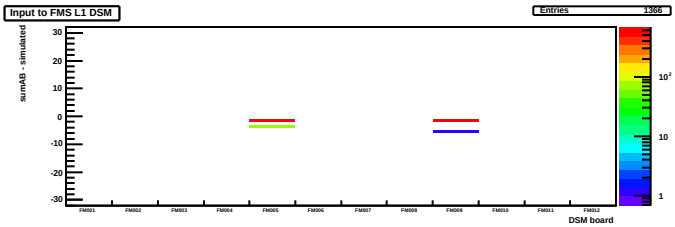

Input to FMS L1 DSM
Entries 798096
sumBC
DSM board

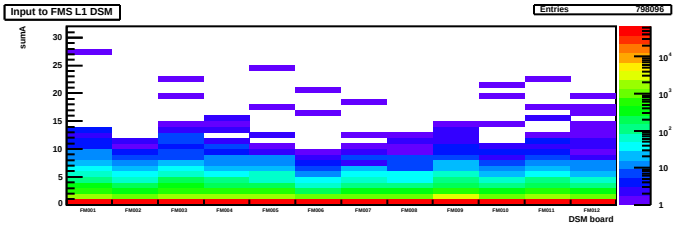

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

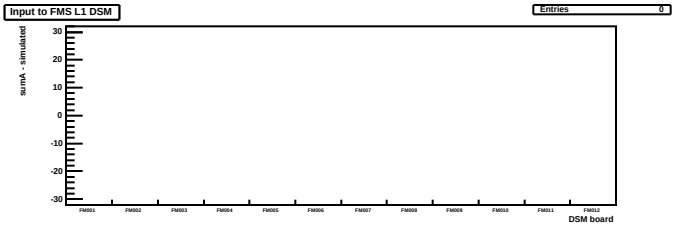

Input to FMS L1 DSM
Entries 798096
sumB
DSM board

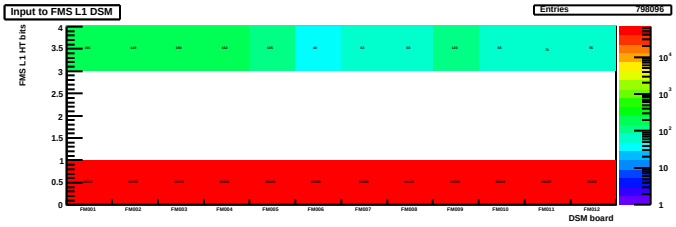

Input to FMS L1 DSM
Entries 1227
sumB - simulated
DSM board

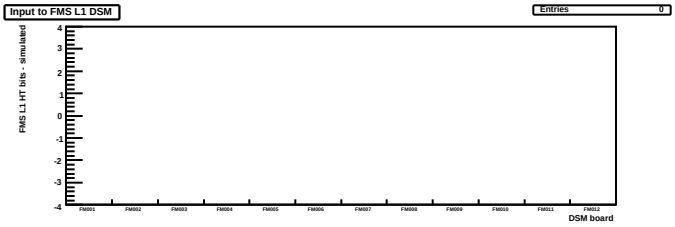

Input to FMS L1 DSM
Entries 798096
sumAB
DSM board

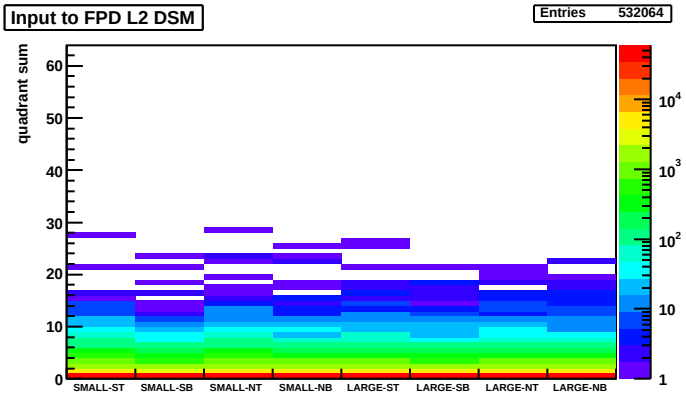

Input to FMS L1 DSM
Entries 1366
sumAB - simulated
DSM board

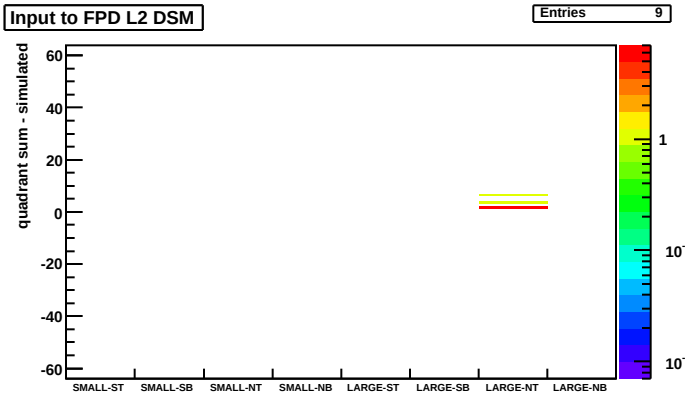

Input to FMS L1 DSM
Entries 798096
sumA
DSM board

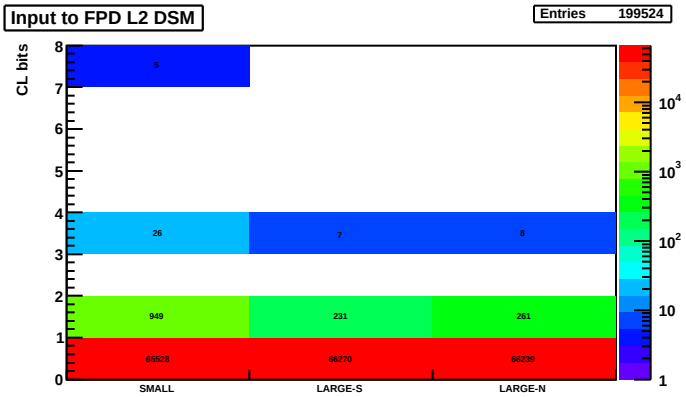

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

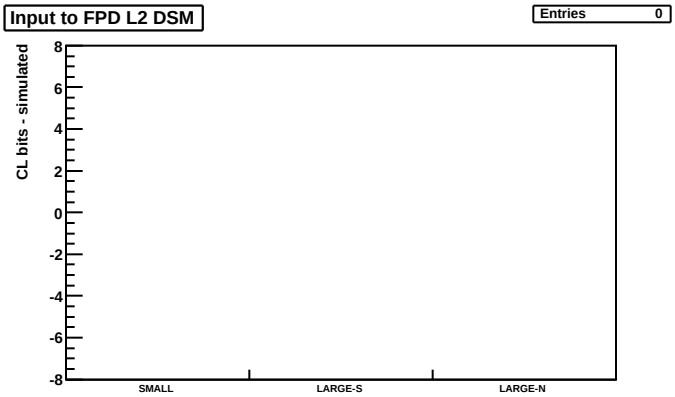

Input to FMS L1 DSM
Entries 798096
FMS L1 HT bits
DSM board

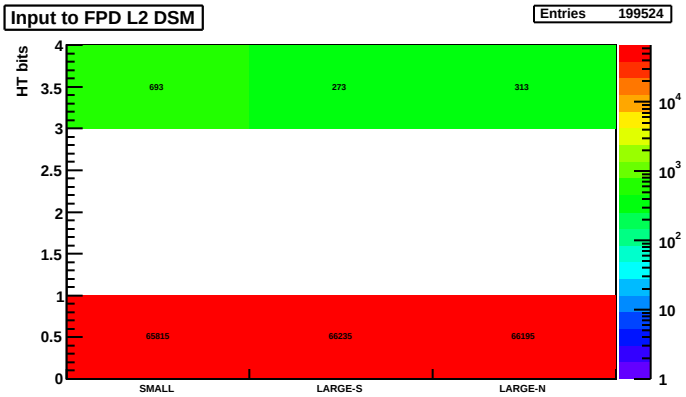

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

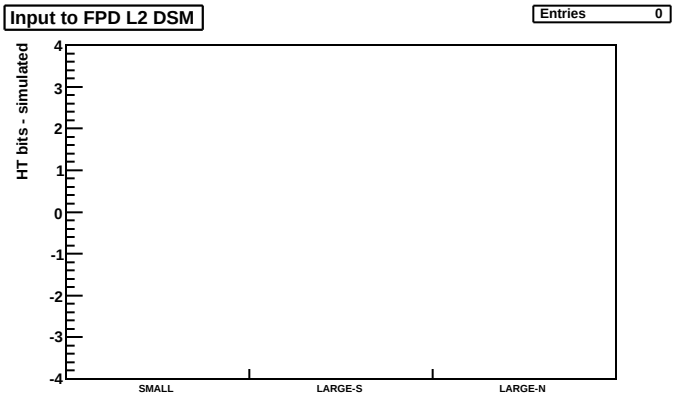

Input to FPD L2 DSM
Entries 532064
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

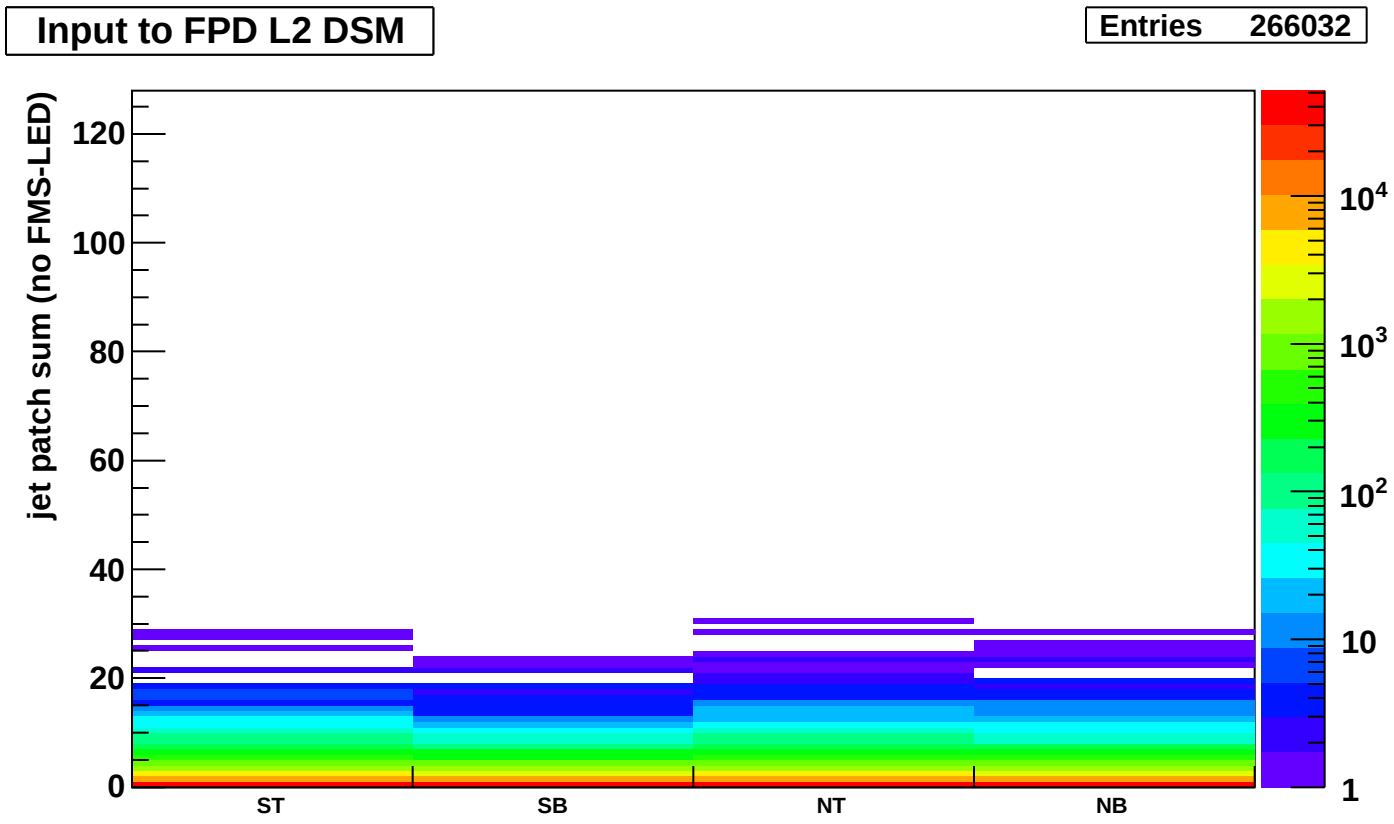

Input to FPD L2 DSM
Entries 9
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

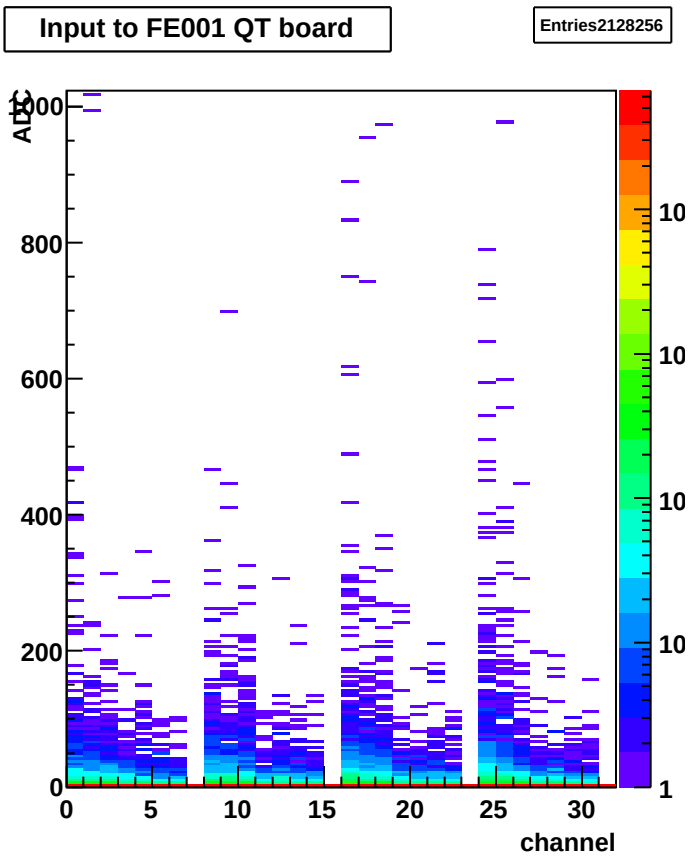

Input to FPD L2 DSM
Entries 199524
CL bits
5
26
7
8
949
231
261
65528
66270
66239
SMALL
LARGE-S
LARGE-N

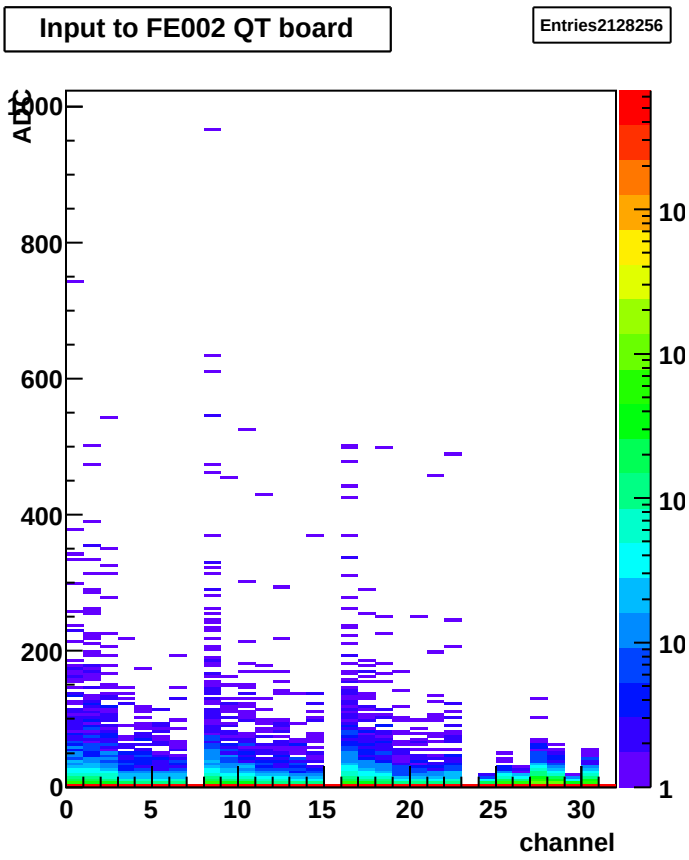

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

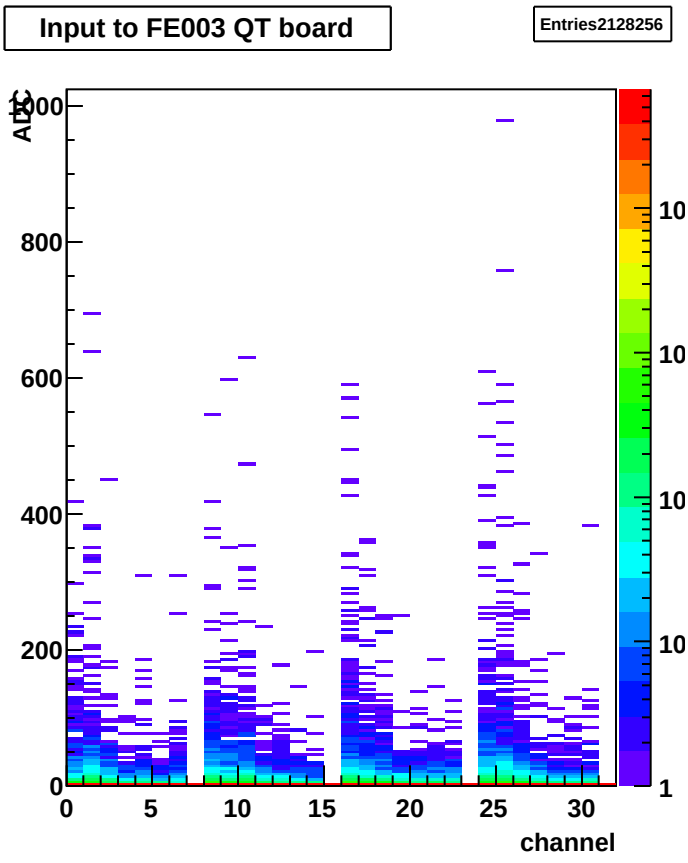

Input to FPD L2 DSM
Entries 199524
HT bits
693
273
313
65815
66235
66195
SMALL
LARGE-S
LARGE-N

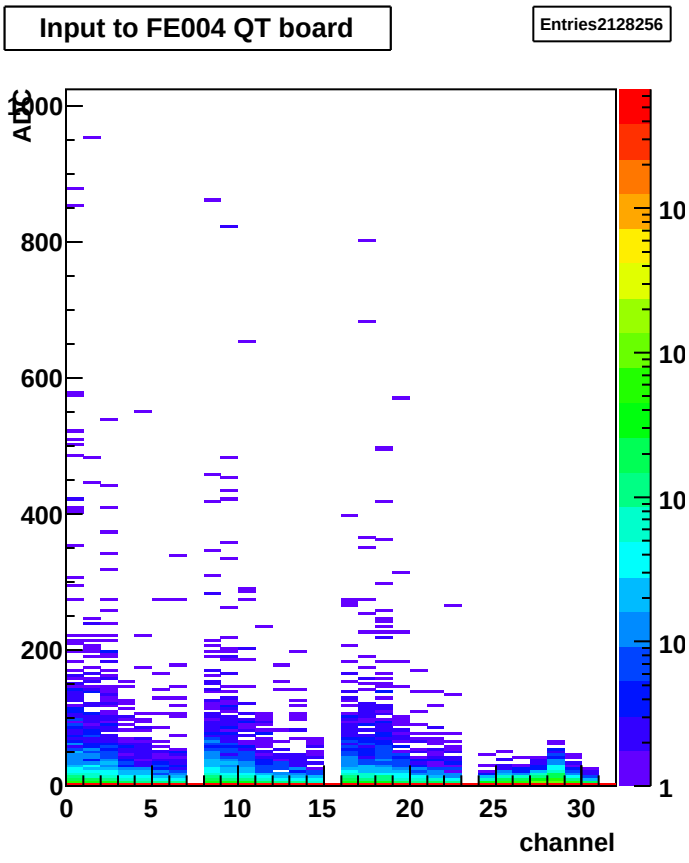

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

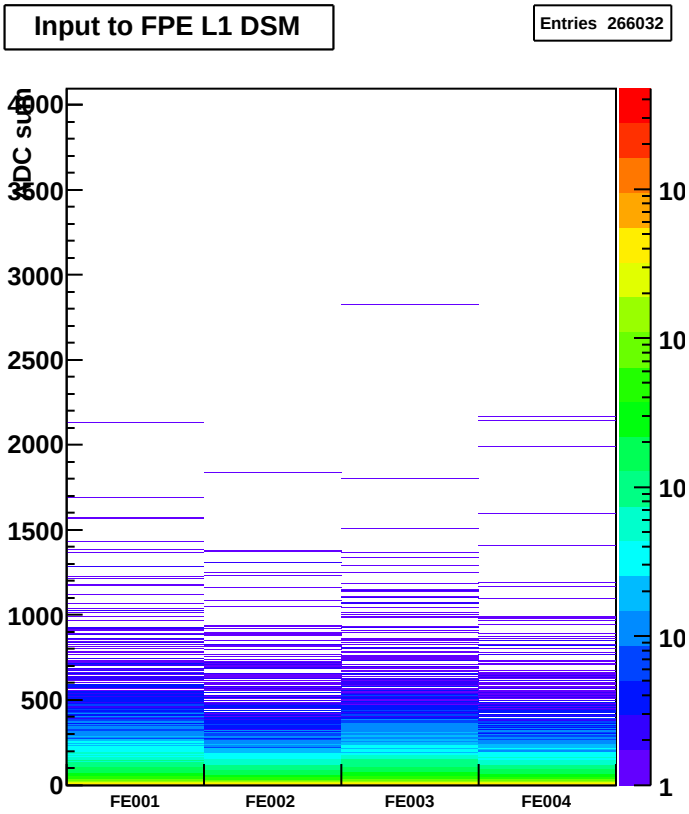

Input to FPD L2 DSM
Entries 266032
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

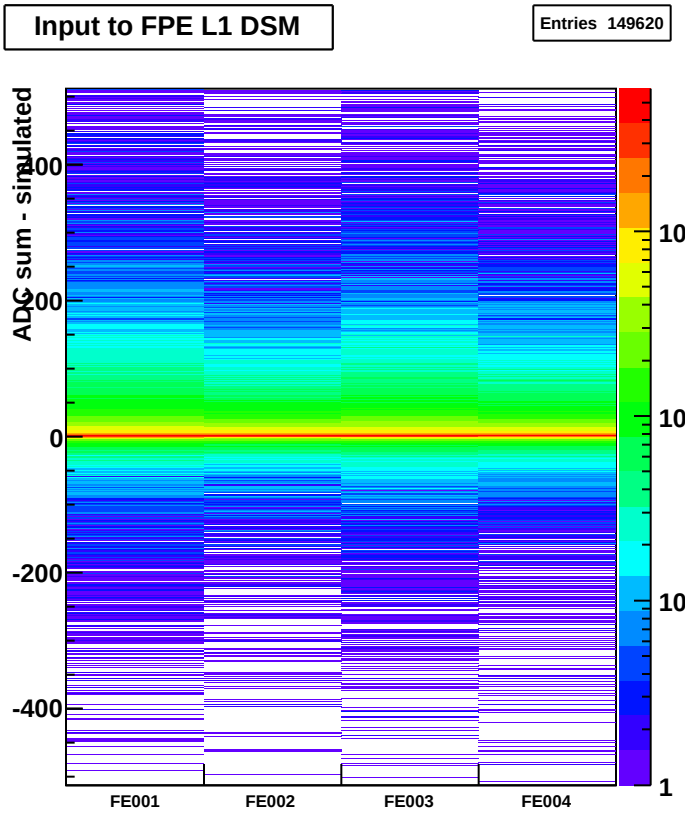

Input to FE001 QT board
Entries2128256
ADC
channel


Input to FE002 QT board
Entries2128256
ADC
channel

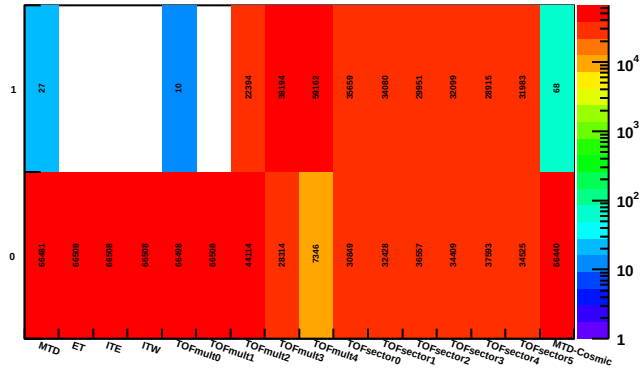

Input to FE003 QT board
Entries2128256
ADC
channel


Input to FE004 QT board
Entries2128256
ADC
channel

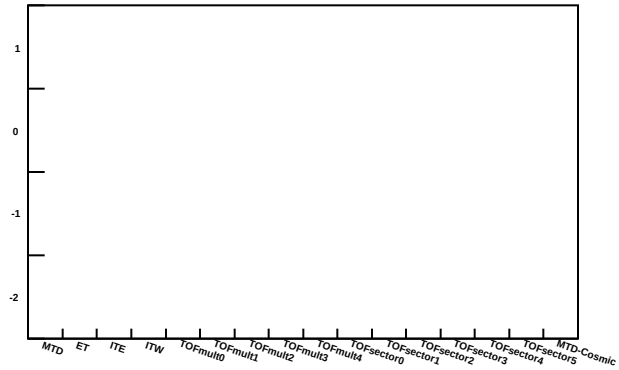

Input to FPE L1 DSM
Entries 266032
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 149620
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
1

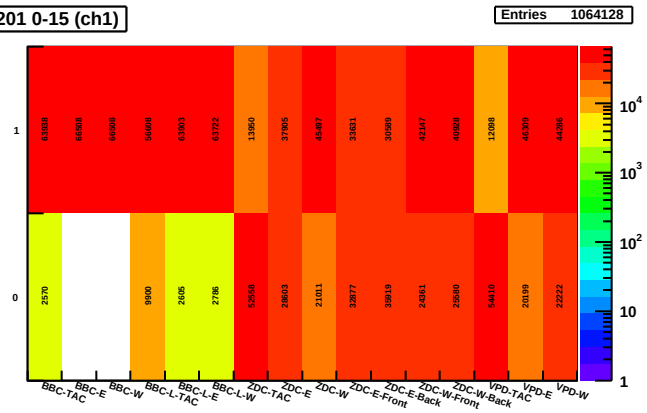
TF201 0-15 (ch0)



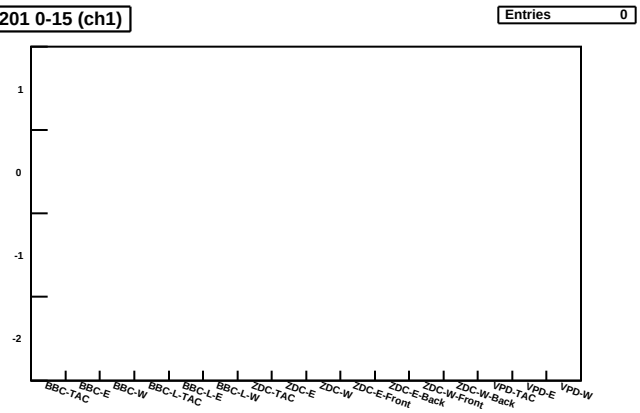
TF201 0-15 (ch0)



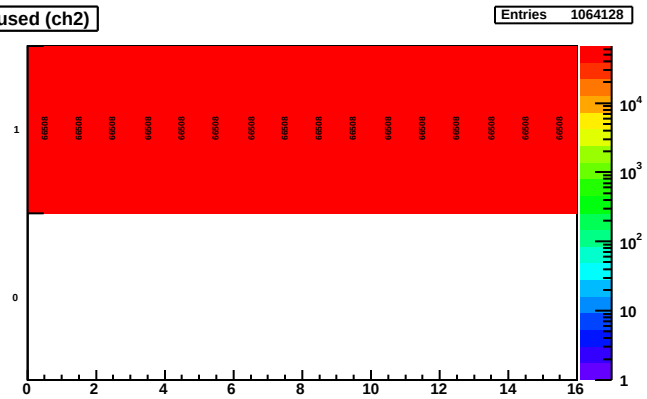
VT201 0-15 (ch1)

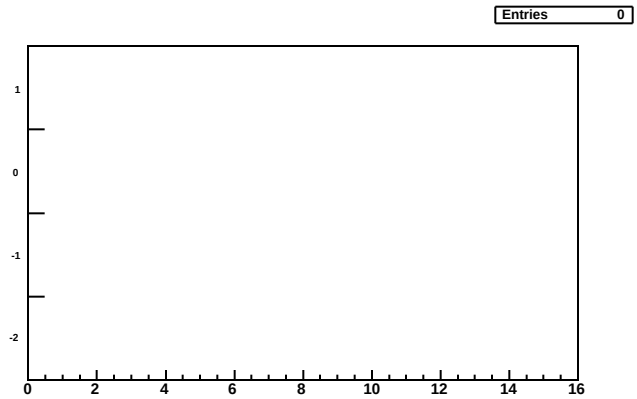


VT201 0-15 (ch1)

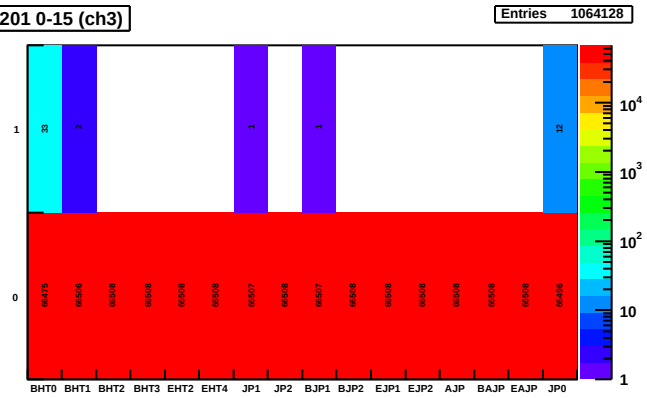


Unused (ch2)

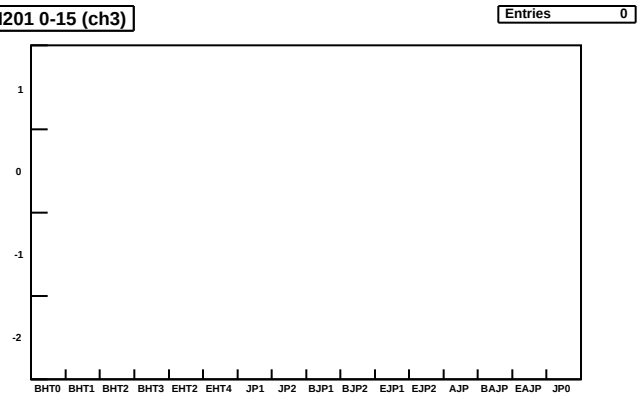




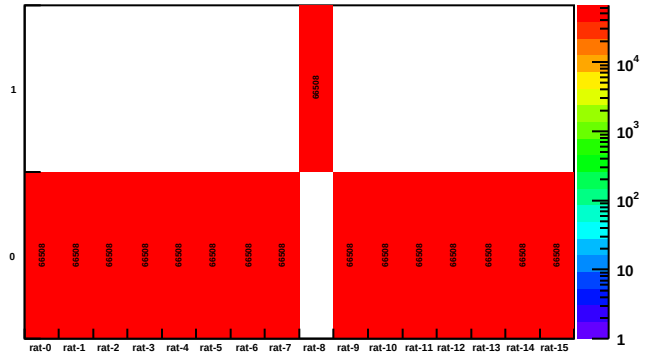
EM201 0-15 (ch3)

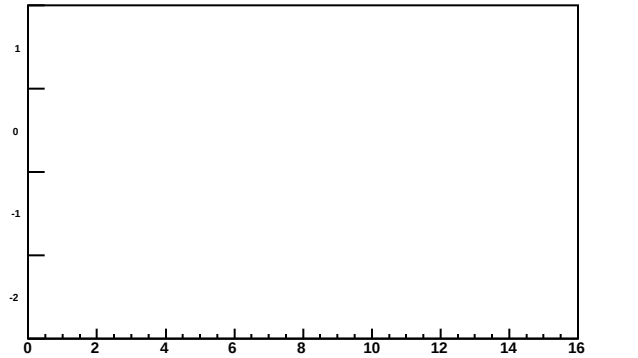


EM201 0-15 (ch3)

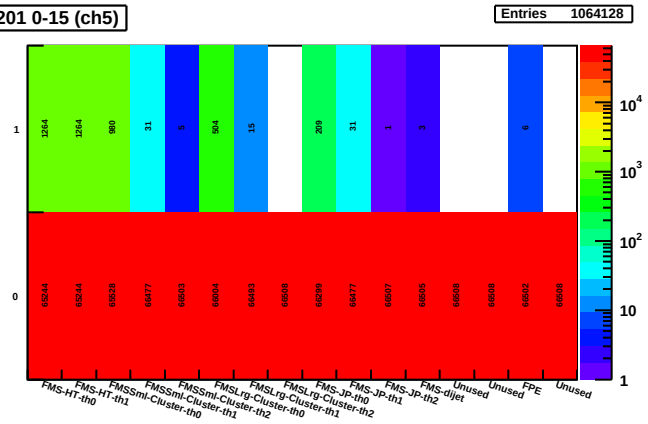


RAT board (ch4)

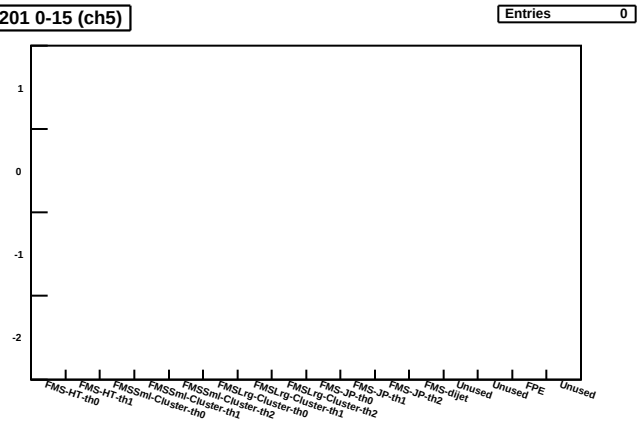




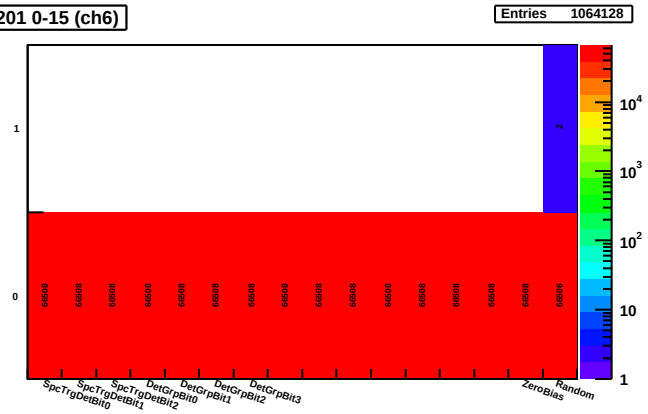
FP201 0-15 (ch5)

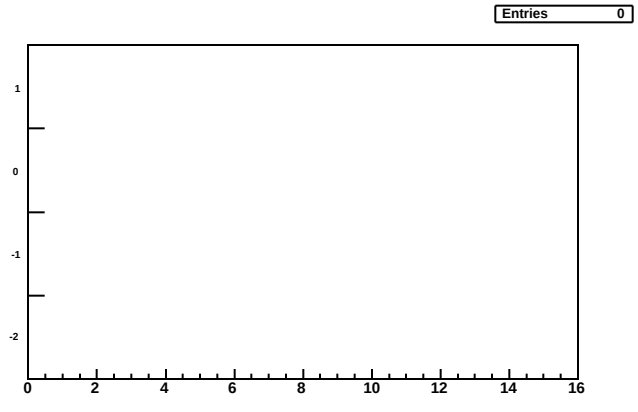


FP201 0-15 (ch5)

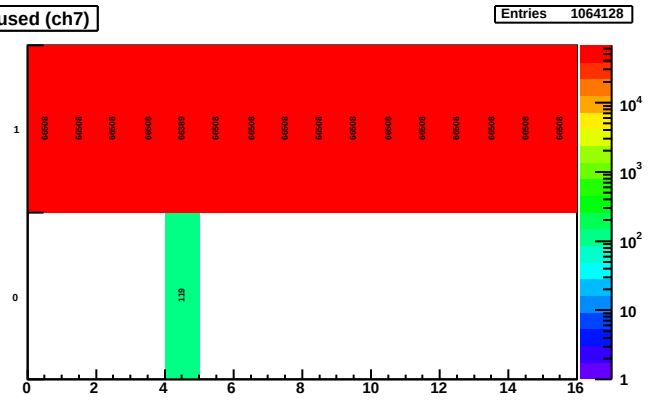


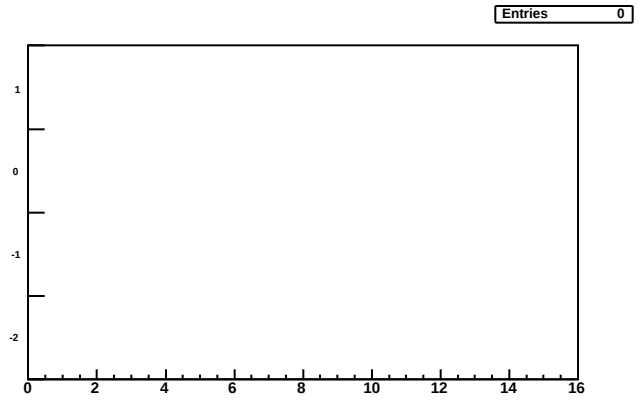
ST201 0-15 (ch6)

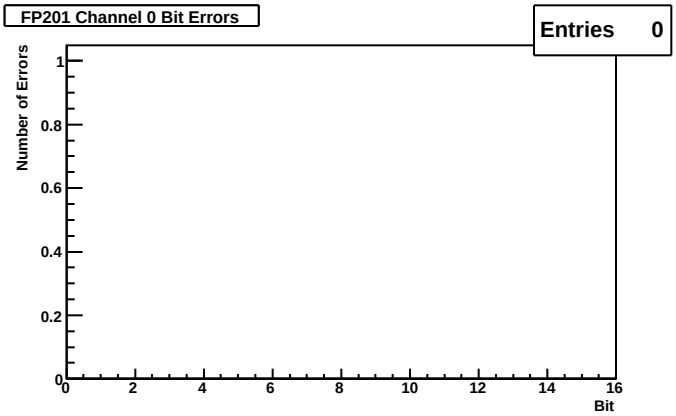


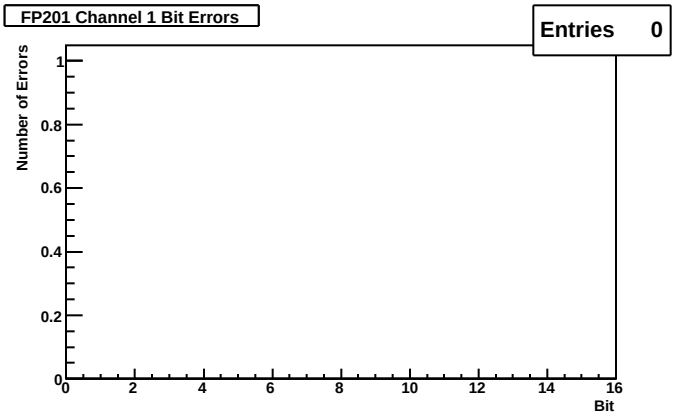


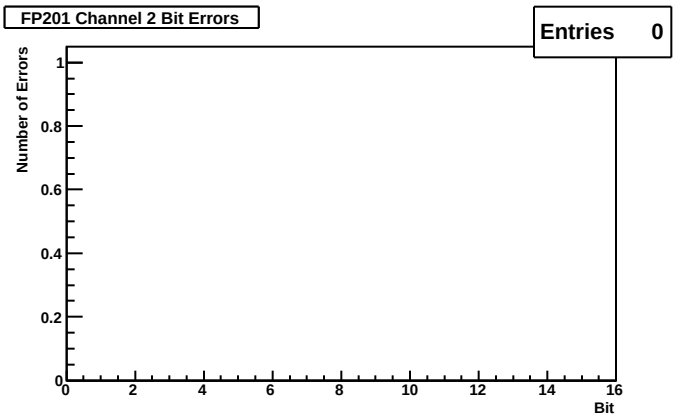
Unused (ch7)

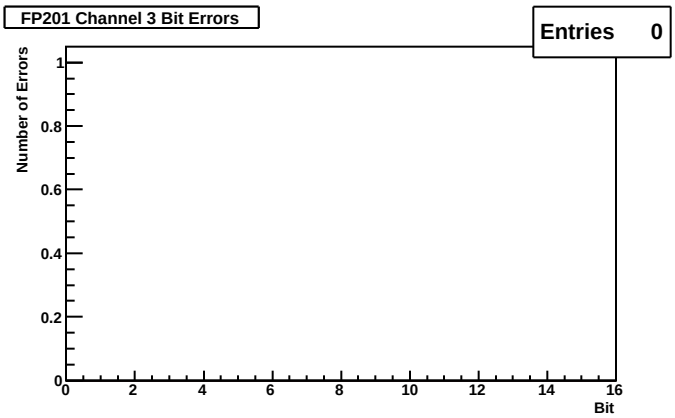


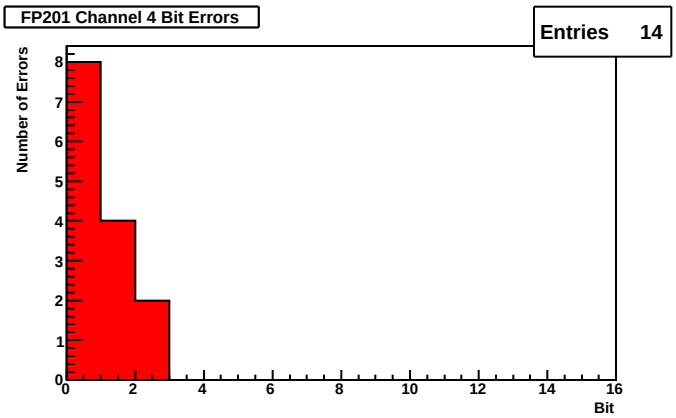


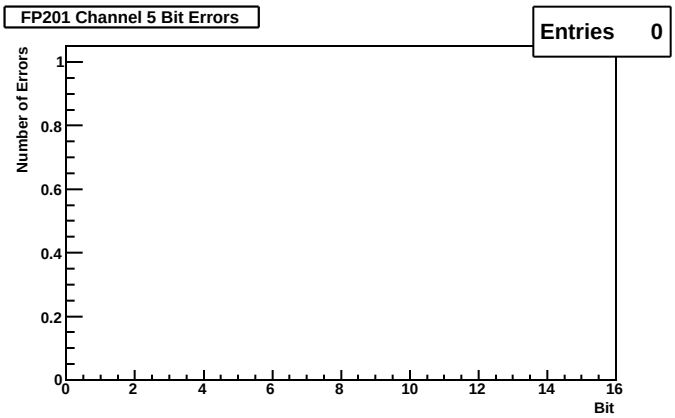

FP201 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

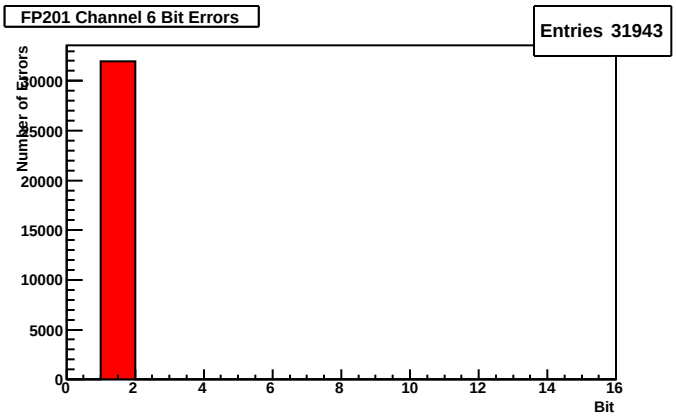

FP201 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

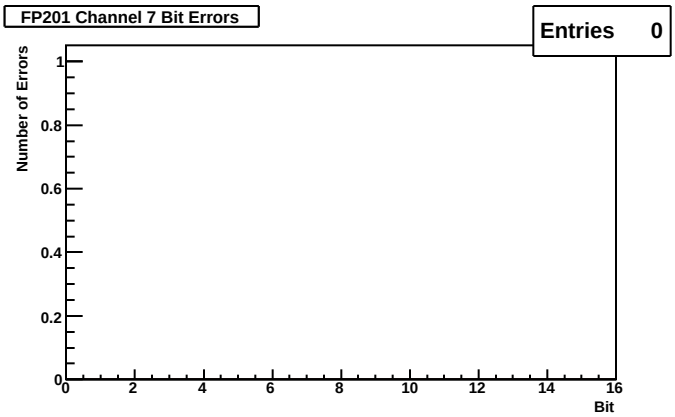

FP201 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

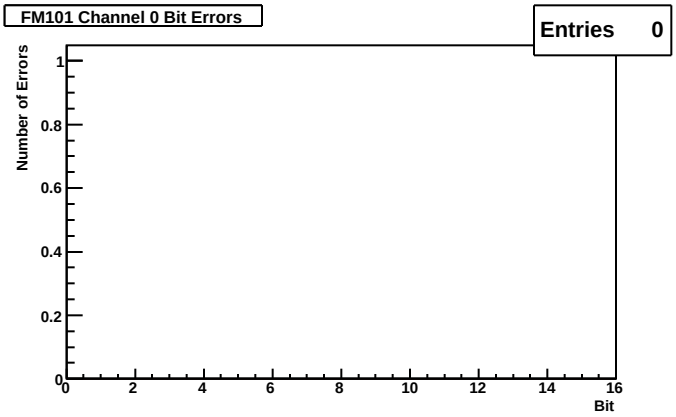

FP201 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

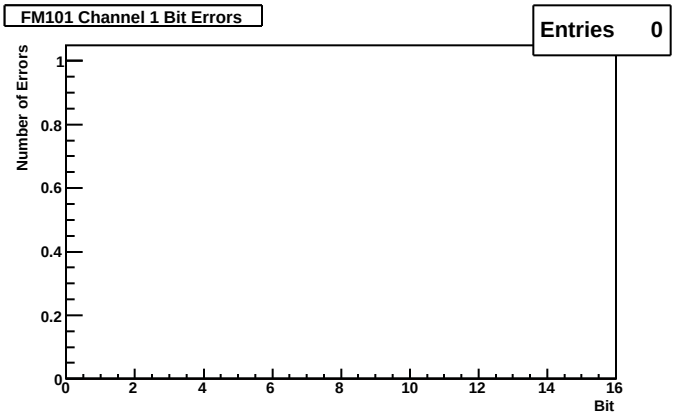

FP201 Channel 4 Bit Errors
Entries 14
Number of Errors
Bit

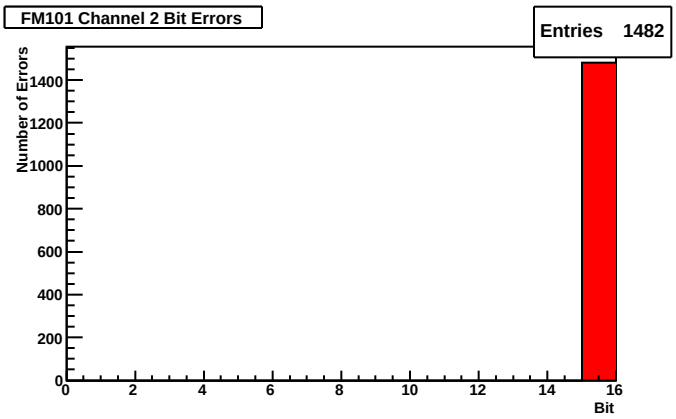

FP201 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

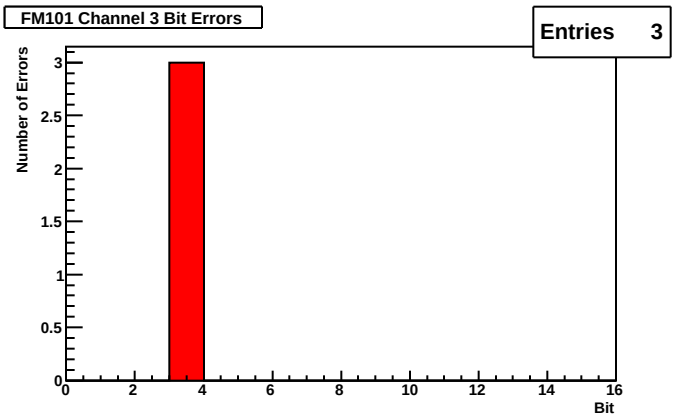

FP201 Channel 6 Bit Errors
Entries 31943
Number of Errors
Bit

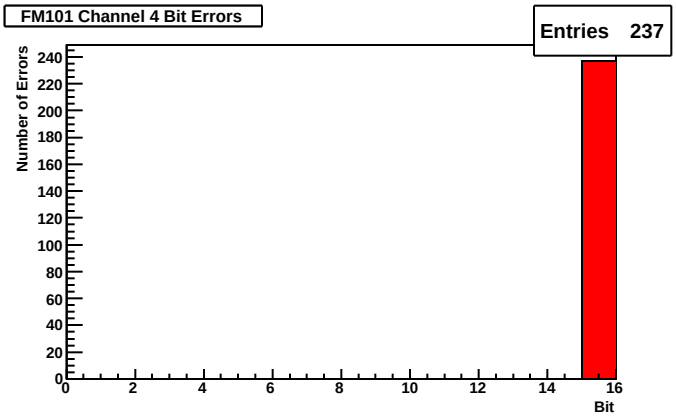

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

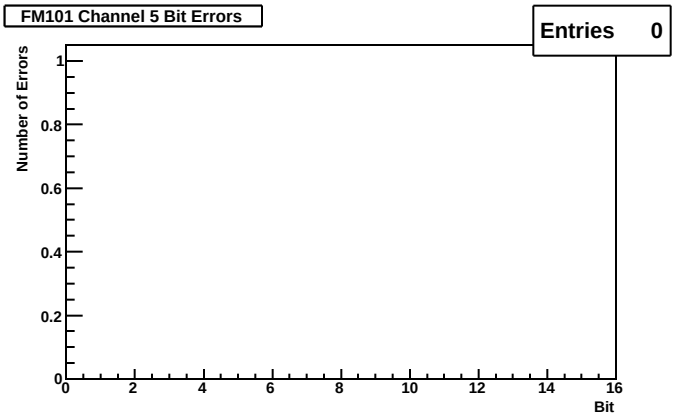

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

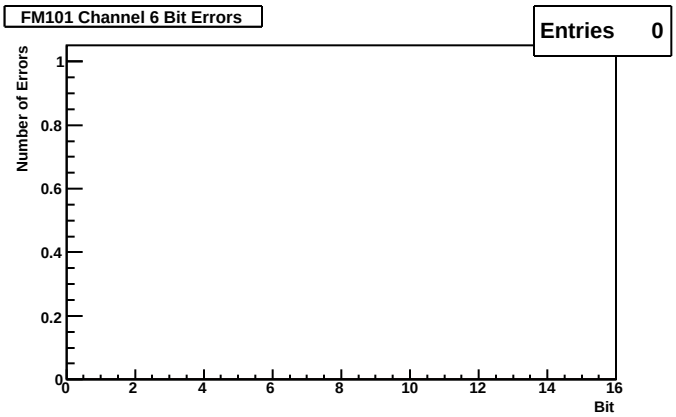

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

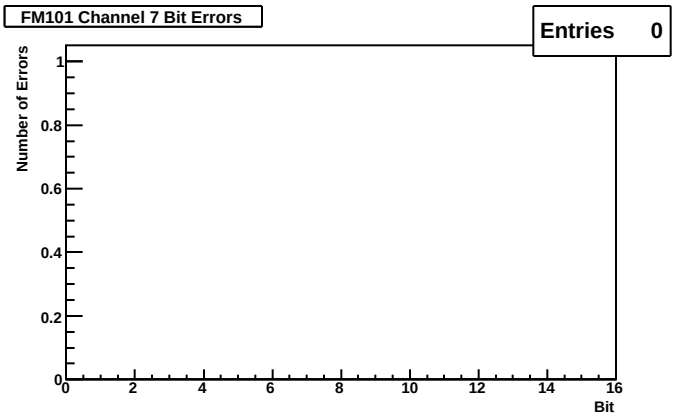

FM101 Channel 2 Bit Errors
Entries 1482
Number of Errors
Bit

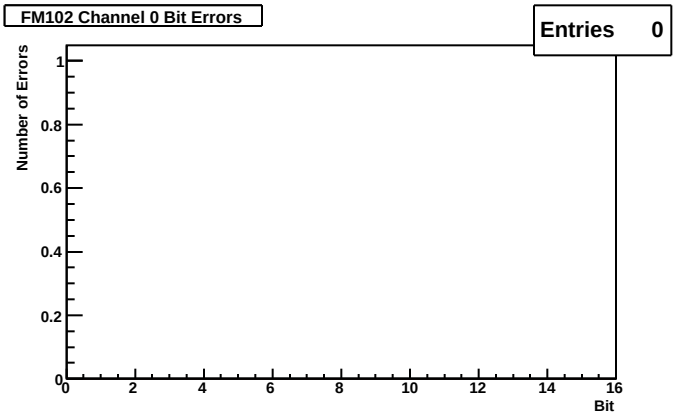

FM101 Channel 3 Bit Errors
Entries 3
Number of Errors
Bit

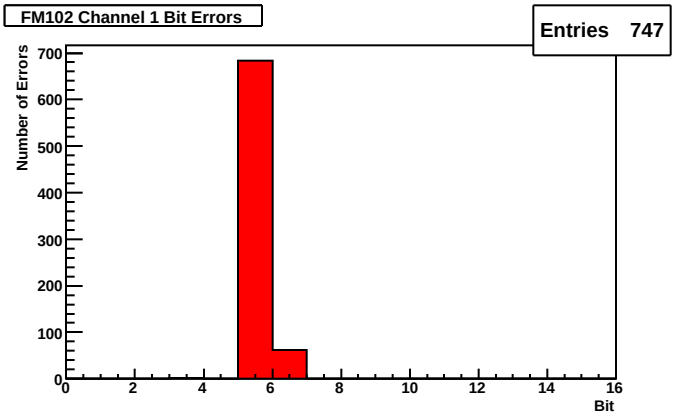

FM101 Channel 4 Bit Errors
Entries 237
Number of Errors
Bit

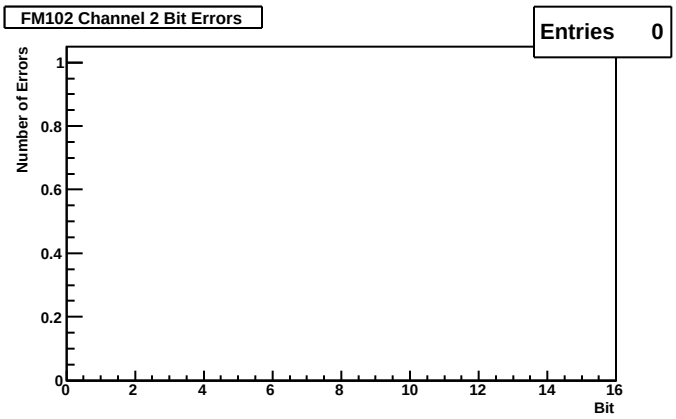

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

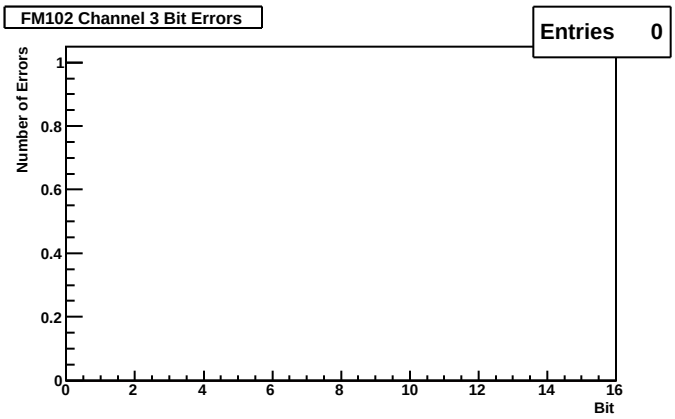

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

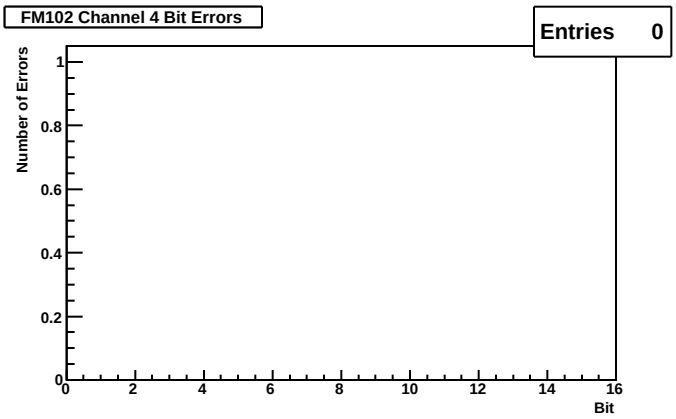

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

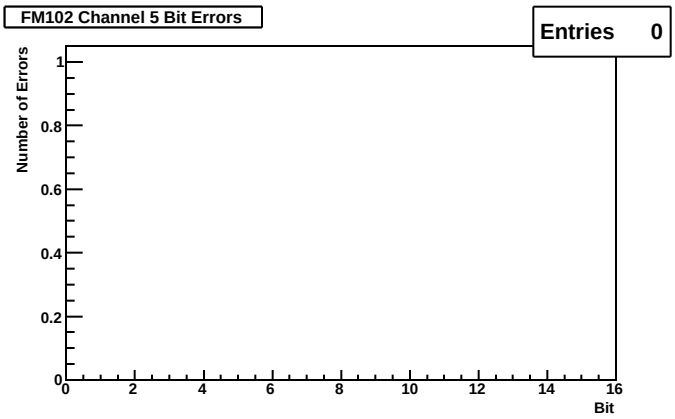

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

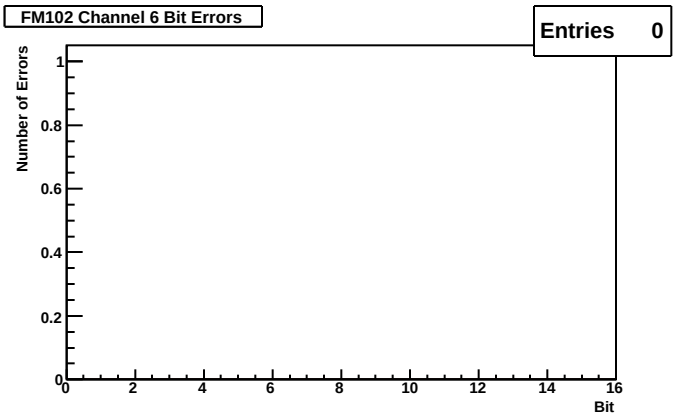

FM102 Channel 1 Bit Errors
Entries 747
Number of Errors
Bit

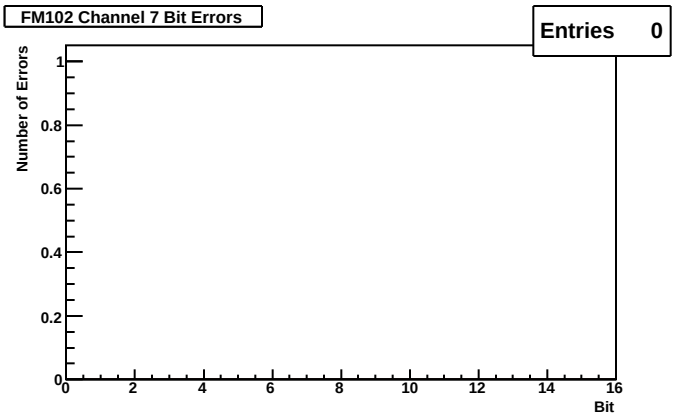

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

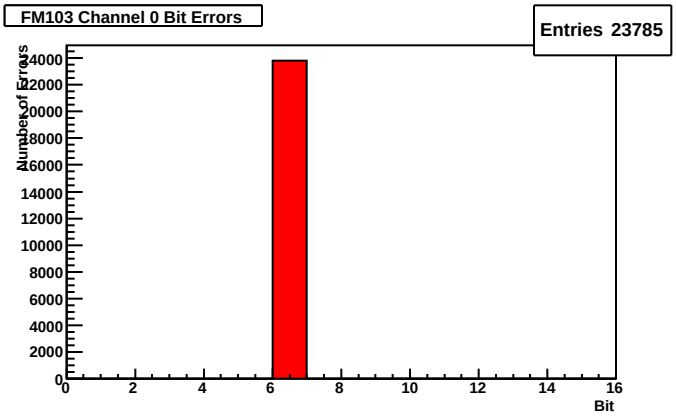

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

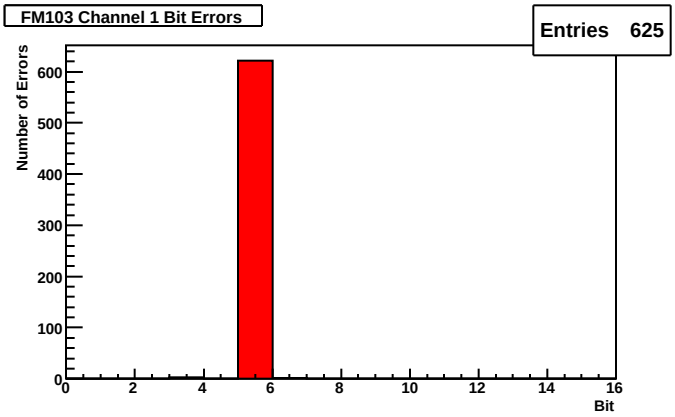

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

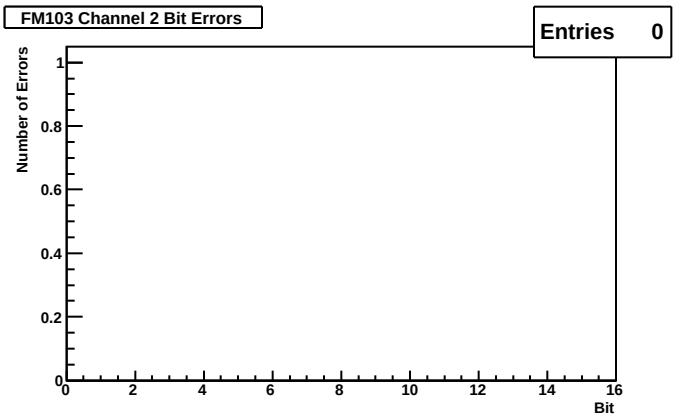

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

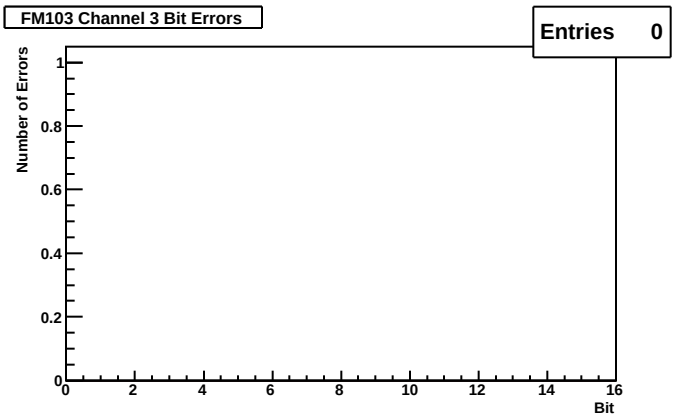

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

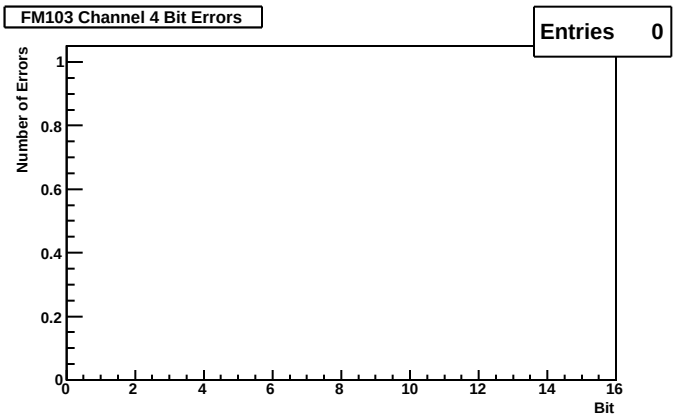

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

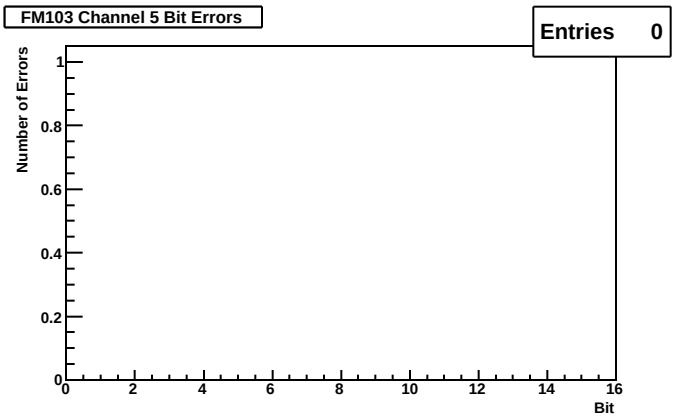

FM103 Channel 0 Bit Errors
Entries 23785
Number of Errors
Bit

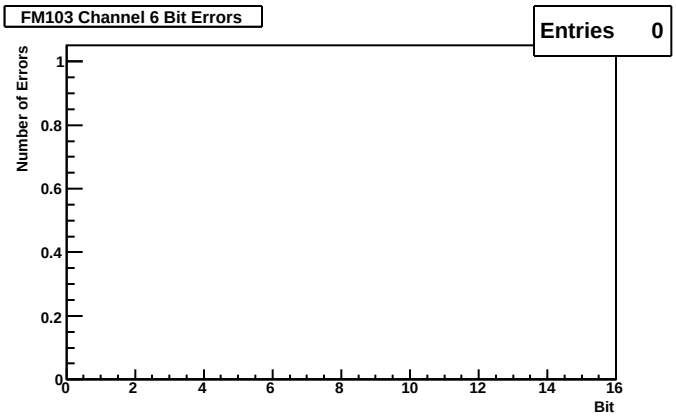

FM103 Channel 1 Bit Errors
Entries 625
Number of Errors
Bit

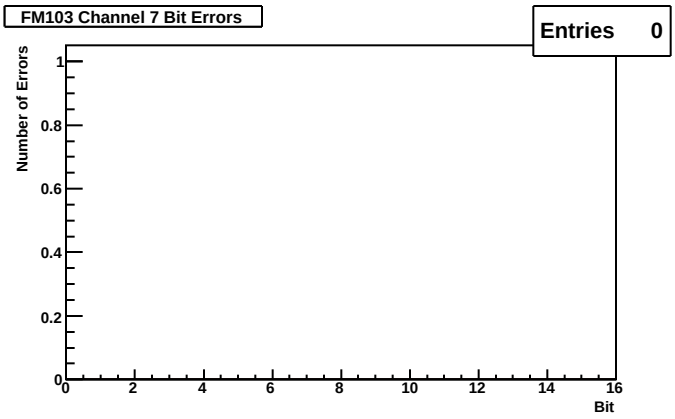

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

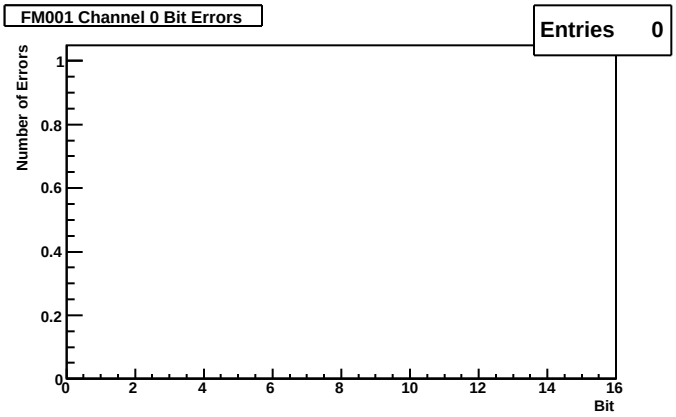

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

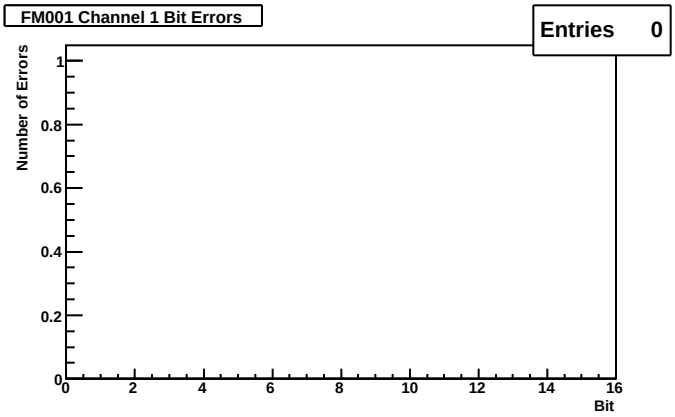

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

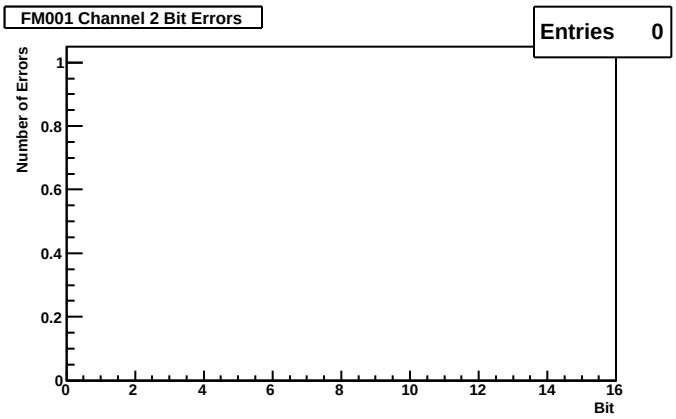

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

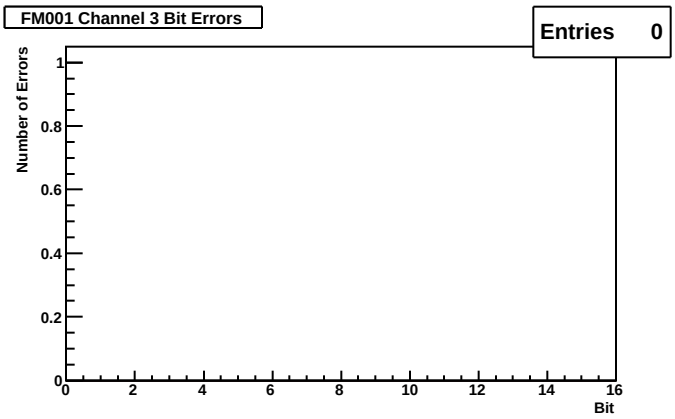

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

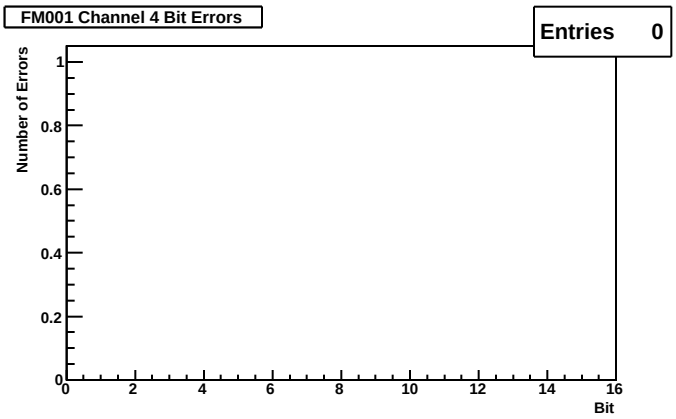

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

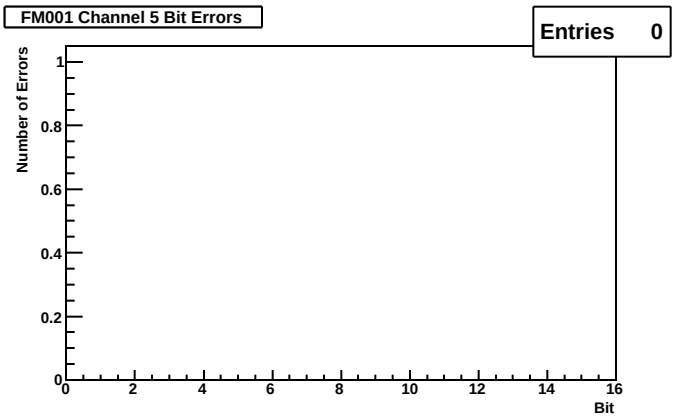

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

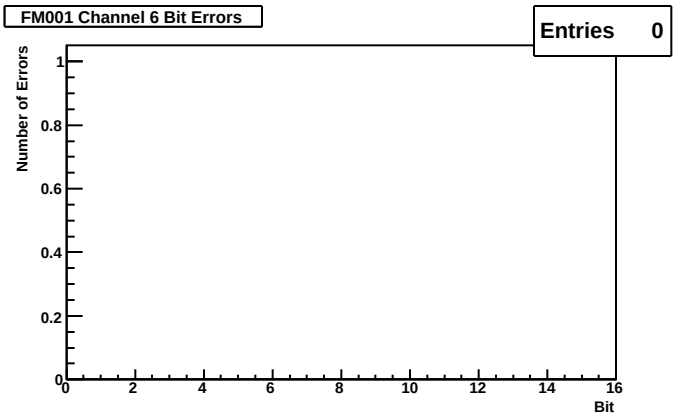

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

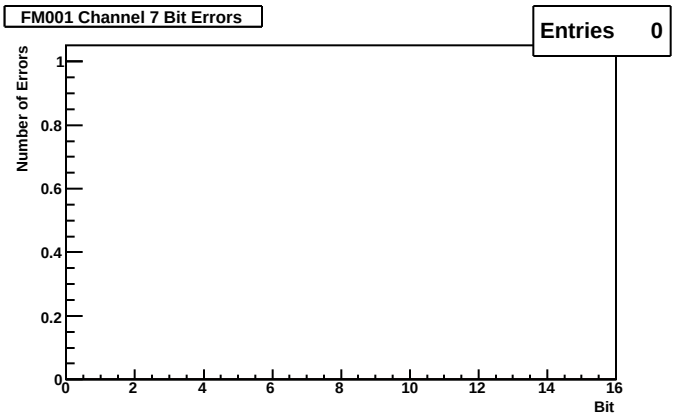

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

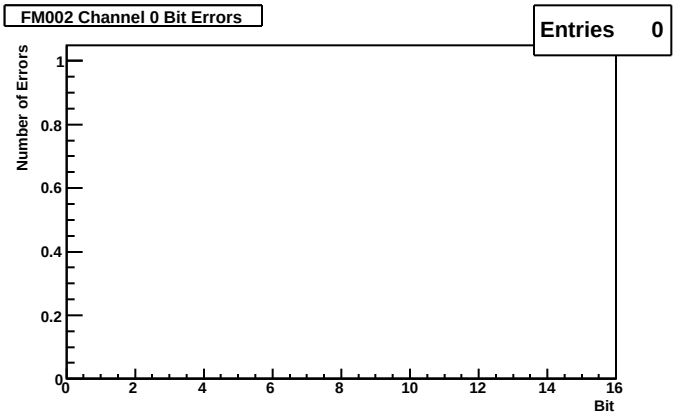

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

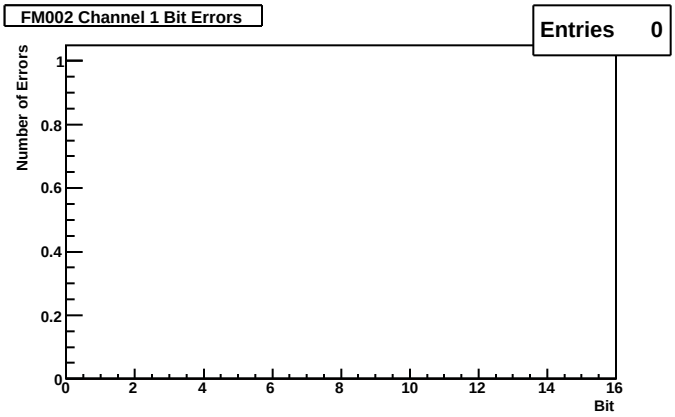

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

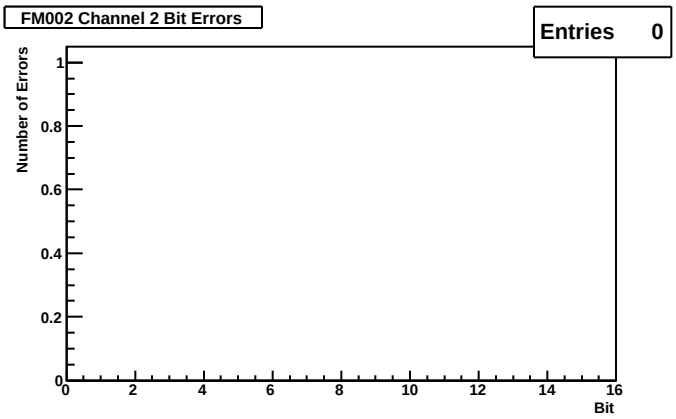

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

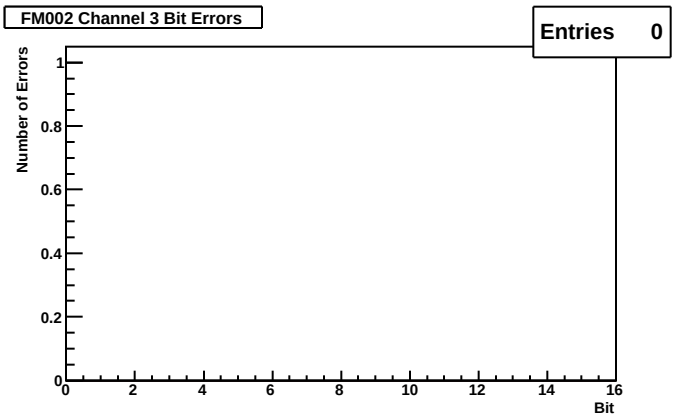

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

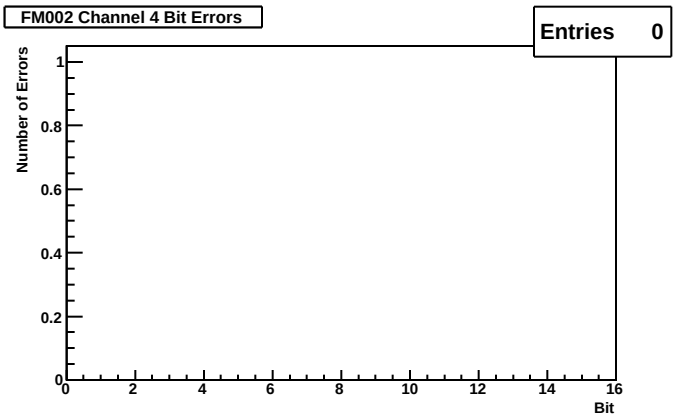

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

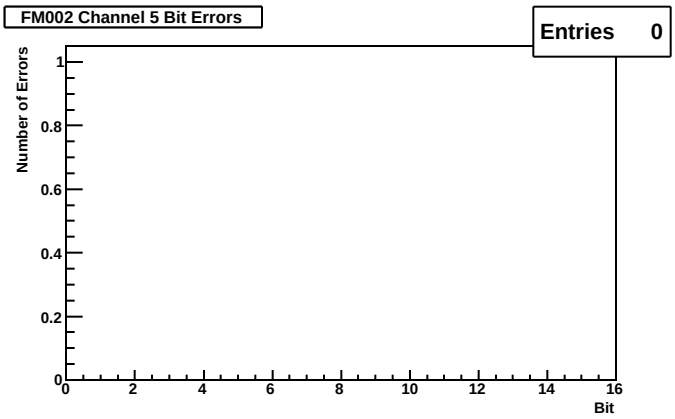

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

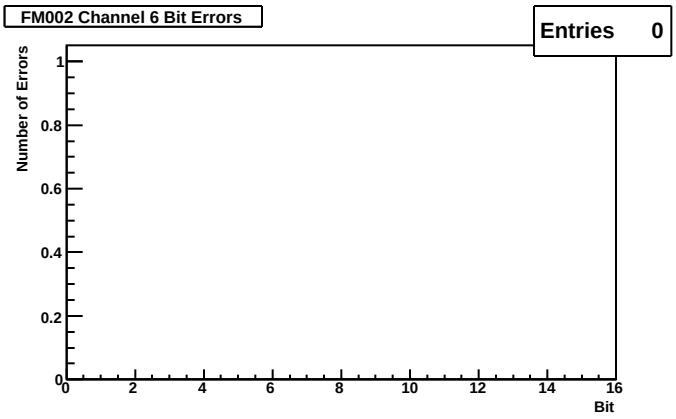

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

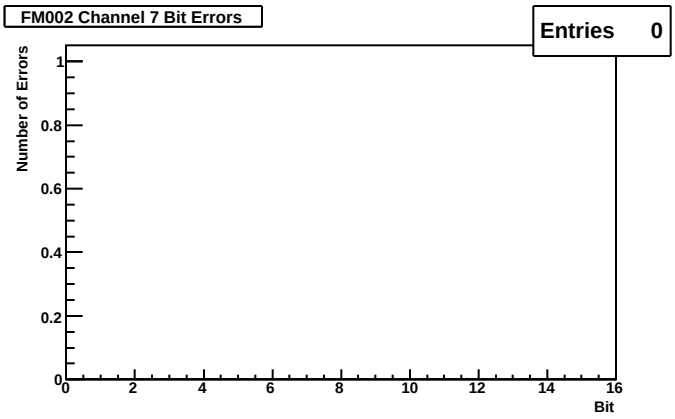

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

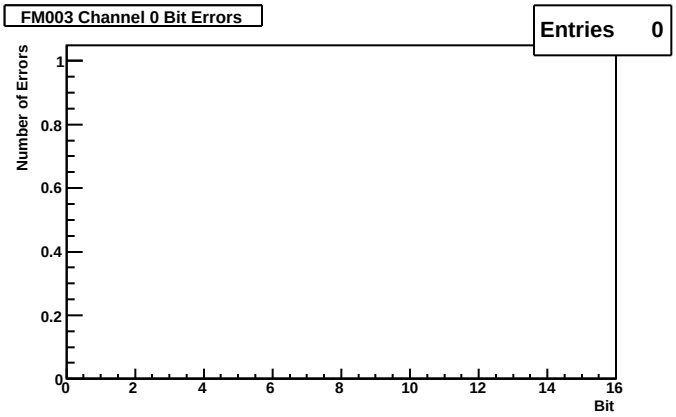

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

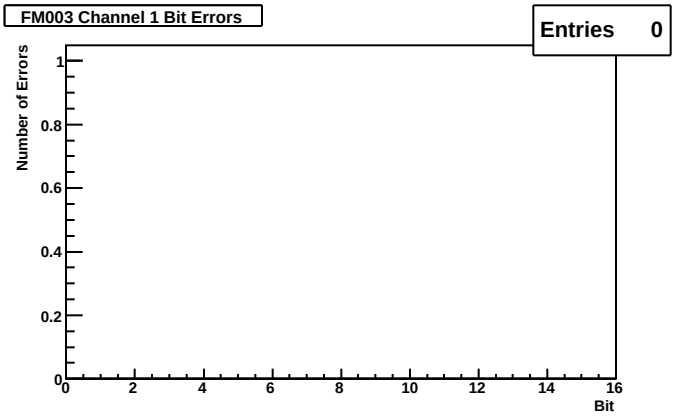

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

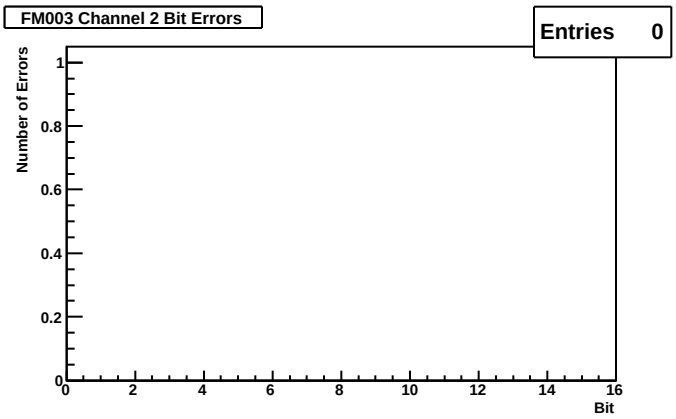

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

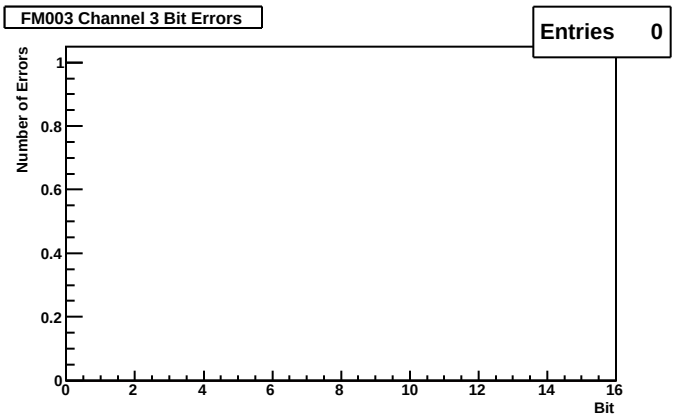

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

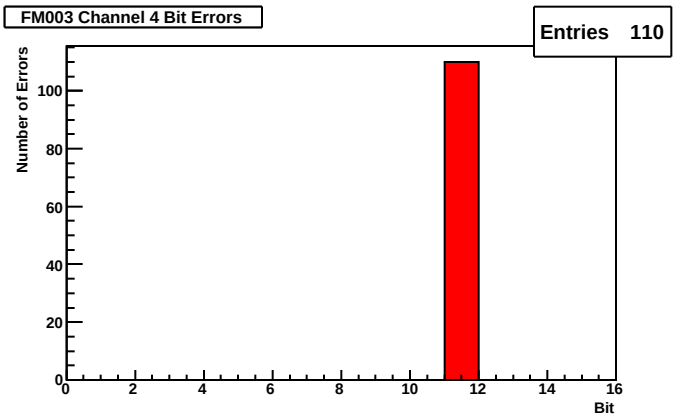

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

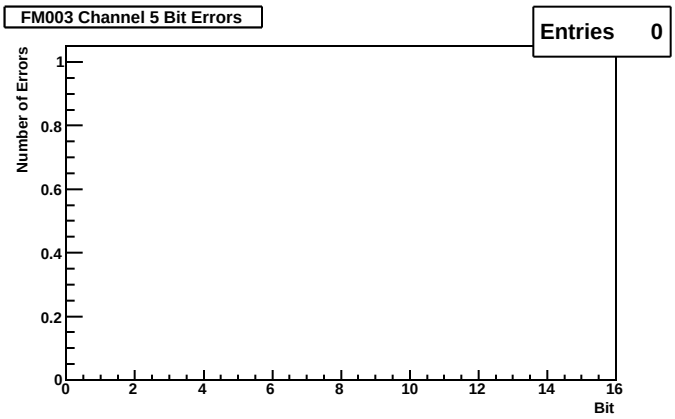

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

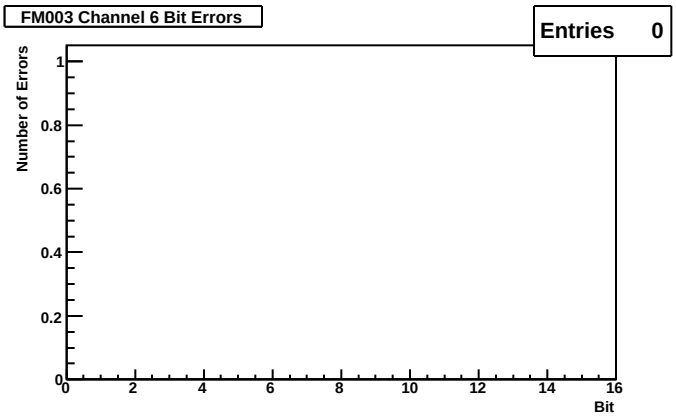

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

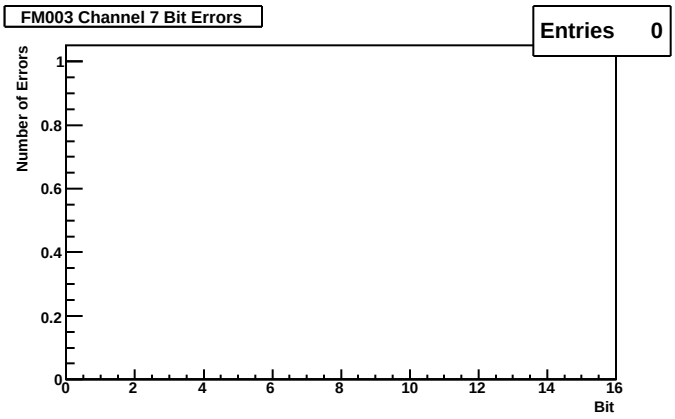

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

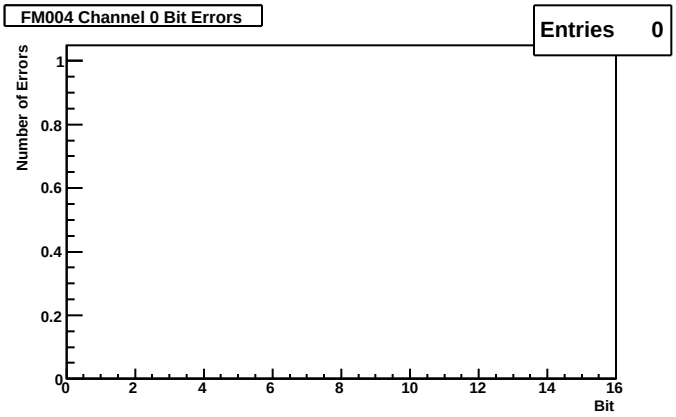

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

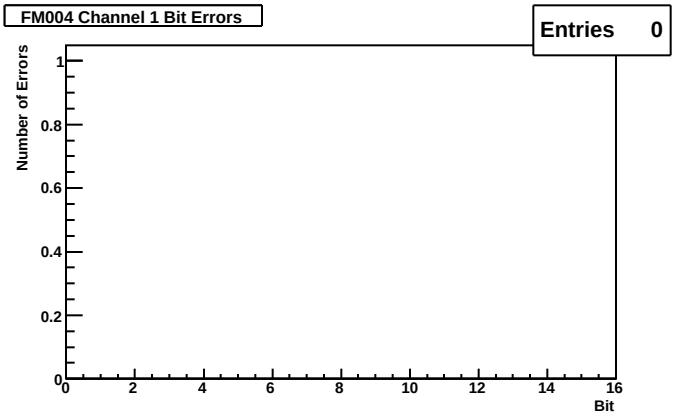

FM003 Channel 4 Bit Errors
Entries 110
Number of Errors
Bit

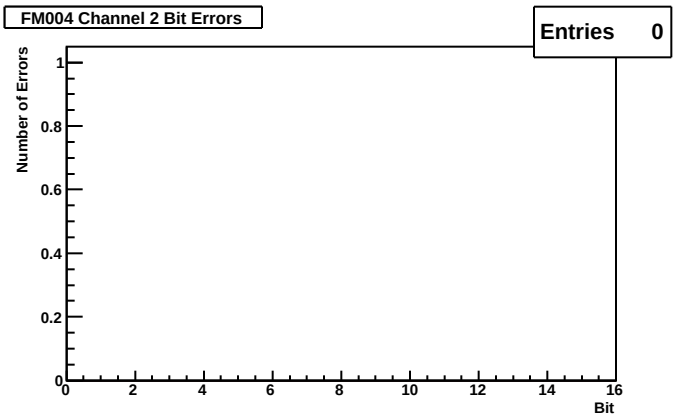

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

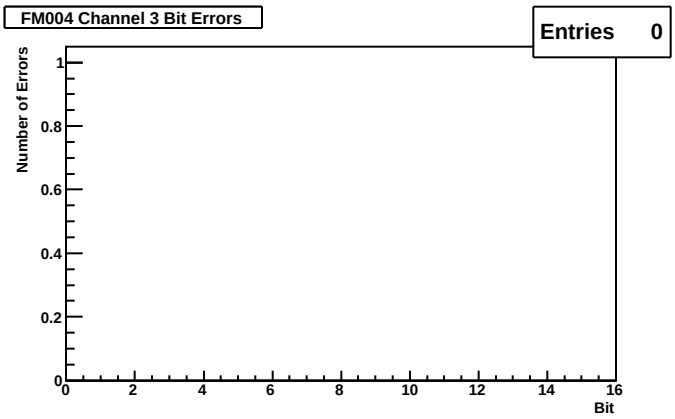

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

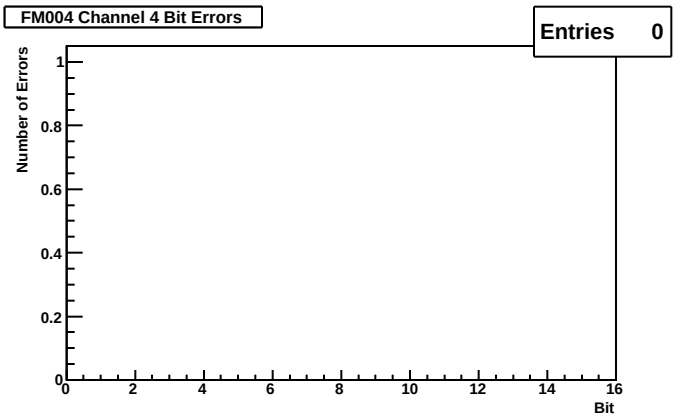

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

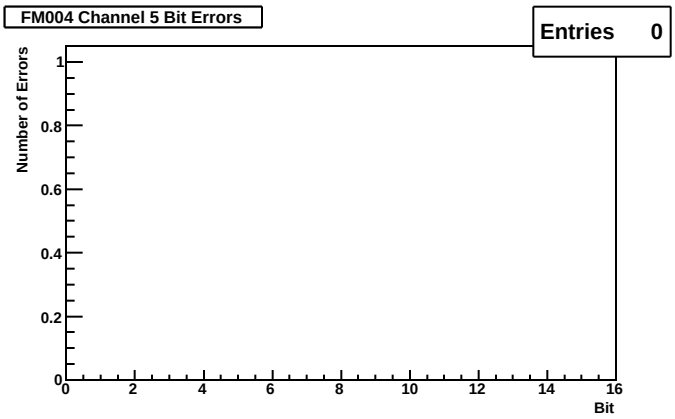

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

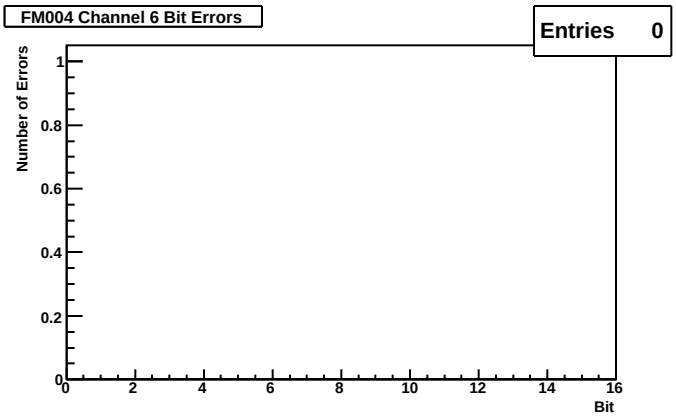

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

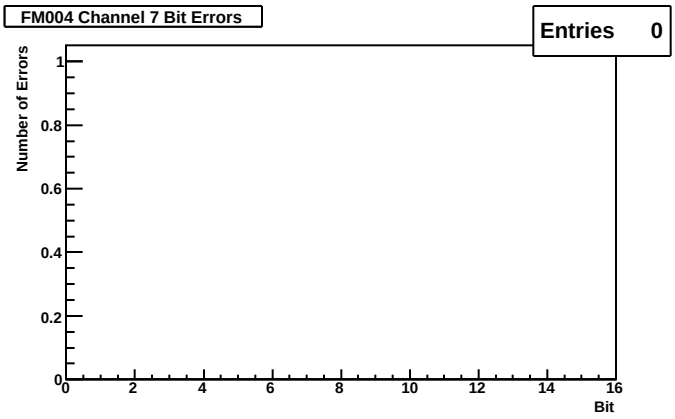

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

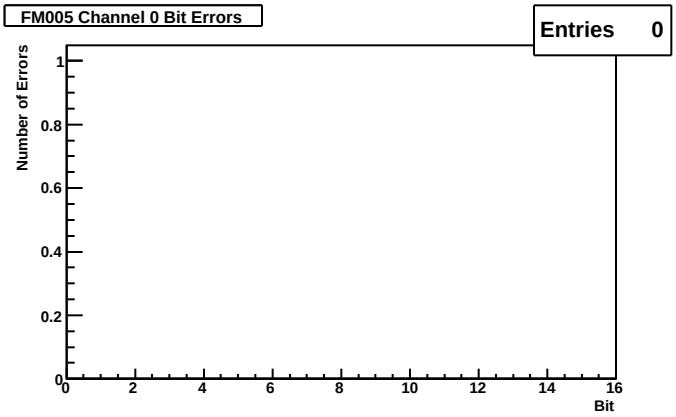

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

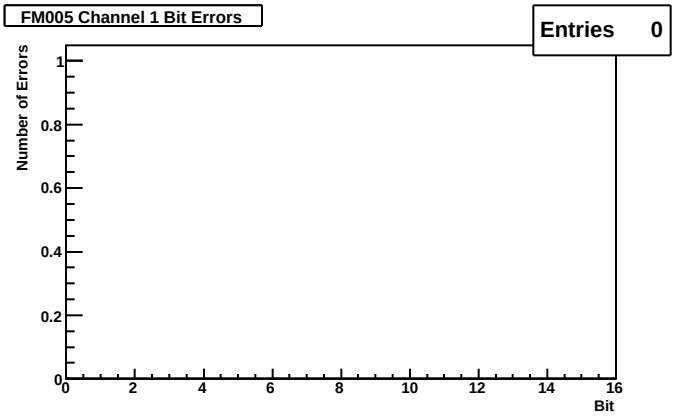

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

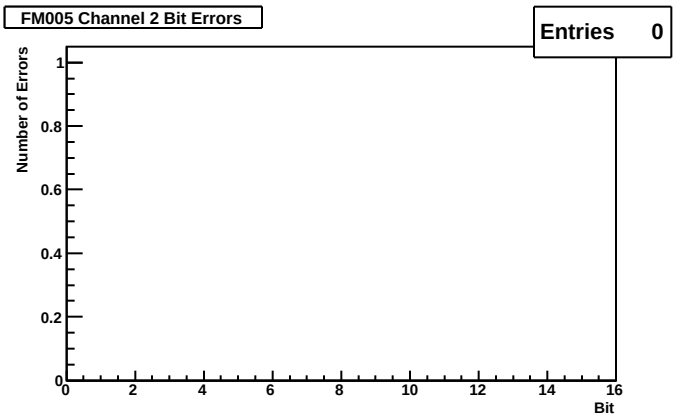

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

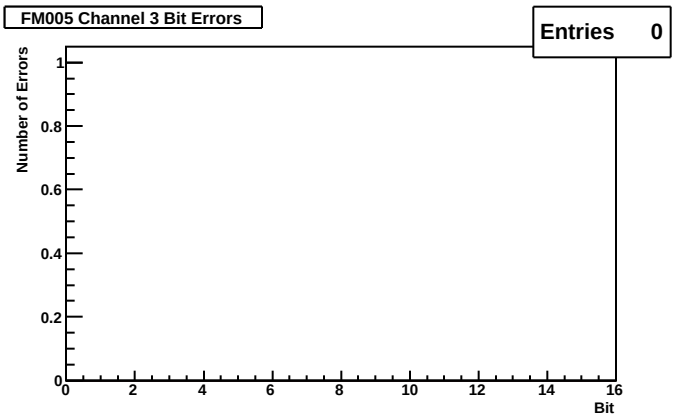

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

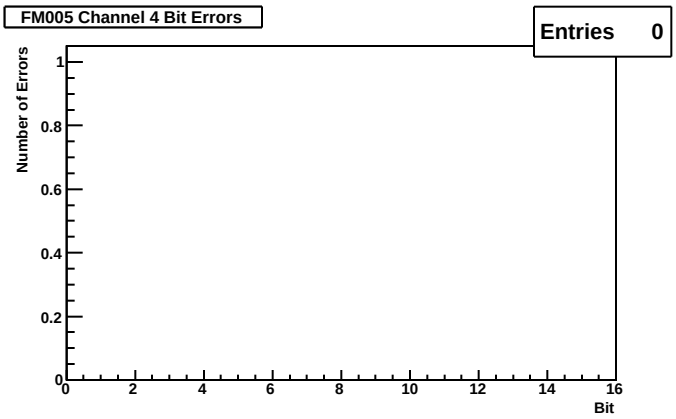

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

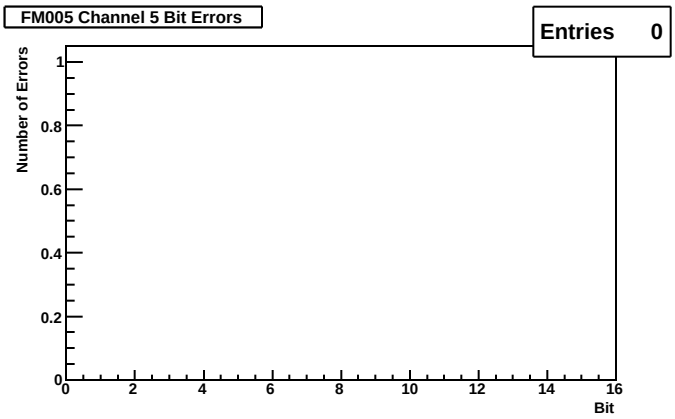

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

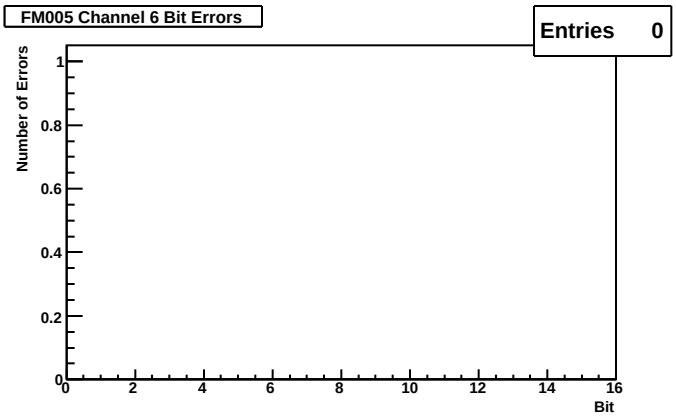

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

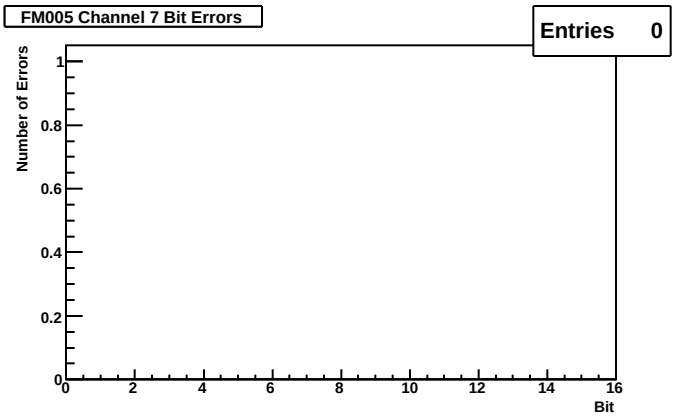

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

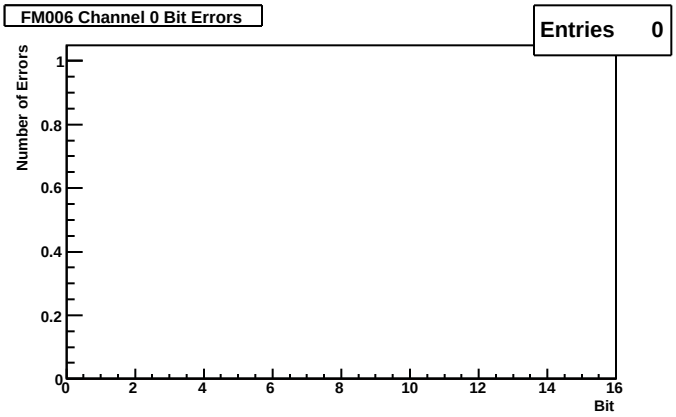

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

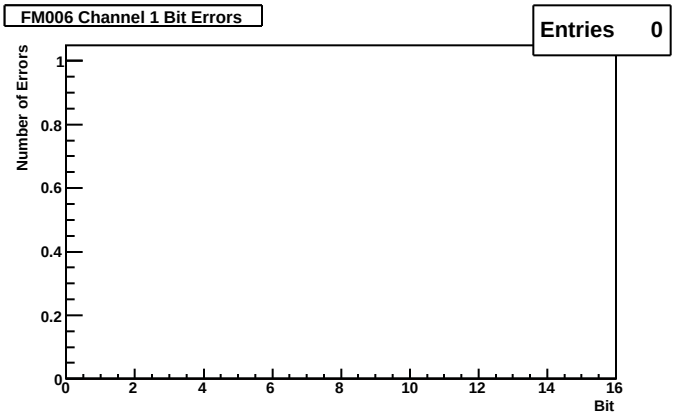

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

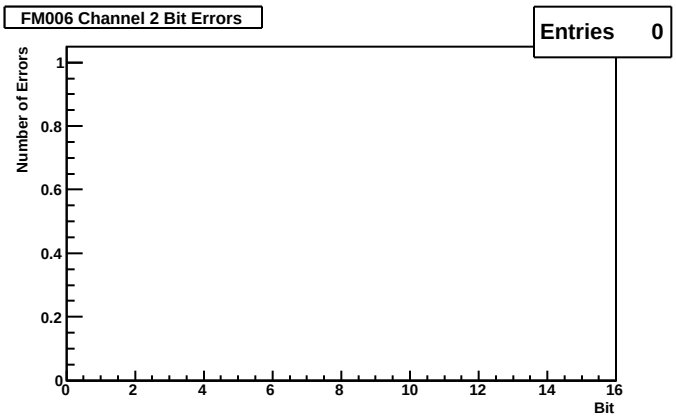

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

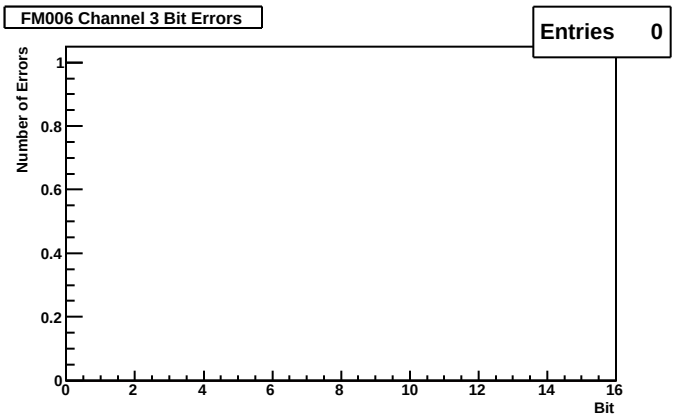

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

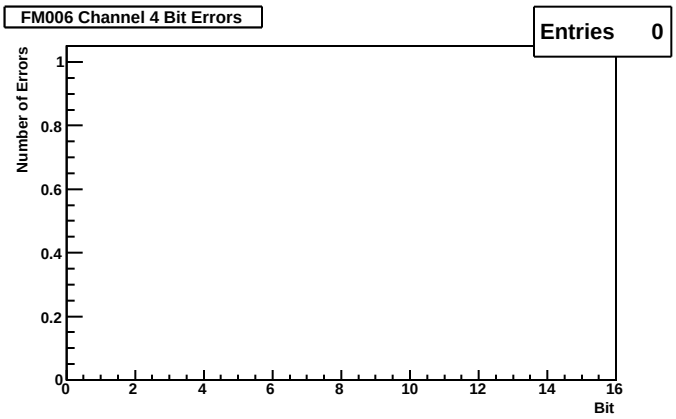

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

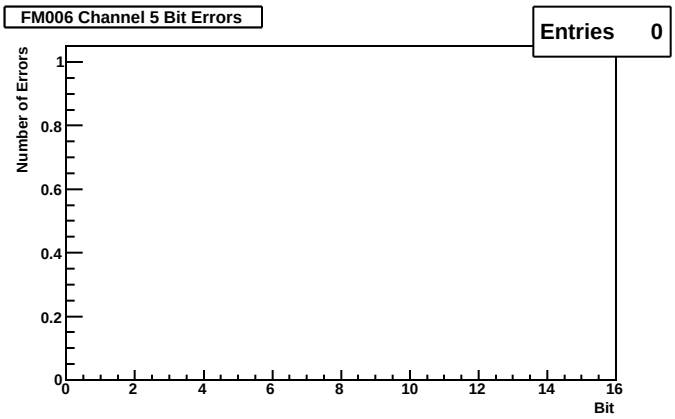

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

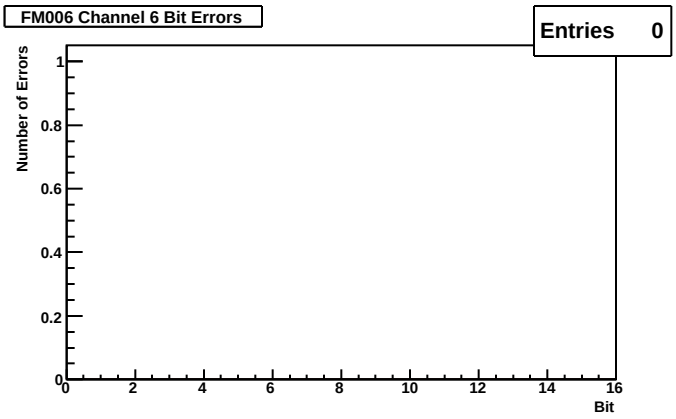

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

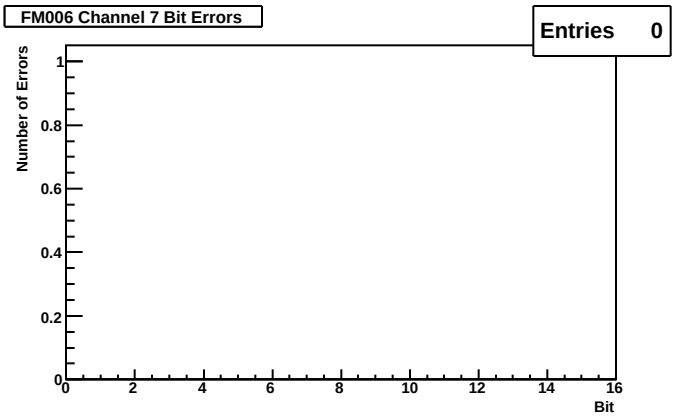

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

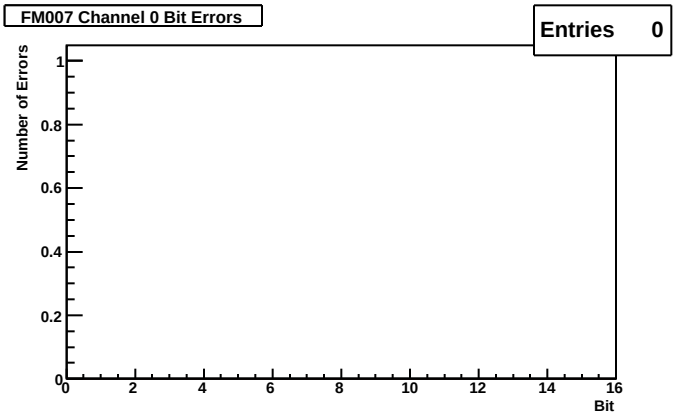

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

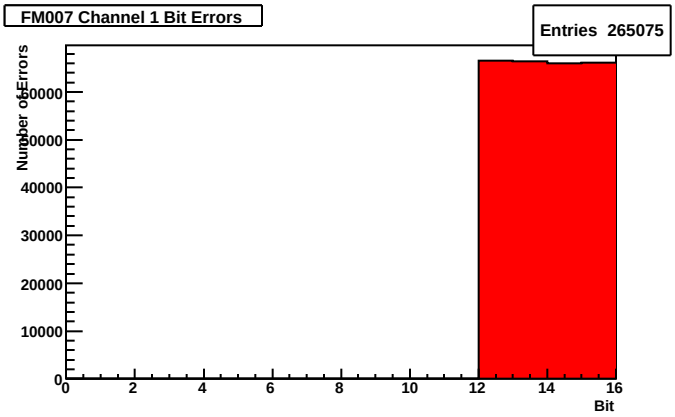

FM006 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

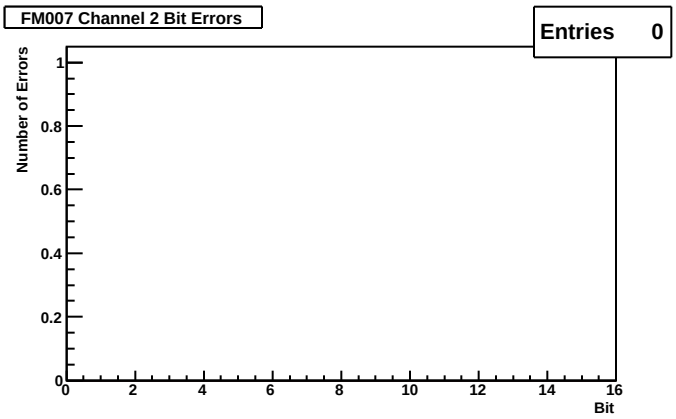

FM006 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

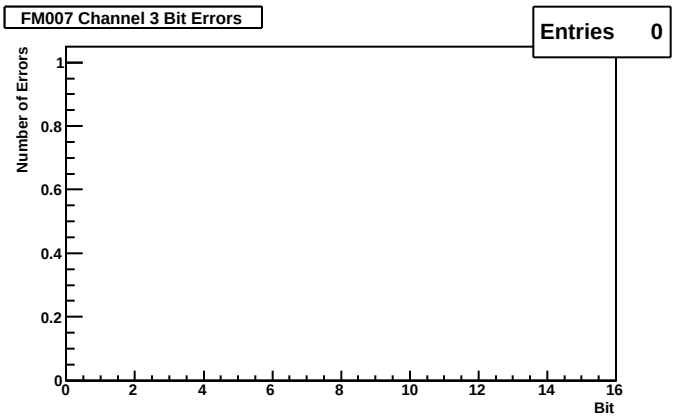

FM006 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

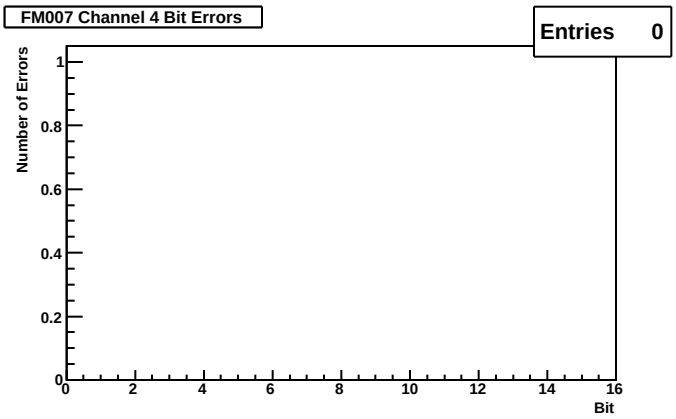

FM006 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

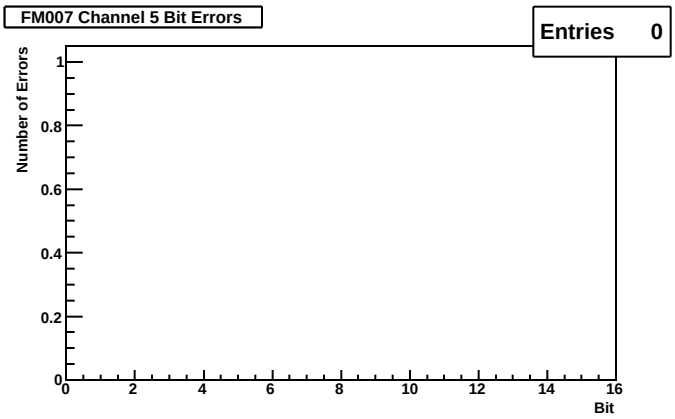

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

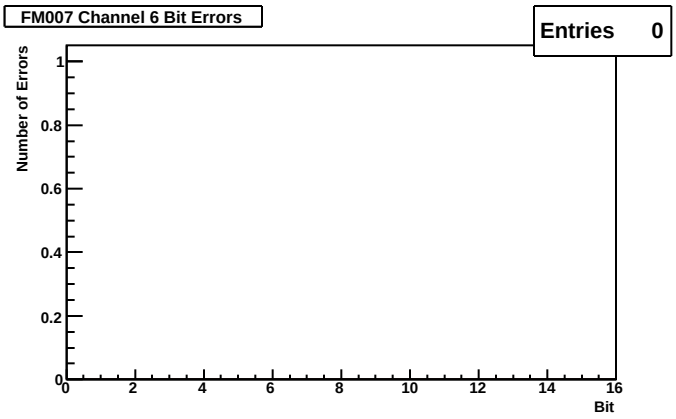

FM007 Channel 1 Bit Errors
Entries 265075
Number of Errors
Bit

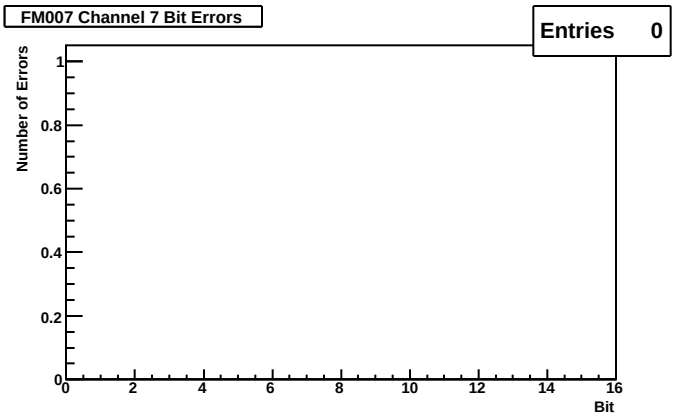

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

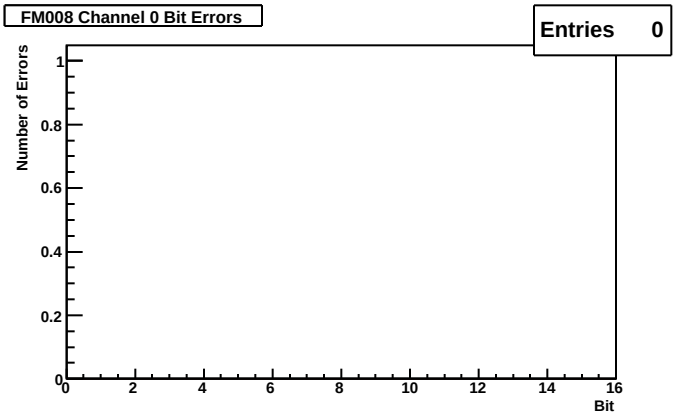

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

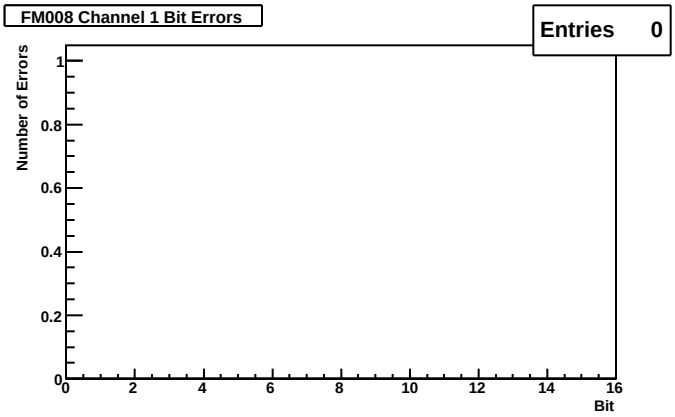

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

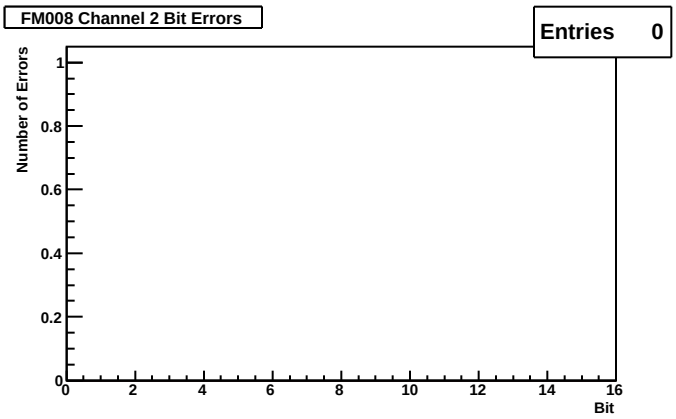

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

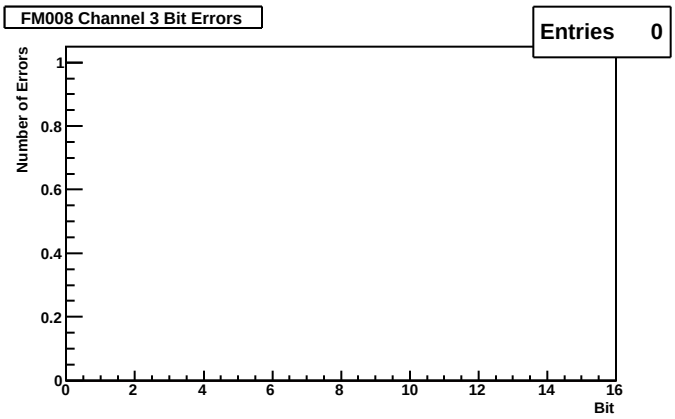

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

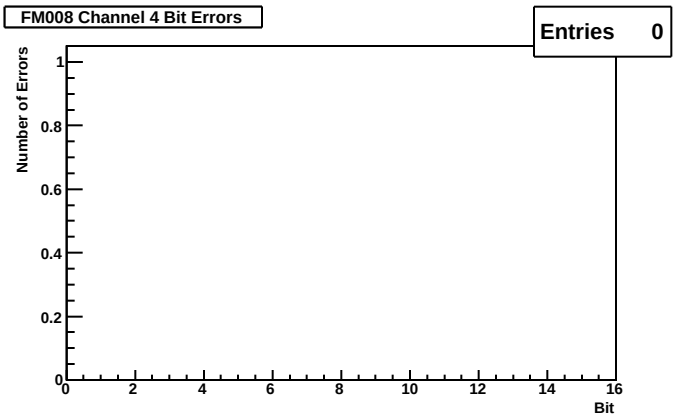

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

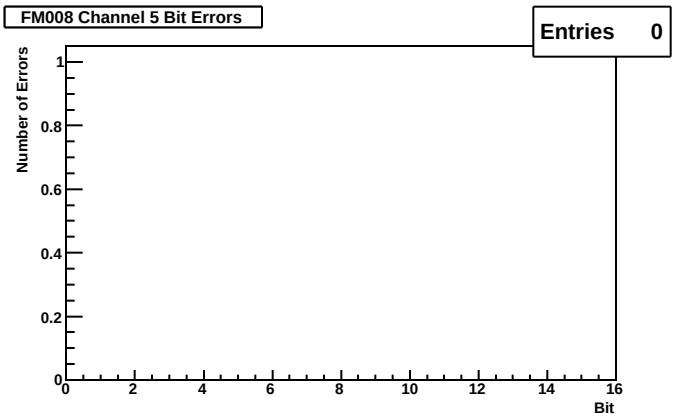

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

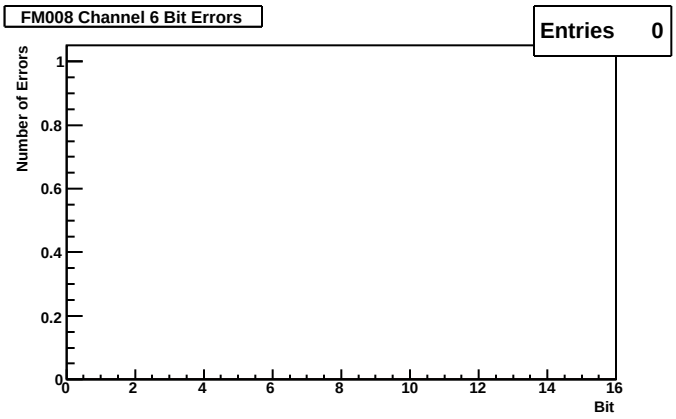

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

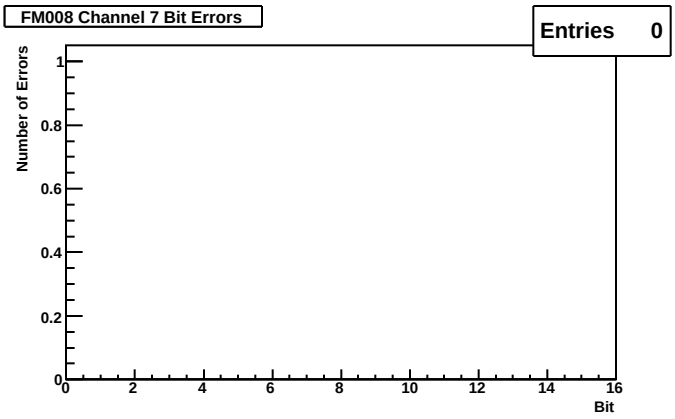

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

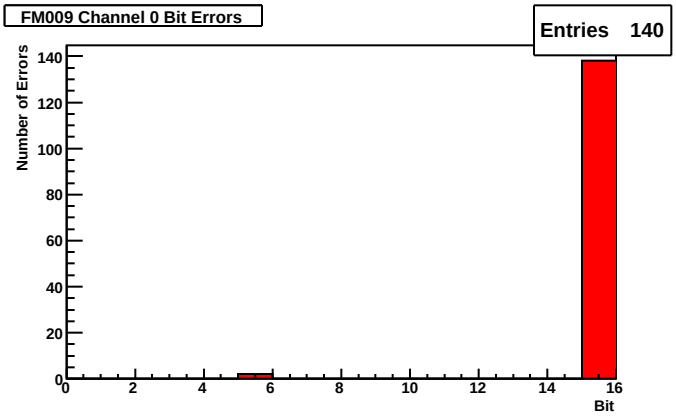

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

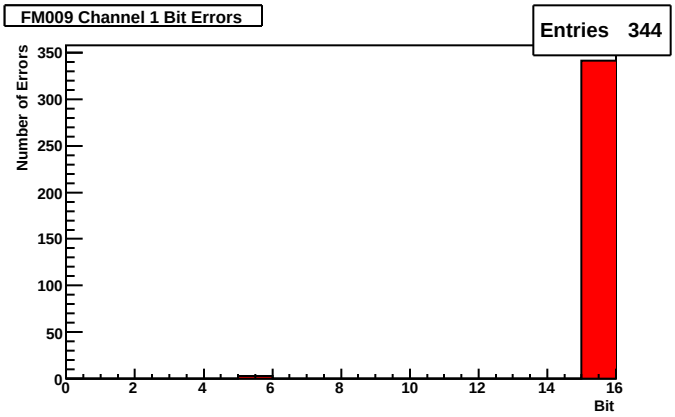

FM008 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

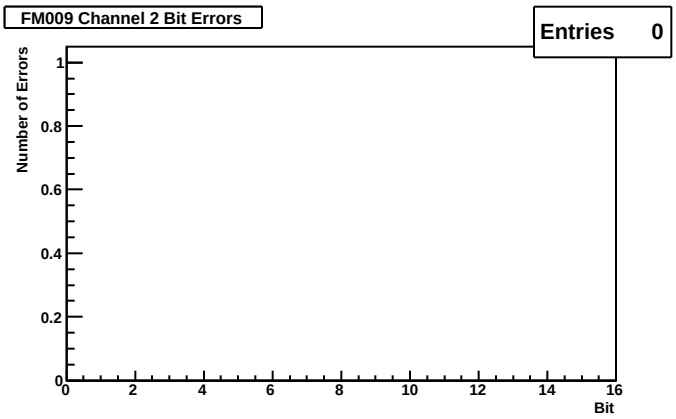

FM008 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

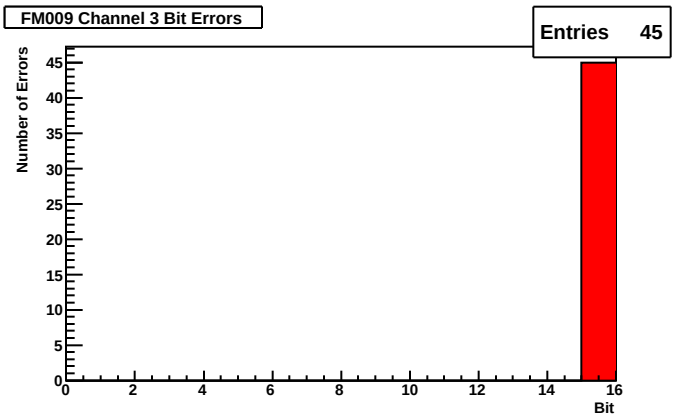

FM008 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

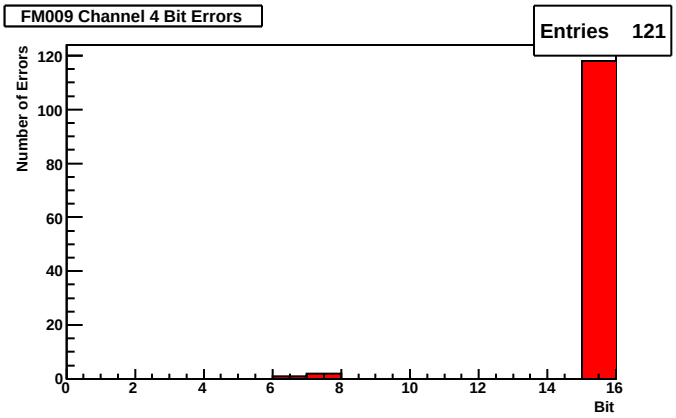

FM008 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

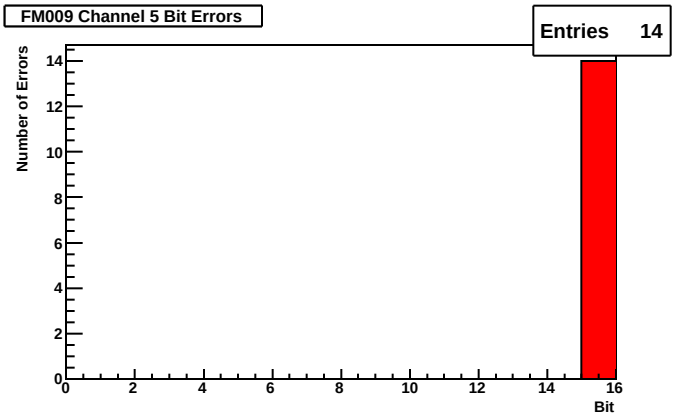

FM009 Channel 0 Bit Errors
Entries 140
Number of Errors
Bit

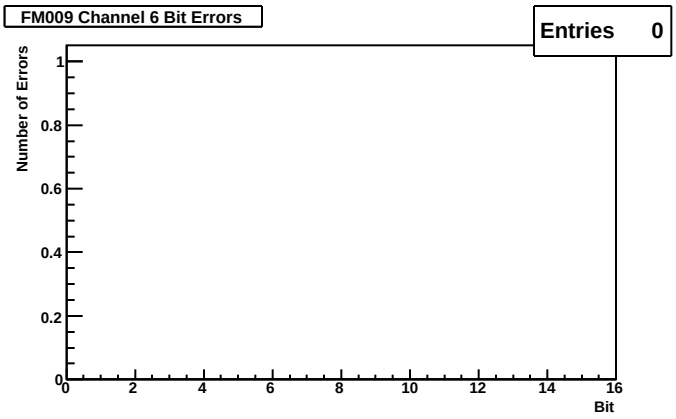

FM009 Channel 1 Bit Errors
Entries 344
Number of Errors
Bit

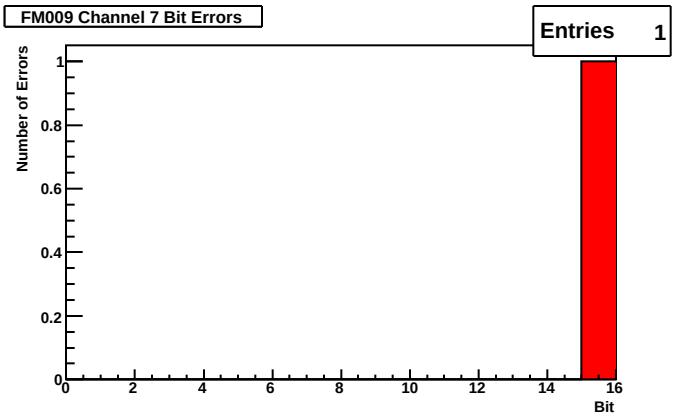

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

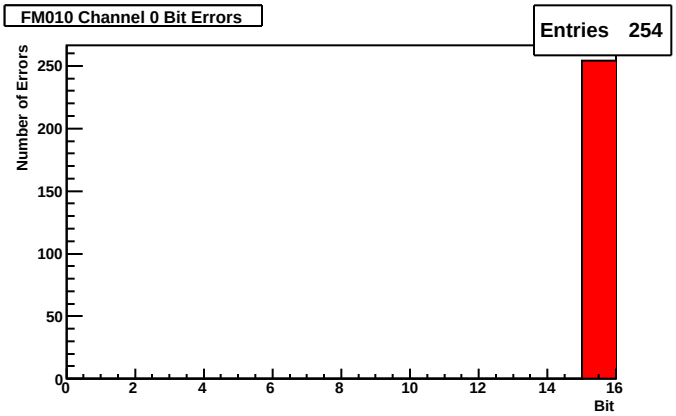

FM009 Channel 3 Bit Errors
Entries 45
Number of Errors
Bit

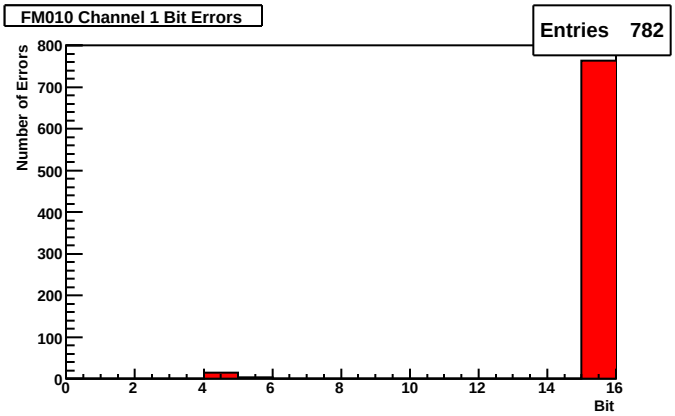

FM009 Channel 4 Bit Errors
Entries 121
Number of Errors
Bit

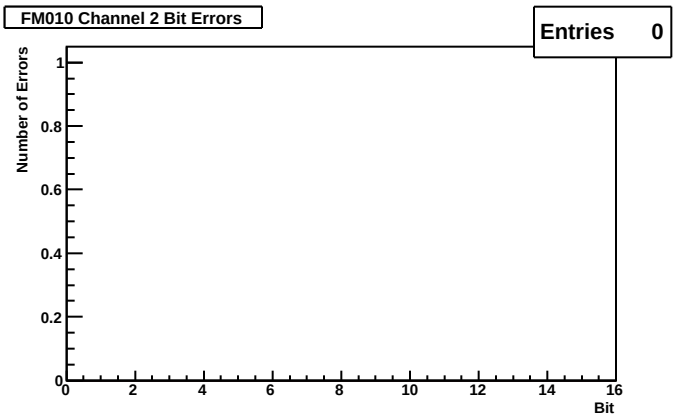

FM009 Channel 5 Bit Errors
Entries 14
Number of Errors
Bit

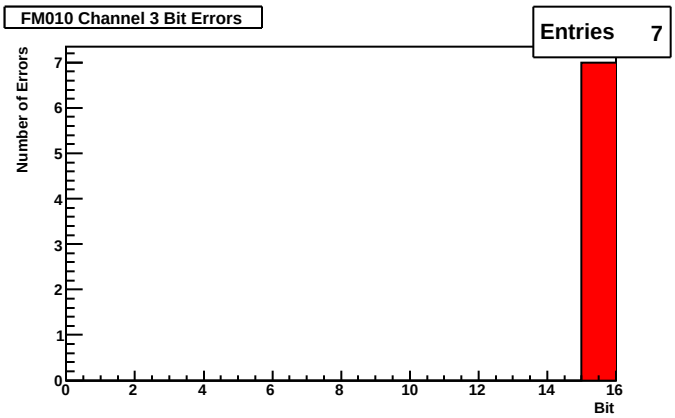

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

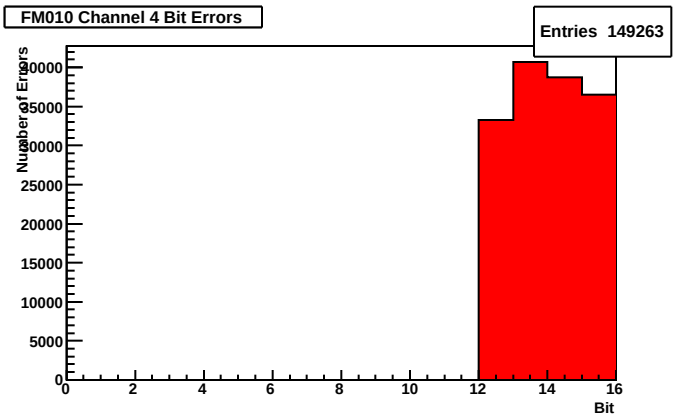

FM009 Channel 7 Bit Errors
Entries 1
Number of Errors
Bit


FM010 Channel 0 Bit Errors
Entries 254
Number of Errors
Bit

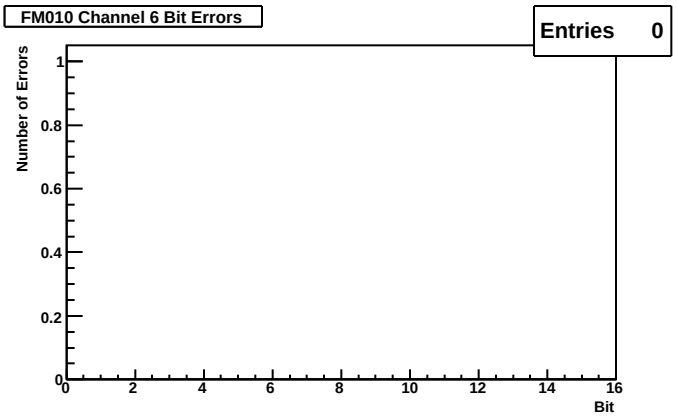

FM010 Channel 1 Bit Errors
Entries 782
Number of Errors
Bit

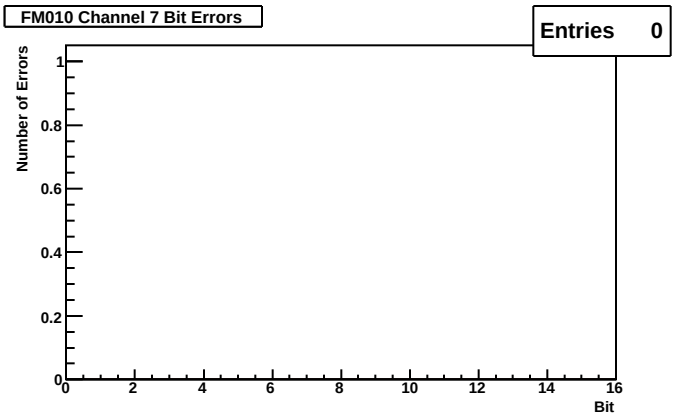

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

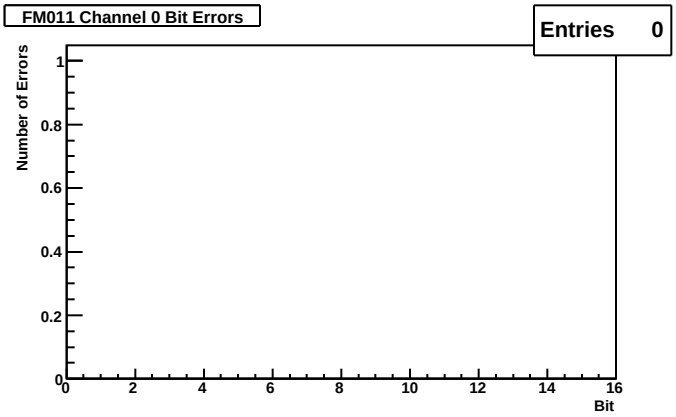

FM010 Channel 3 Bit Errors
Entries 7
Number of Errors
Bit

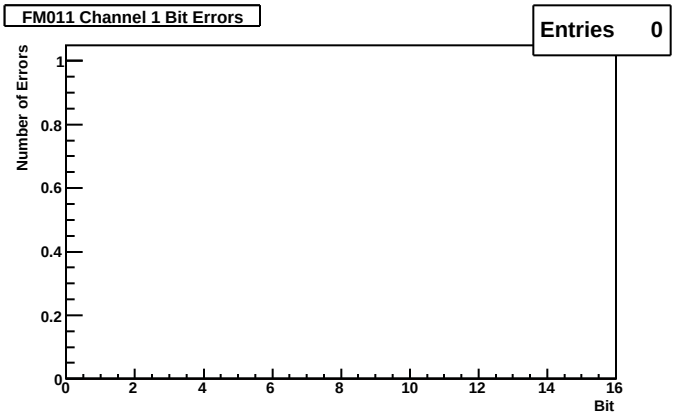

FM010 Channel 4 Bit Errors
Entries 149263
Number of Errors
Bit

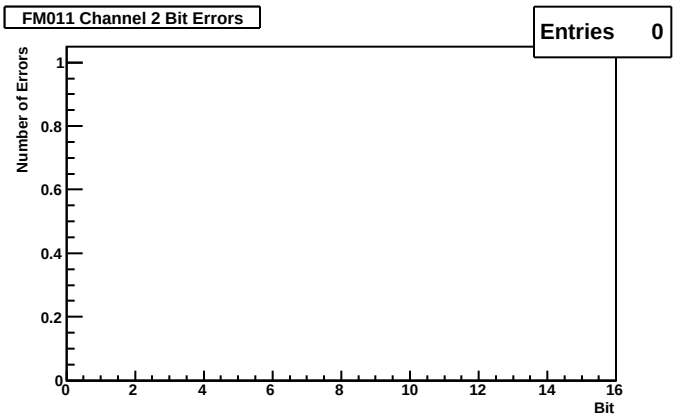
FM010 Channel 5 Bit Errors
Entries110188
Number of Errors
Bit

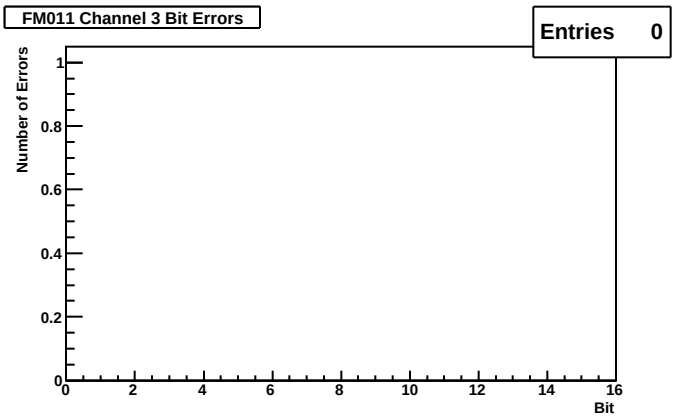

FM010 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

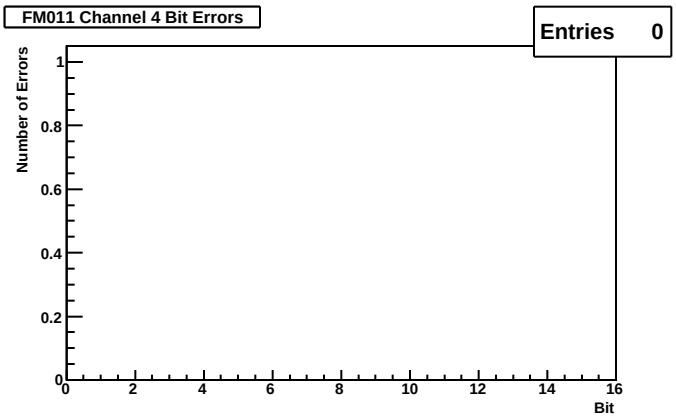

FM010 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

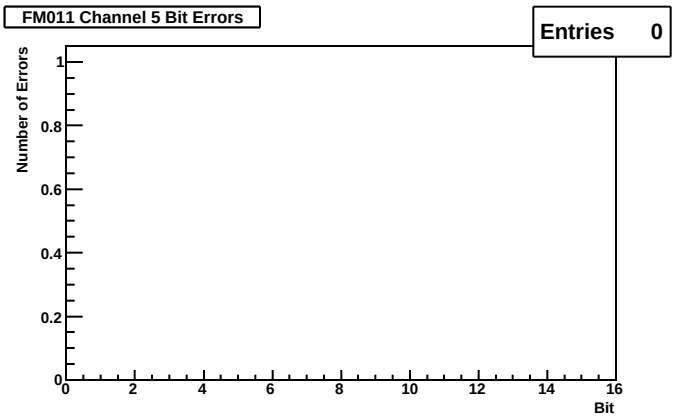

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

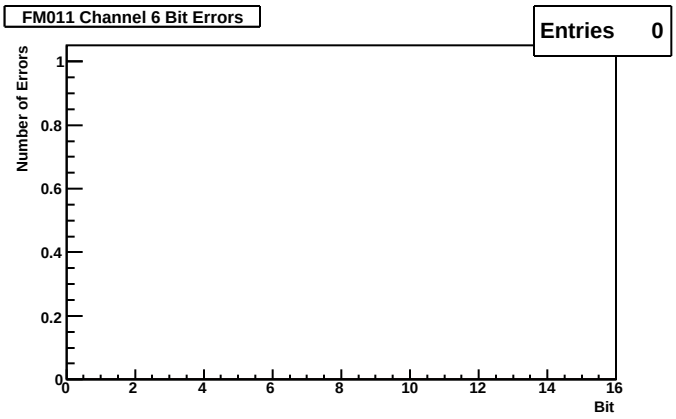

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

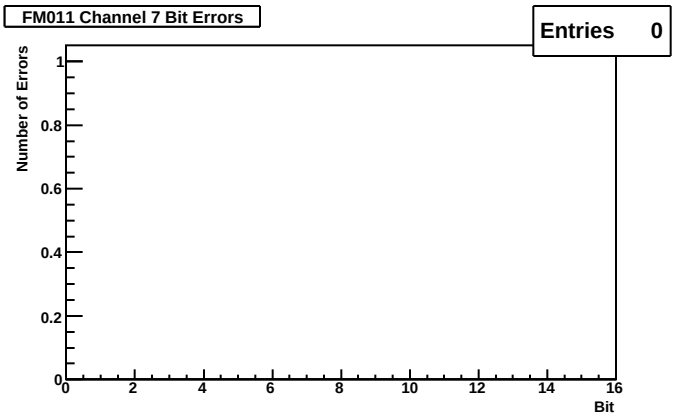

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

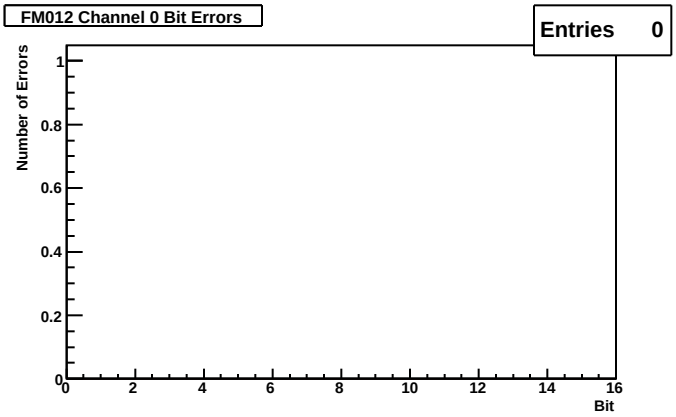

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

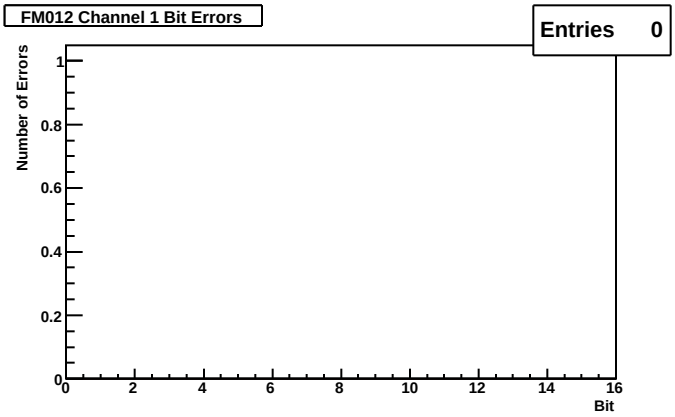

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

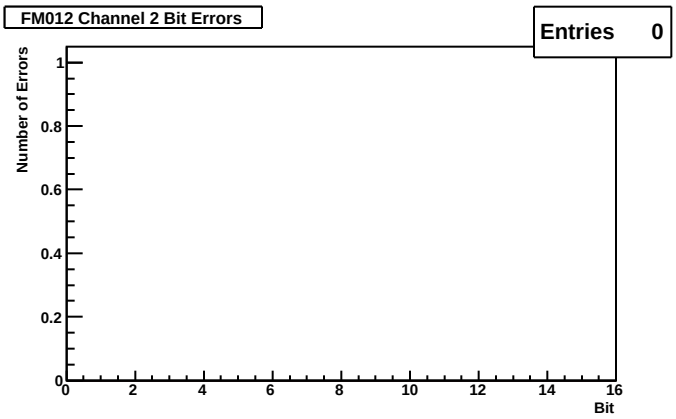

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

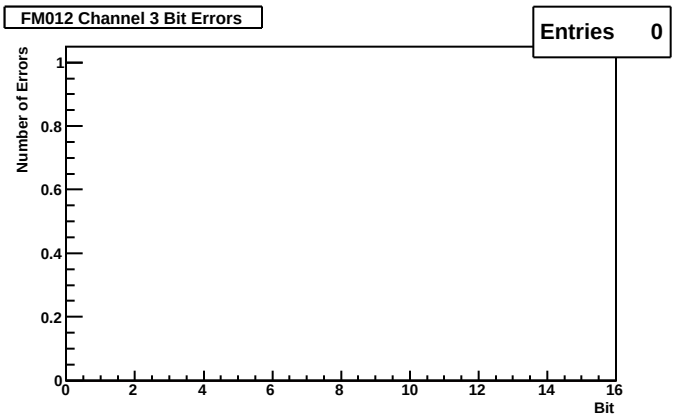

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

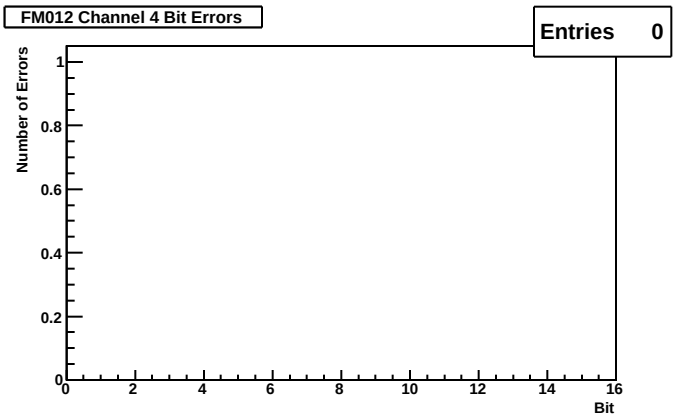

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

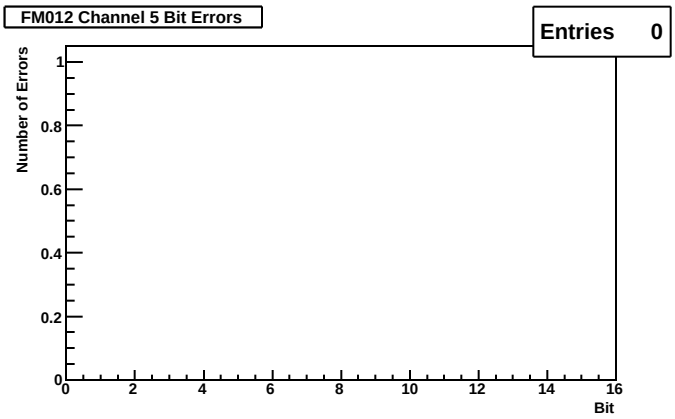

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

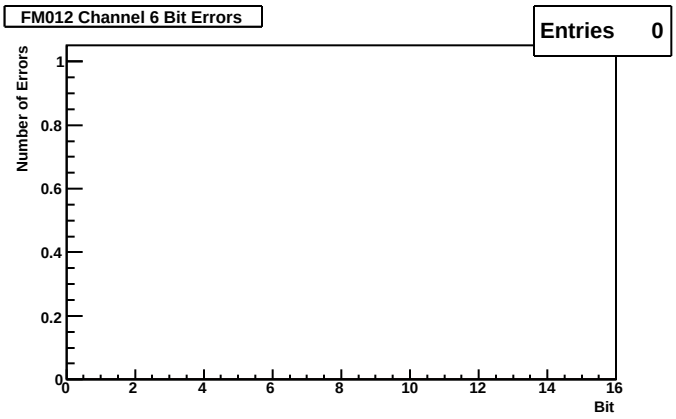

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

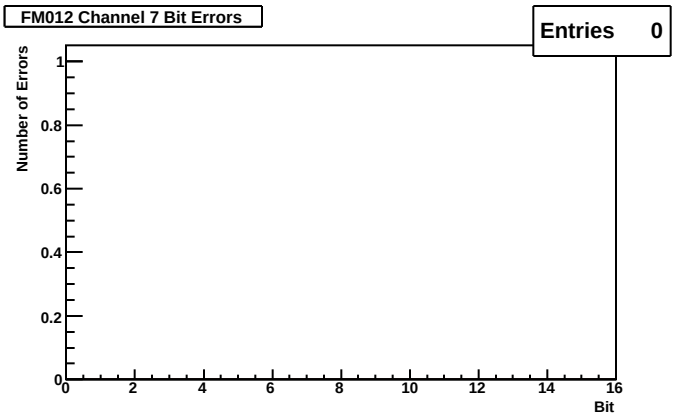

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit