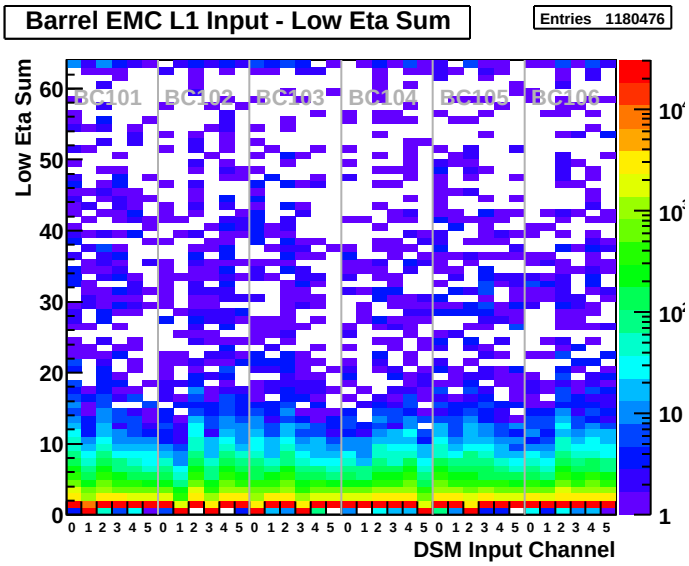
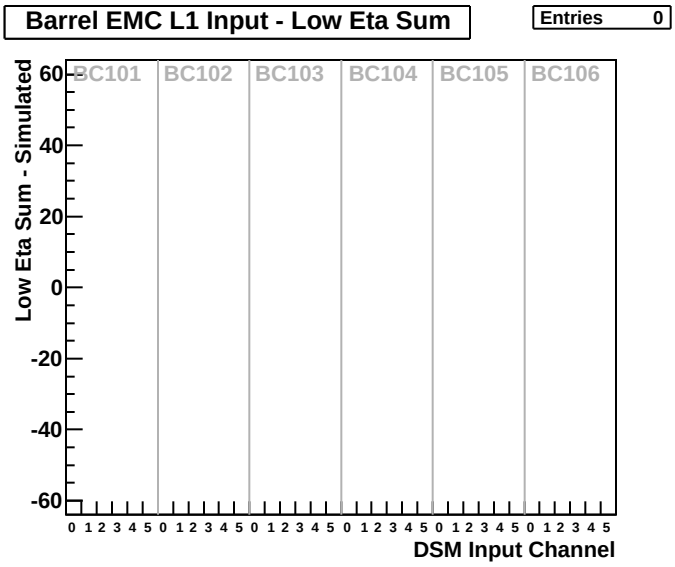
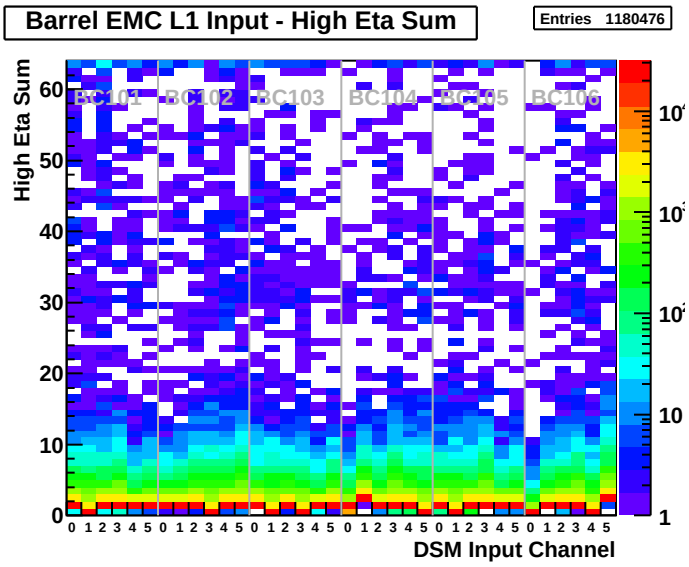
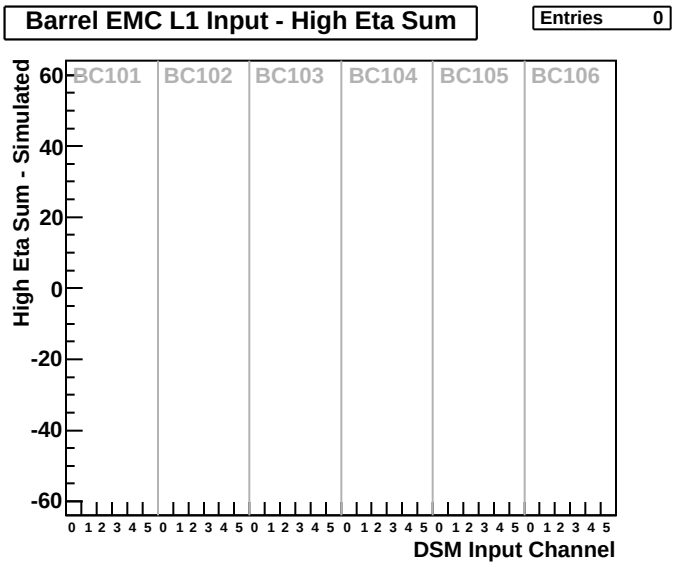
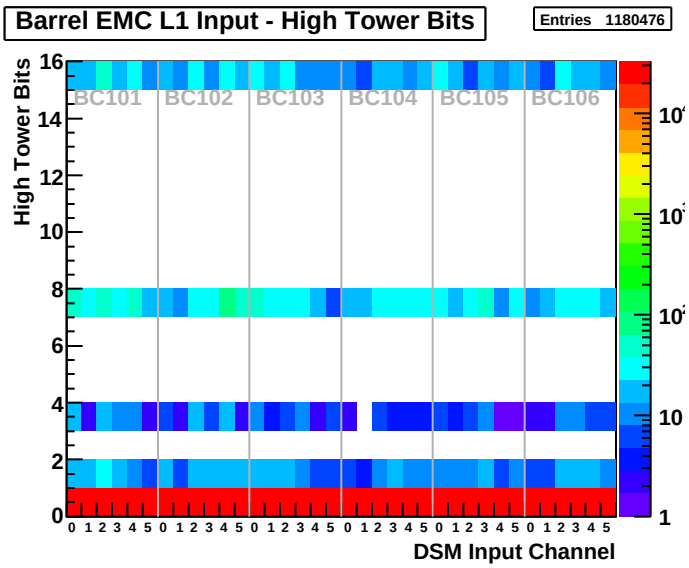
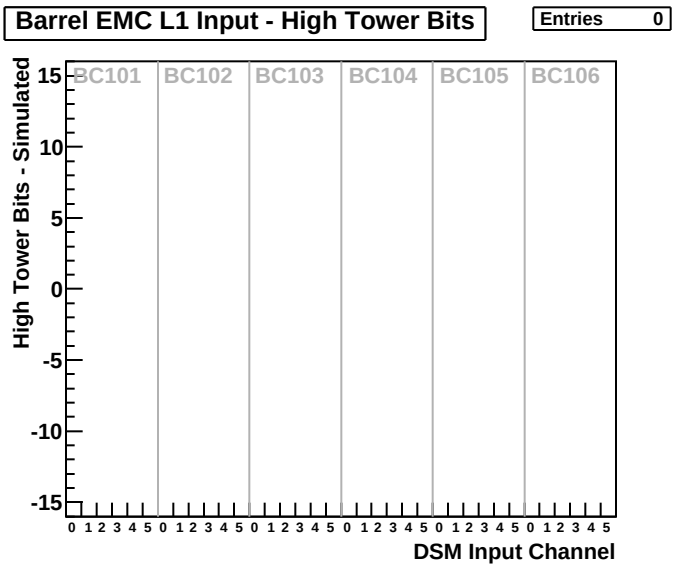

Barrel EMC L1 Input - Low Eta Sum
Entries 1180476
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

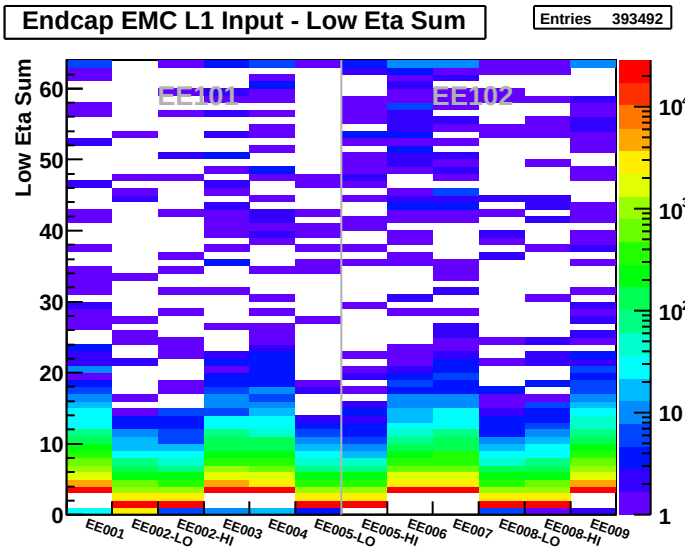

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

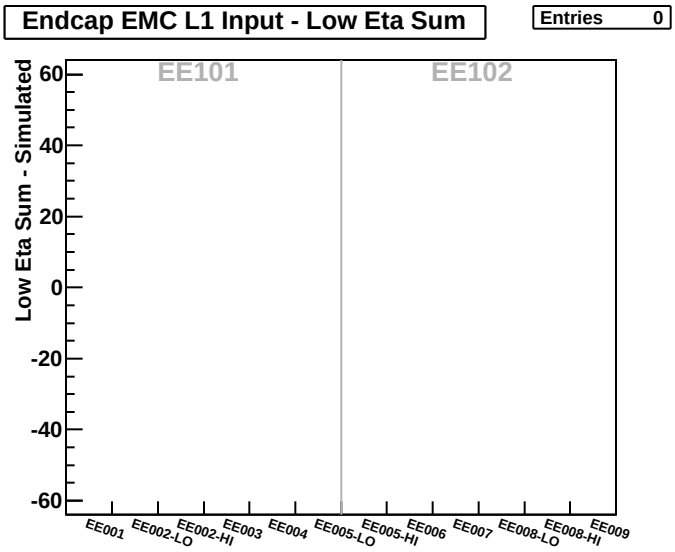

Barrel EMC L1 Input - High Eta Sum
Entries 1180476
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

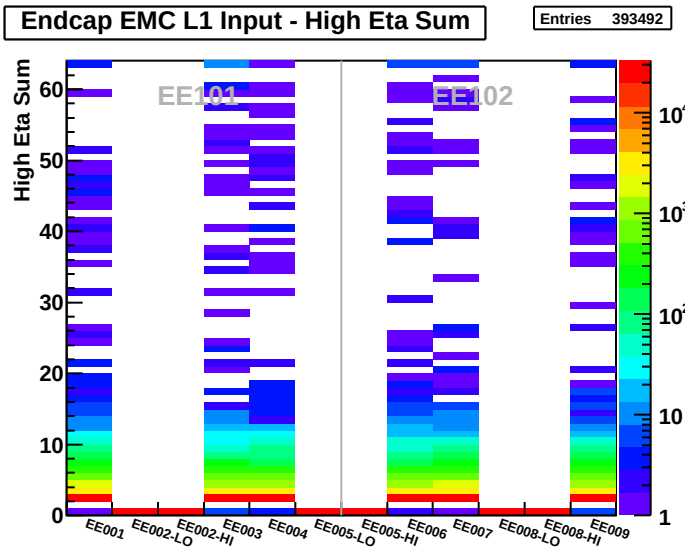

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

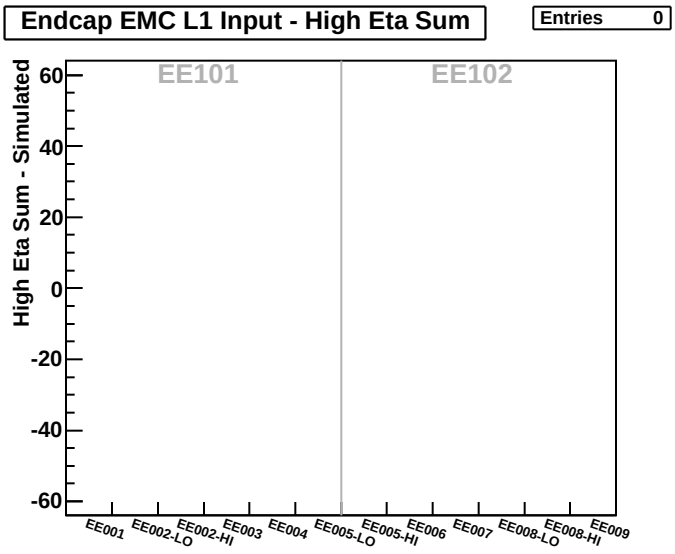

Barrel EMC L1 Input - High Tower Bits
Entries 1180476
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

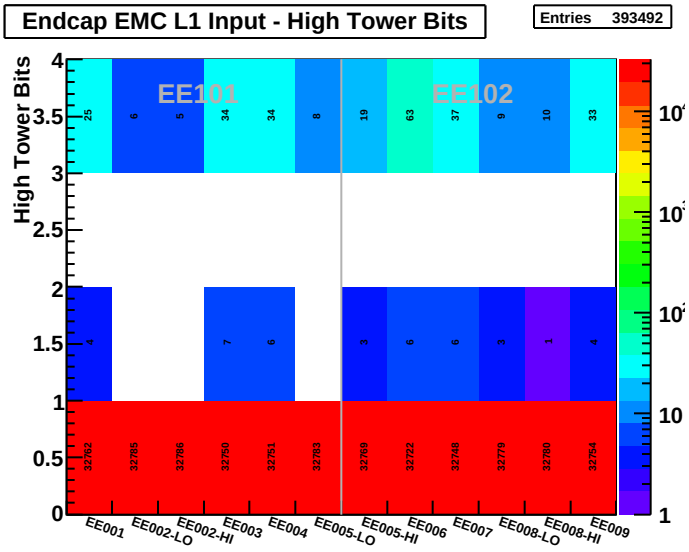

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

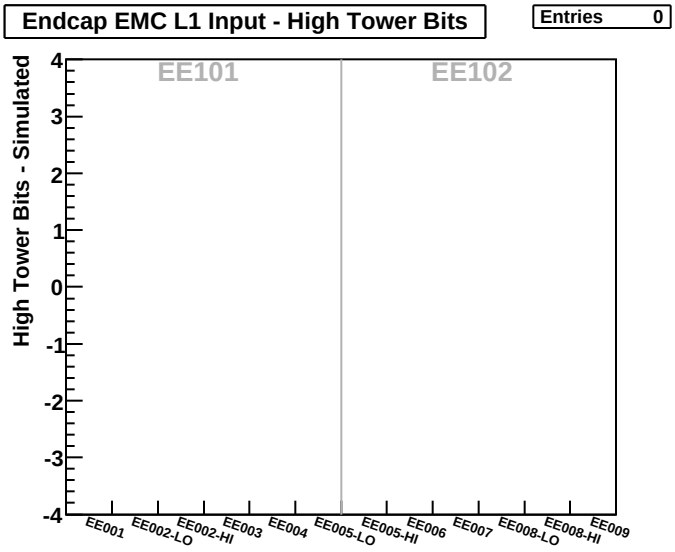

Endcap EMC L1 Input - Low Eta Sum
Entries 393492
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

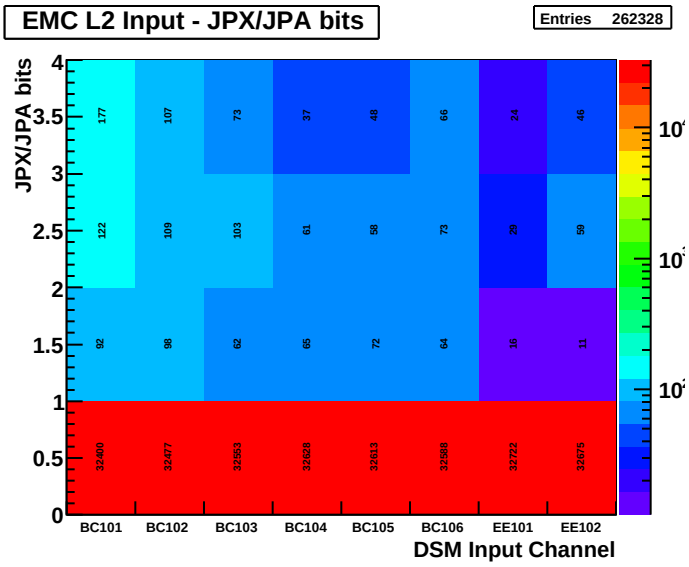

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

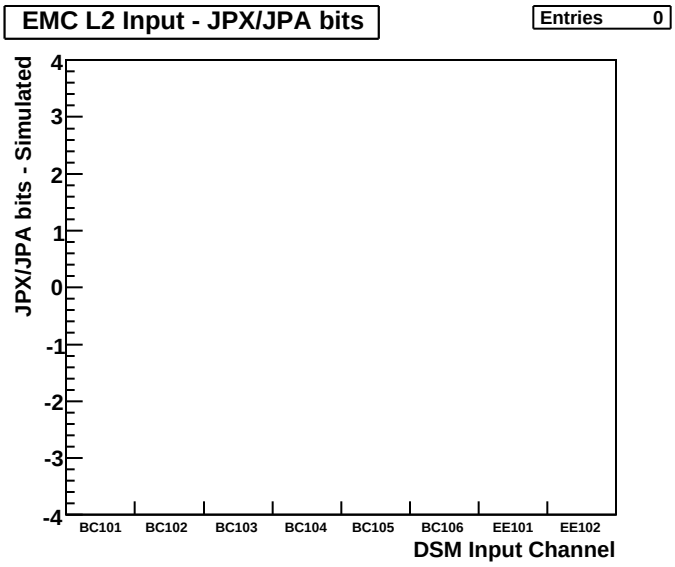

Endcap EMC L1 Input - High Eta Sum
Entries 393492
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

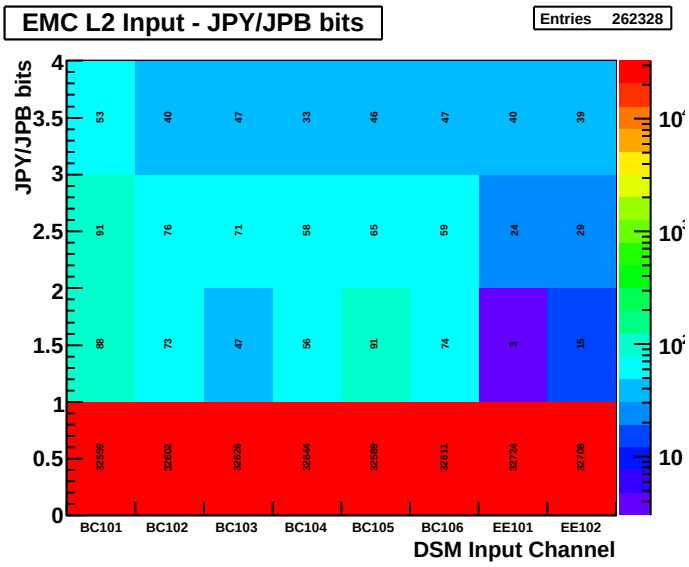

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

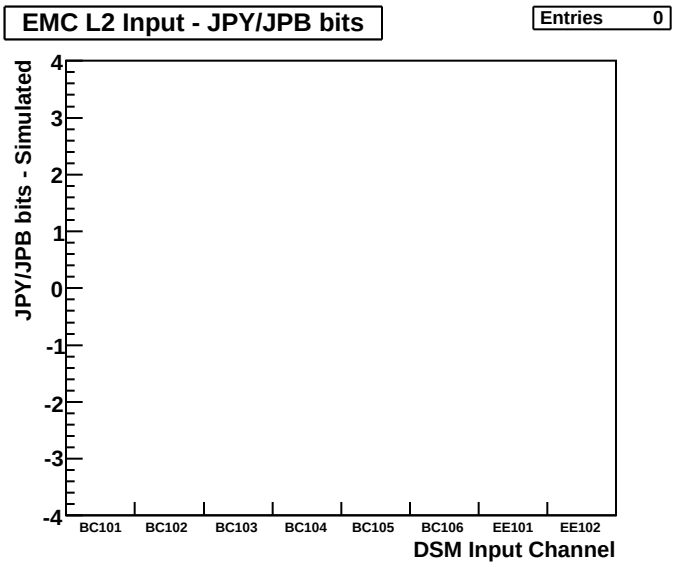

Endcap EMC L1 Input - High Tower Bits
Entries 393492
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

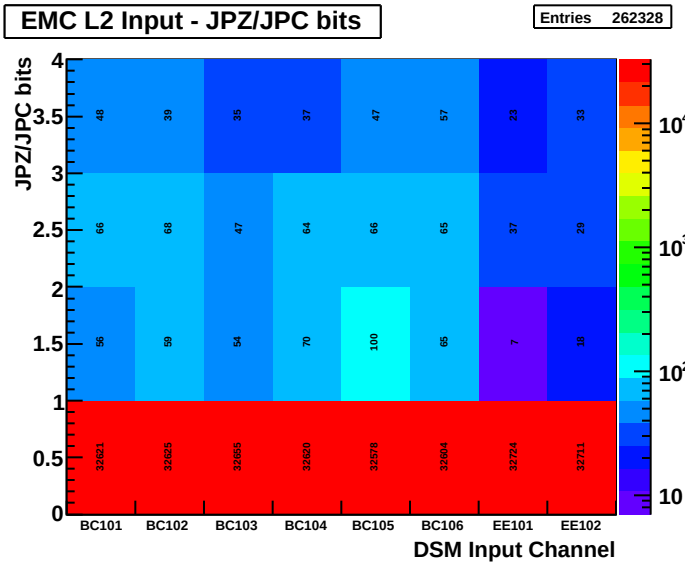

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

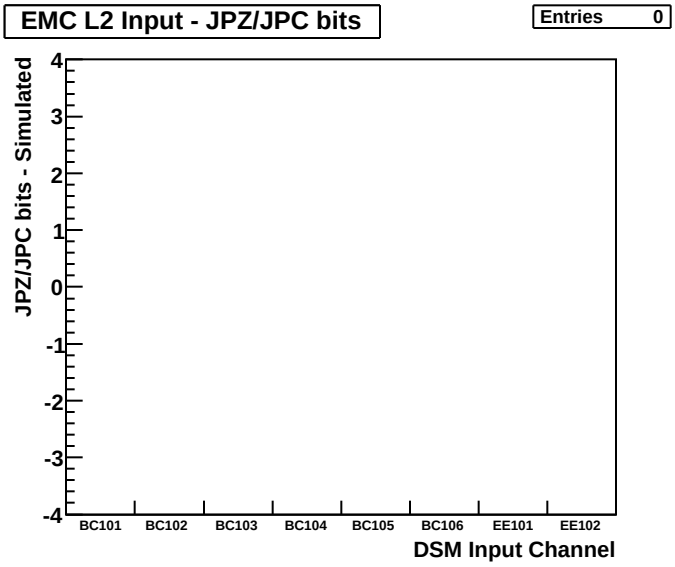

EMC L2 Input - JPX/JPA bits
Entries 262328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
177 107 73 37 48 66 24 46
122 109 103 61 58 73 29 59
92 98 62 65 72 64 16 11
32400 32477 32553 32628 32613 32598 32722 32675

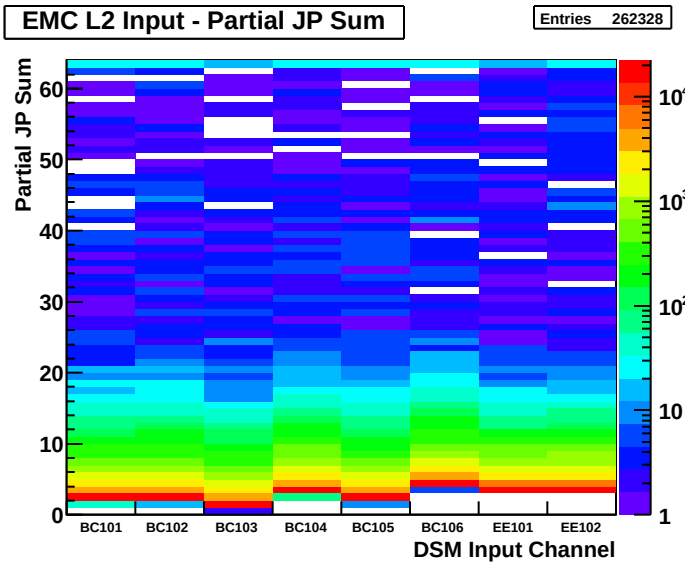

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

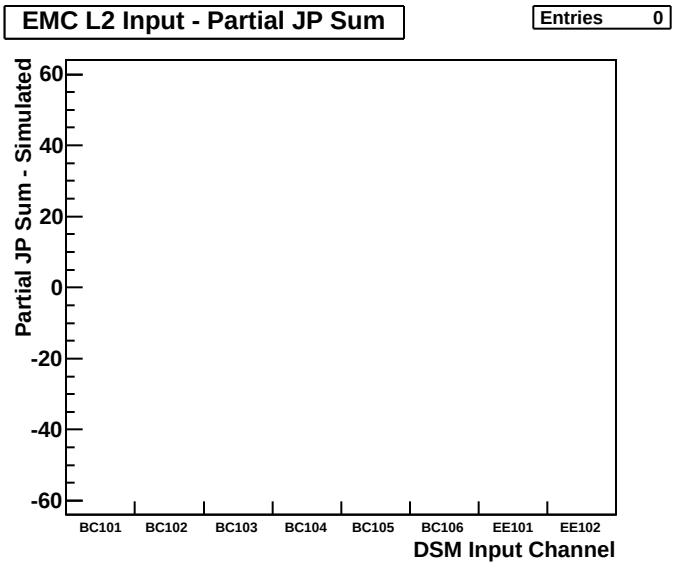

EMC L2 Input - JPY/JPB bits
Entries 262328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
53 40 47 33 46 47 40 39
91 76 71 58 65 59 24 29
88 73 47 56 91 74 3 15
32589 32602 32626 32644 32589 32611 32724 32708

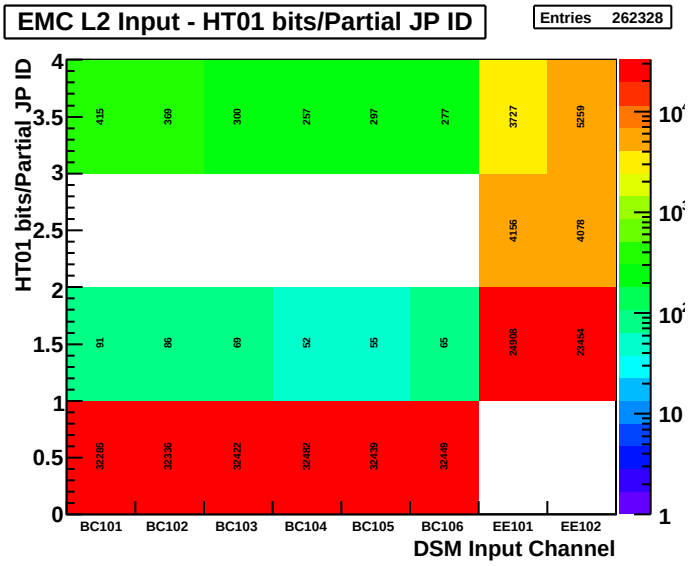

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

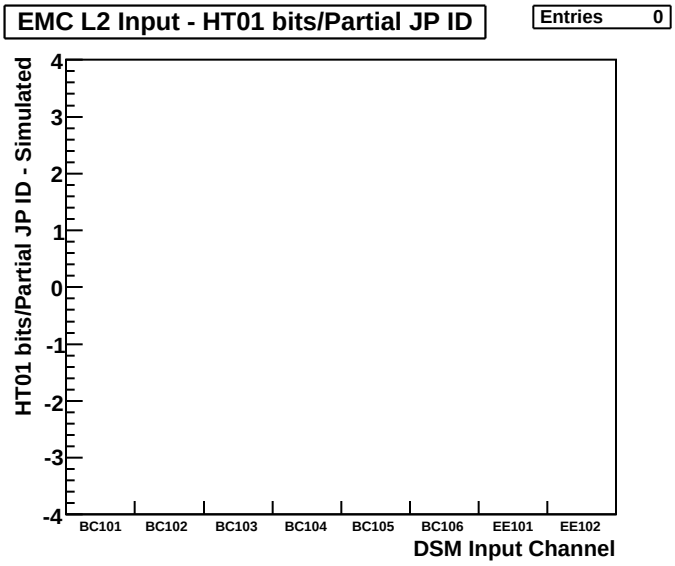

EMC L2 Input - JPZ/JPC bits
Entries 262328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
48 39 35 37 47 57 23 33
66 68 47 64 66 65 37 29
56 59 54 70 100 65 7 18
32621 32625 32655 32620 32578 32604 32724 32711

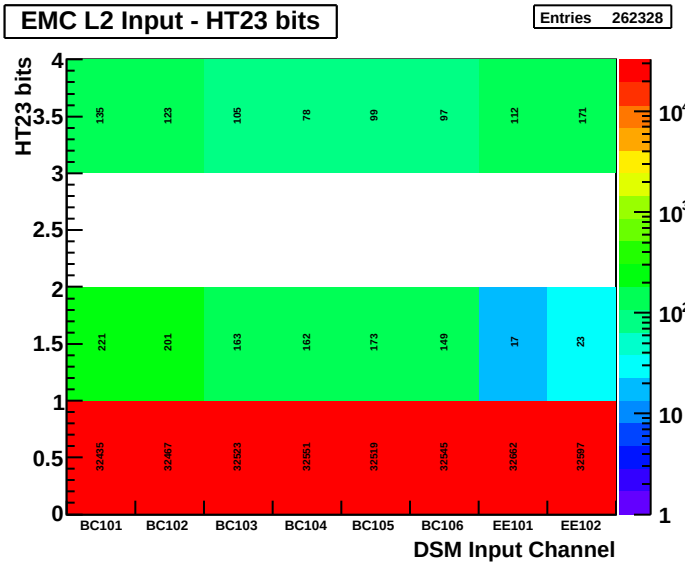

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

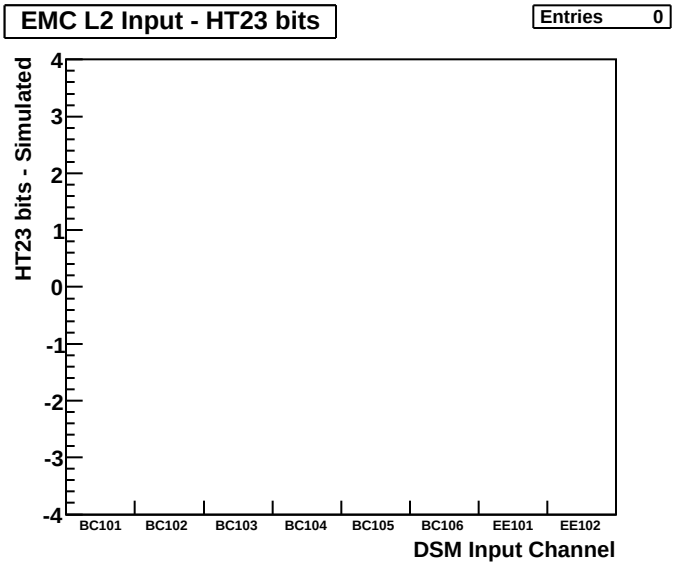

EMC L2 Input - Partial JP Sum
Entries 262328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

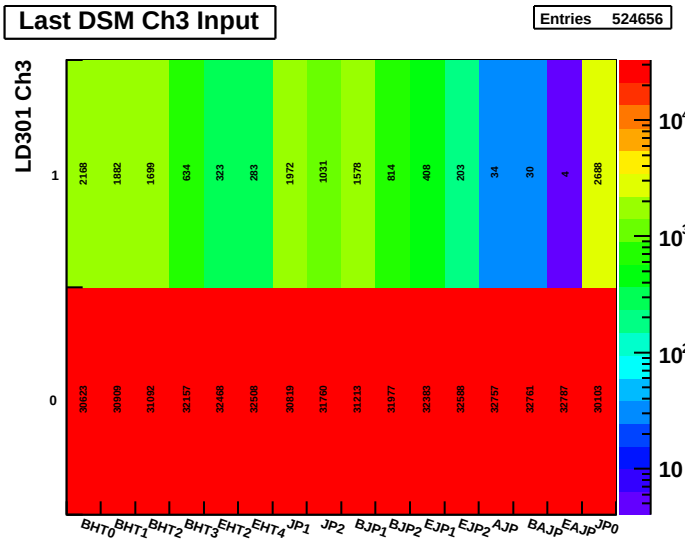

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

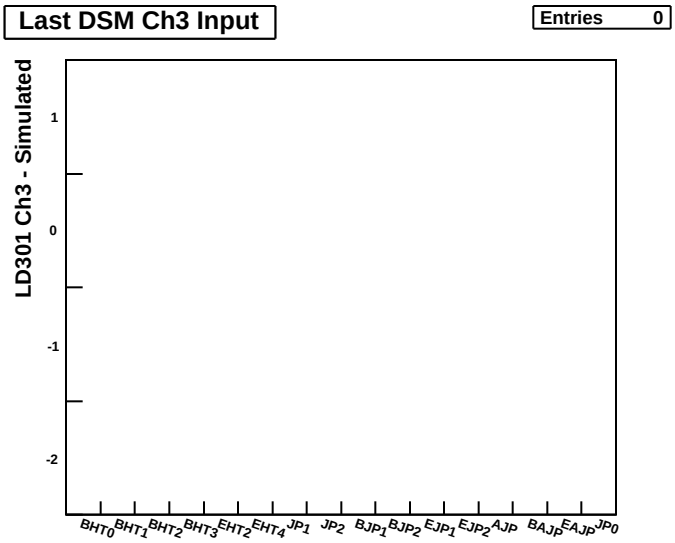

EMC L2 Input - HT01 bits/Partial JP ID
Entries 262328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

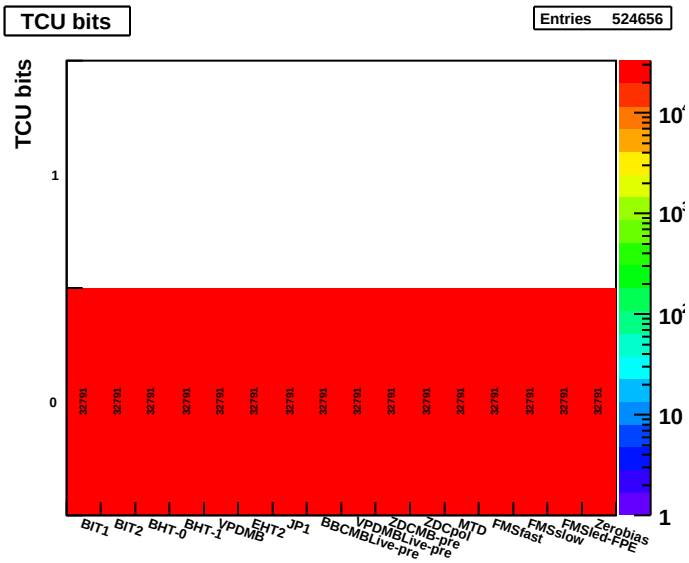

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

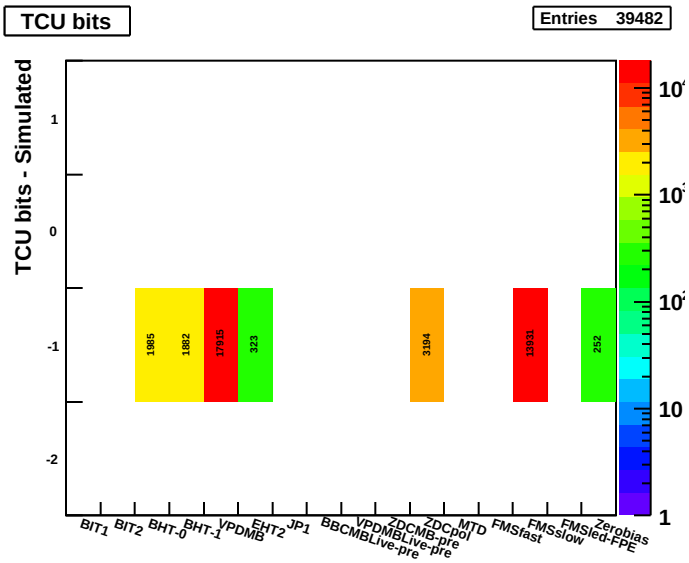

EMC L2 Input - HT23 bits
Entries 262328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

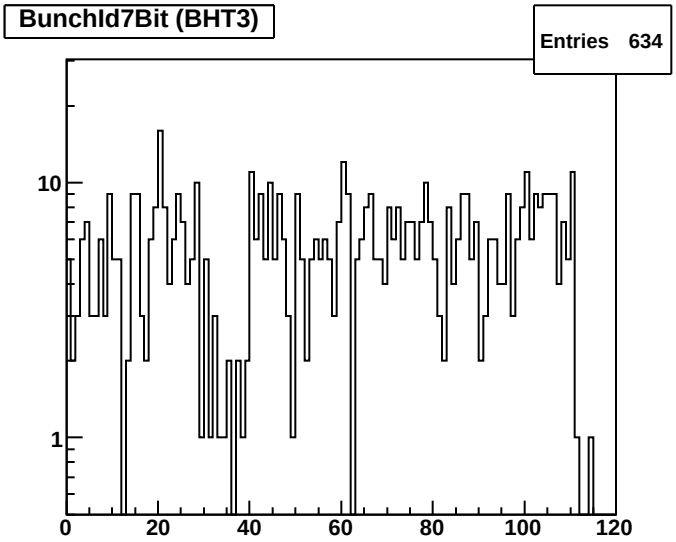

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

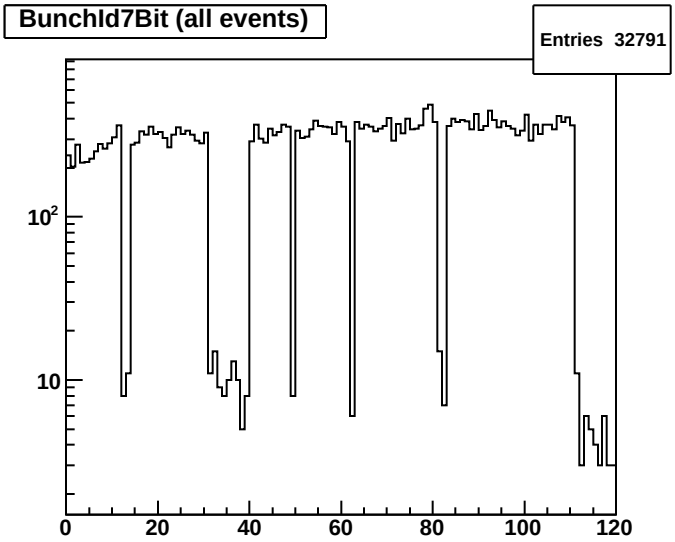

Last DSM Ch3 Input
Entries 524656
LD301 Ch3
2168 1882 1699 634 323 283 1972 1031 1578 814 408 203 34 30 4 2688
30623 30909 31092 32157 32468 32508 30819 31760 31213 31977 32383 32588 32757 32761 32787 30103
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

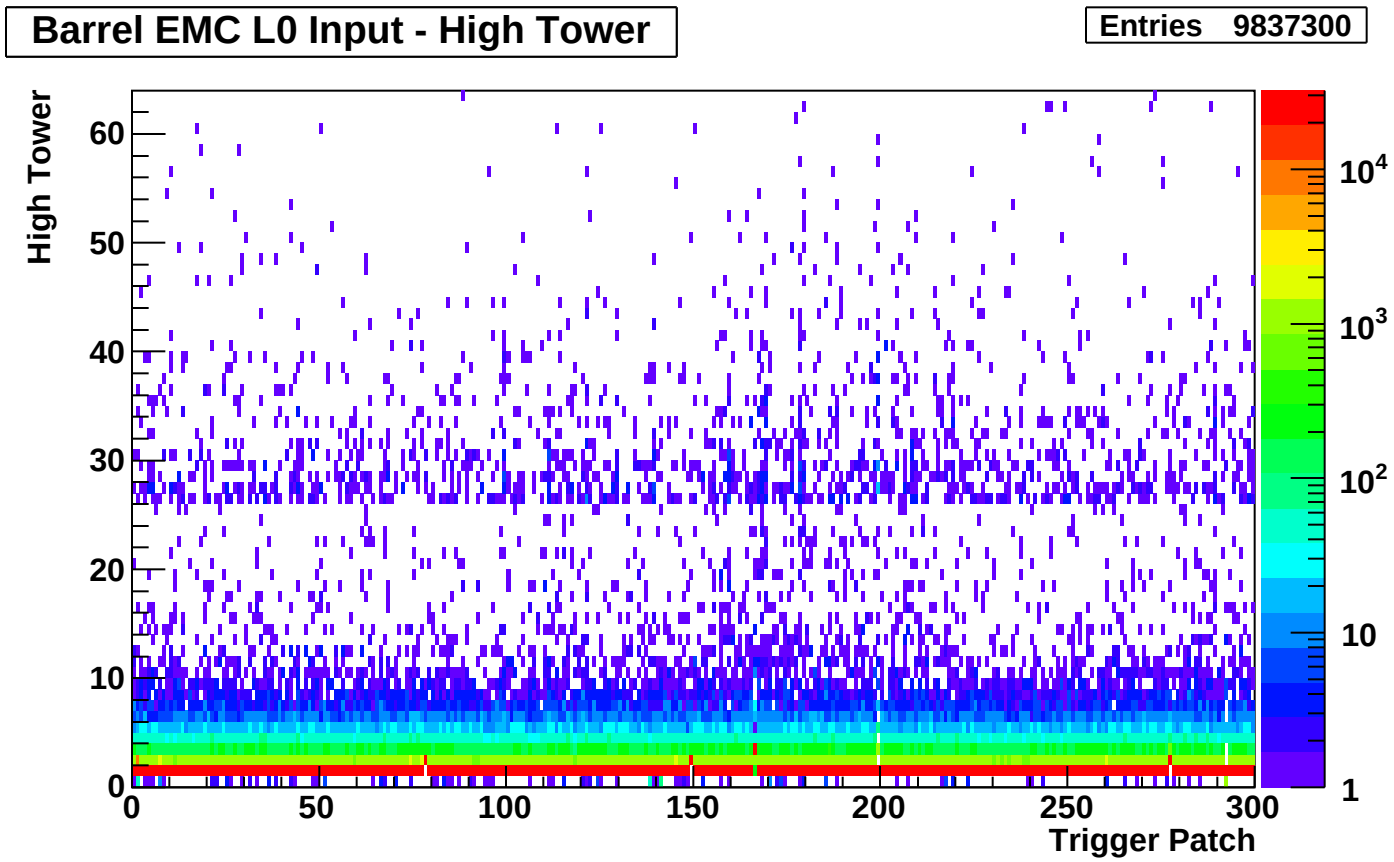

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

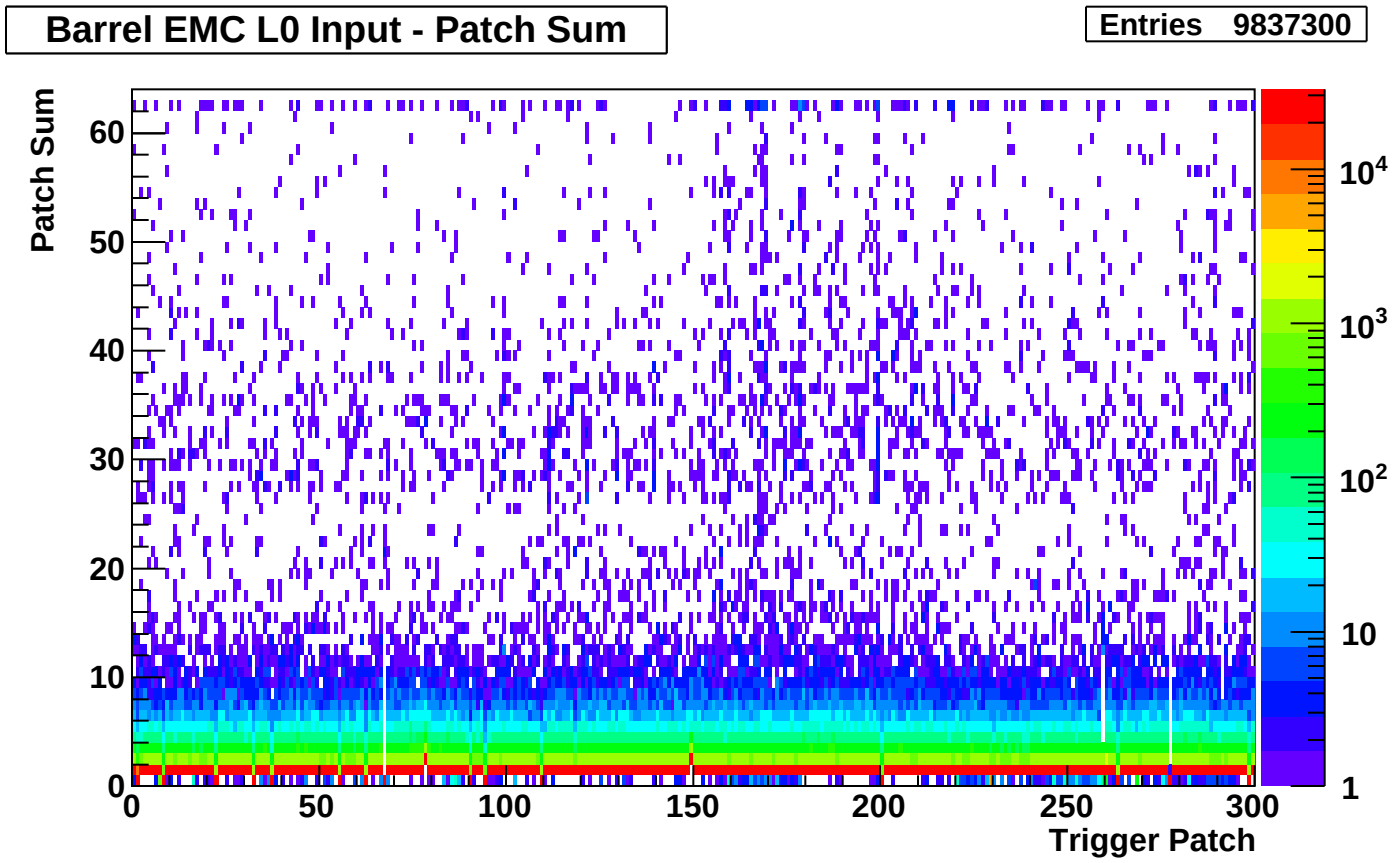

TCU bits
Entries 524656
TCU bits
32791 32791 32791 32791 32791 32791 32791 32791 32791 32791 32791 32791 32791 32791 32791 32791
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

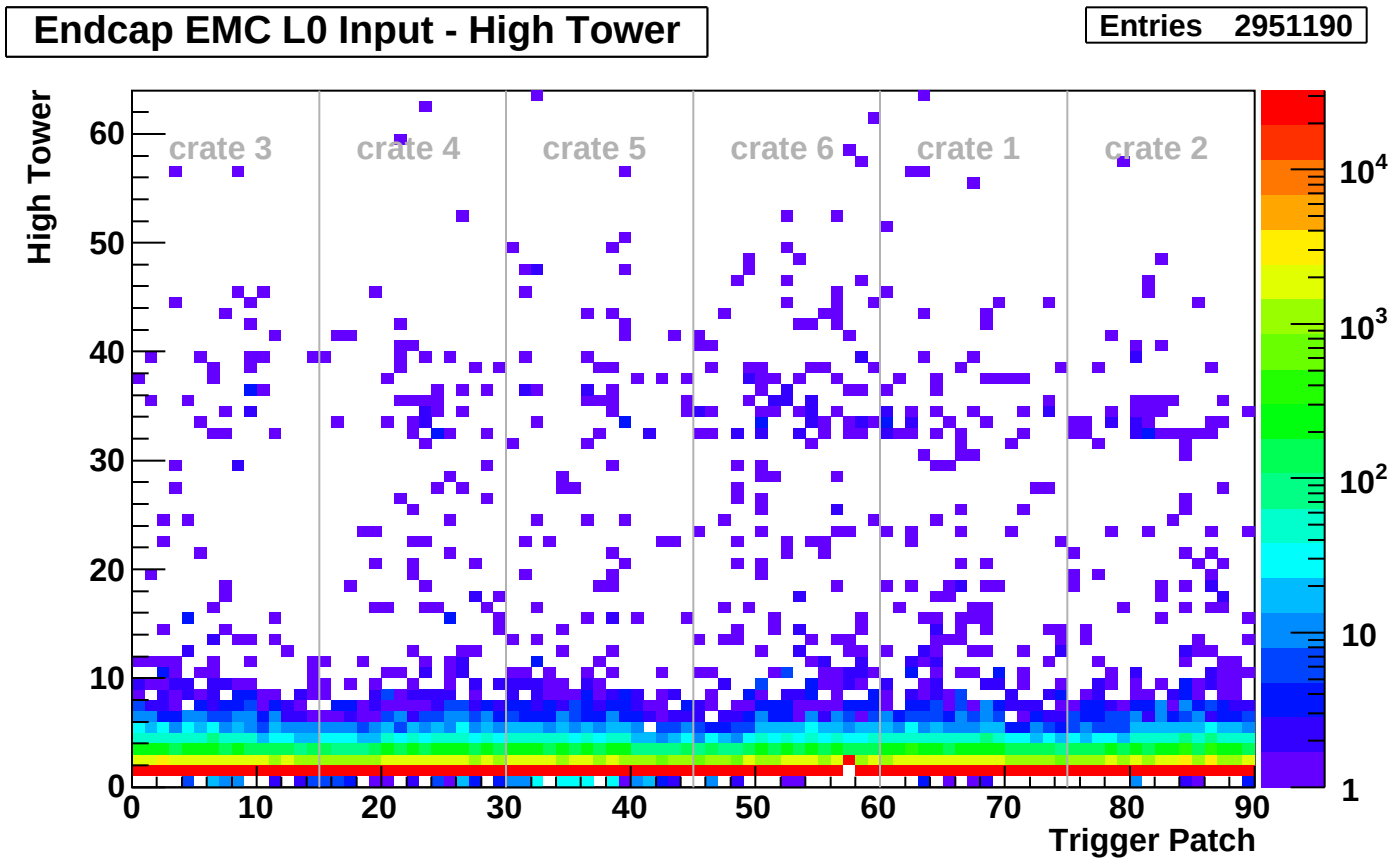

TCU bits
Entries 39482
TCU bits - Simulated
1985 1882 17915 323 3194 13931 252
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

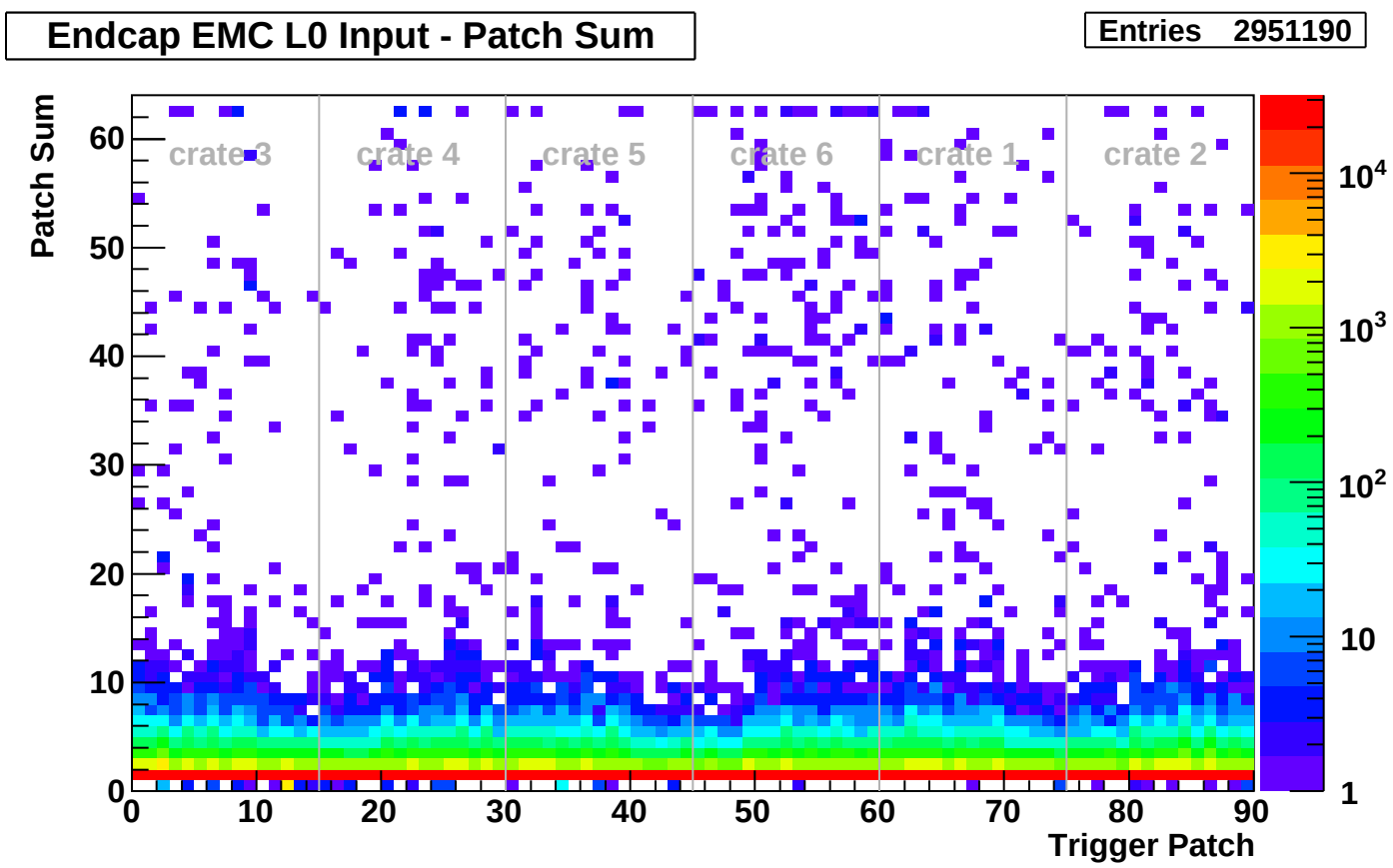

BunchId7Bit (BHT3)
Entries 634

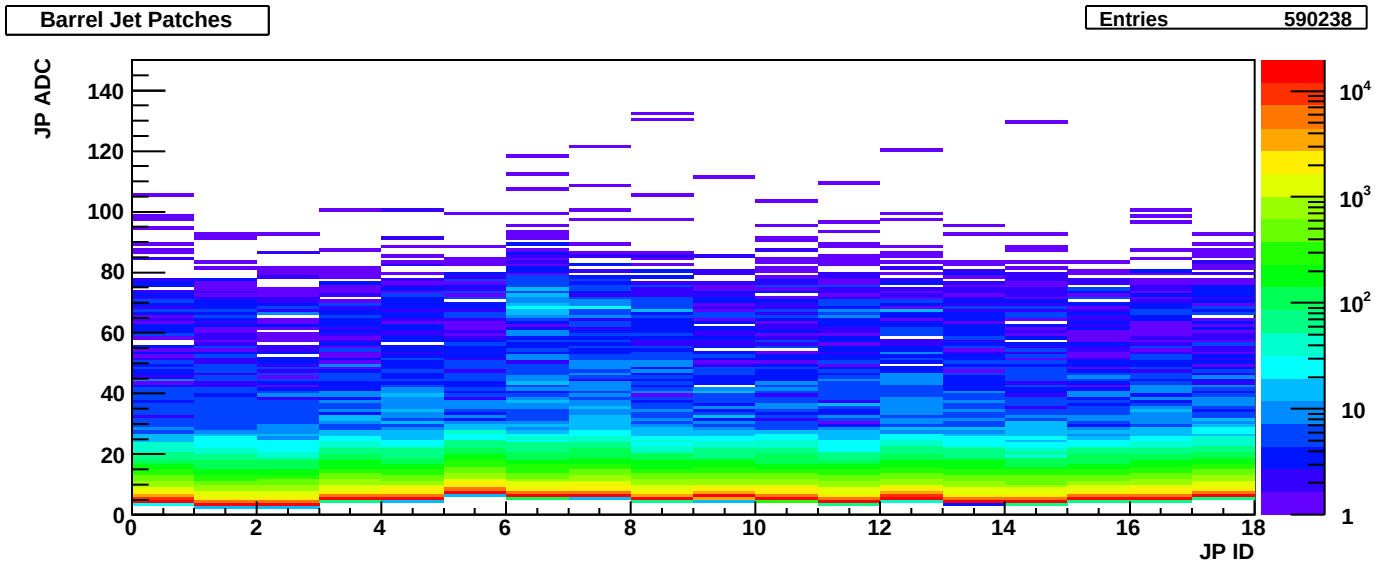

BunchId7Bit (all events)
Entries 32791

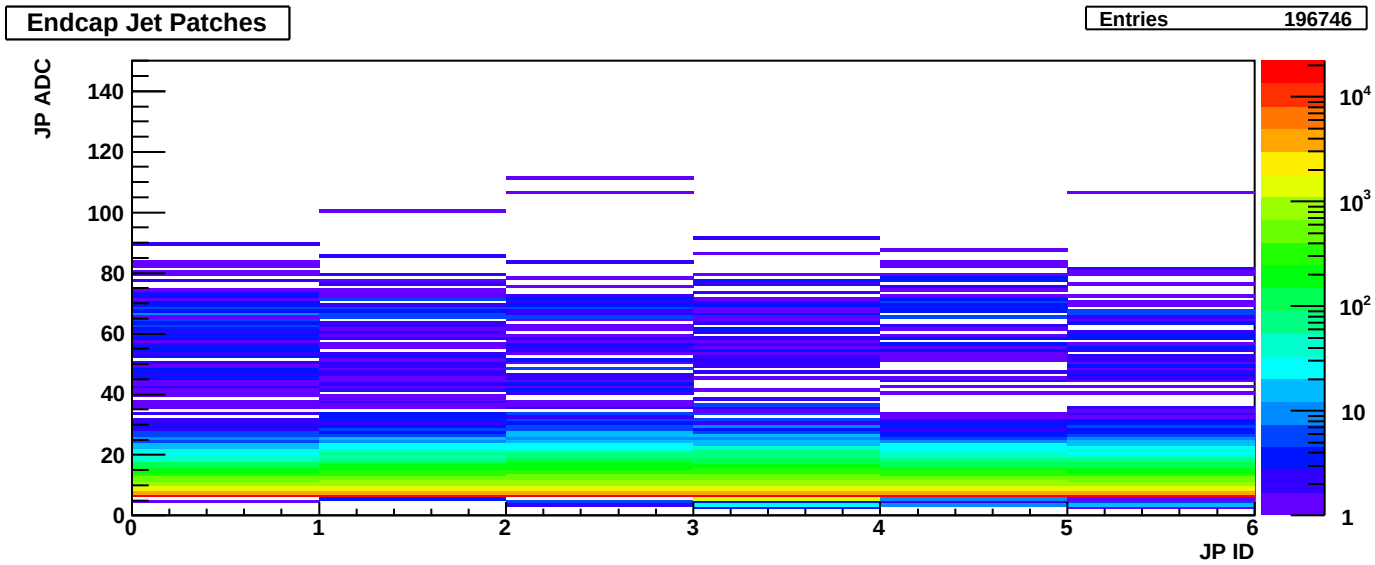

Barrel EMC L0 Input - High Tower
Entries 9837300
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

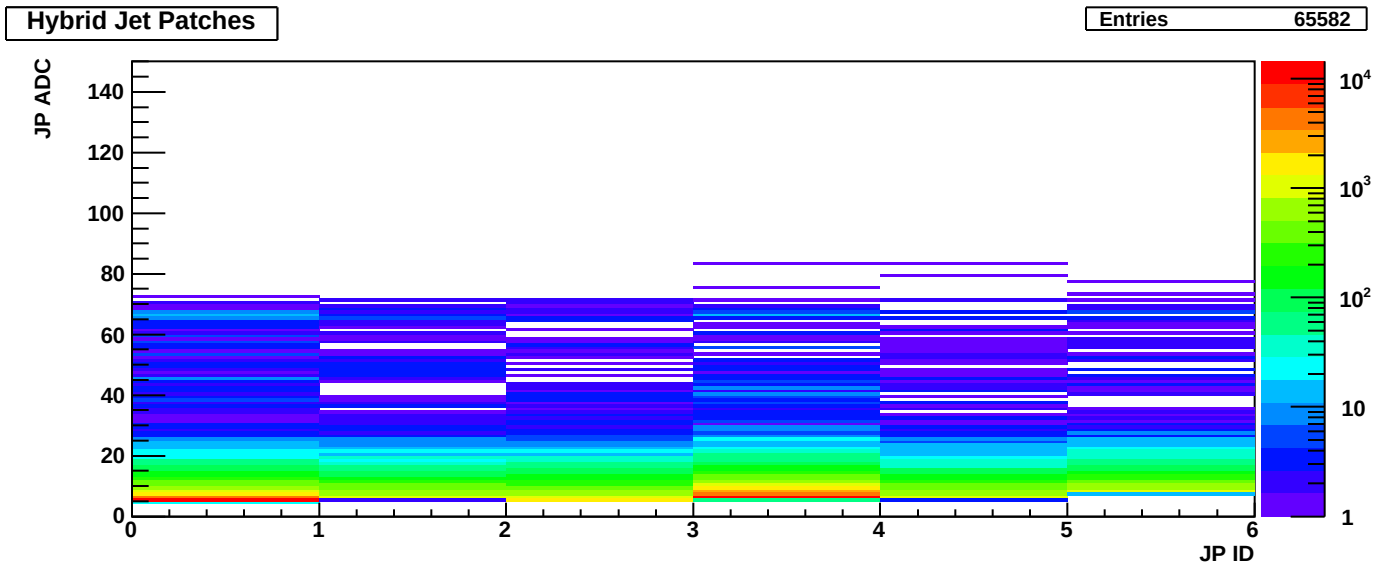

Barrel EMC L0 Input - Patch Sum
Entries 9837300
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

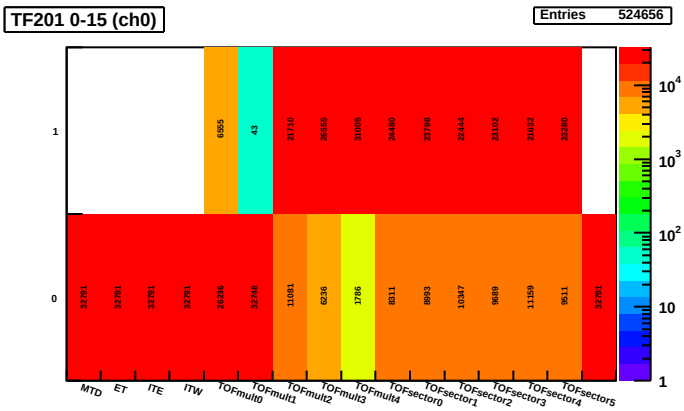

Endcap EMC L0 Input - High Tower
Entries 2951190
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

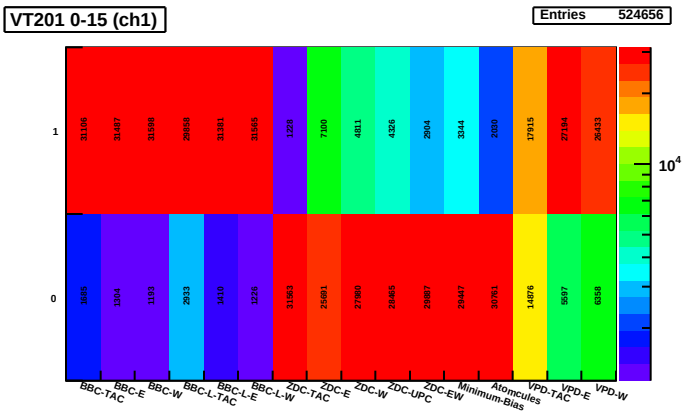

Endcap EMC L0 Input - Patch Sum
Entries 2951190
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

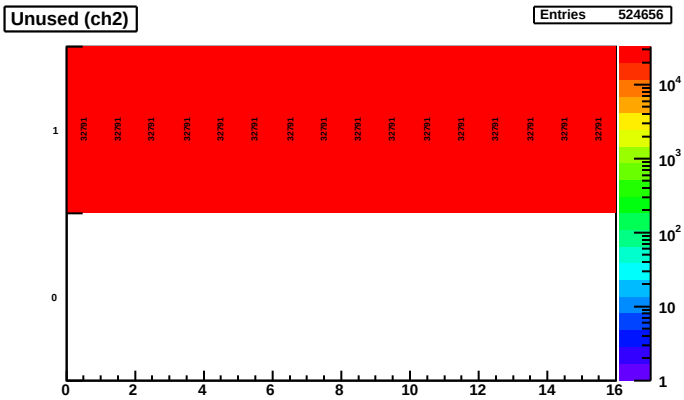

Barrel Jet Patches
Entries 590238
JP ADC
JP ID

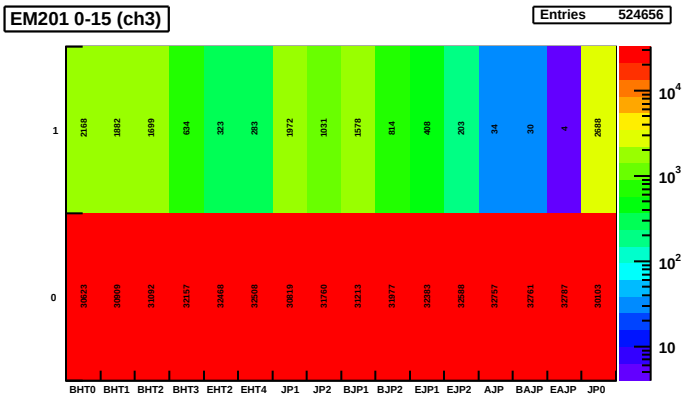

Endcap Jet Patches
Entries 196746
JP ADC
JP ID

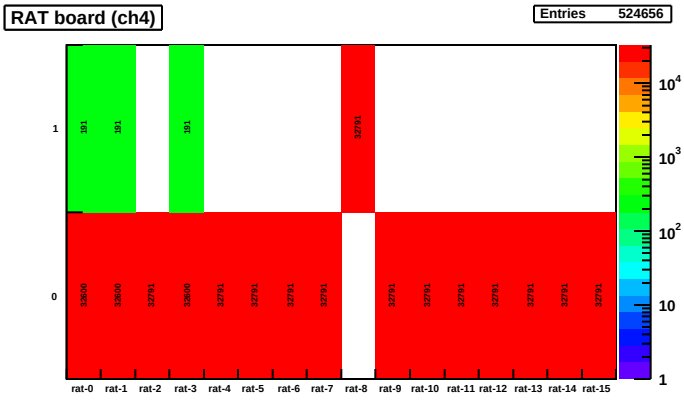

Hybrid Jet Patches
Entries 65582
JP ADC
JP ID

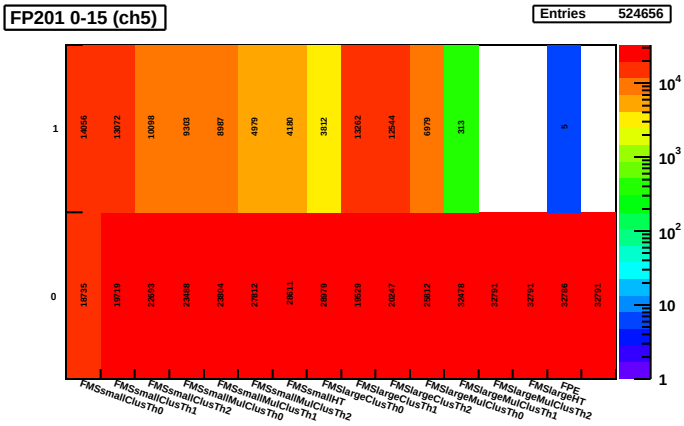

TF201 0-15 (ch0)
Entries 524656
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

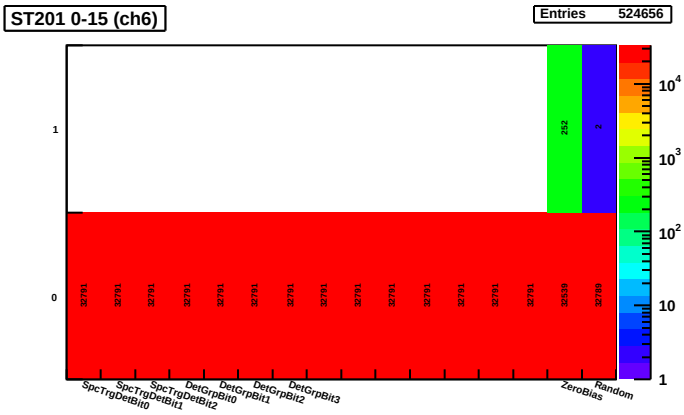

VT201 0-15 (ch1)
Entries 524656
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

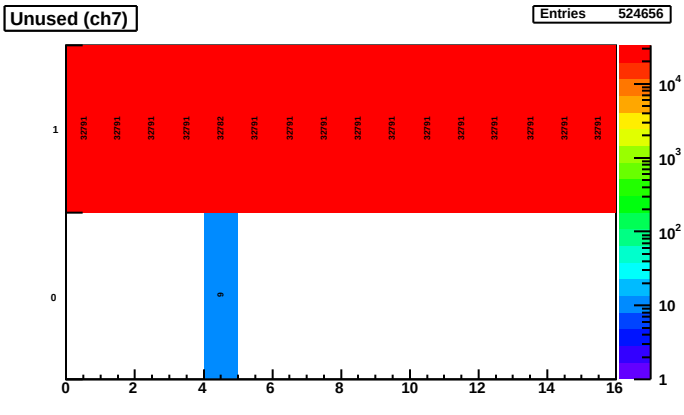

Unused (ch2)
Entries 524656

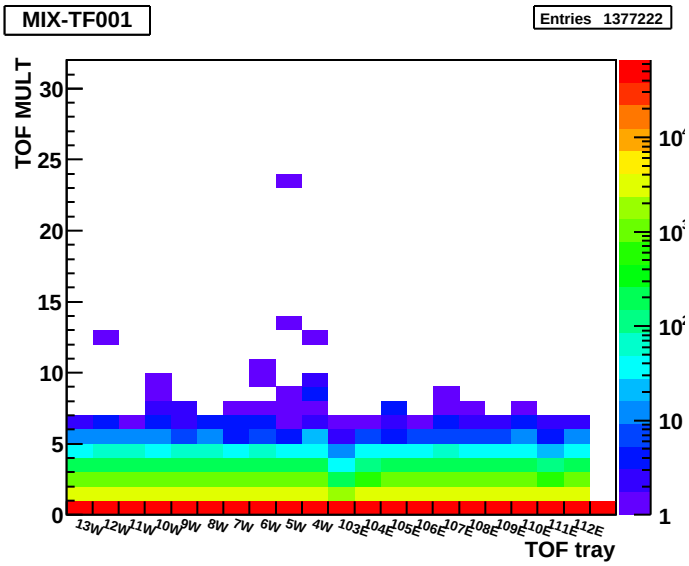

EM201 0-15 (ch3)
Entries 524656
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

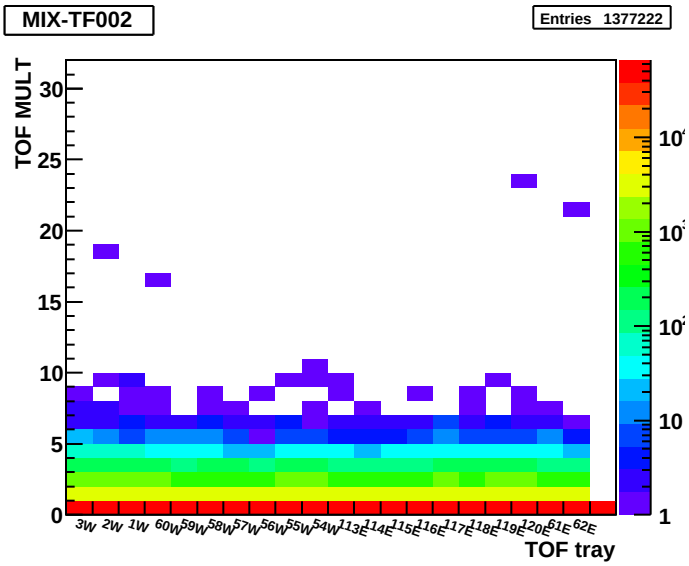

RAT board (ch4)
Entries 524656
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

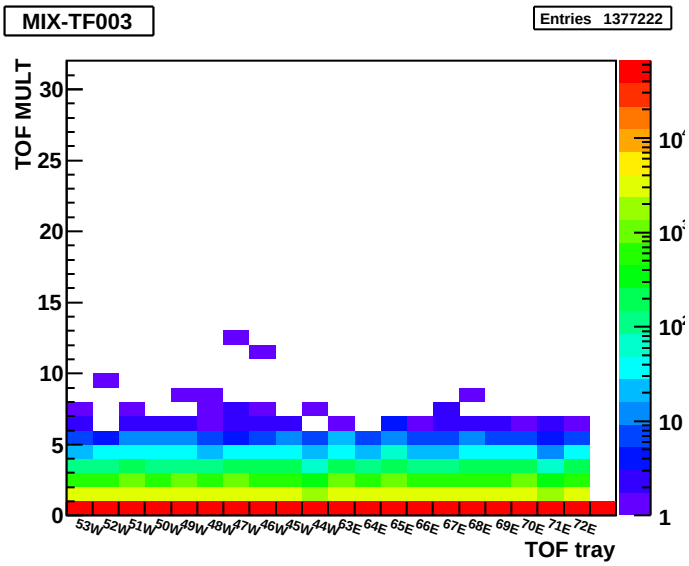

FP201 0-15 (ch5)
Entries 524656
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

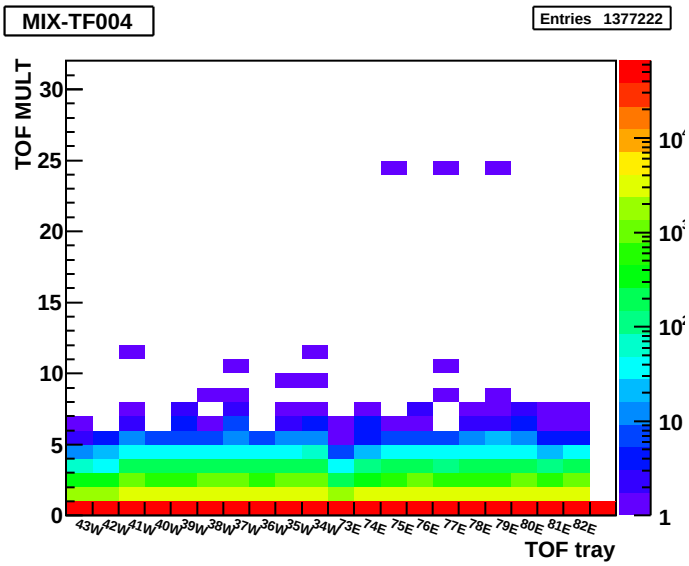

ST201 0-15 (ch6)
Entries 524656
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

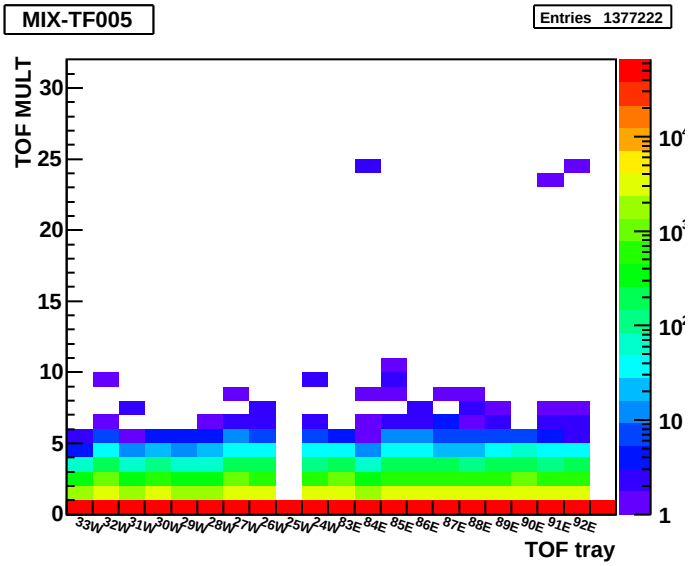

Unused (ch7)
Entries 524656

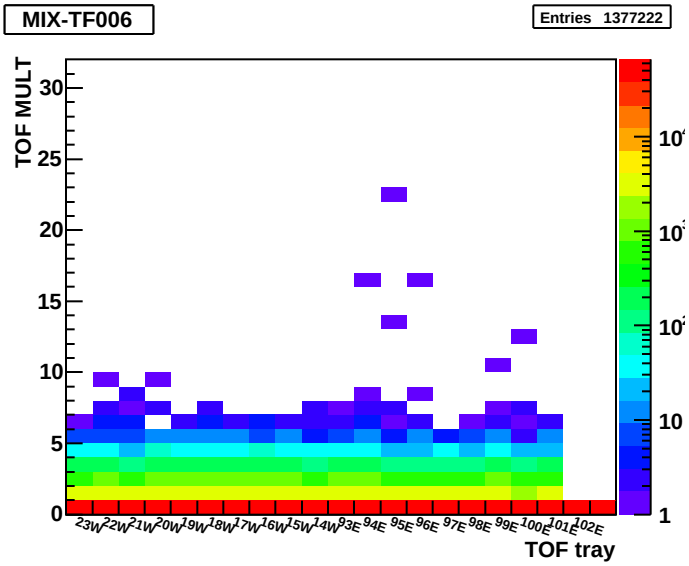

MIX-TF001
Entries 1377222
TOF MULT
TOF tray

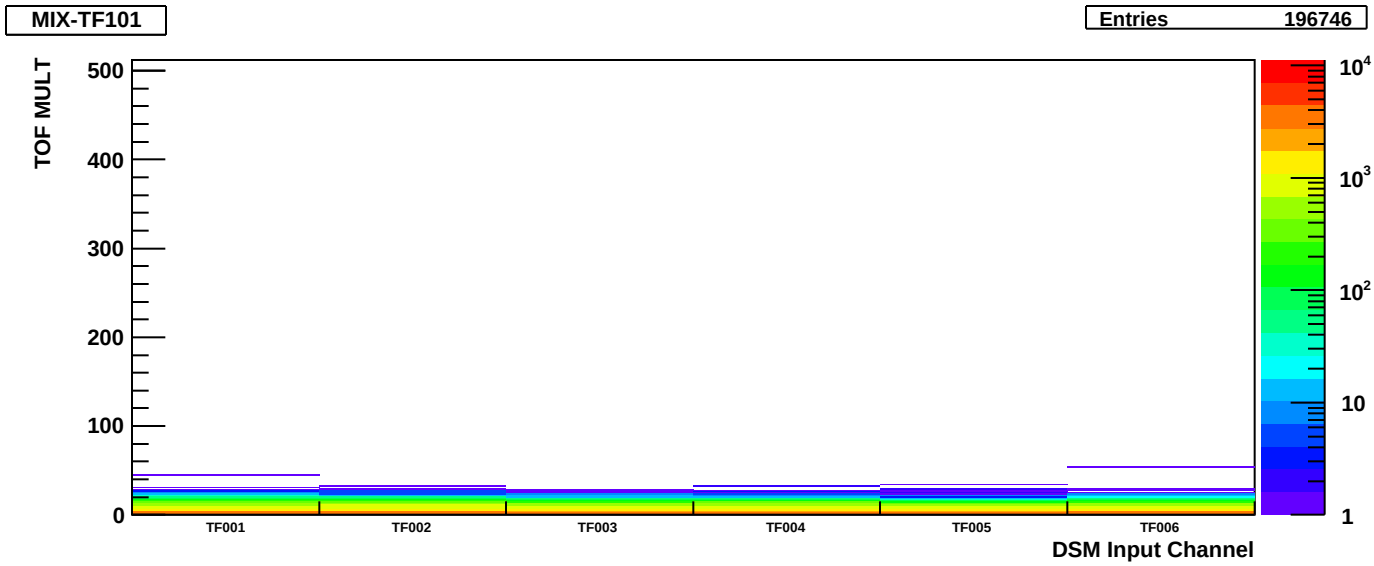

MIX-TF002
Entries 1377222
TOF MULT
TOF tray

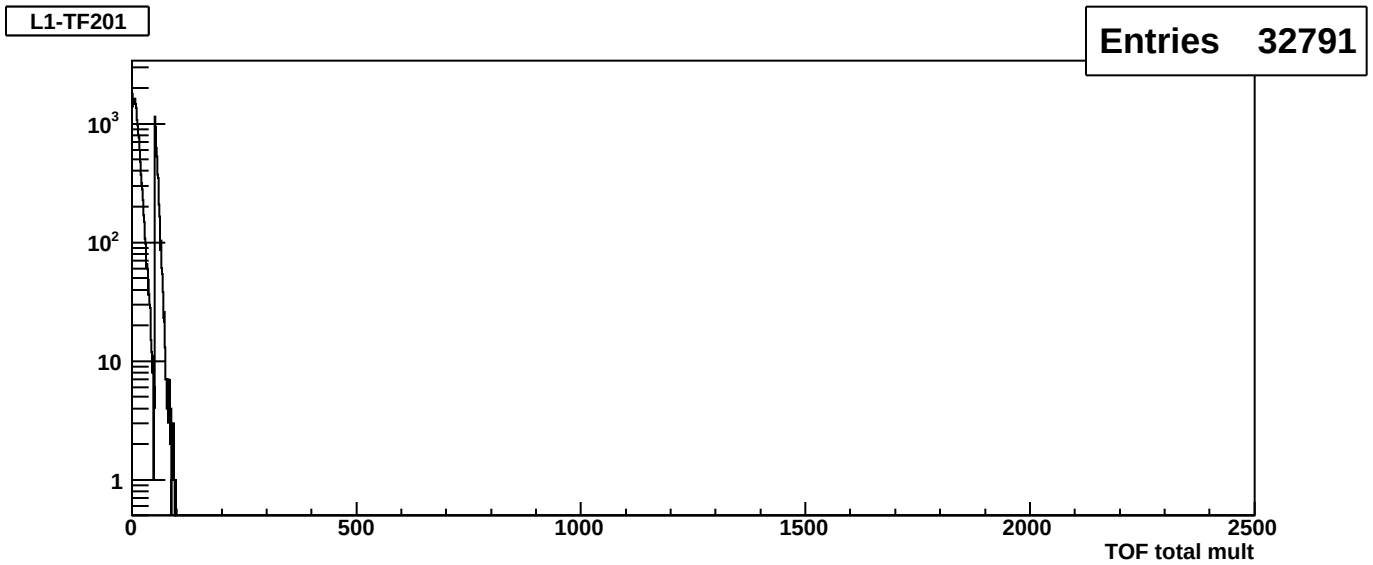

MIX-TF003
Entries 1377222
TOF MULT
TOF tray

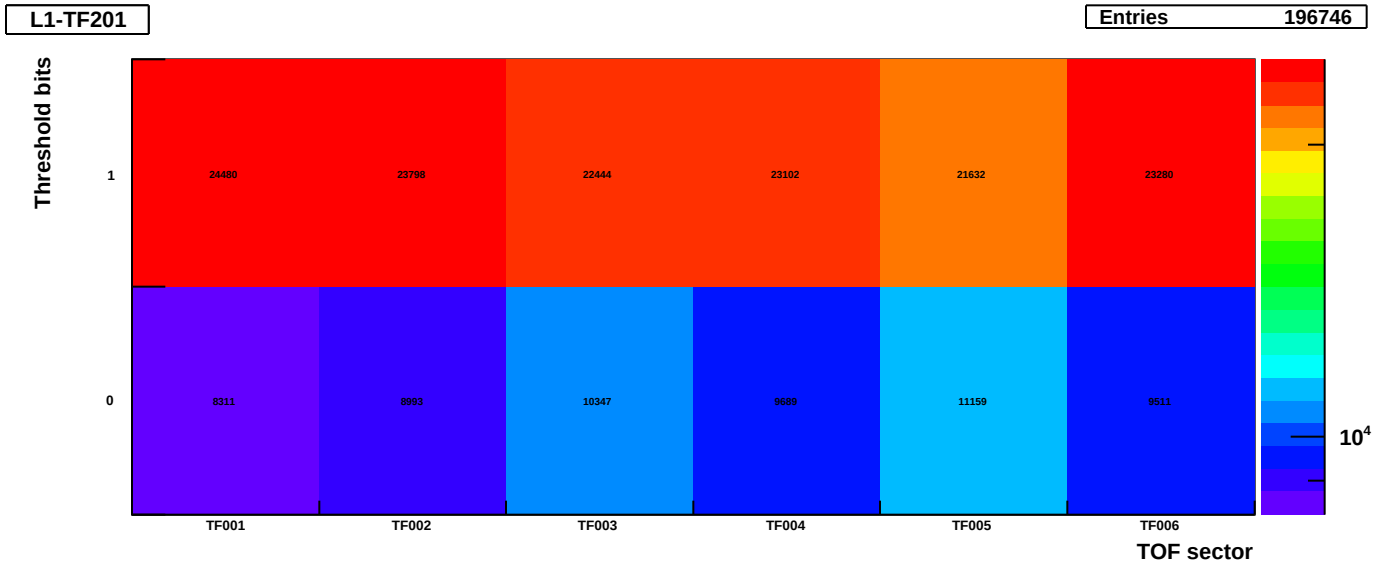

MIX-TF004
Entries 1377222
TOF MULT
TOF tray

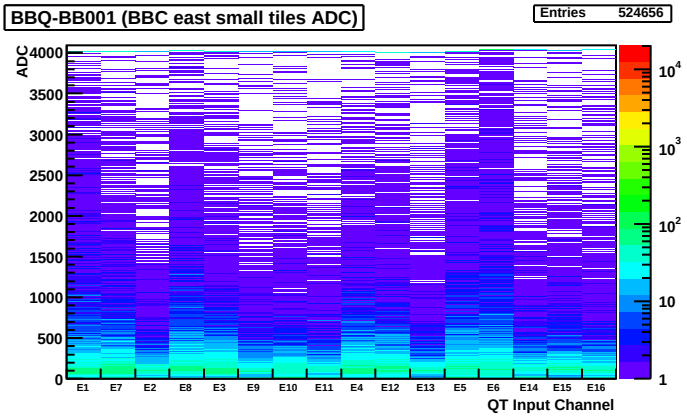

MIX-TF005
Entries 1377222
TOF MULT
TOF tray

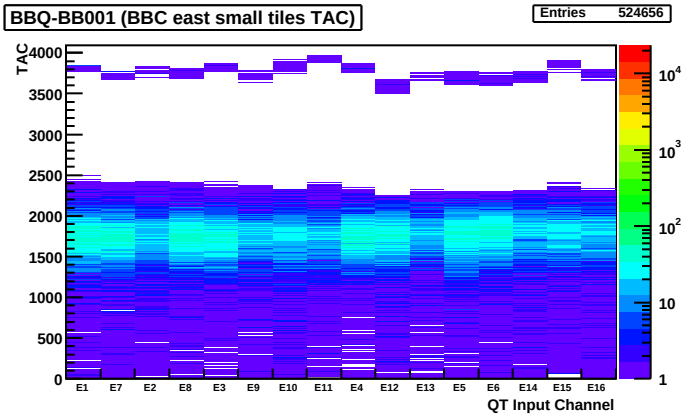

MIX-TF006
Entries 1377222
TOF MULT
TOF tray

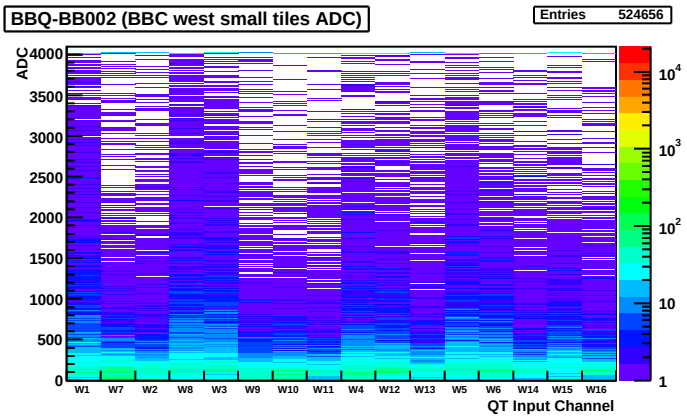

MIX-TF101
Entries 196746
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

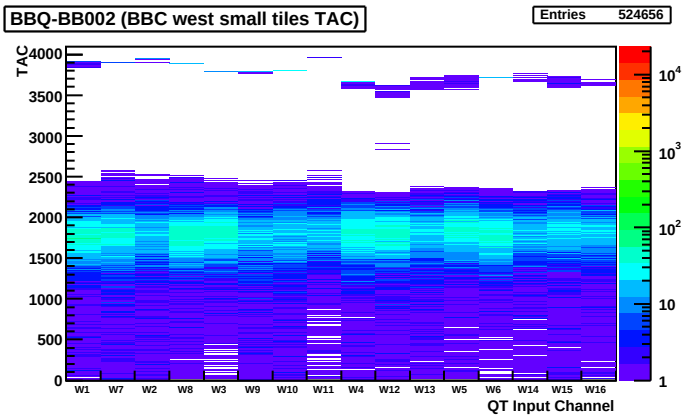

L1-TF201
Entries 32791
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

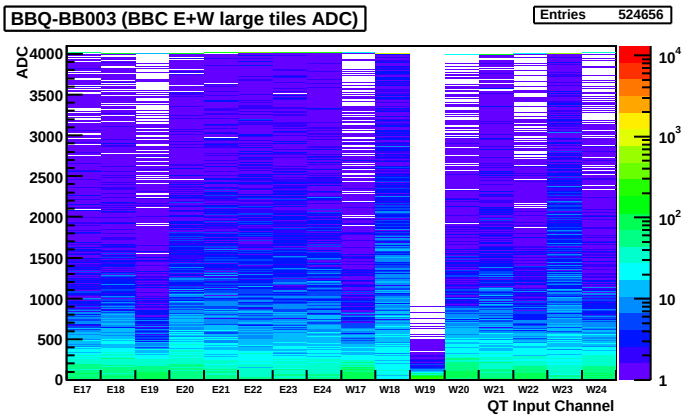

L1-TF201
Entries 196746
Threshold bits
1
0
24480
23798
22444
23102
21632
23280
8311
8993
10347
9689
11159
9511
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

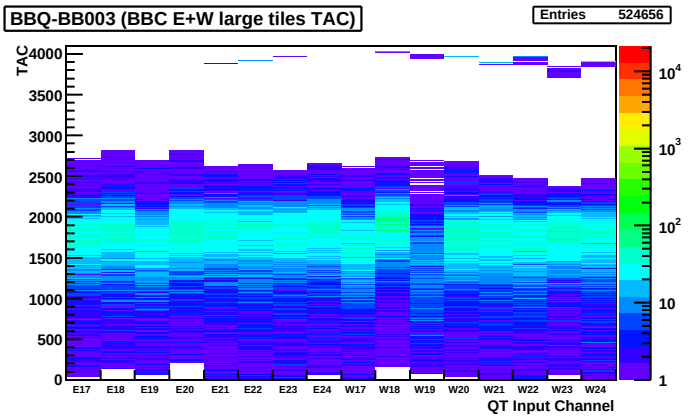

BBQ-BB001 (BBC east small tiles ADC)
Entries 524656
ADC
QT Input Channel

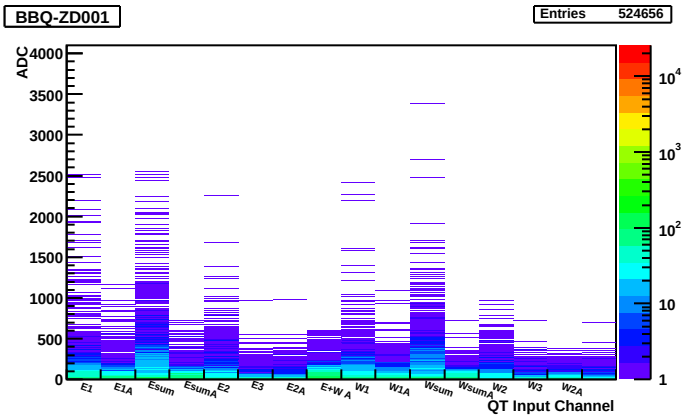

BBQ-BB001 (BBC east small tiles TAC)
Entries 524656
TAC
QT Input Channel

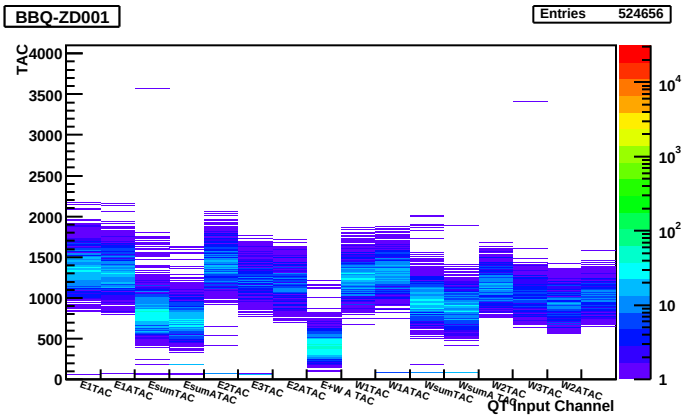

BBQ-BB002 (BBC west small tiles ADC)
Entries 524656
ADC
QT Input Channel

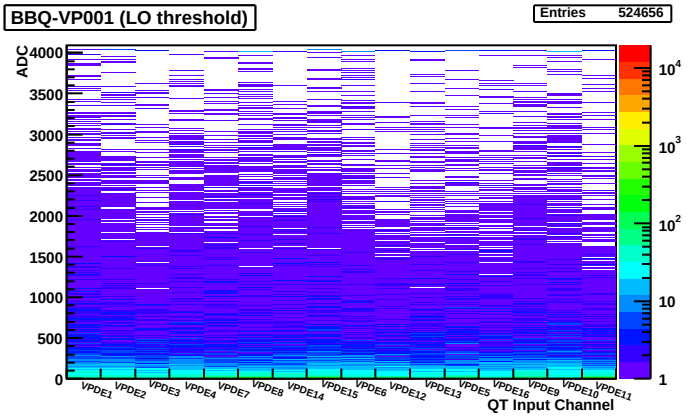

BBQ-BB002 (BBC west small tiles TAC)
Entries 524656
TAC
QT Input Channel

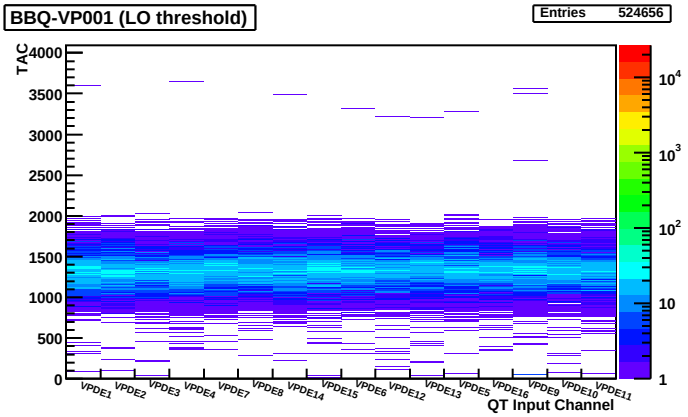

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 524656
ADC
QT Input Channel

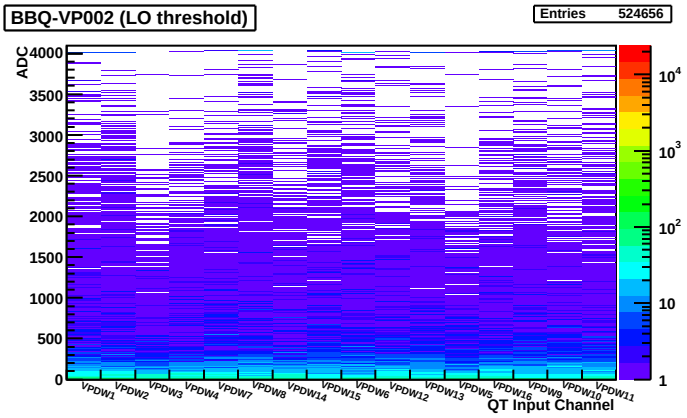

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 524656
TAC
QT Input Channel

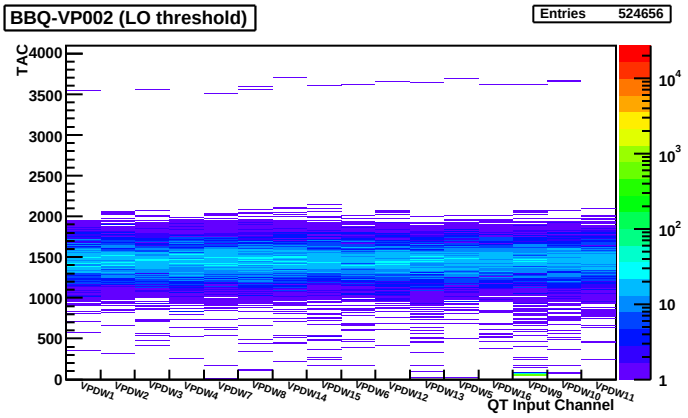

BBQ-ZD001
Entries 524656
ADC
QT Input Channel

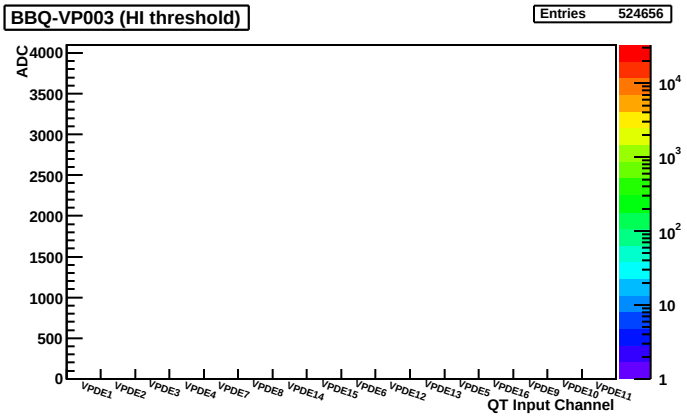

BBQ-ZD001
Entries 524656
TAC
QT Input Channel

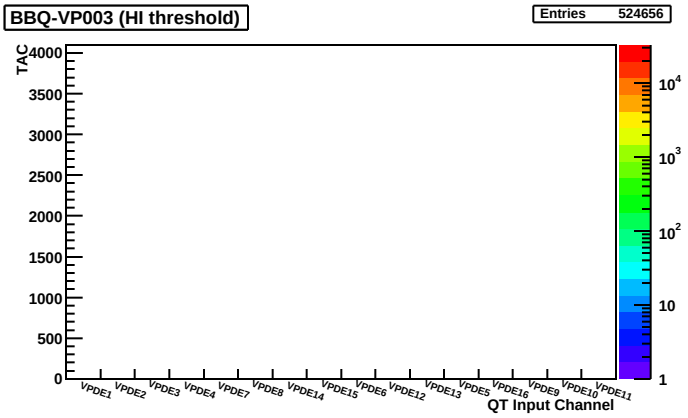

BBQ-VP001 (LO threshold)
Entries 524656
ADC
QT Input Channel

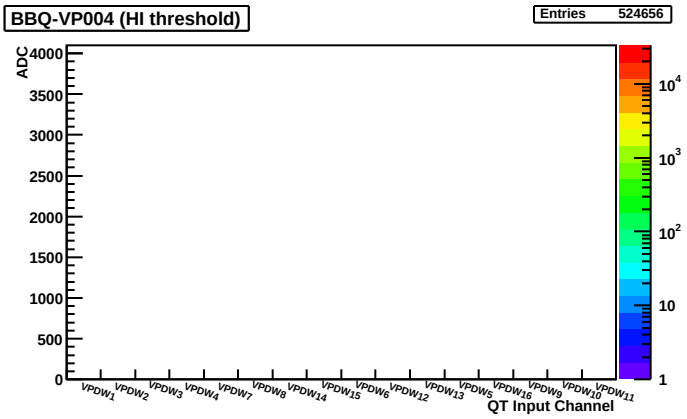

BBQ-VP001 (LO threshold)
Entries 524656
TAC
QT Input Channel

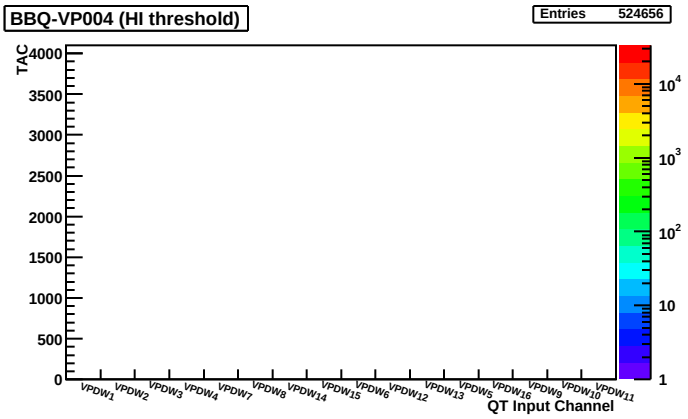

BBQ-VP002 (LO threshold)
Entries 524656
ADC
QT Input Channel

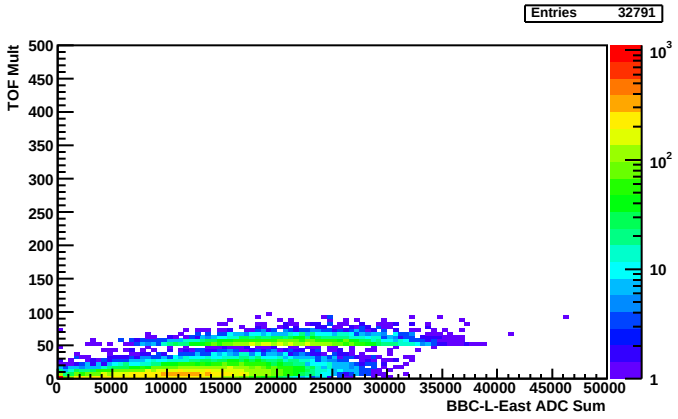

BBQ-VP002 (LO threshold)
Entries 524656
TAC
QT Input Channel

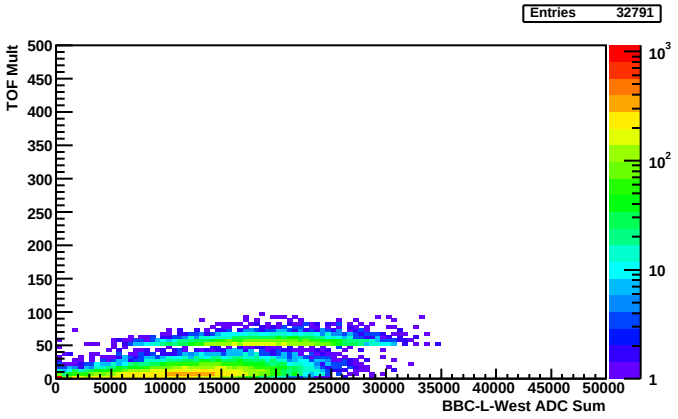

BBQ-VP003 (HI threshold)
Entries 524656
ADC
QT Input Channel

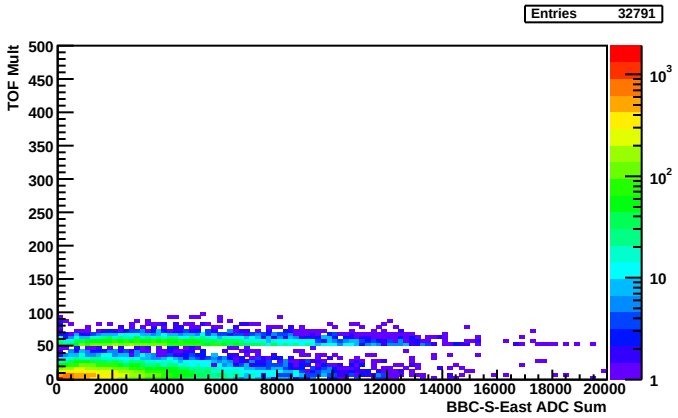

BBQ-VP003 (HI threshold)
Entries 524656
TAC
QT Input Channel

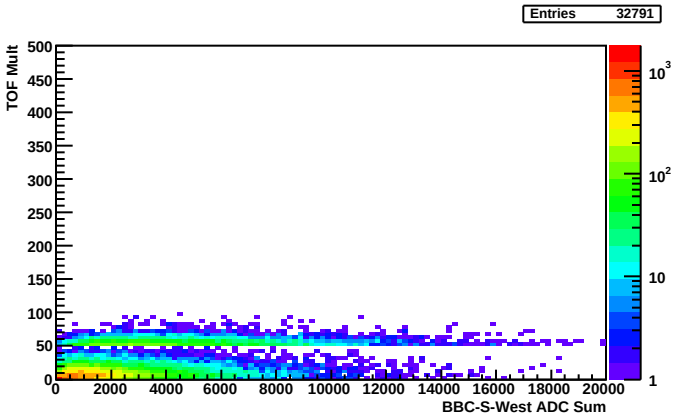

BBQ-VP004 (HI threshold)
Entries 524656
ADC
QT Input Channel

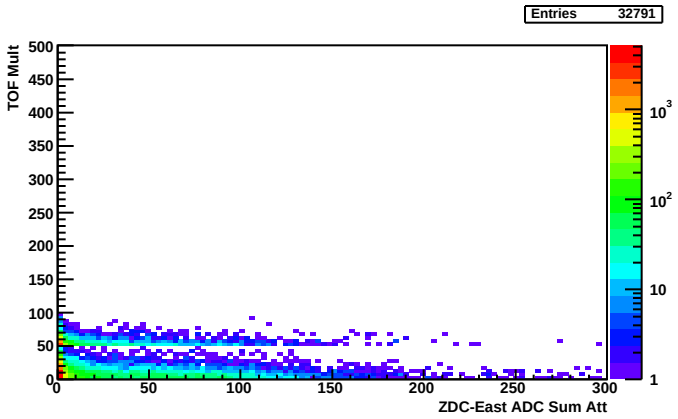

BBQ-VP004 (HI threshold)
Entries 524656
TAC
QT Input Channel

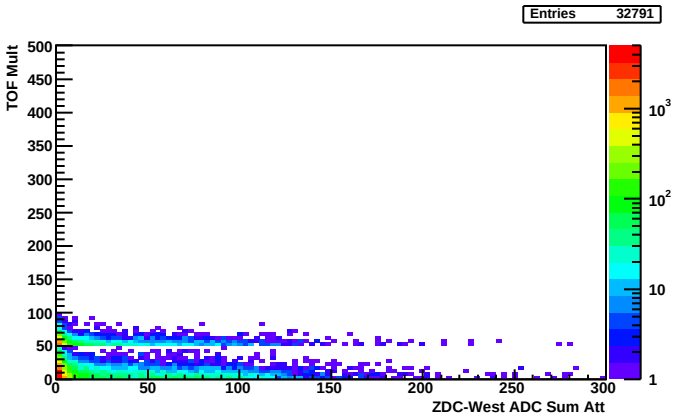

Entries 32791
TOF Mult
BBC-L-East ADC Sum

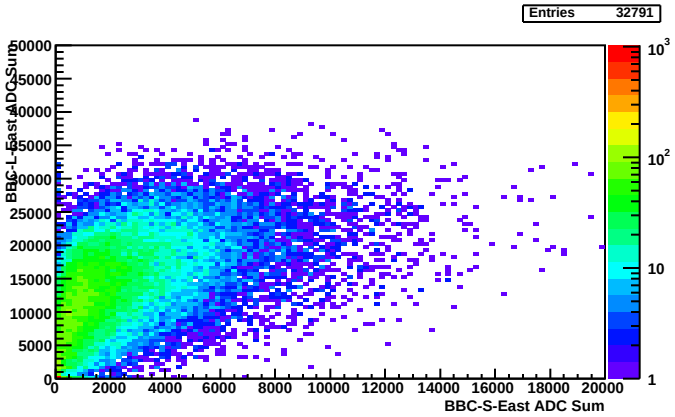

Entries 32791
TOF Mult
BBC-L-West ADC Sum

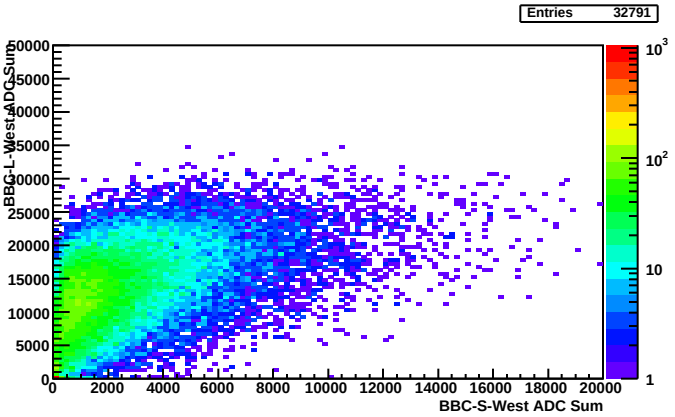

Entries 32791
TOF Mult
BBC-S-East ADC Sum

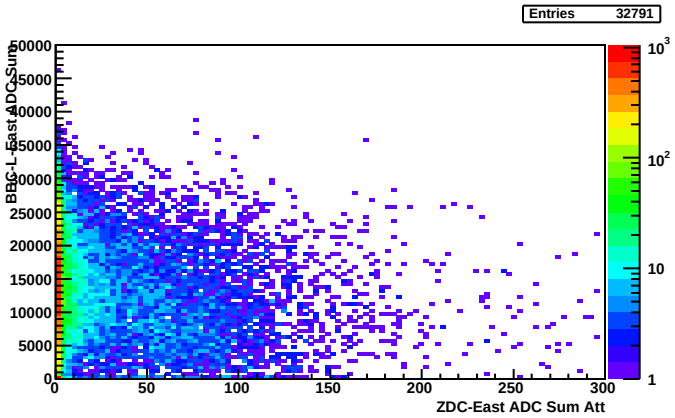

Entries 32791
TOF Mult
BBC-S-West ADC Sum

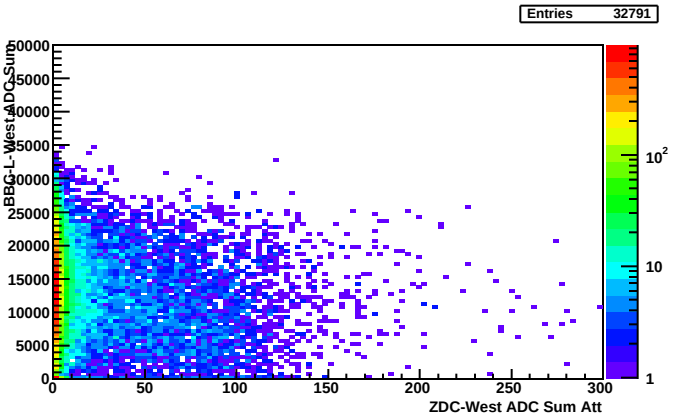

Entries 32791
TOF Mult
ZDC-East ADC Sum Att

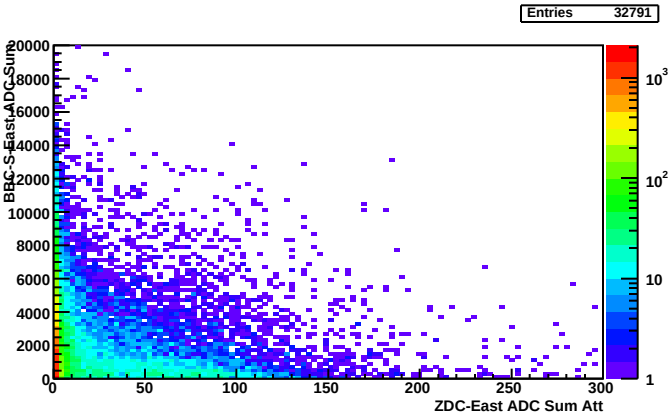

Entries 32791
TOF Mult
ZDC-West ADC Sum Att

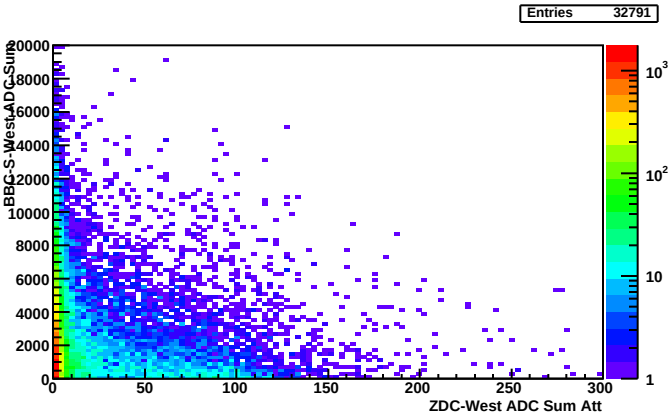

Entries 32791
BBC-L-East ADC Sum
BBC-S-East ADC Sum

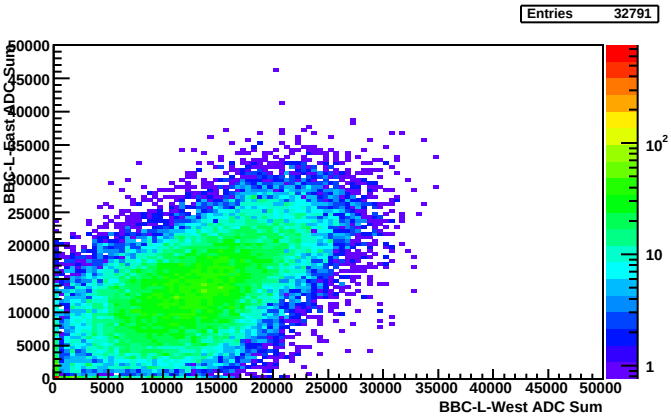

Entries 32791
BBC-L-West ADC Sum
BBC-S-West ADC Sum

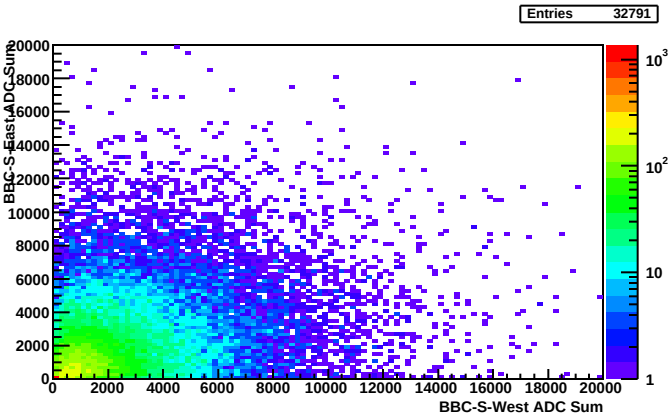

Entries 32791
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

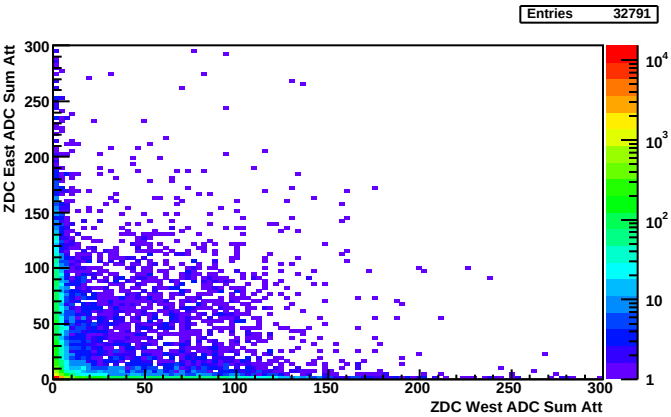

Entries 32791
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

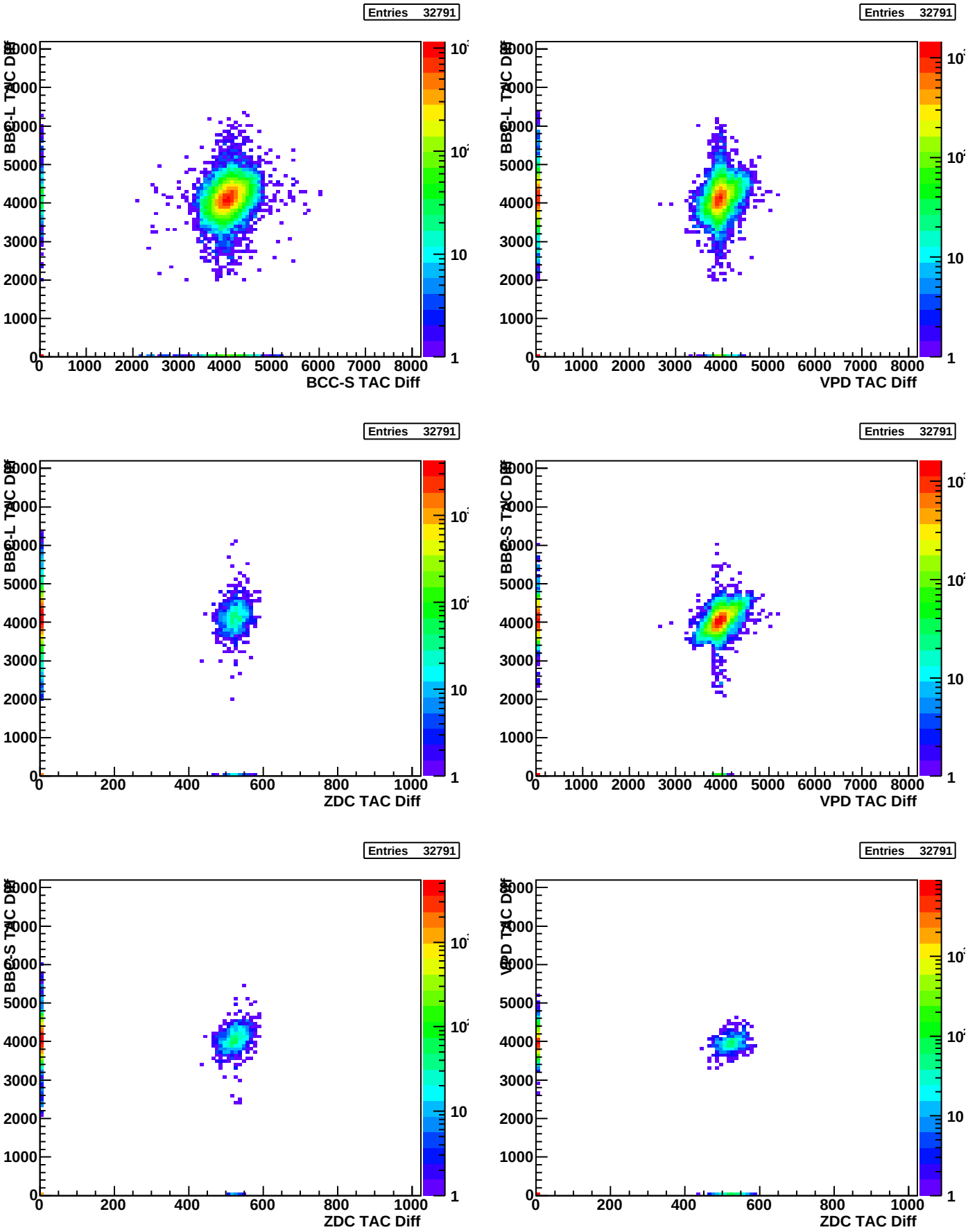

Entries 32791
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

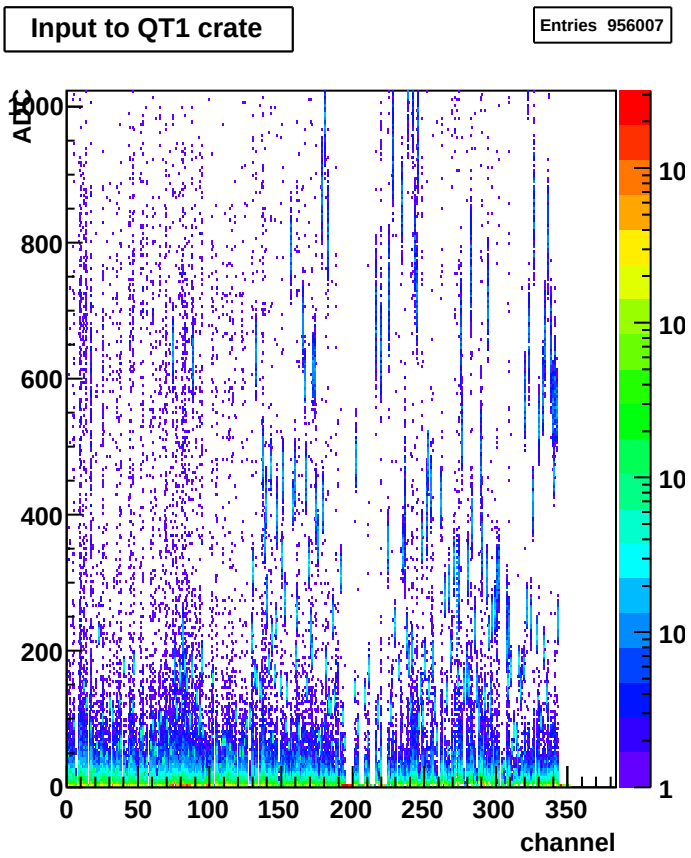

Entries 32791
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

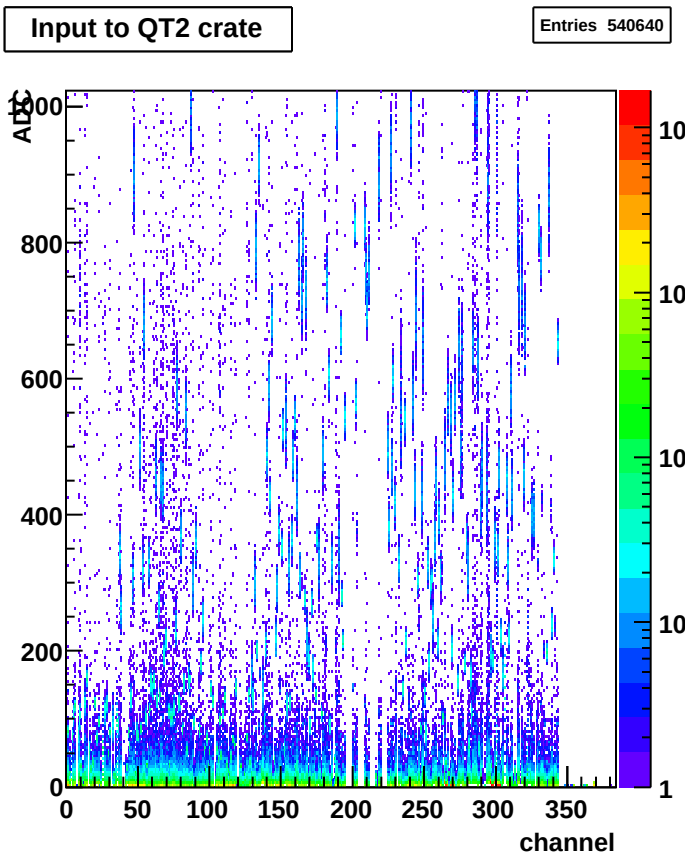

Entries 32791
BBC-L-East ADC Sum
BBC-L-West ADC Sum

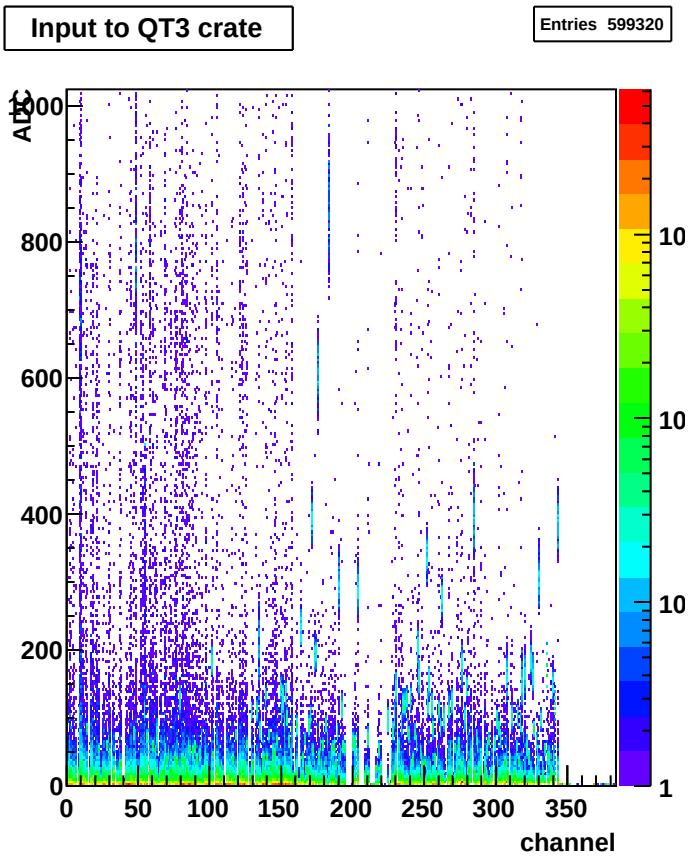

Entries 32791
BBC-S-East ADC Sum
BBC-S-West ADC Sum

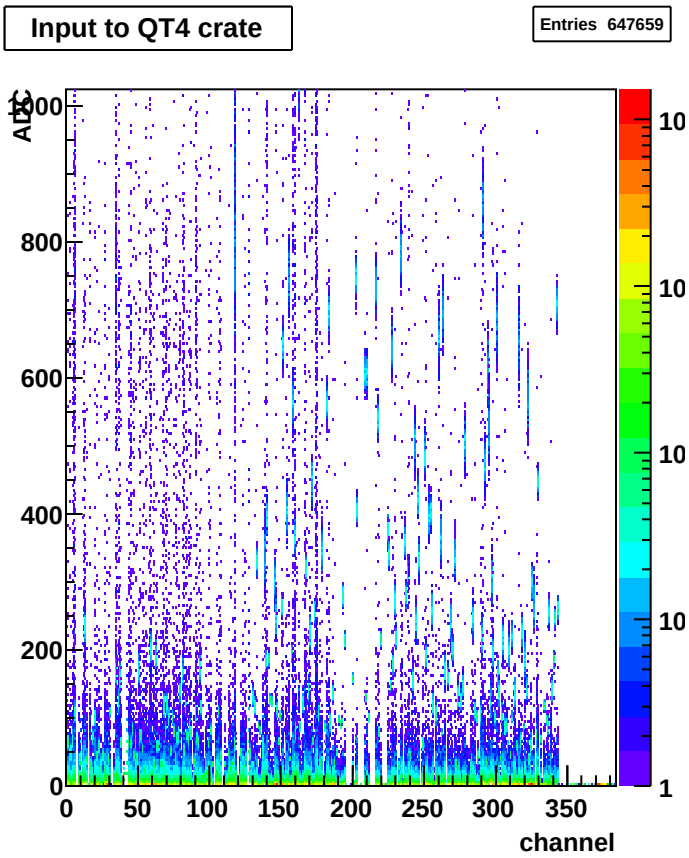

Entries 32791
ZDC East ADC Sum Att
ZDC West ADC Sum Att

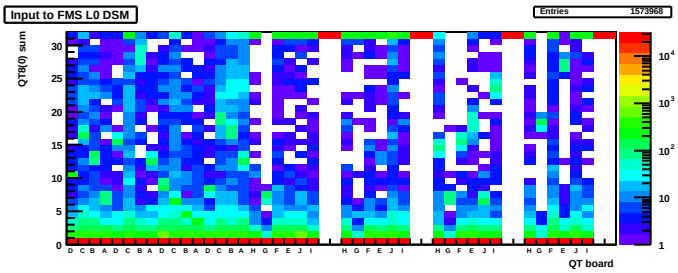

Entries 32791
BBC-L TAC Diff
BCC-S TAC Diff
Entries 32791
BBC-L TAC Diff
VPD TAC Diff
Entries 32791
BBC-L TAC Diff
ZDC TAC Diff
Entries 32791
BBC-S TAC Diff
VPD TAC Diff
Entries 32791
BBC-S TAC Diff
ZDC TAC Diff
Entries 32791
VPD TAC Diff
ZDC TAC Diff

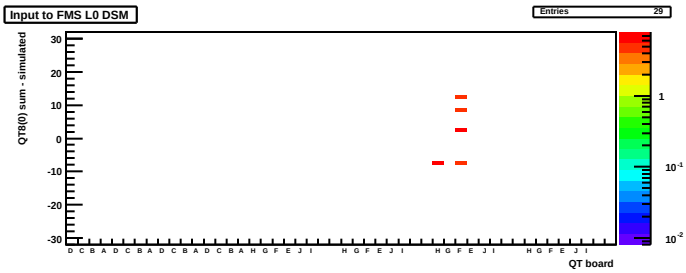

Input to QT1 crate
Entries 956007
ADC
channel

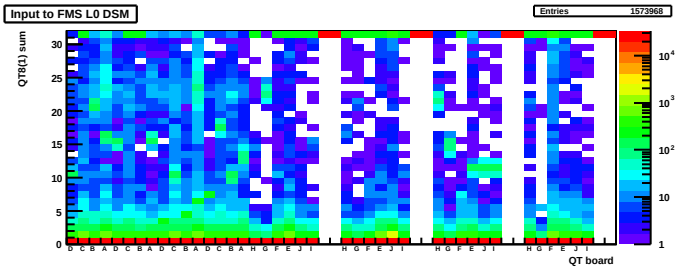

Input to QT2 crate
Entries 540640
ADC
channel

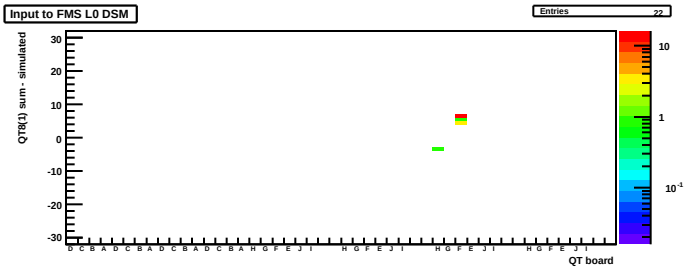

Input to QT3 crate
Entries 599320
ADC
channel

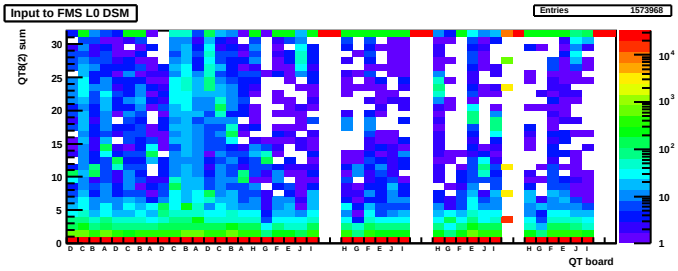

Input to QT4 crate
Entries 647659
ADC
channel

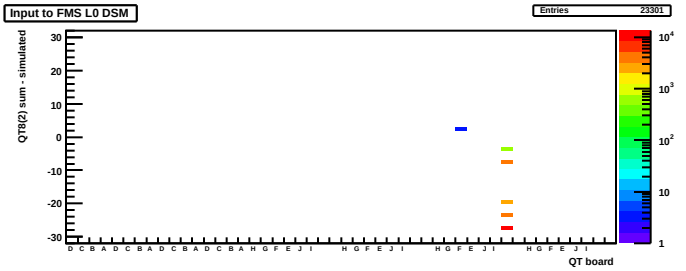

Input to FMS L0 DSM
Entries 1573968
QT8(0) sum
QT board

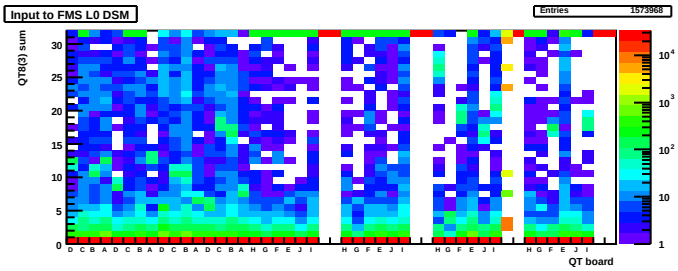

Input to FMS L0 DSM
Entries 29
QT8(0) sum - simulated
QT board

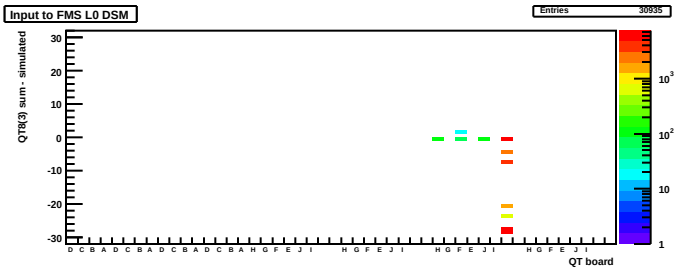

Input to FMS L0 DSM
Entries 1573968
QT8(1) sum
QT board

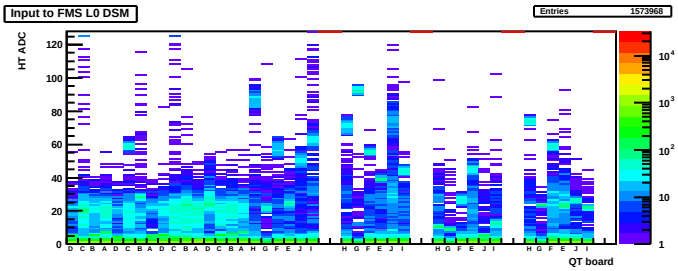

Input to FMS L0 DSM
Entries 22
QT8(1) sum - simulated
QT board

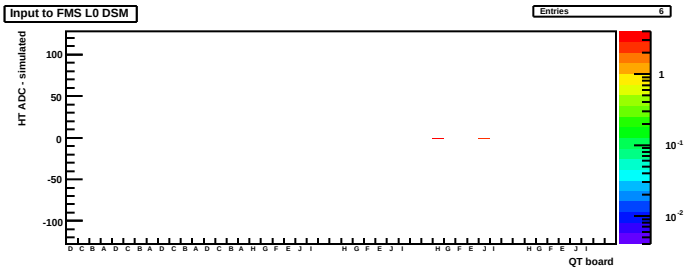

Input to FMS L0 DSM
Entries 1573968
QT8(2) sum
QT board

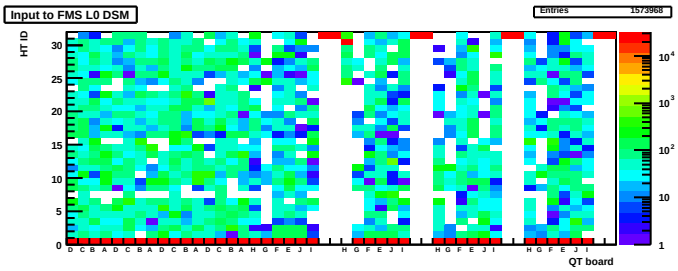

Input to FMS L0 DSM
Entries 23301
QT8(2) sum - simulated
QT board

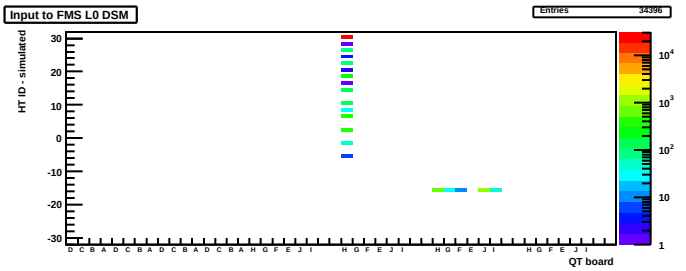

Input to FMS L0 DSM
Entries 1573968
QT8(3) sum
QT board

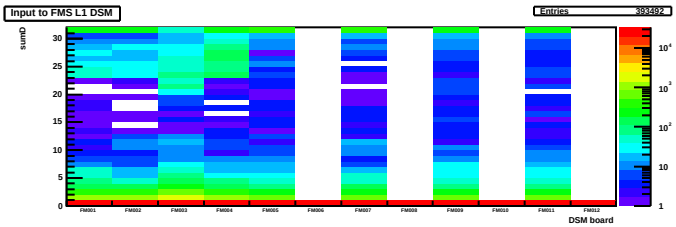

Input to FMS L0 DSM
Entries 30935
QT8(3) sum - simulated
QT board

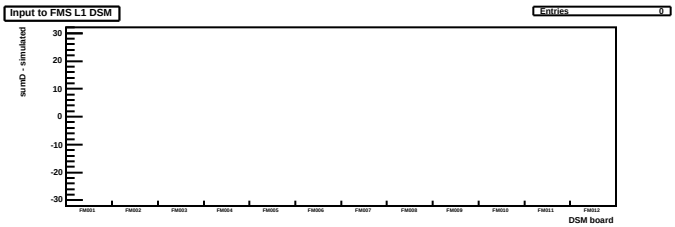

Input to FMS L0 DSM
Entries 1573968
HT ADC
QT board

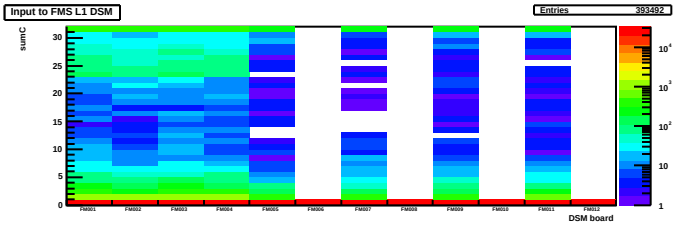

Input to FMS L0 DSM
Entries 6
HT ADC - simulated
QT board

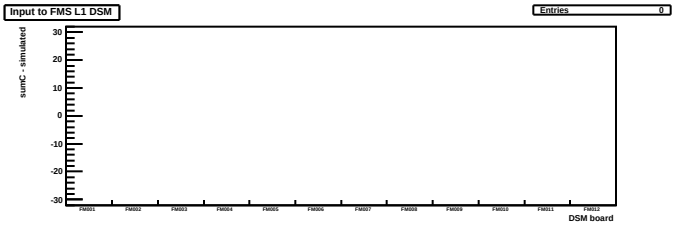

Input to FMS L0 DSM
Entries 1573968
HT ID
QT board

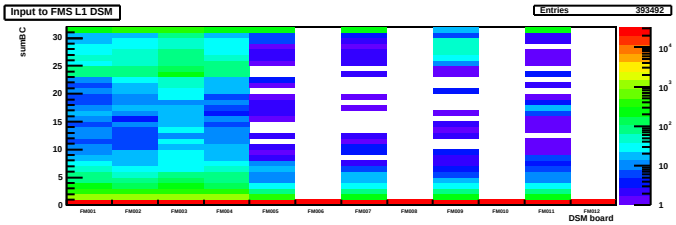

Input to FMS L0 DSM
Entries 34396
HT ID - simulated
QT board

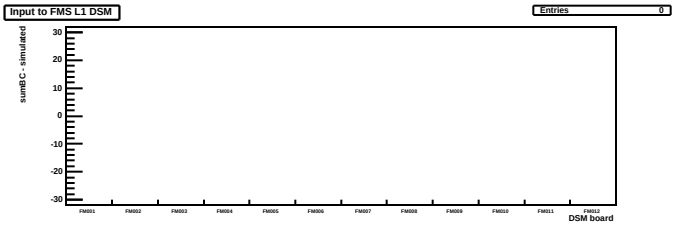

Input to FMS L1 DSM
Entries 393492
sumD
DSM board

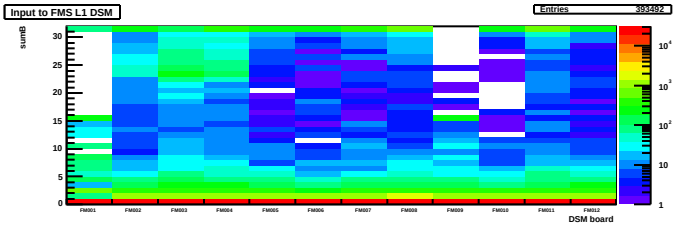

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

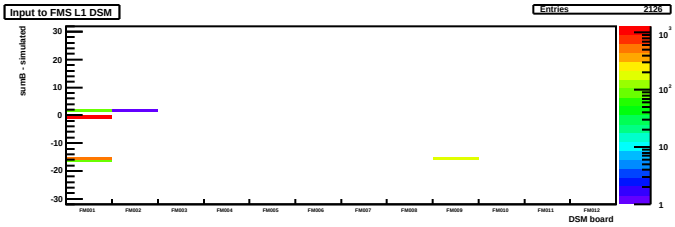

Input to FMS L1 DSM
Entries 393492
sumC
DSM board

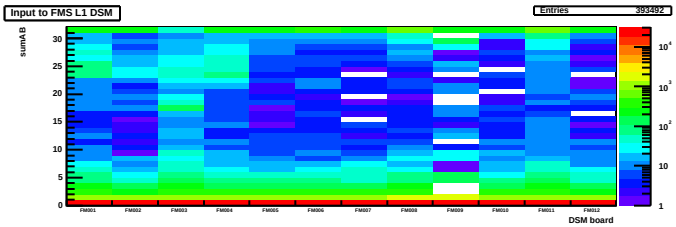

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

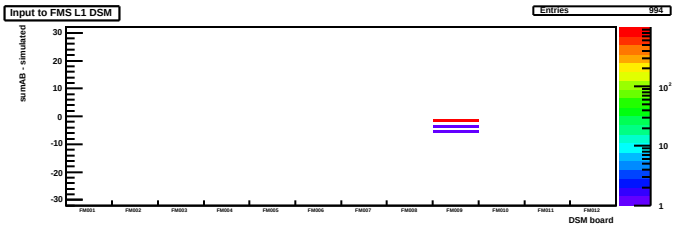

Input to FMS L1 DSM
Entries 393492
sumBC
DSM board

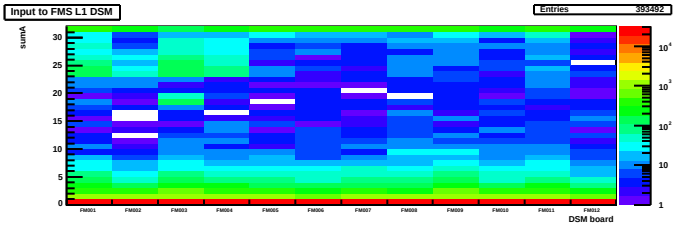

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

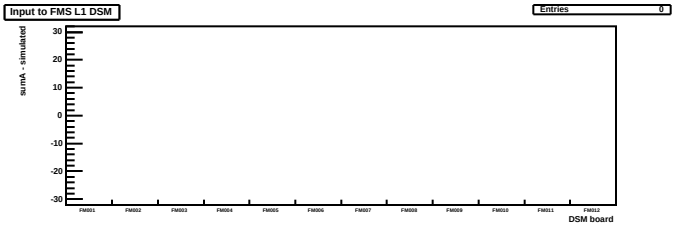

Input to FMS L1 DSM
Entries 393492
sumB
DSM board

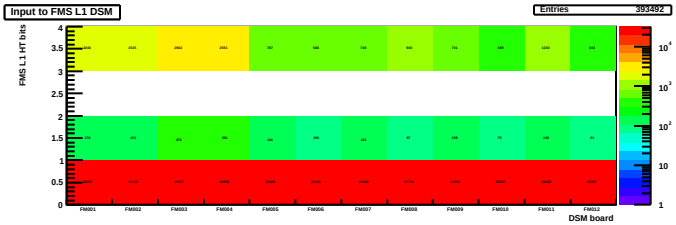

Input to FMS L1 DSM
Entries 2126
sumB - simulated
DSM board

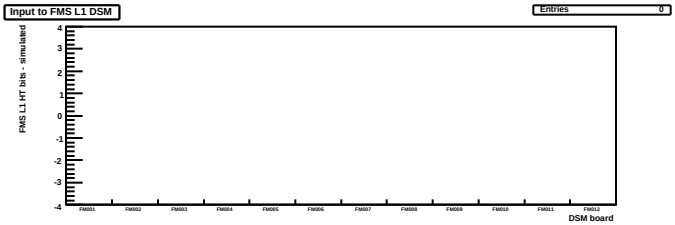

Input to FMS L1 DSM
Entries 393492
sumAB
DSM board

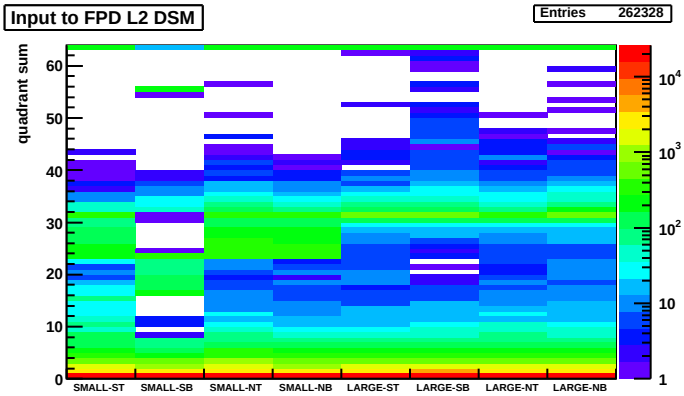

Input to FMS L1 DSM
Entries 994
sumAB - simulated
DSM board

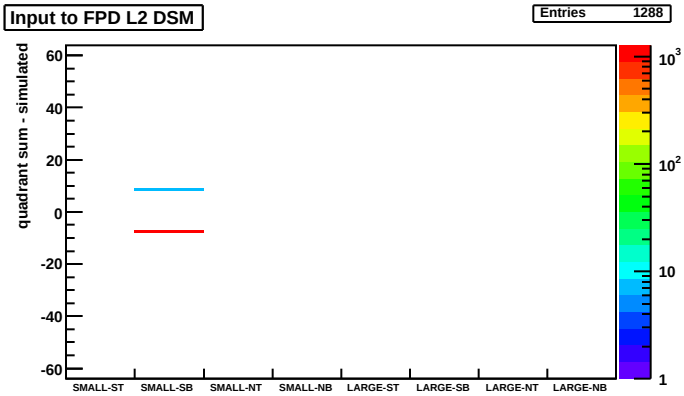

Input to FMS L1 DSM
Entries 393492
sumA
DSM board

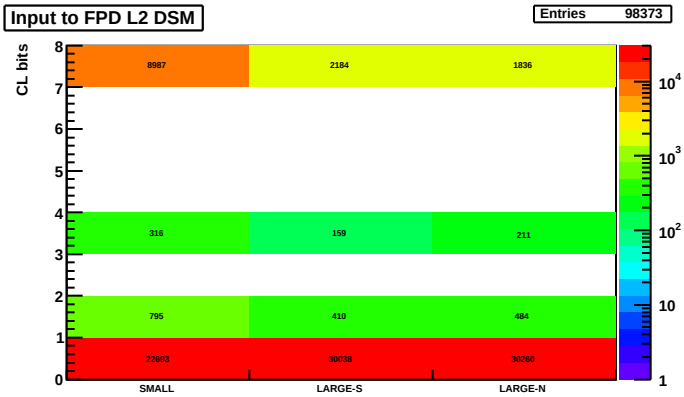

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

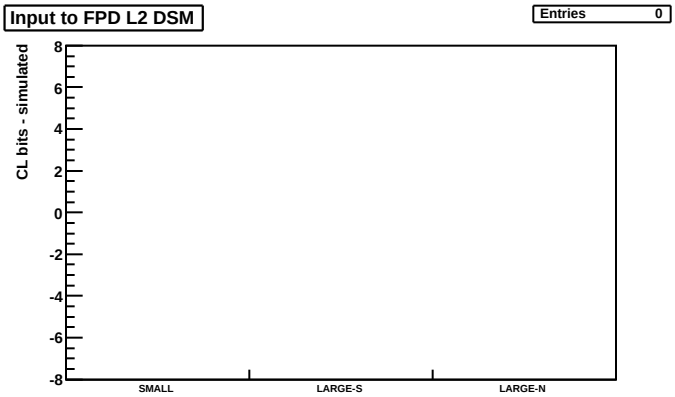

Input to FMS L1 DSM
Entries 393492
FMS L1 HT bits
DSM board

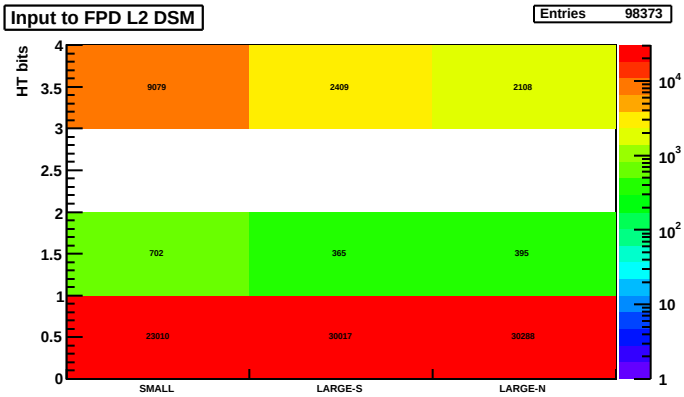

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

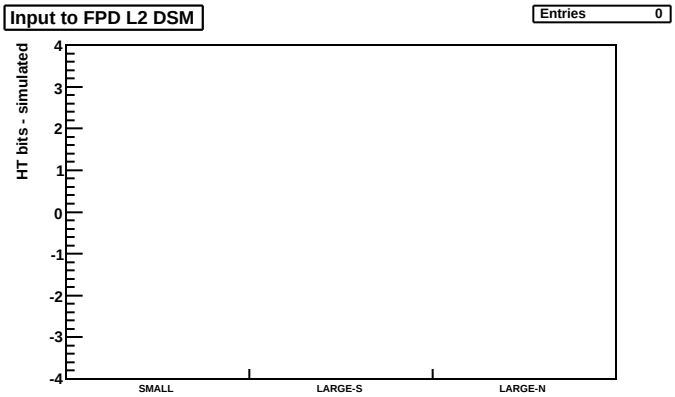

Input to FPD L2 DSM
Entries 262328
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

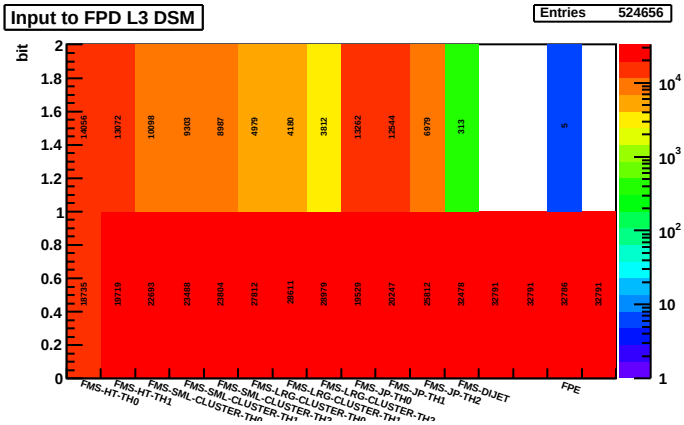

Input to FPD L2 DSM
Entries 1288
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

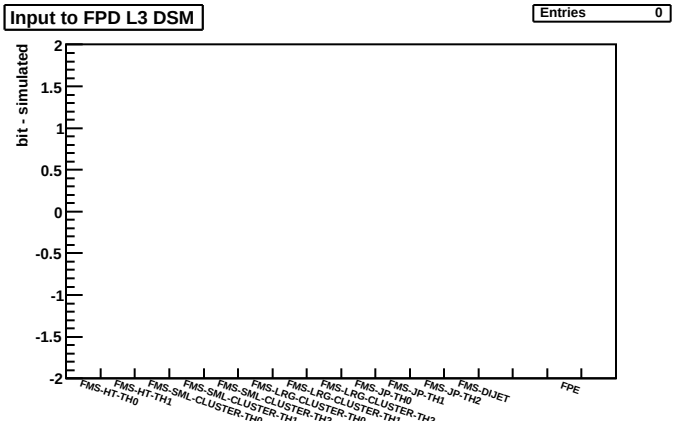

Input to FPD L2 DSM
Entries 98373
CL bits
8987
2184
1836
316
159
211
795
410
484
22693
30038
30260
SMALL
LARGE-S
LARGE-N

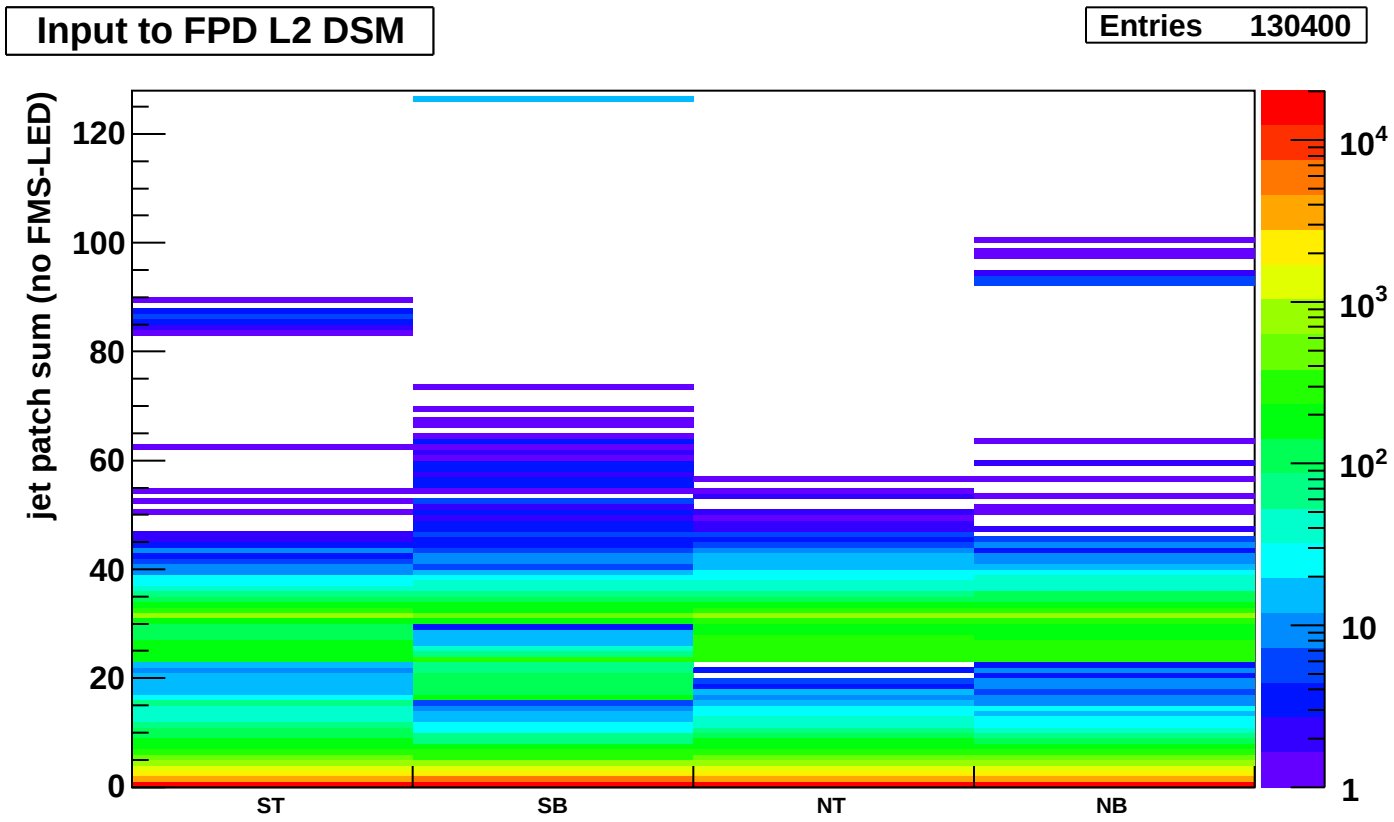

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

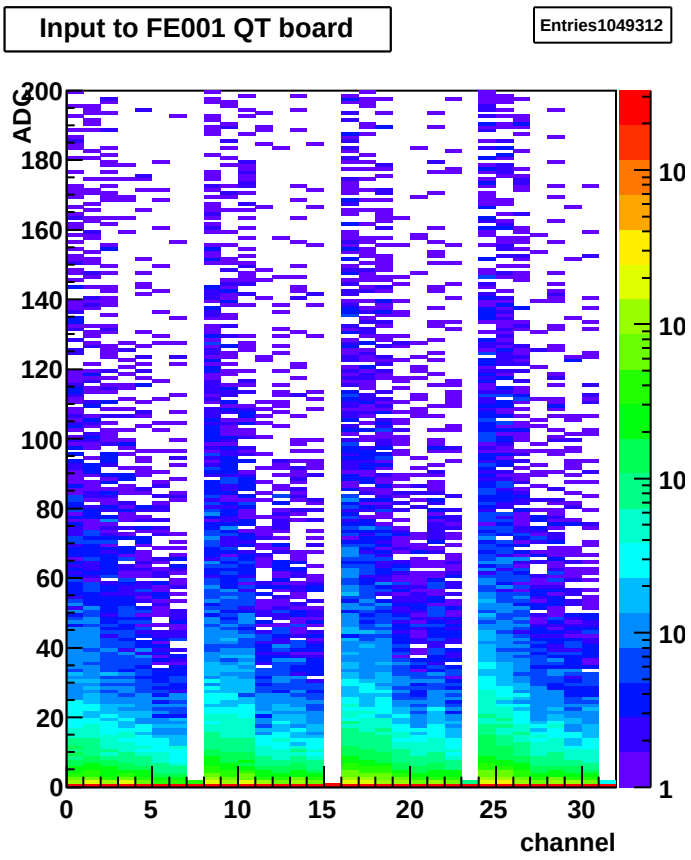

Input to FPD L2 DSM
Entries 98373
HT bits
9079
2409
2108
702
365
395
23010
30017
30288
SMALL
LARGE-S
LARGE-N

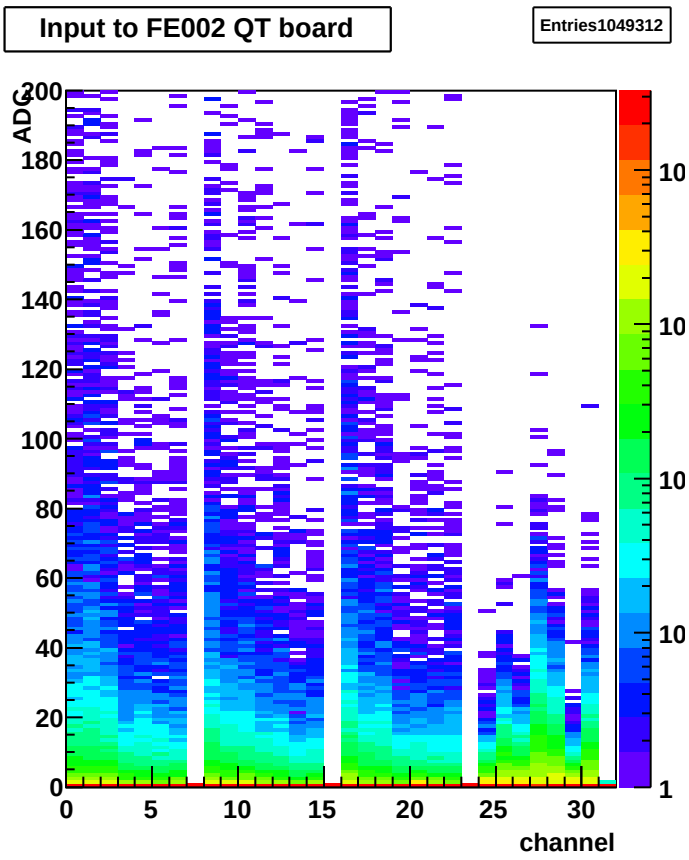

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

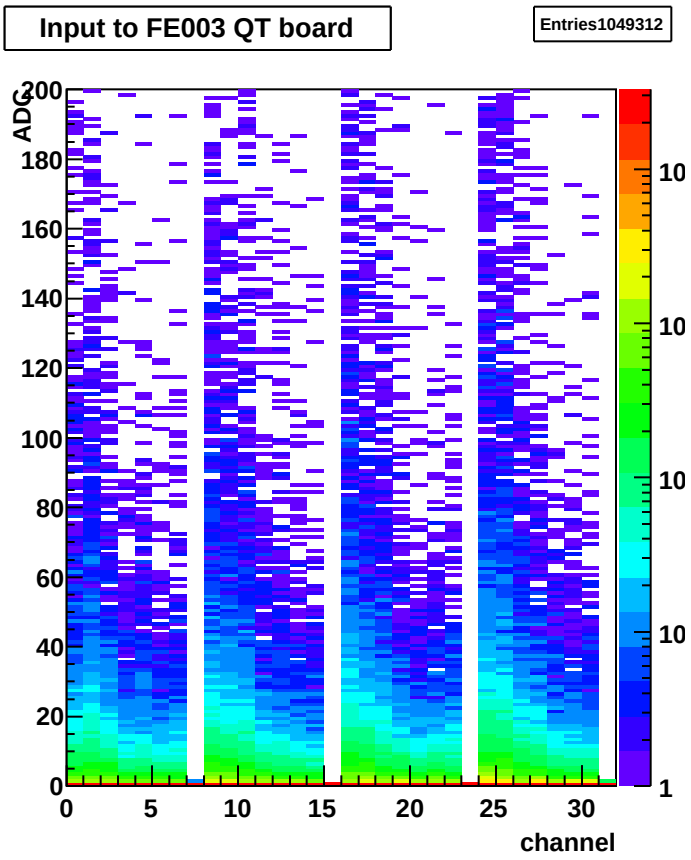

Input to FPD L3 DSM
Entries 524656
bit
14095
13072
10098
9303
8987
4979
4180
3812
13262
12544
6979
313
5
18735
19710
22693
22488
22804
27812
28611
28979
19529
20247
25812
32478
32791
32791
32786
32791
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

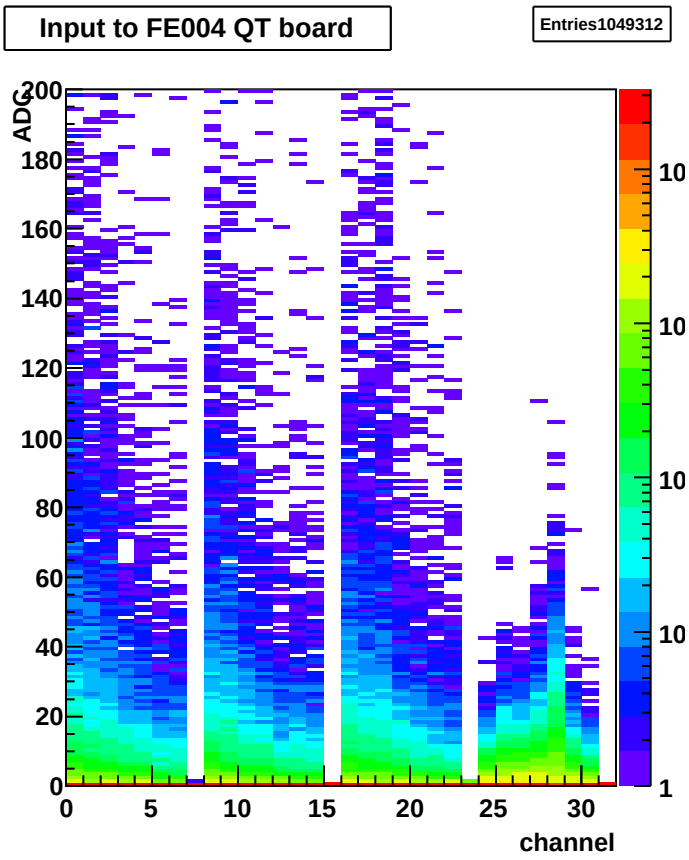

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

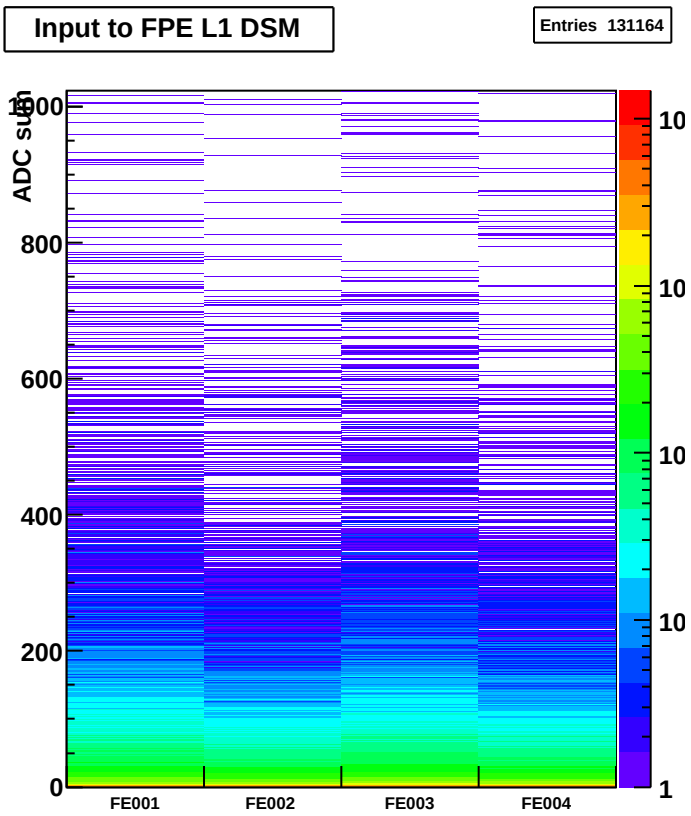

Input to FPD L2 DSM
Entries 130400
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

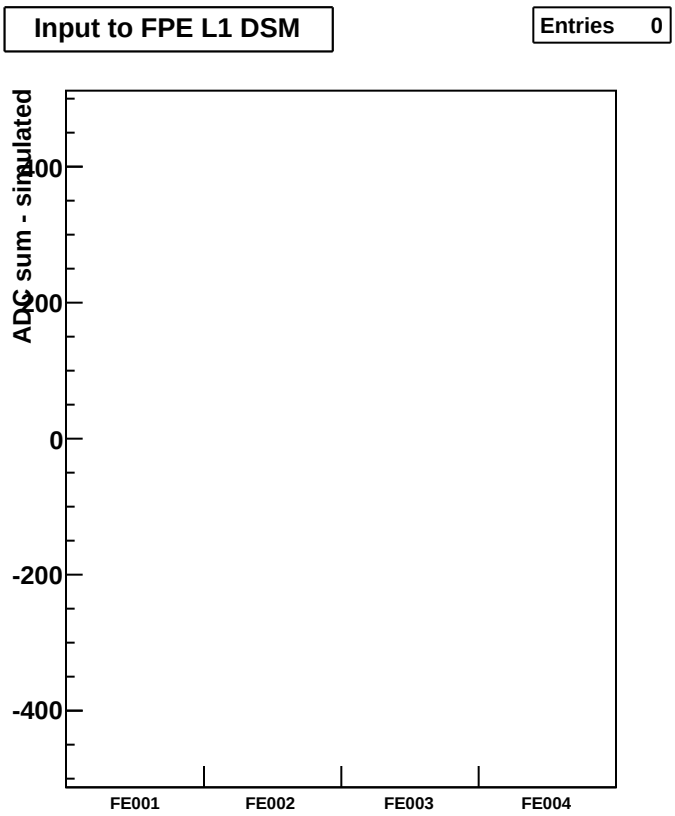

Input to FE001 QT board
Entries1049312
ADC
channel

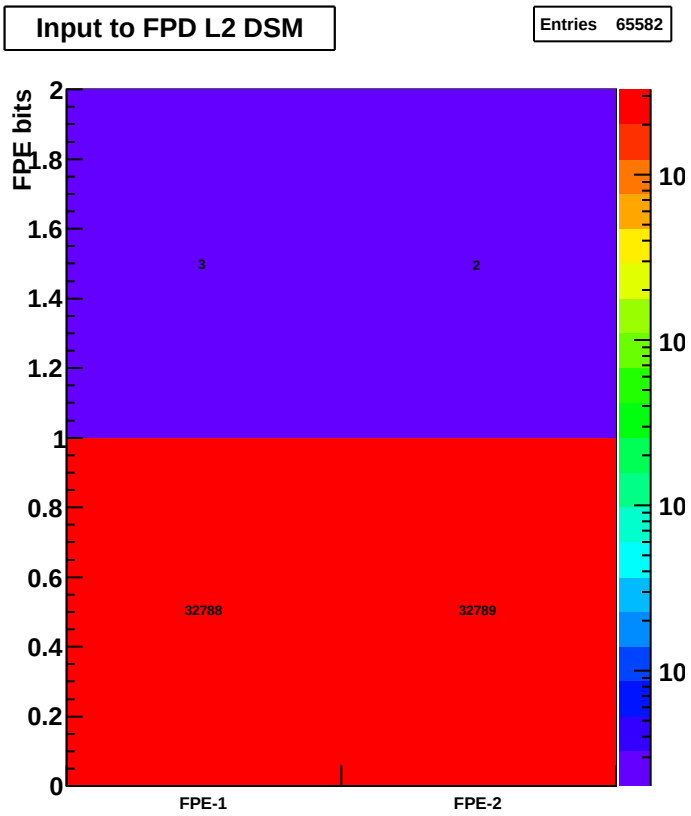

Input to FE002 QT board
Entries1049312
ADC
channel

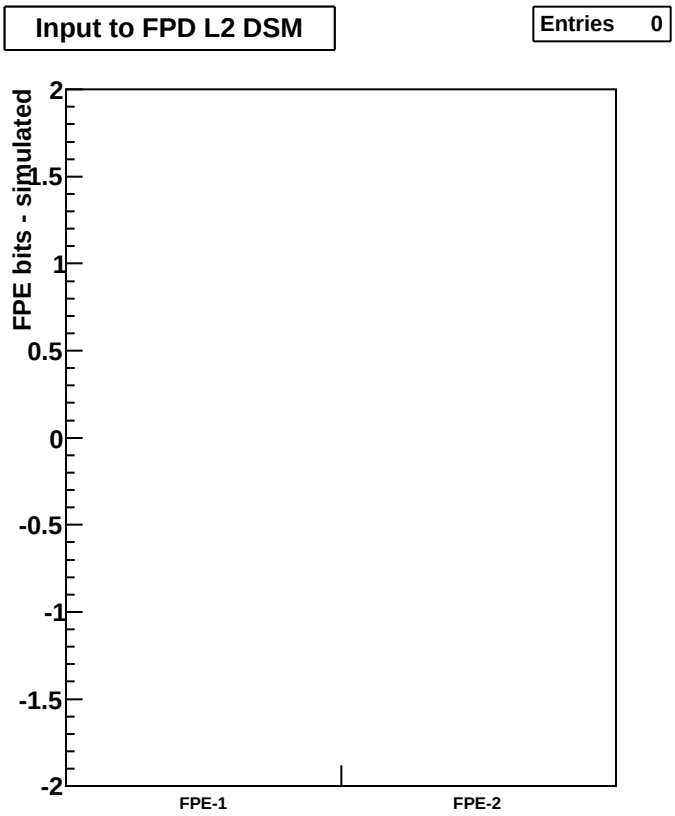

Input to FE003 QT board
Entries1049312
ADC
channel


Input to FE004 QT board
Entries1049312
ADC
channel


Input to FPE L1 DSM
Entries 131164
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 65582
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3
2
32788
32789
FPE-1
FPE-2
10
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2