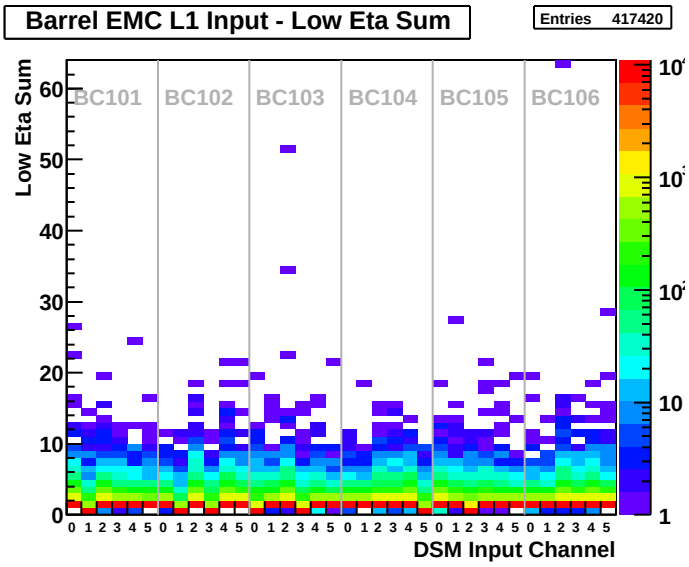
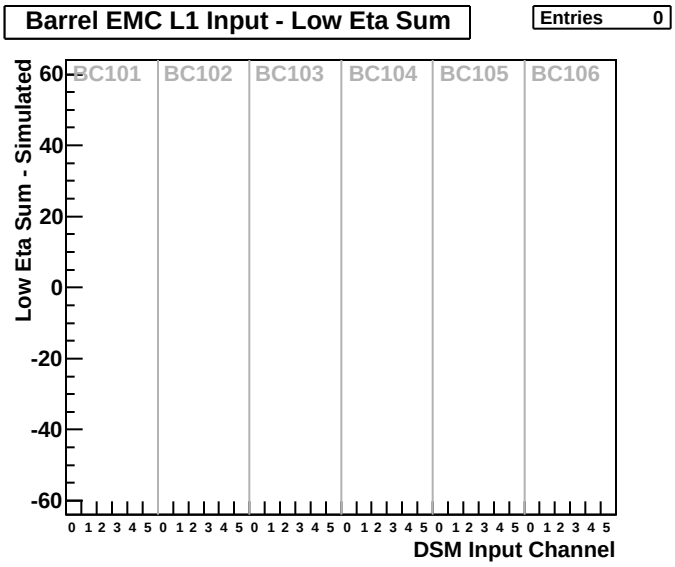
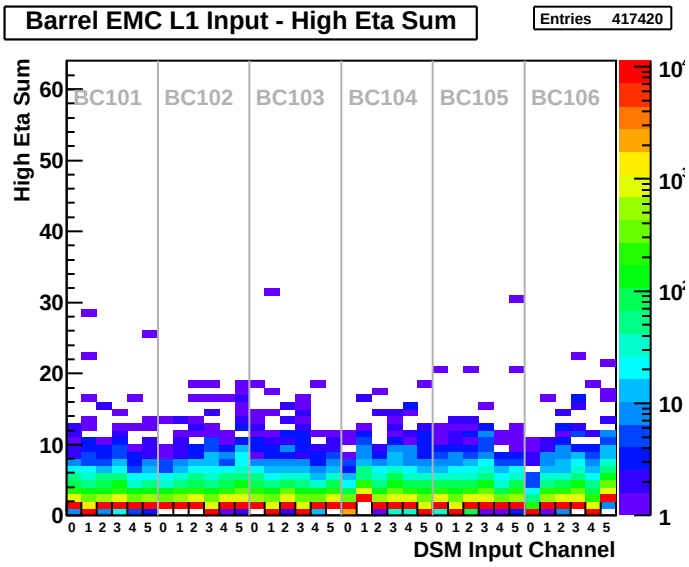
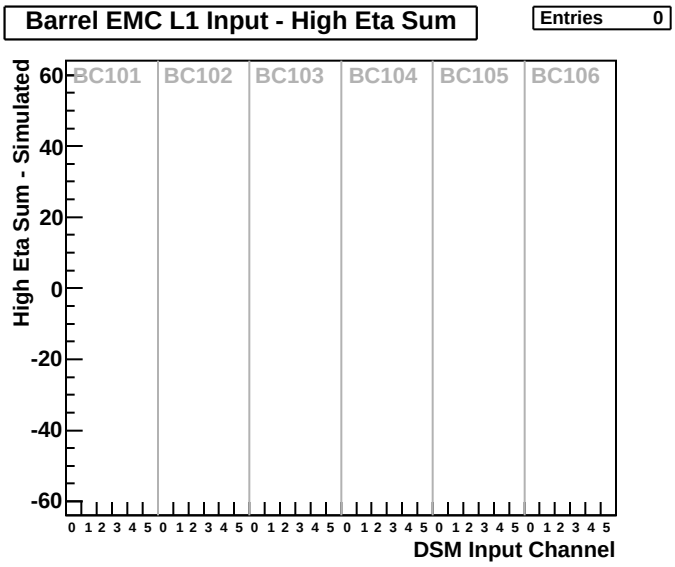
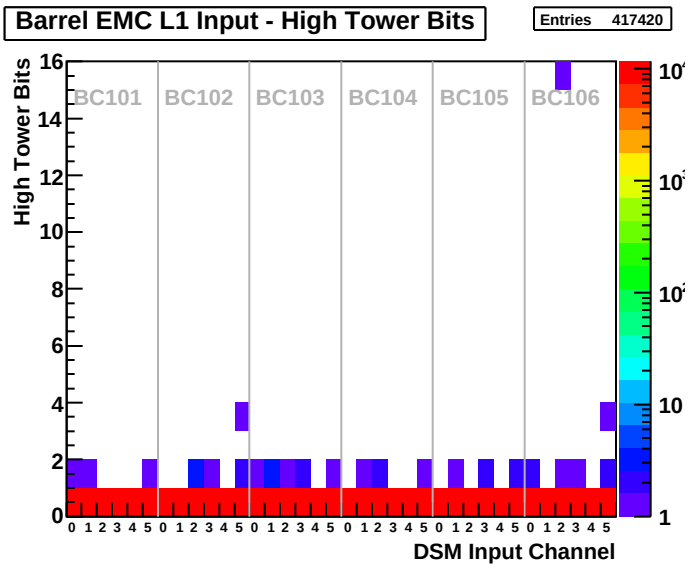
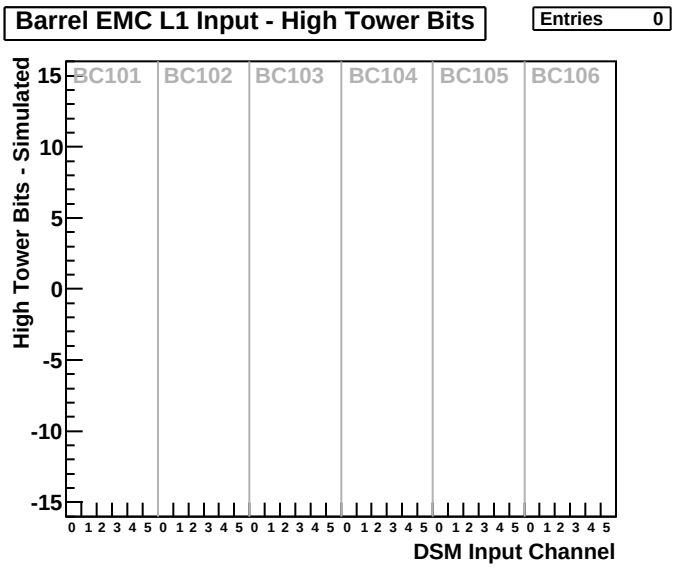

Barrel EMC L1 Input - Low Eta Sum
Entries 417420
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

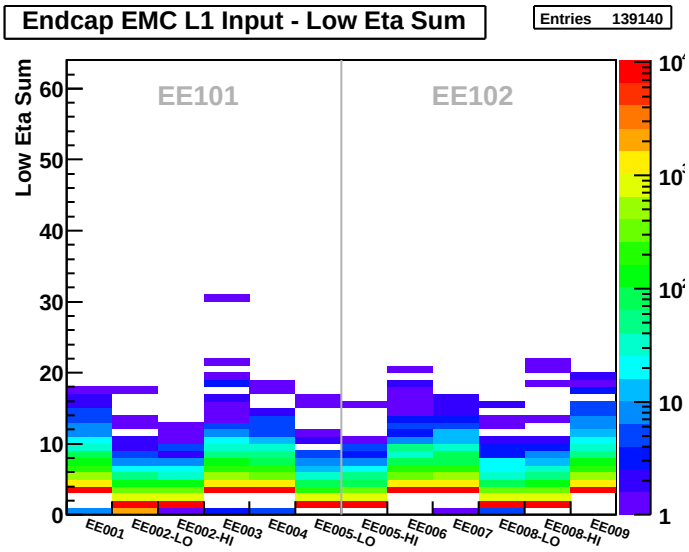

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

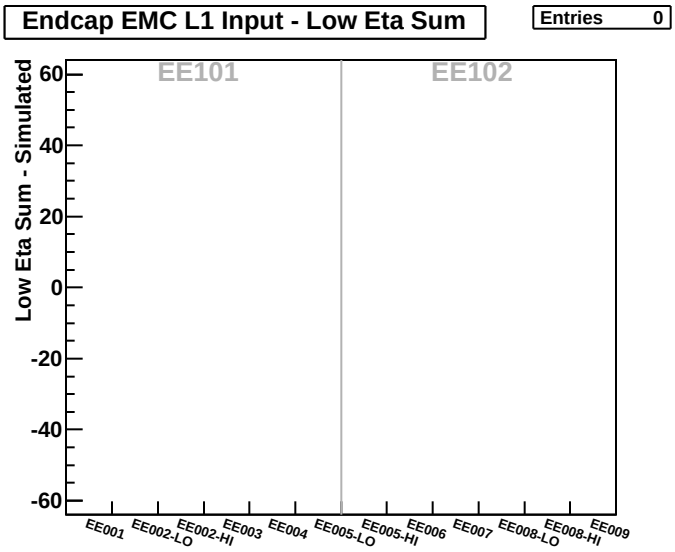

Barrel EMC L1 Input - High Eta Sum
Entries 417420
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

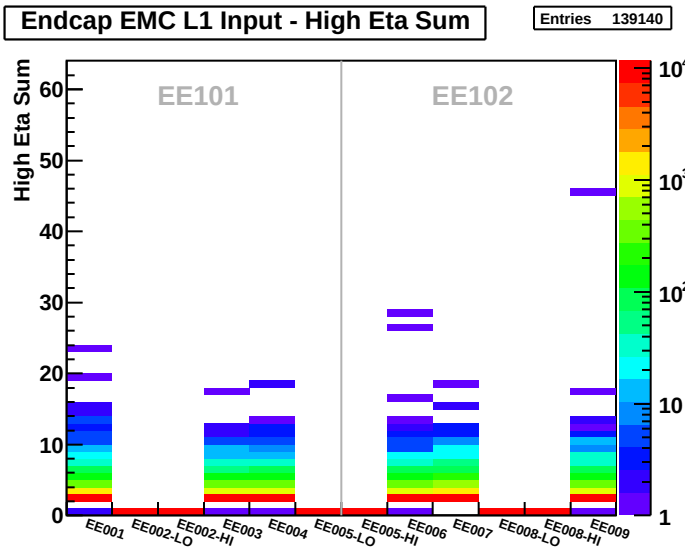

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

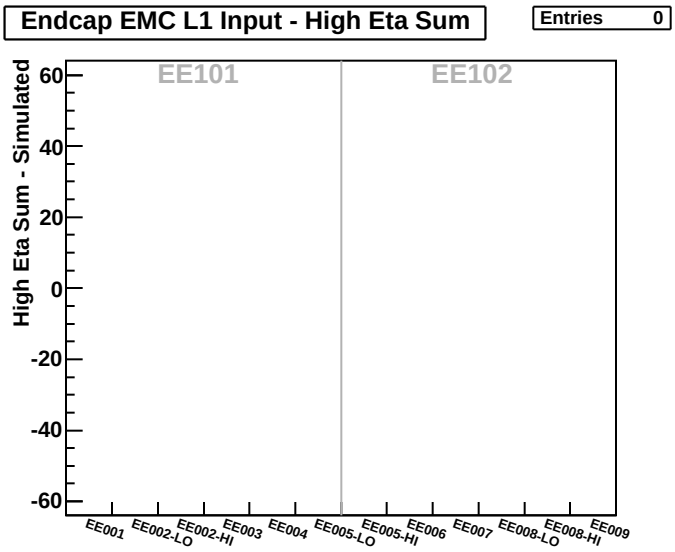

Barrel EMC L1 Input - High Tower Bits
Entries 417420
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

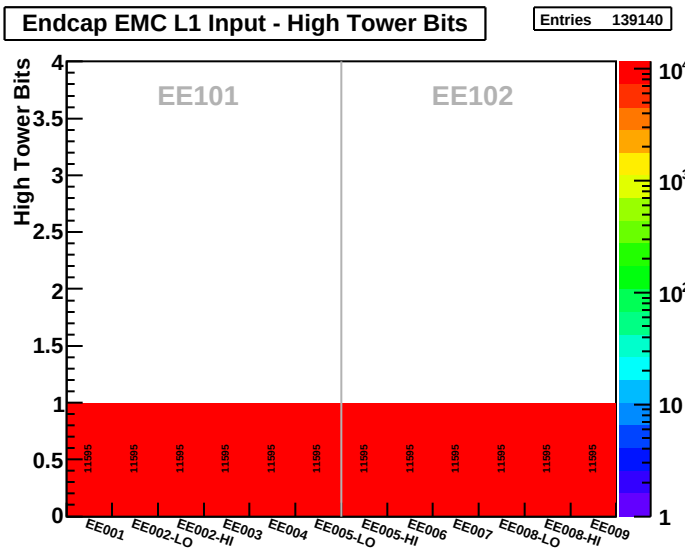

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

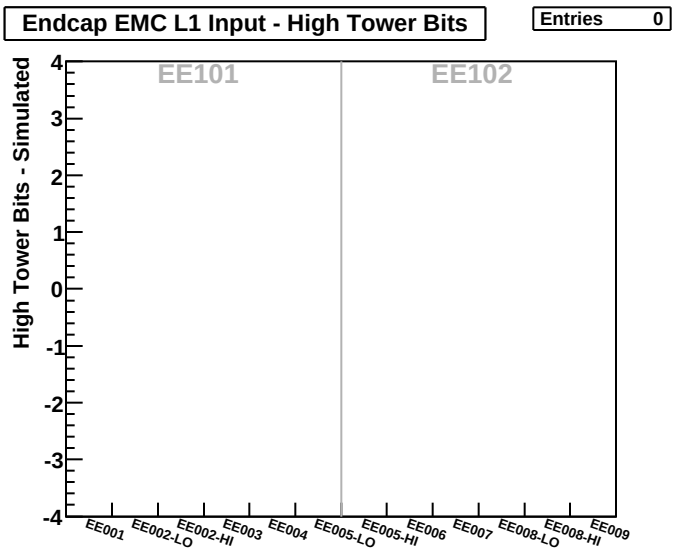

Endcap EMC L1 Input - Low Eta Sum
Entries 139140
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

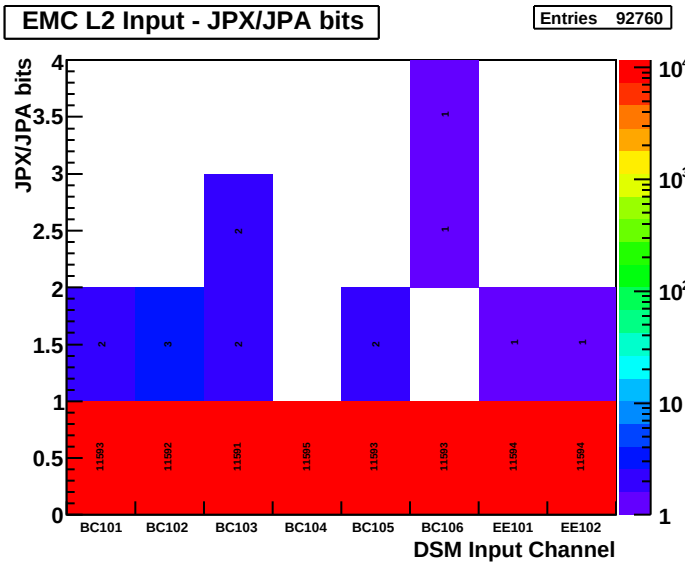

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

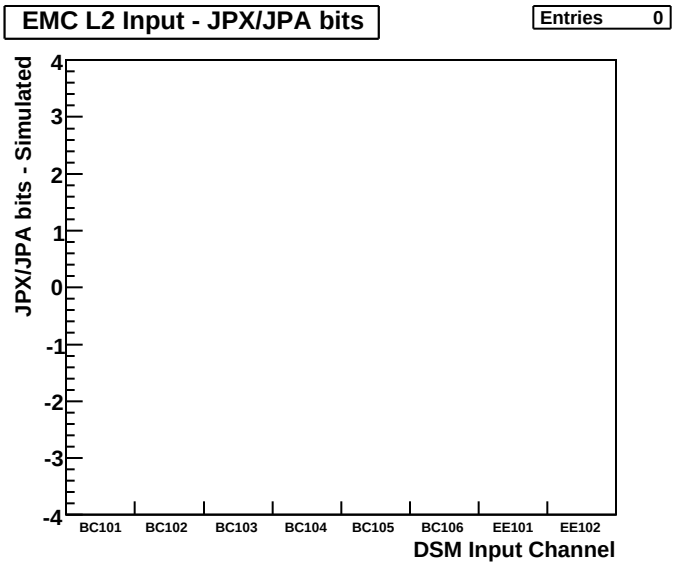

Endcap EMC L1 Input - High Eta Sum
Entries 139140
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

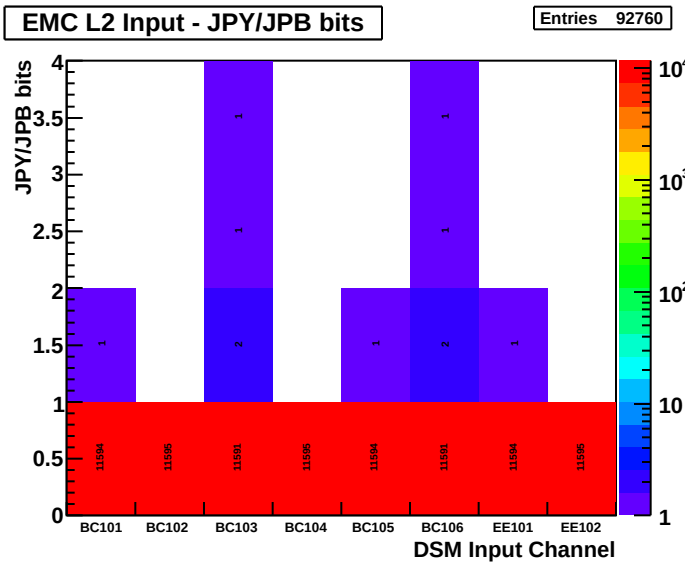

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

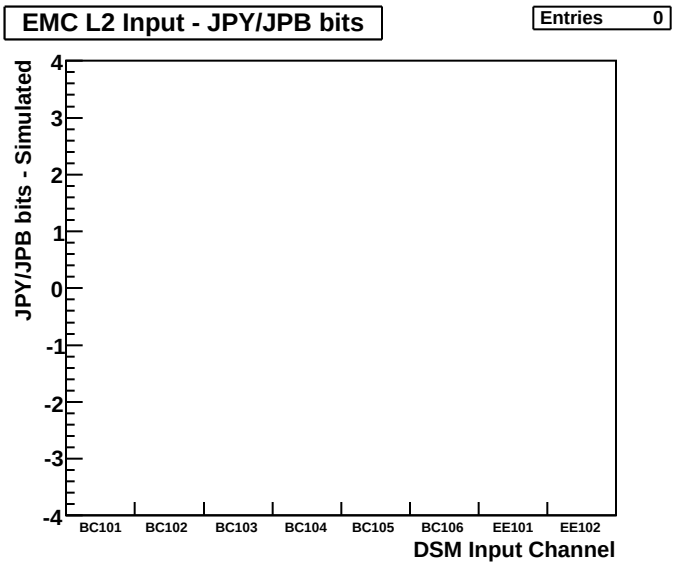

Endcap EMC L1 Input - High Tower Bits
Entries 139140
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

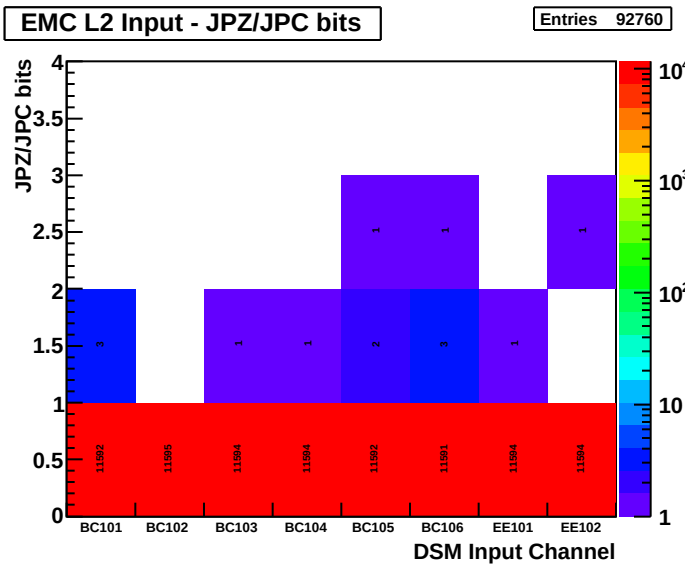

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

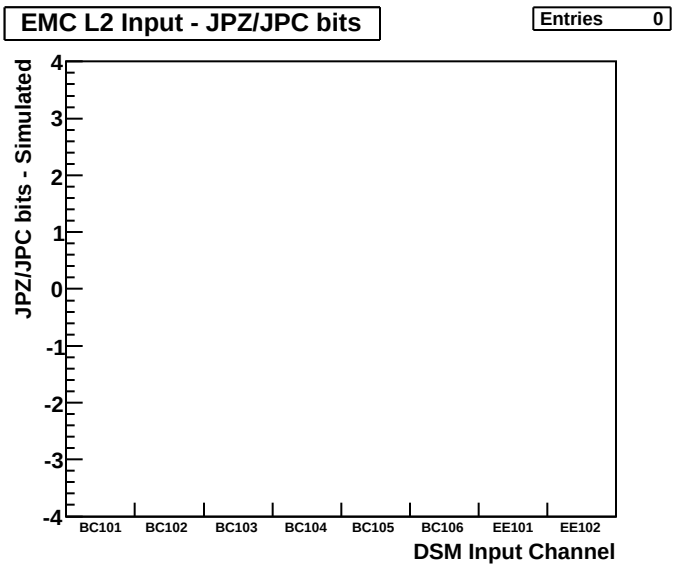

EMC L2 Input - JPX/JPA bits
Entries 92760
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

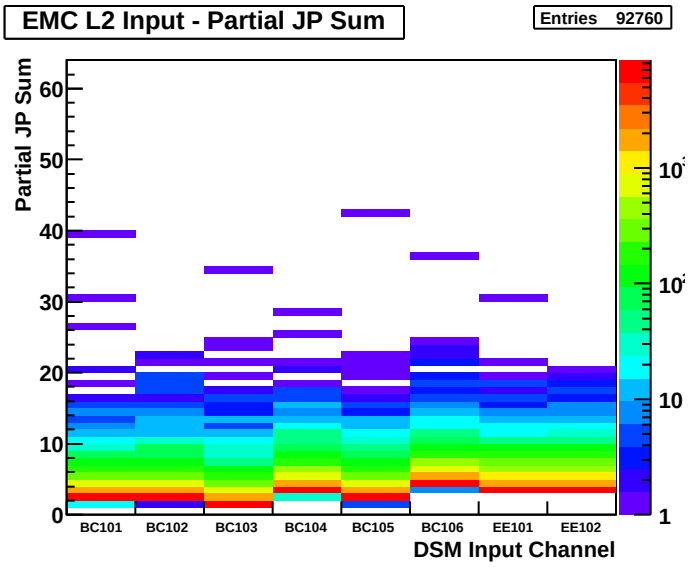

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

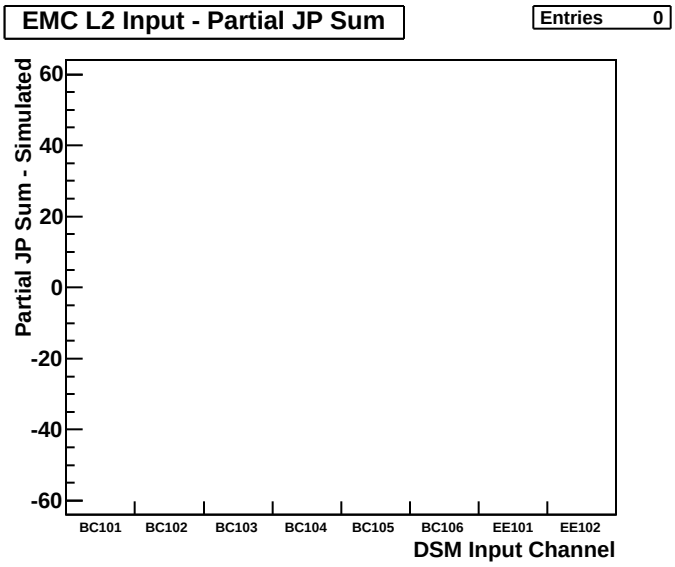

EMC L2 Input - JPY/JPB bits
Entries 92760
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

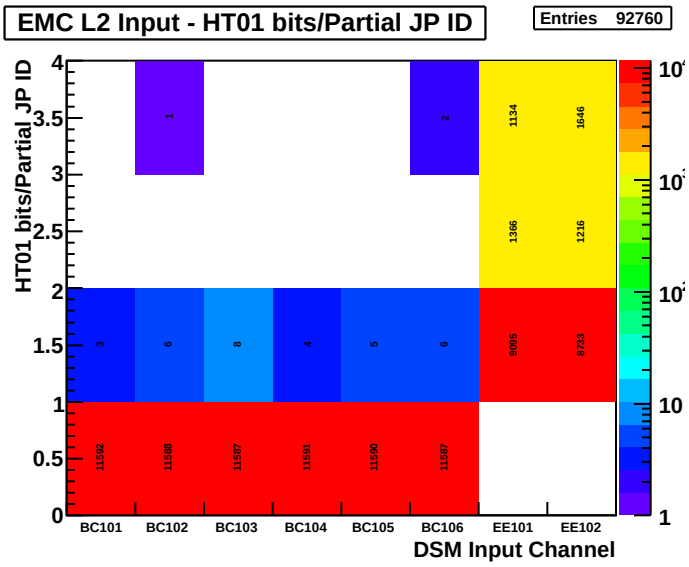

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

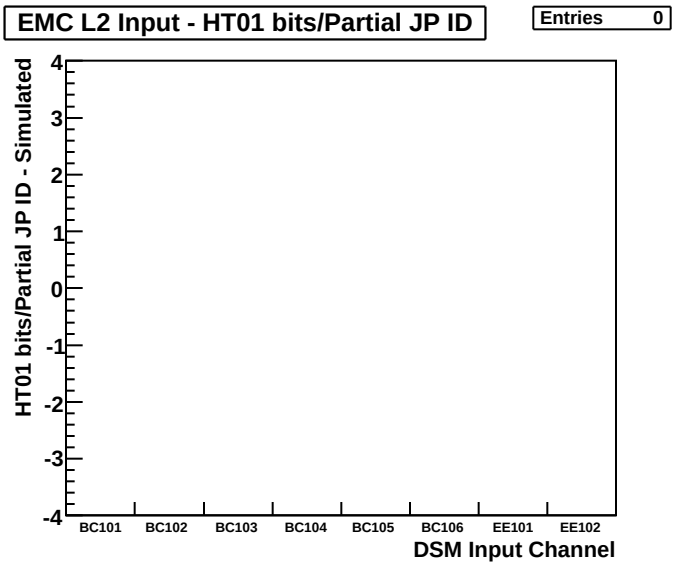

EMC L2 Input - JPZ/JPC bits
Entries 92760
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

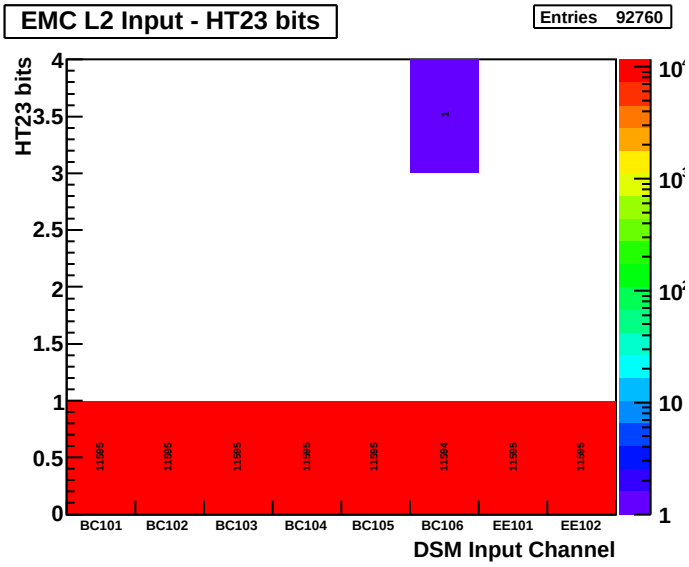

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

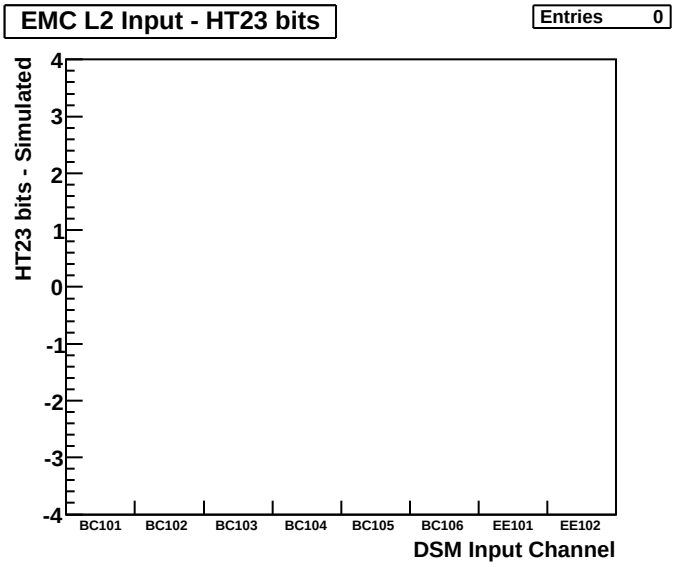

EMC L2 Input - Partial JP Sum
Entries 92760
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

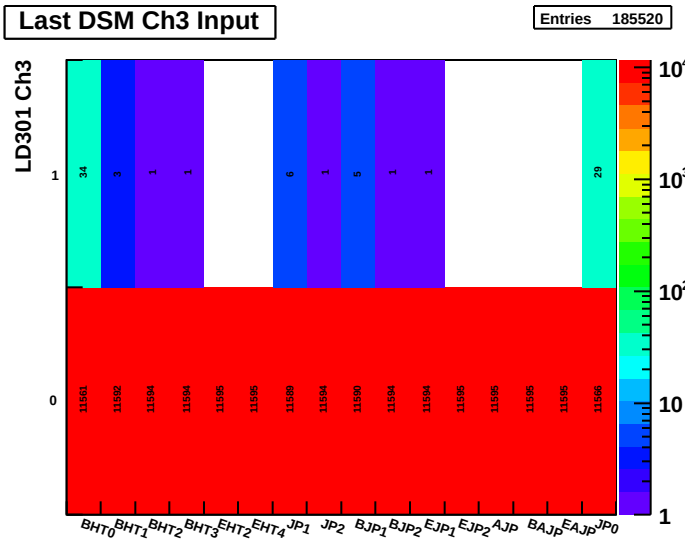

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

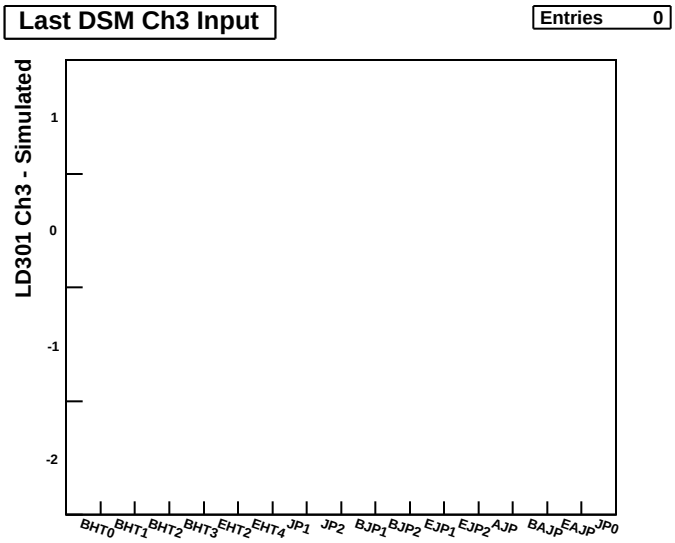

EMC L2 Input - HT01 bits/Partial JP ID
Entries 92760
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

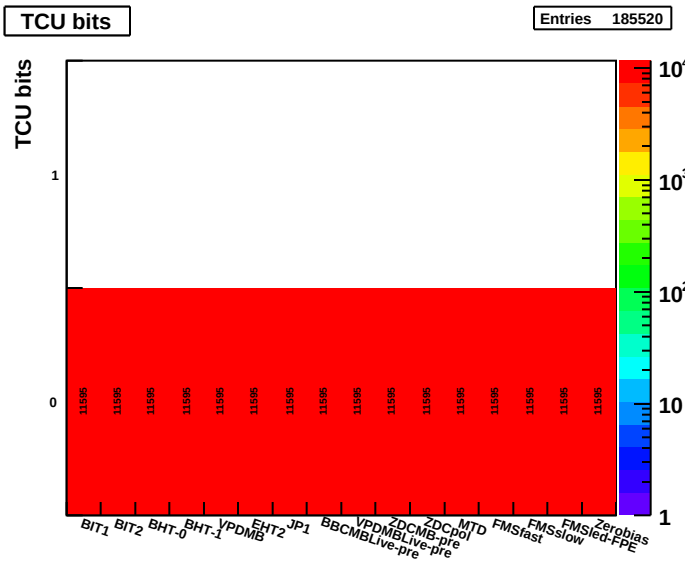

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

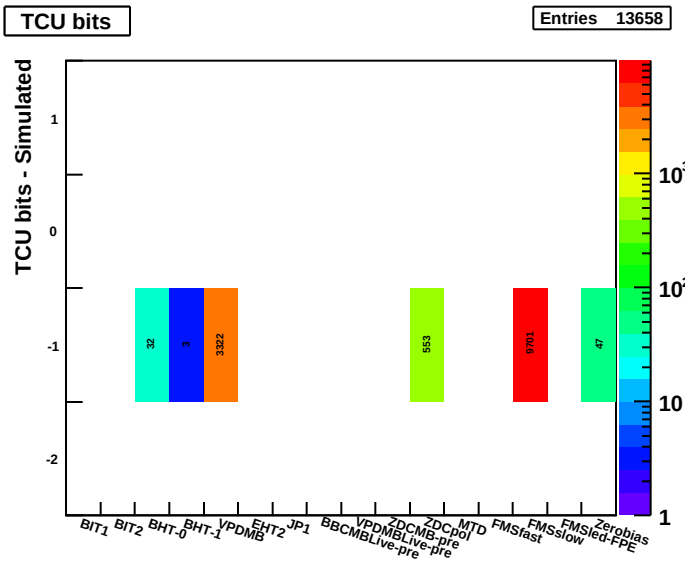

EMC L2 Input - HT23 bits
Entries 92760
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

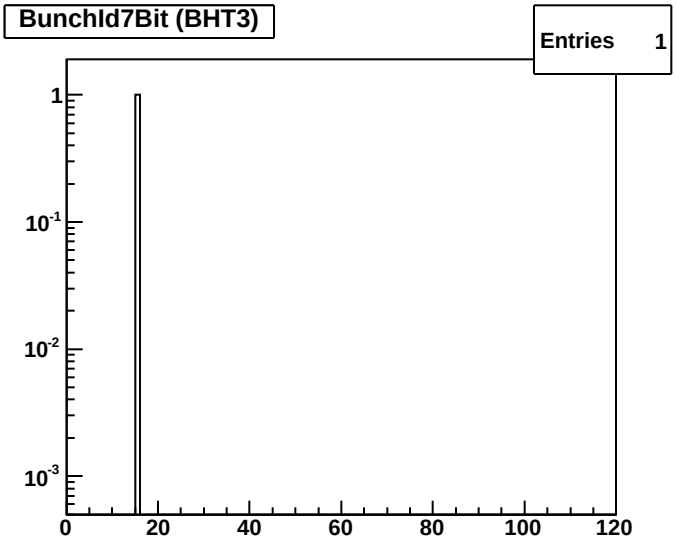

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

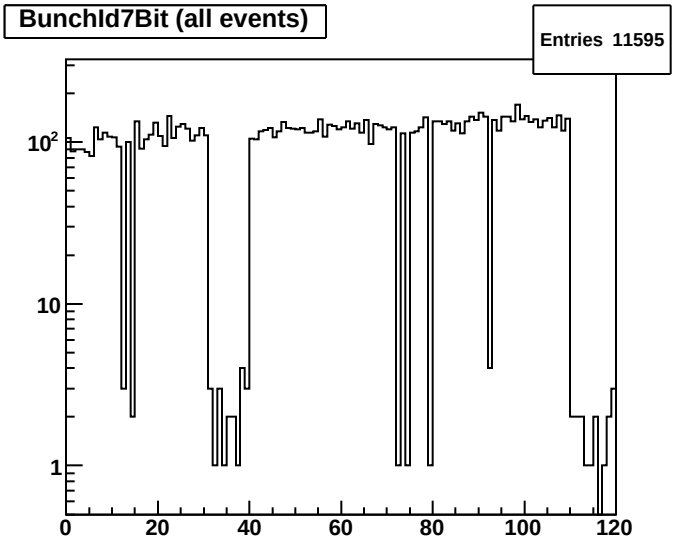

Last DSM Ch3 Input
Entries 185520
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

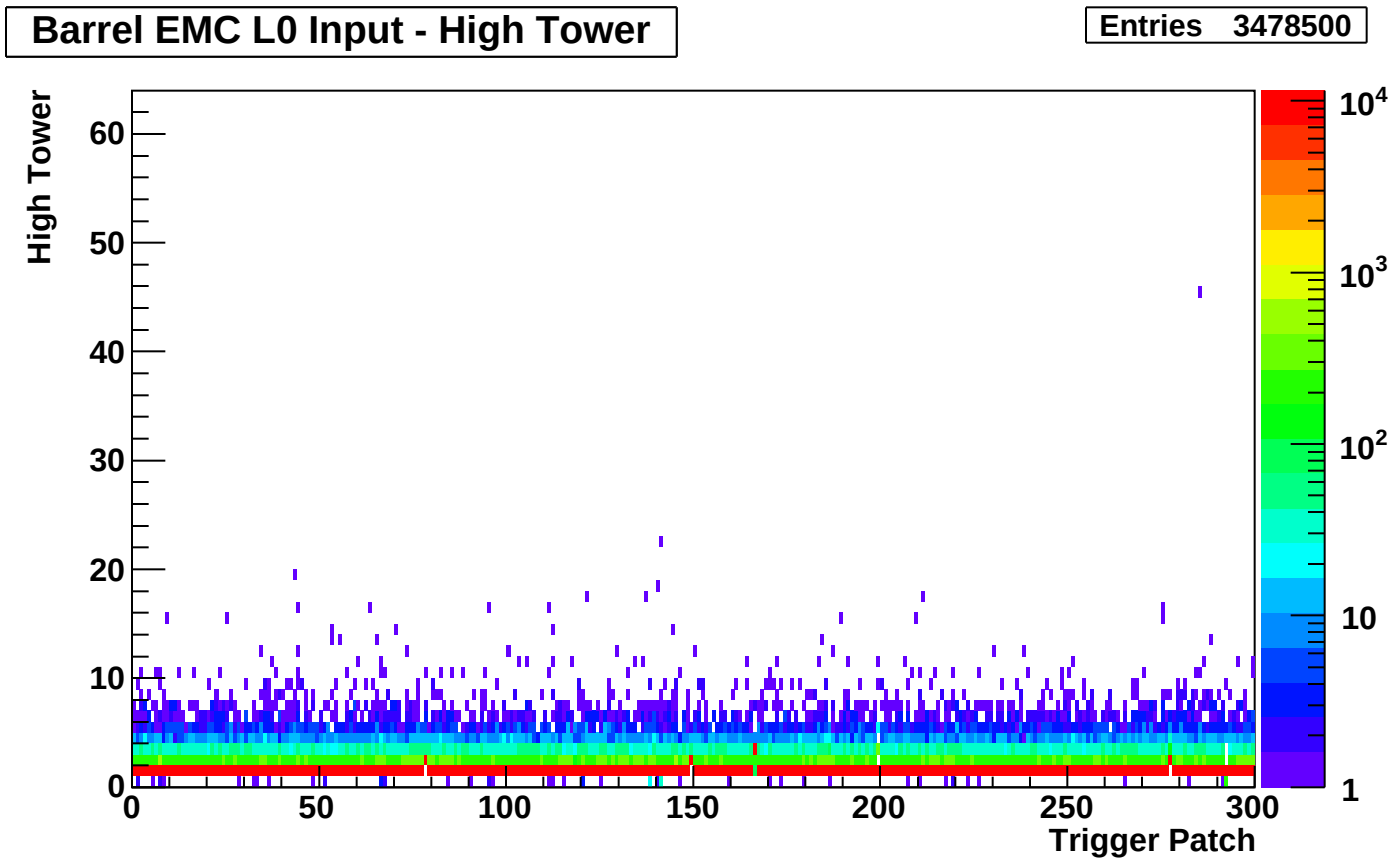

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

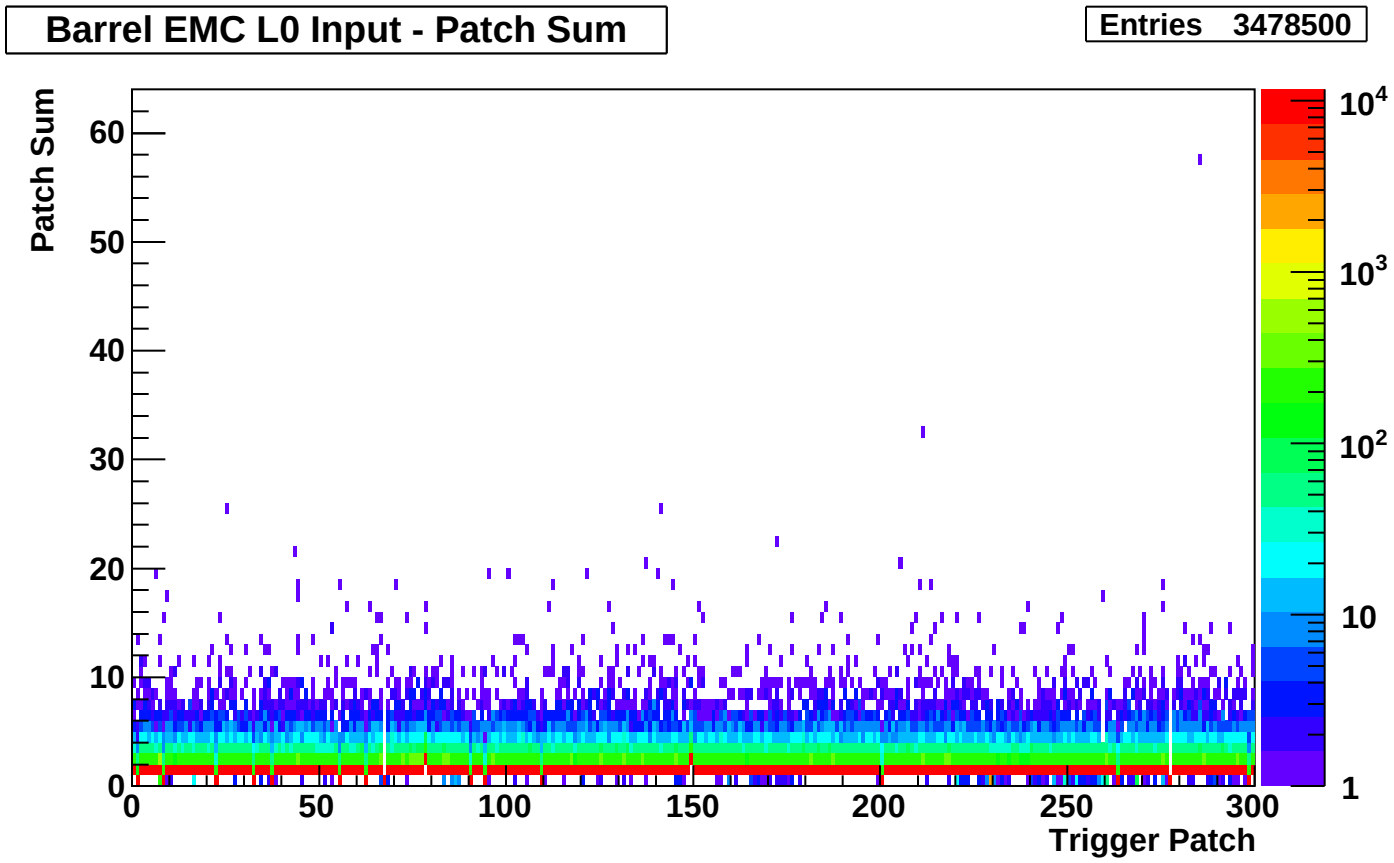

TCU bits
Entries 185520
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

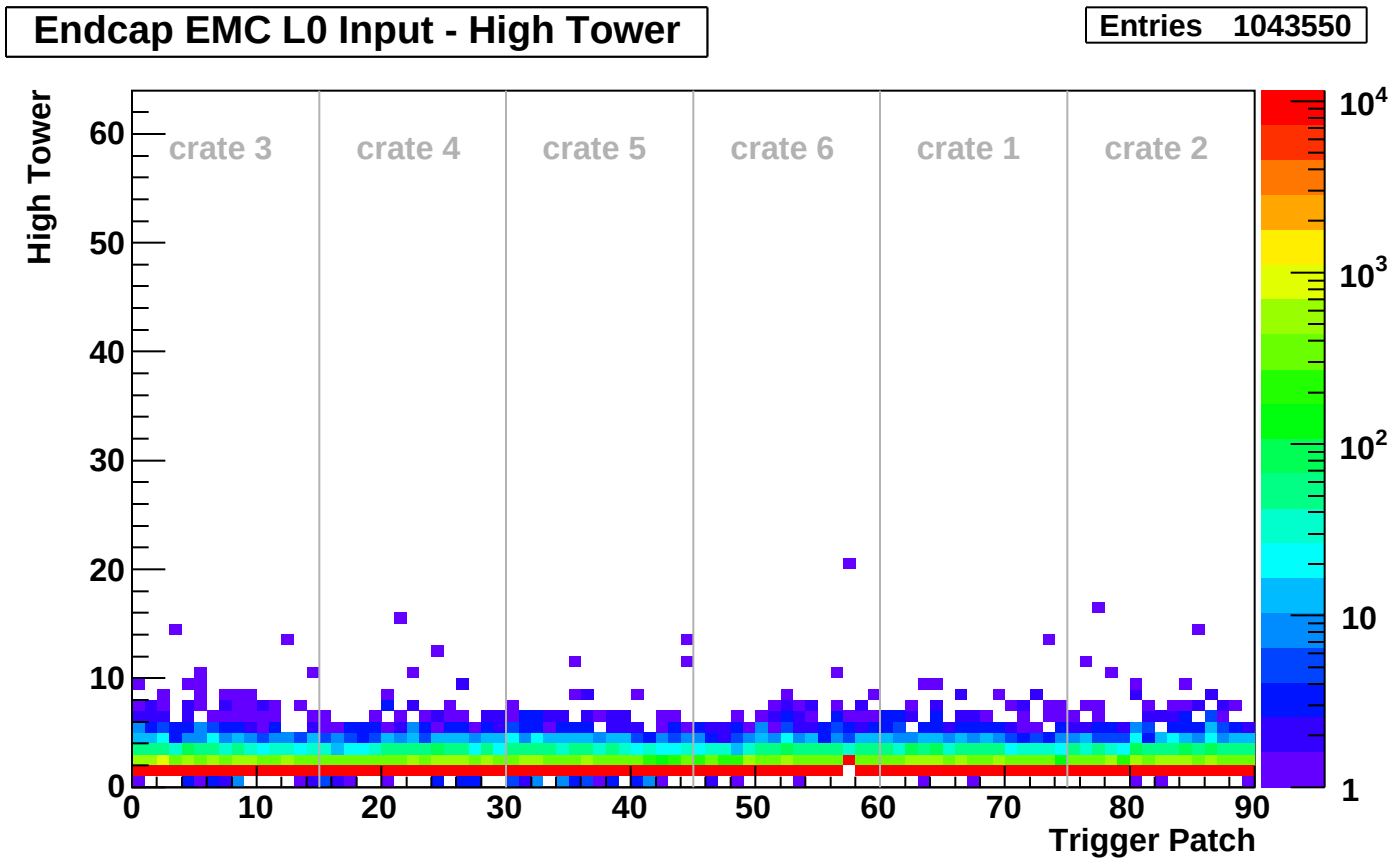

TCU bits
Entries 13658
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

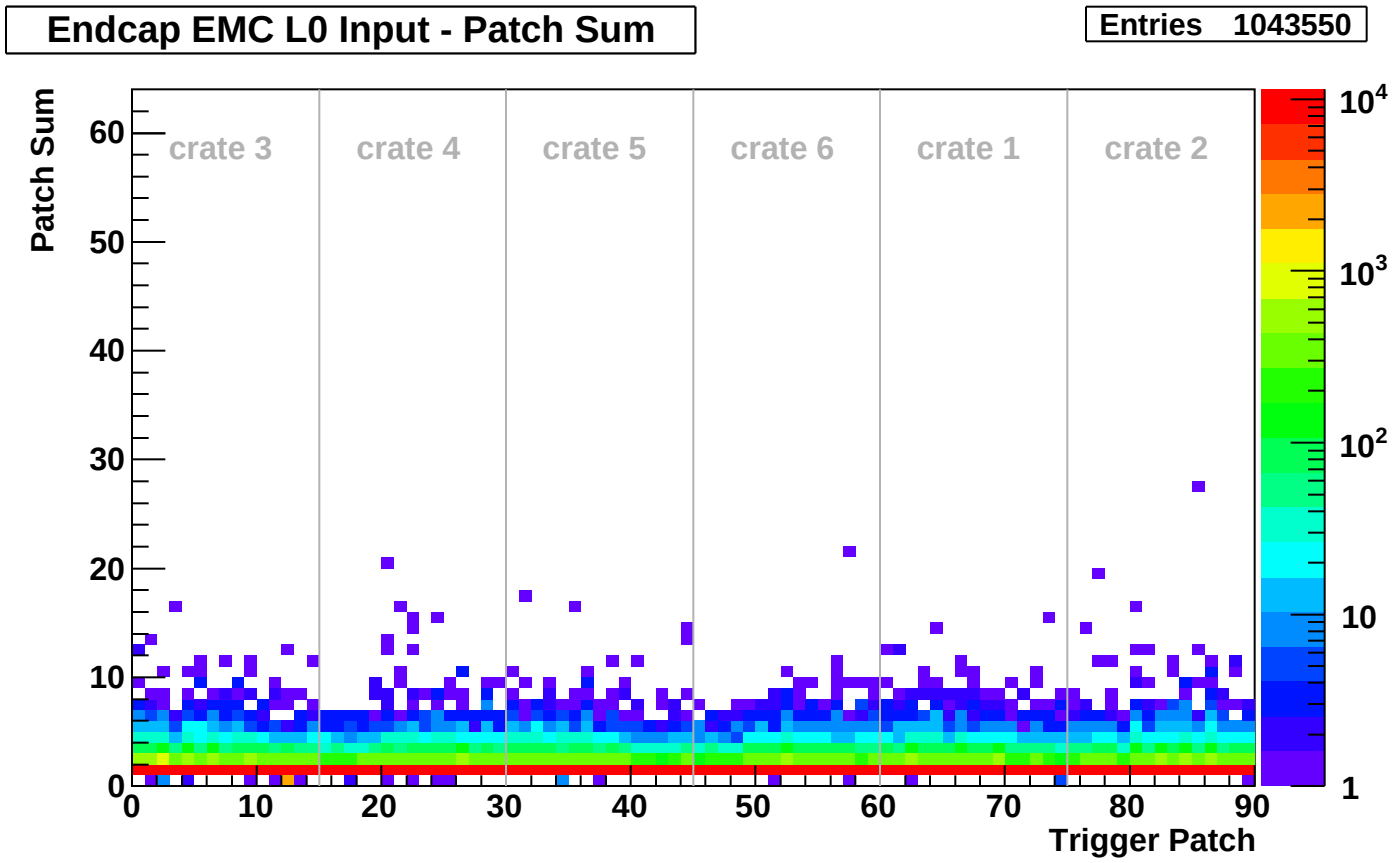

BunchId7Bit (BHT3)
Entries 1

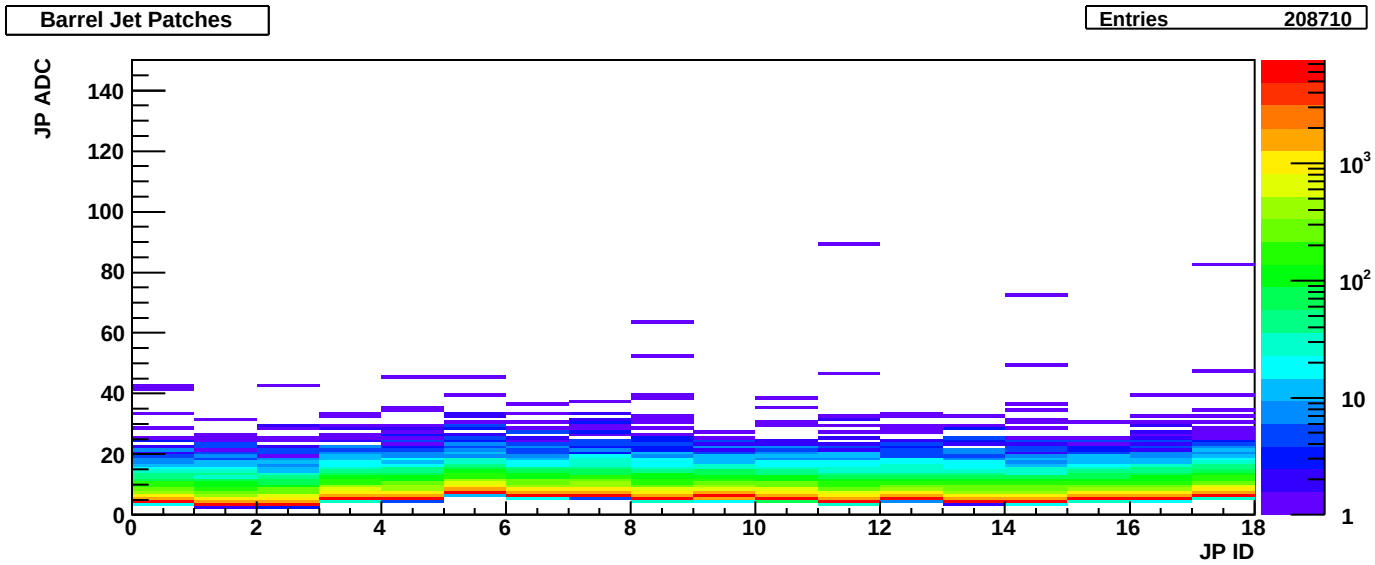

BunchId7Bit (all events)
Entries 11595

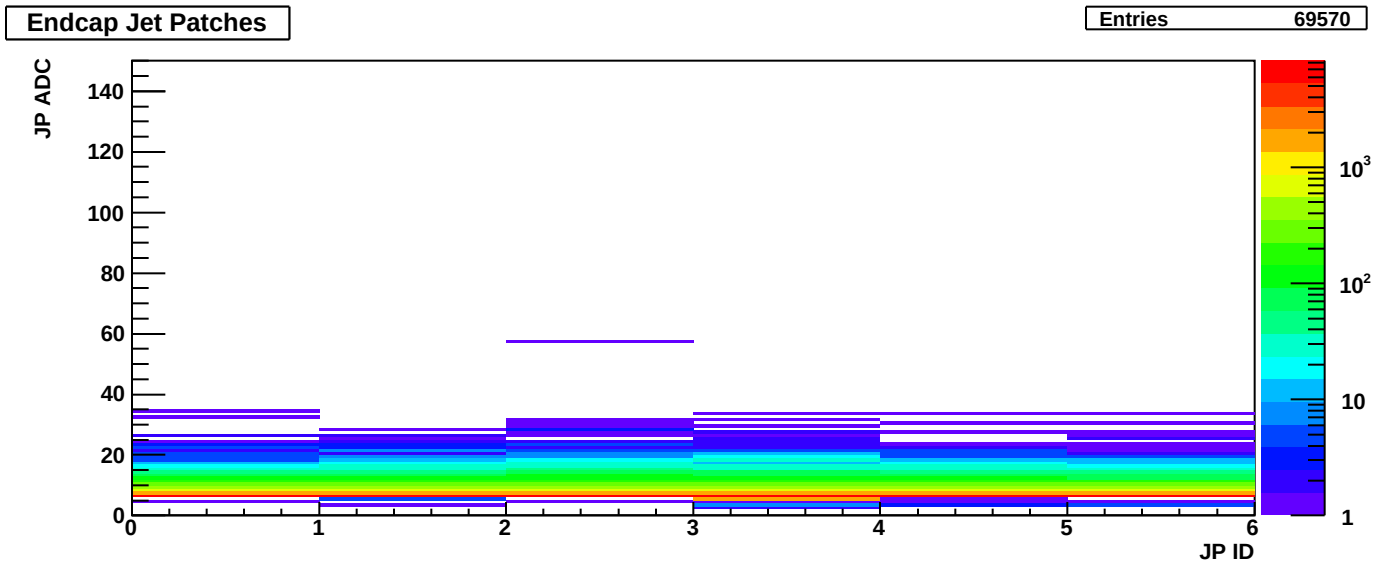

Barrel EMC L0 Input - High Tower
Entries 3478500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

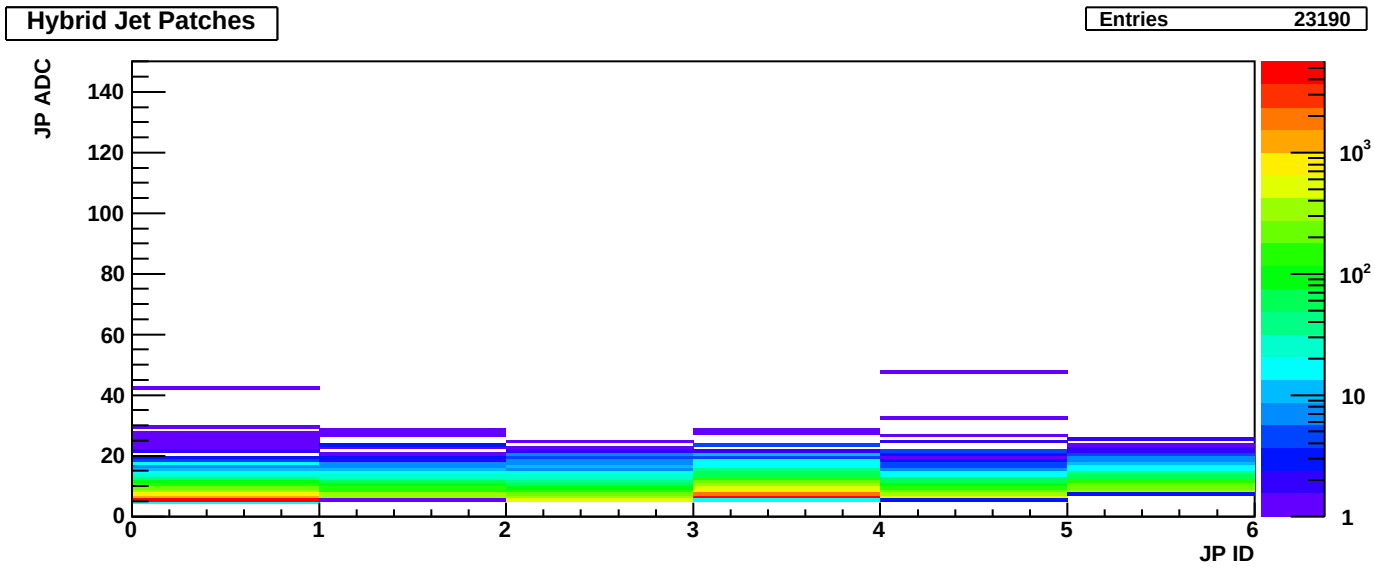

Barrel EMC L0 Input - Patch Sum
Entries 3478500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

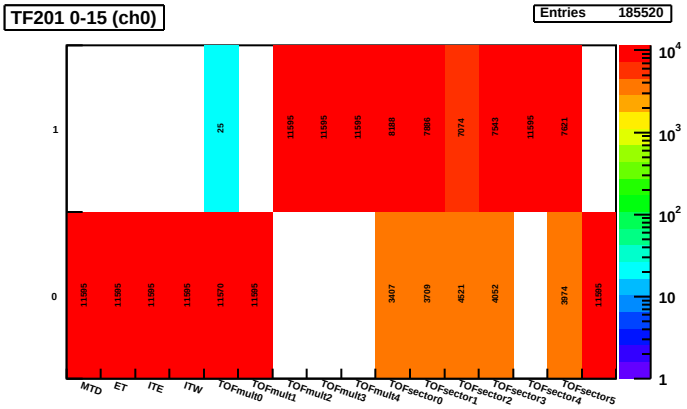

Endcap EMC L0 Input - High Tower
Entries 1043550
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

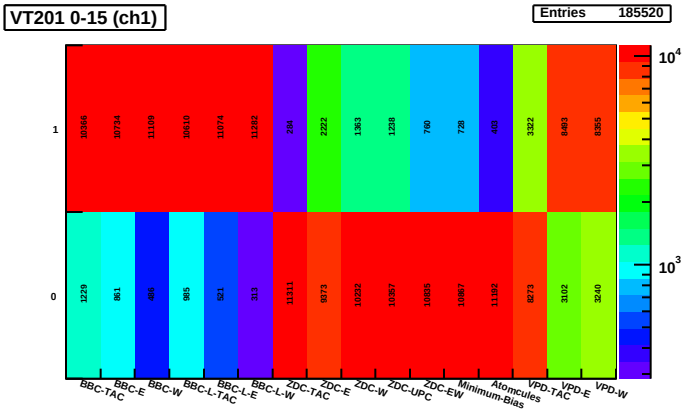

Endcap EMC L0 Input - Patch Sum
Entries 1043550
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

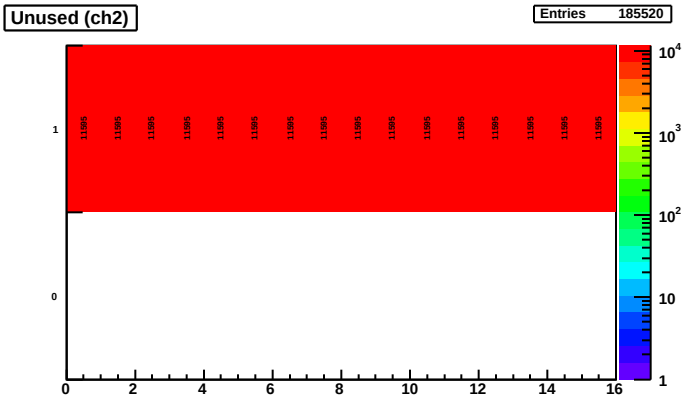

Barrel Jet Patches
Entries 208710
JP ADC
JP ID

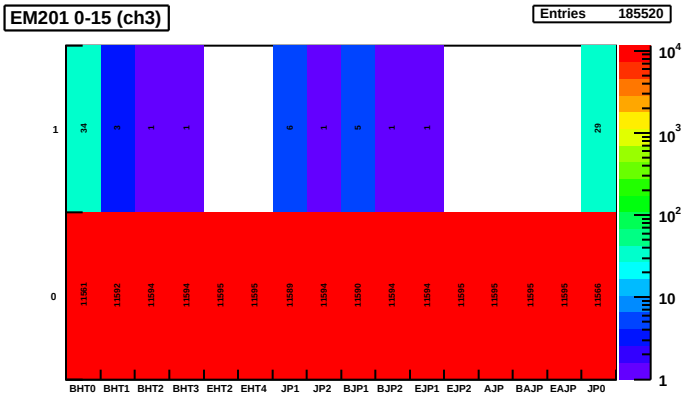

Endcap Jet Patches
Entries 69570
JP ADC
JP ID

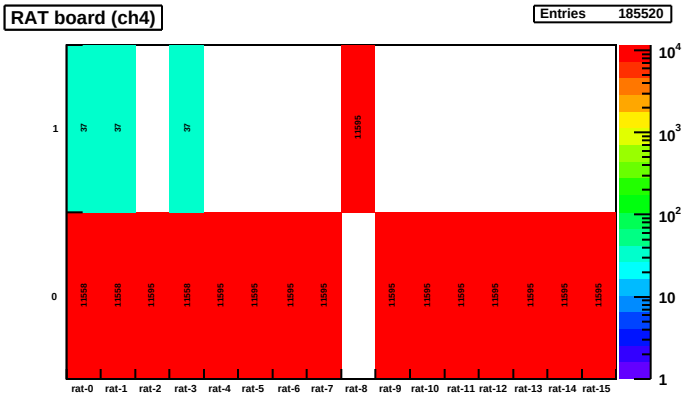

Hybrid Jet Patches
Entries 23190
JP ADC
JP ID

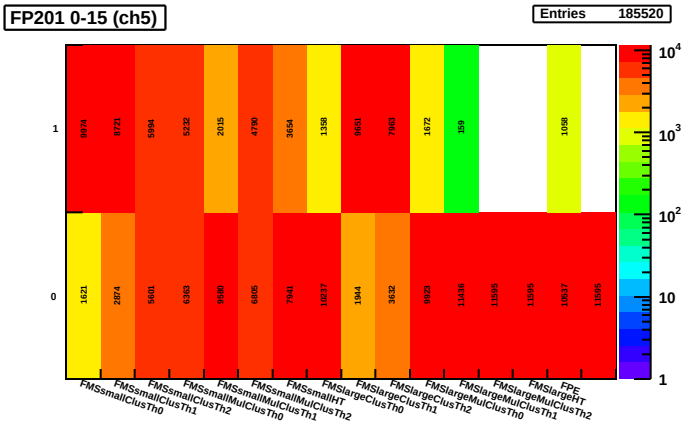

TF201 0-15 (ch0)
Entries 185520
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

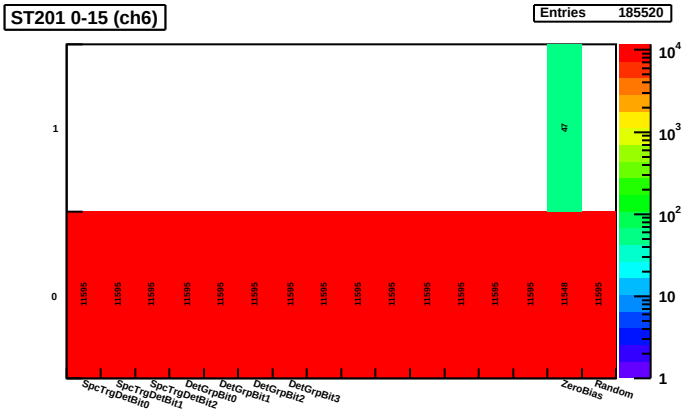

VT201 0-15 (ch1)
Entries 185520
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

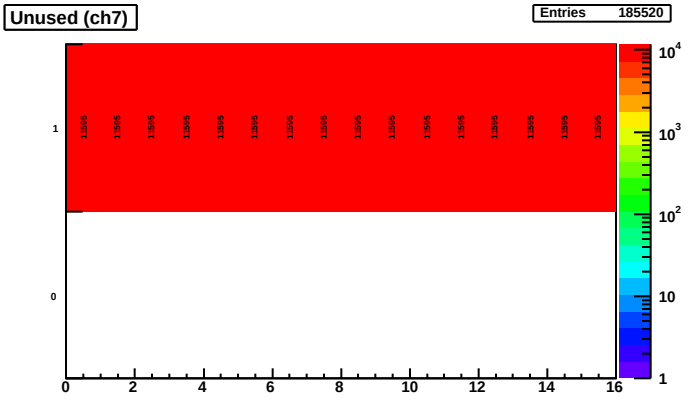

Unused (ch2)
Entries 185520

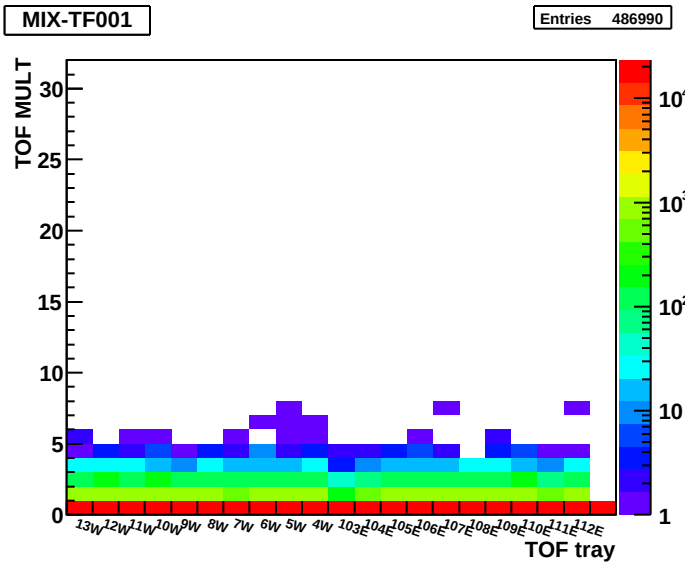

EM201 0-15 (ch3)
Entries 185520
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

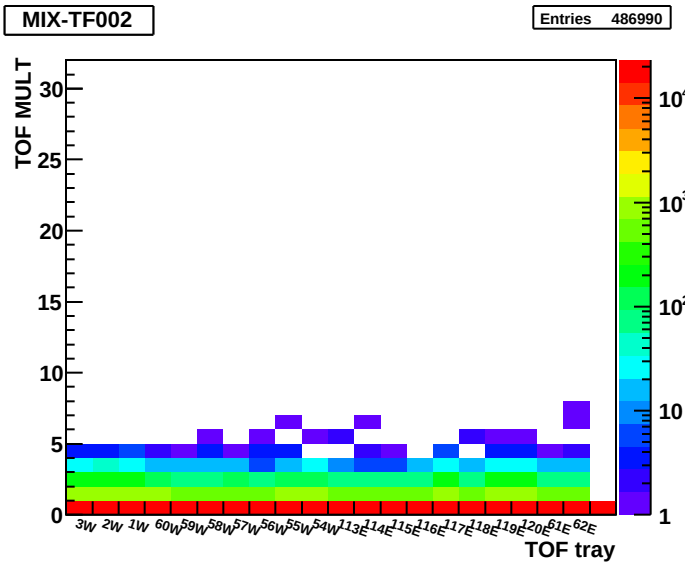

RAT board (ch4)
Entries 185520
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

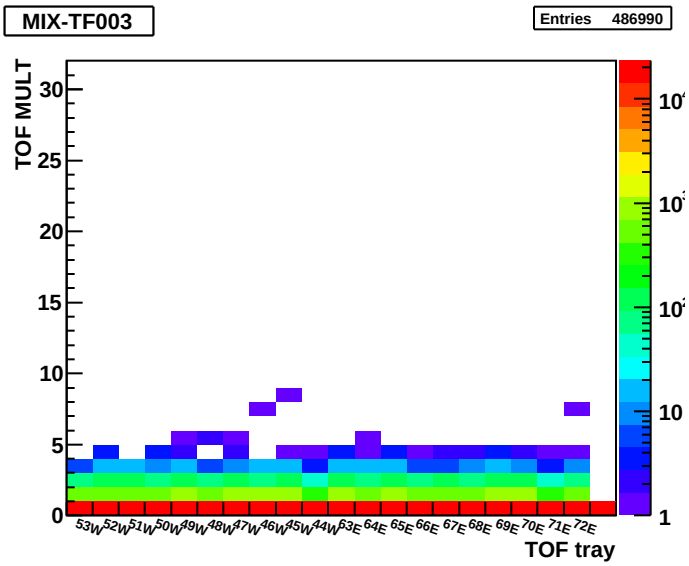

FP201 0-15 (ch5)
Entries 185520
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

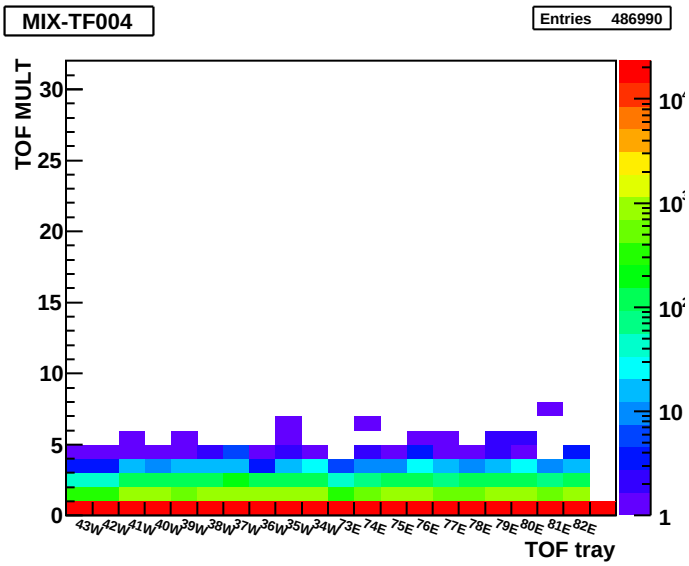

ST201 0-15 (ch6)
Entries 185520
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

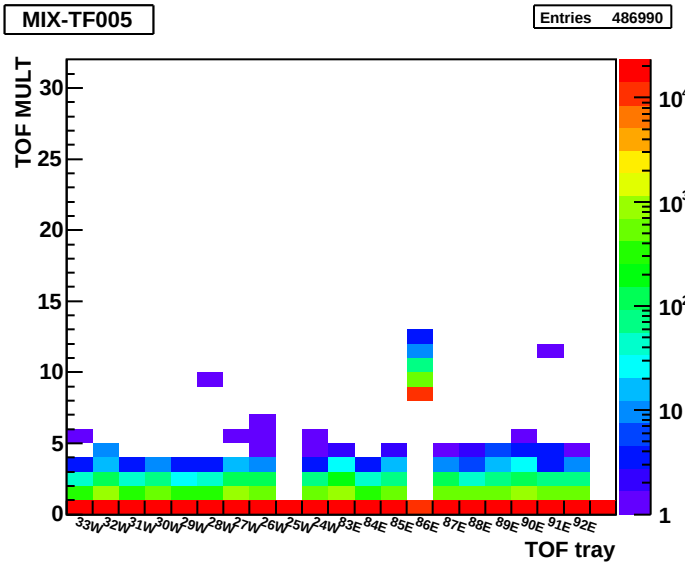

Unused (ch7)
Entries 185520

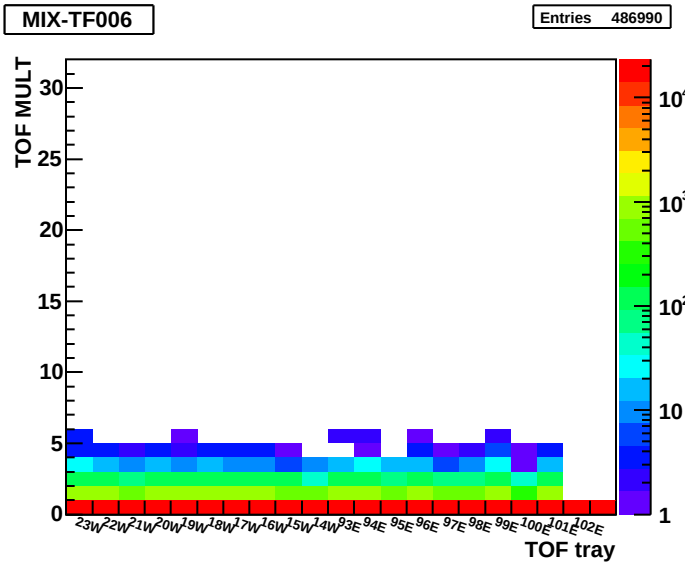

MIX-TF001
Entries 486990
TOF MULT
TOF tray

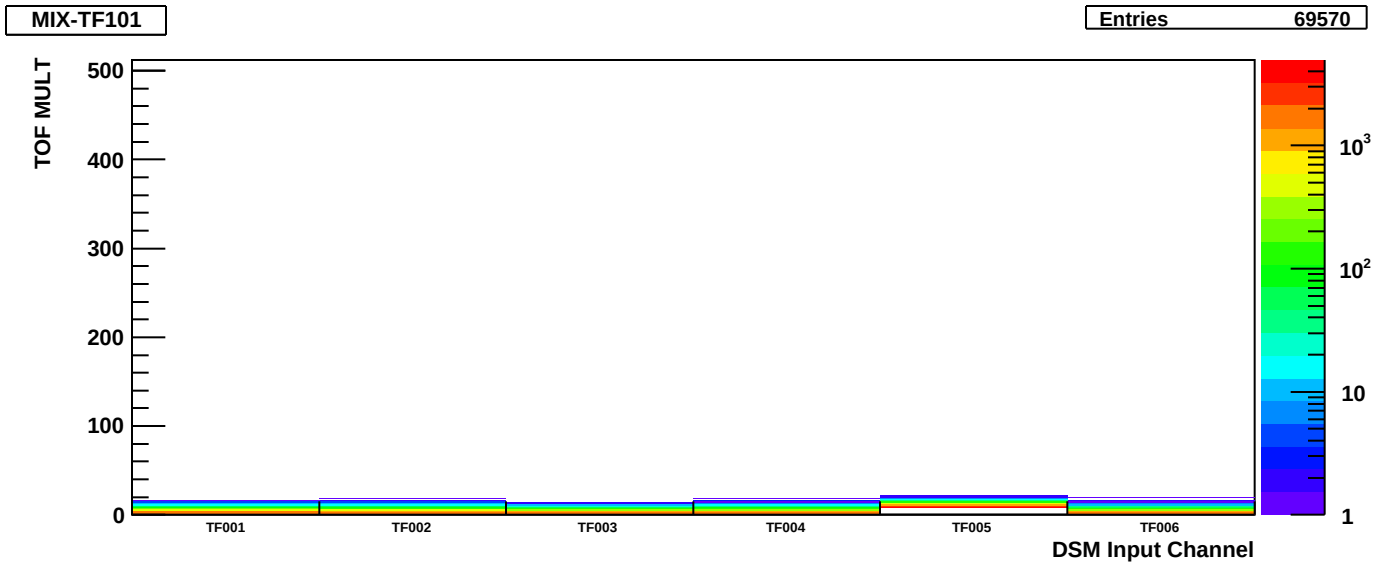

MIX-TF002
Entries 486990
TOF MULT
TOF tray

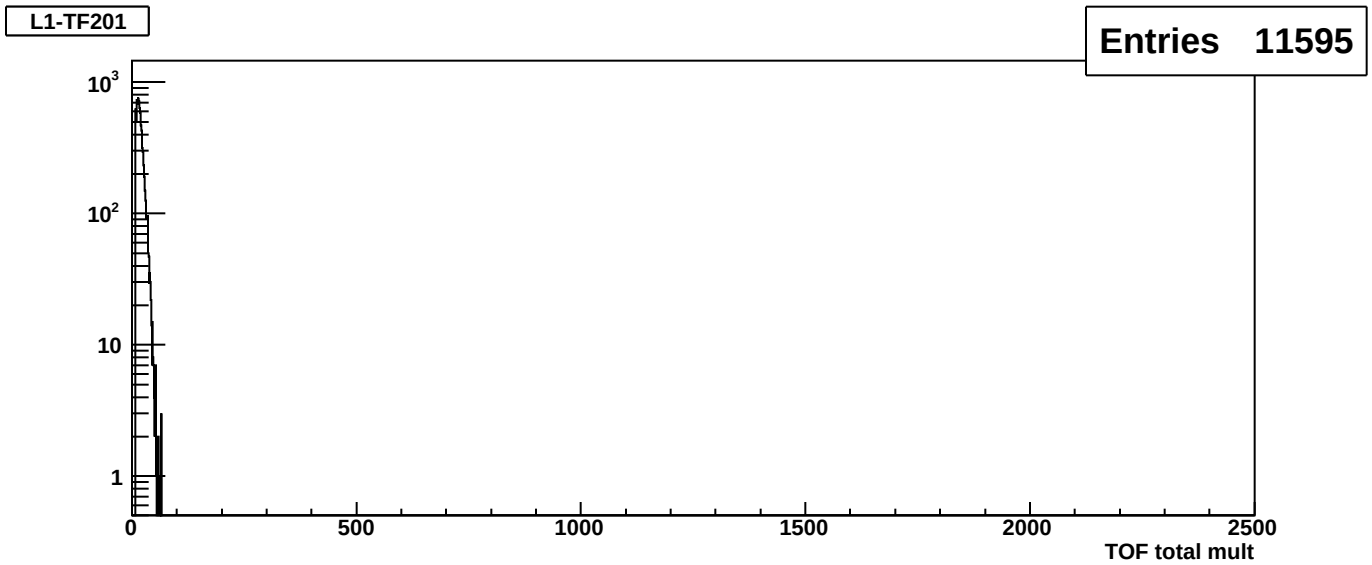

MIX-TF003
Entries 486990
TOF MULT
TOF tray

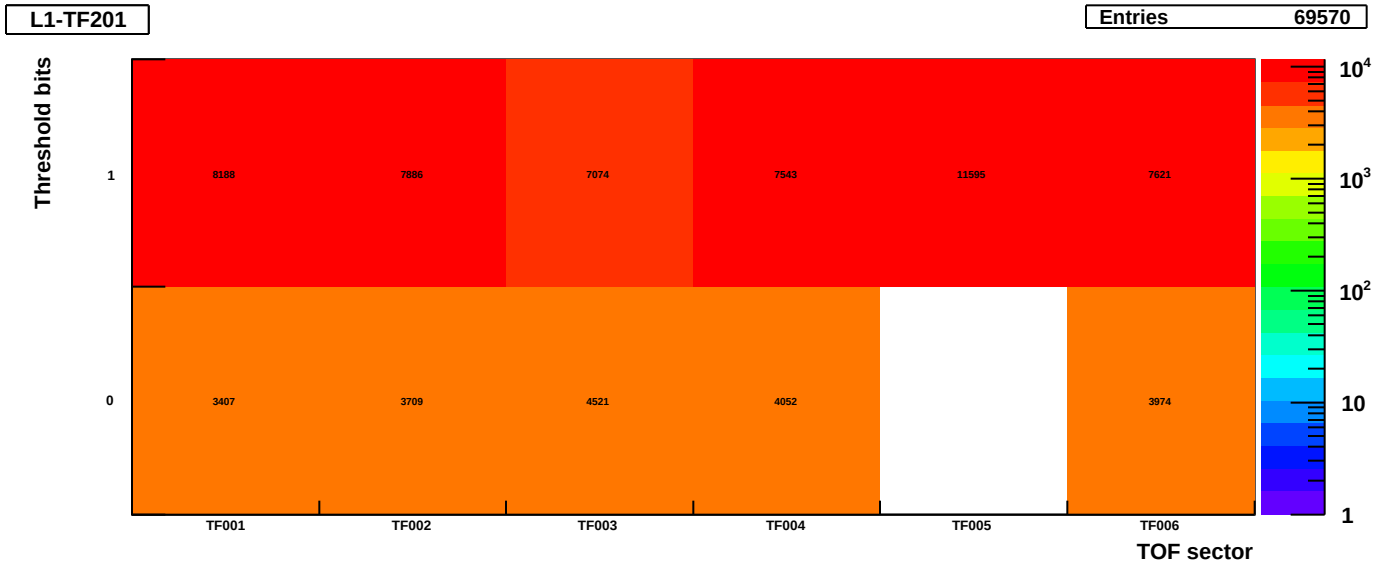

MIX-TF004
Entries 486990
TOF MULT
TOF tray

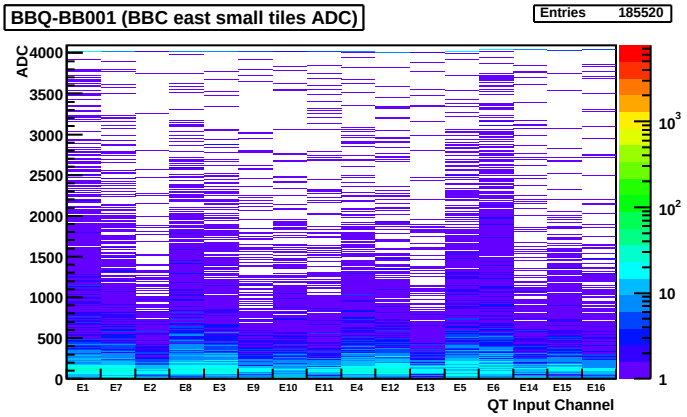

MIX-TF005
Entries 486990
TOF MULT
TOF tray

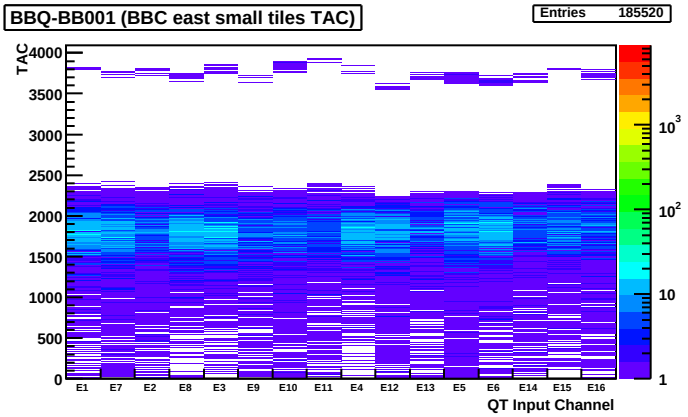

MIX-TF006
Entries 486990
TOF MULT
TOF tray

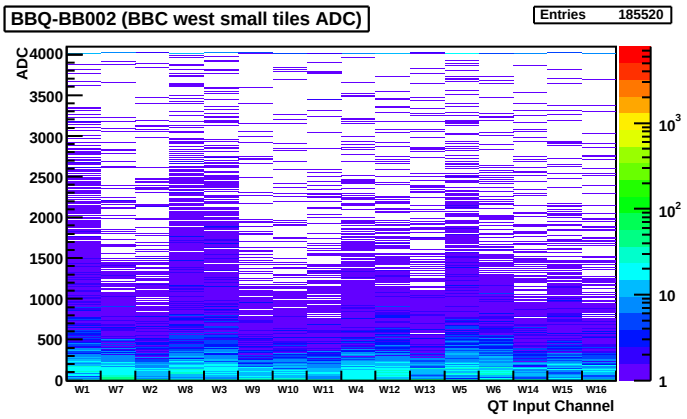

MIX-TF101
Entries 69570
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

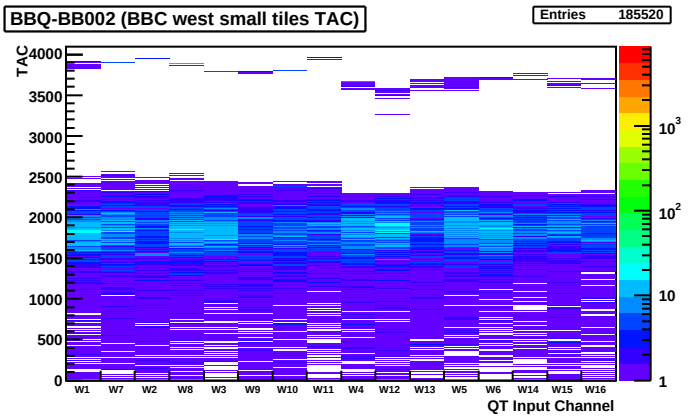

L1-TF201
Entries 11595
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

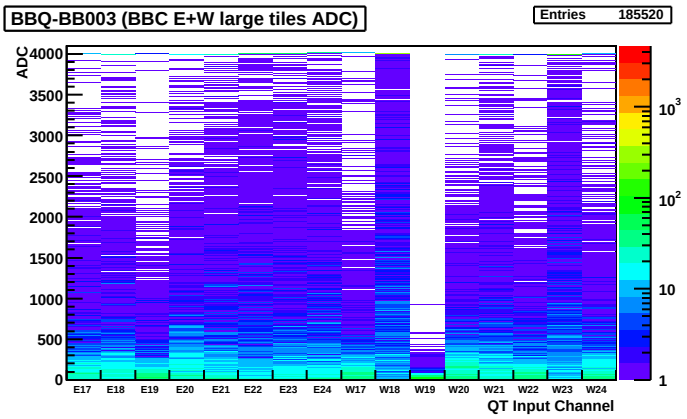

L1-TF201
Entries 69570
Threshold bits
1
0
8188
7886
7074
7543
11595
7621
3407
3709
4521
4052
3974
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3
10^2
10
1

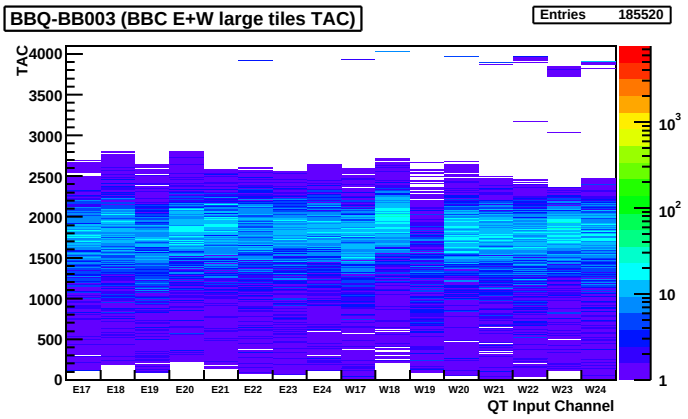

BBQ-BB001 (BBC east small tiles ADC)
Entries 185520
ADC
QT Input Channel

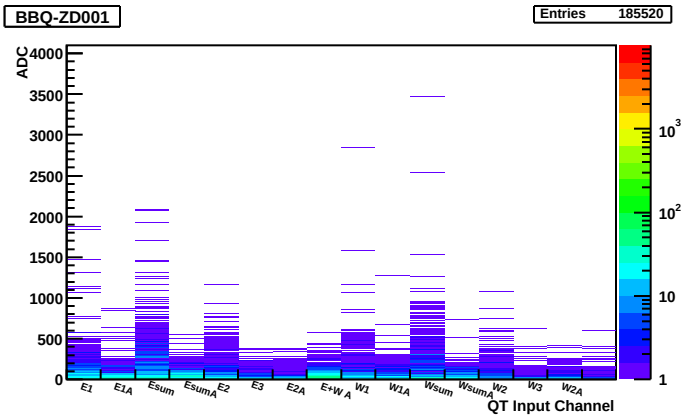

BBQ-BB001 (BBC east small tiles TAC)
Entries 185520
TAC
QT Input Channel

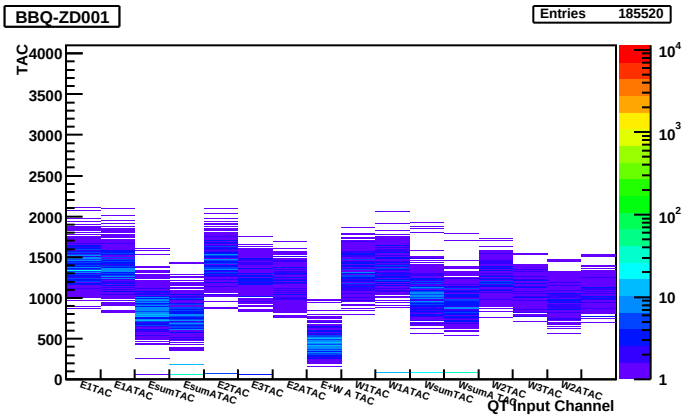

BBQ-BB002 (BBC west small tiles ADC)
Entries 185520
ADC
QT Input Channel

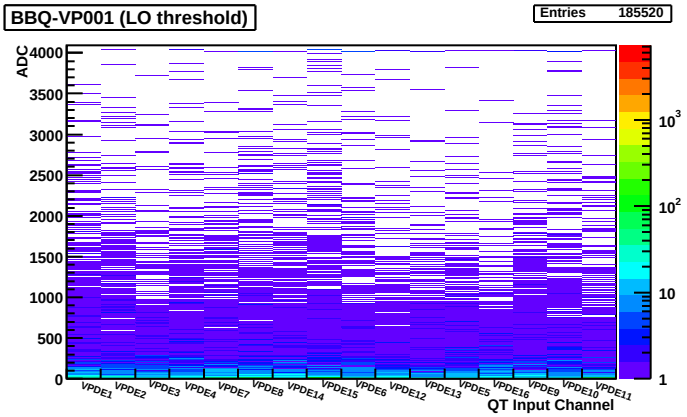

BBQ-BB002 (BBC west small tiles TAC)
Entries 185520
TAC
QT Input Channel

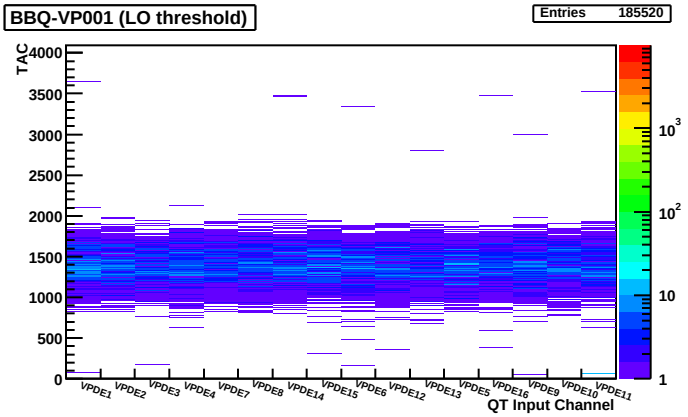

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 185520
ADC
QT Input Channel

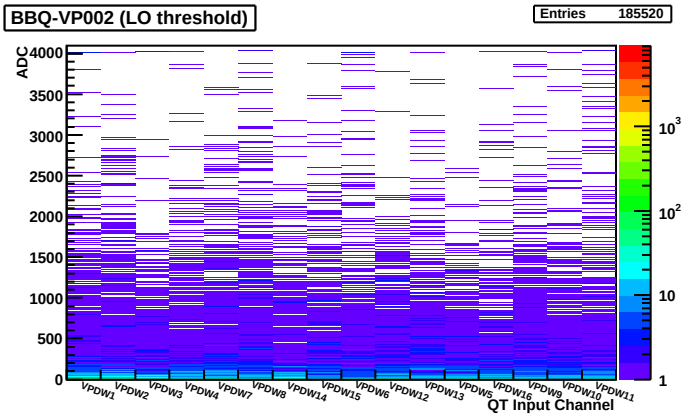

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 185520
TAC
QT Input Channel

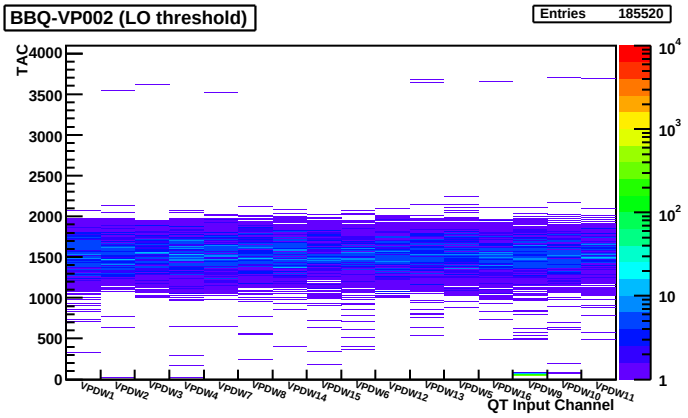

BBQ-ZD001
Entries 185520
ADC
QT Input Channel

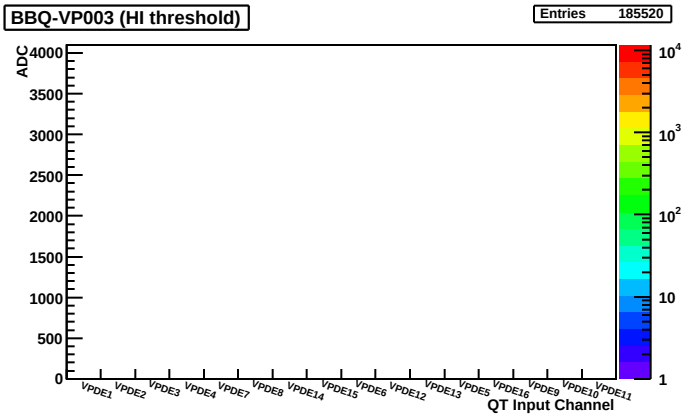

BBQ-ZD001
Entries 185520
TAC
QT Input Channel

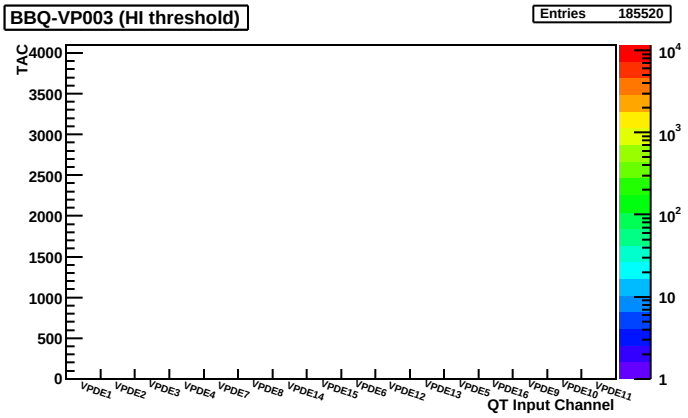

BBQ-VP001 (LO threshold)
Entries 185520
ADC
QT Input Channel

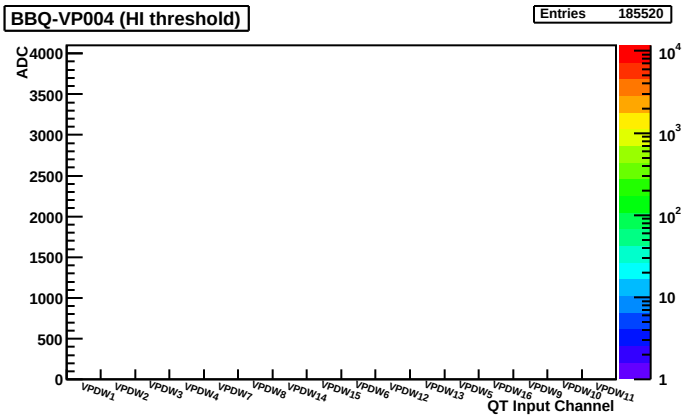

BBQ-VP001 (LO threshold)
Entries 185520
TAC
QT Input Channel

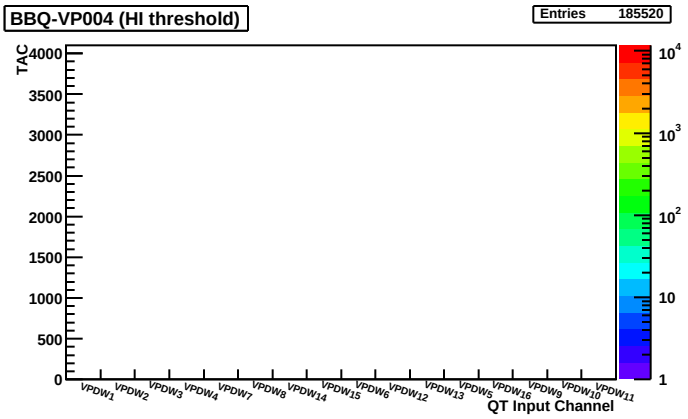

BBQ-VP002 (LO threshold)
Entries 185520
ADC
QT Input Channel

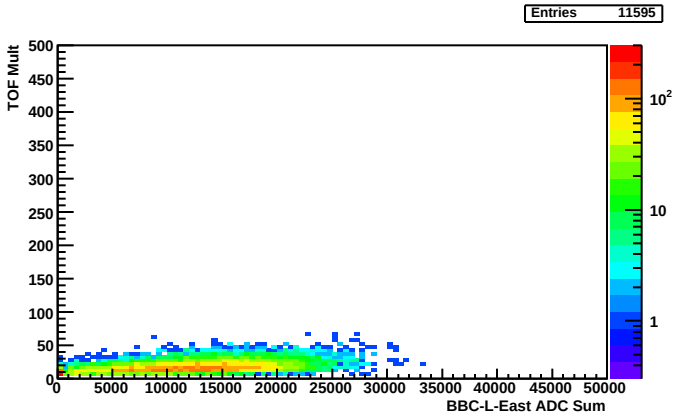

BBQ-VP002 (LO threshold)
Entries 185520
TAC
QT Input Channel

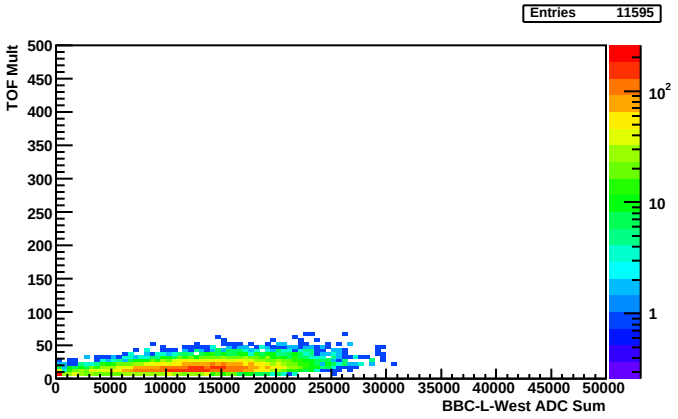

BBQ-VP003 (HI threshold)
Entries 185520
ADC
QT Input Channel

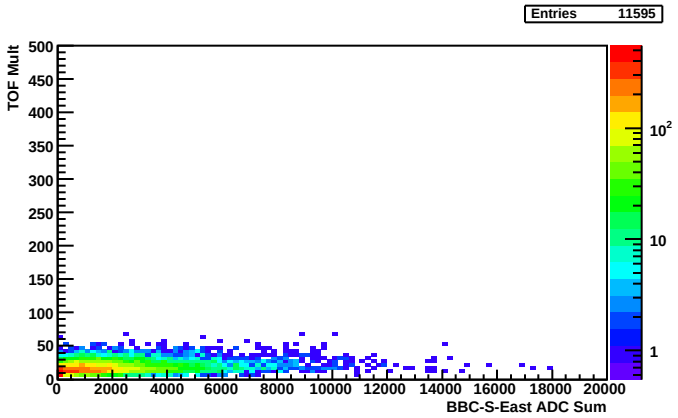

BBQ-VP003 (HI threshold)
Entries 185520
TAC
QT Input Channel

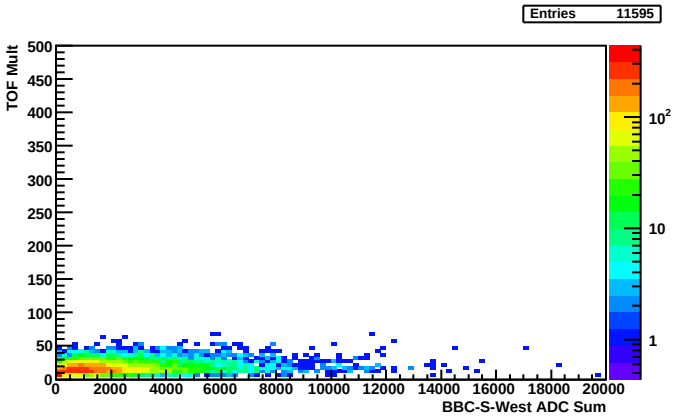

BBQ-VP004 (HI threshold)
Entries 185520
ADC
QT Input Channel

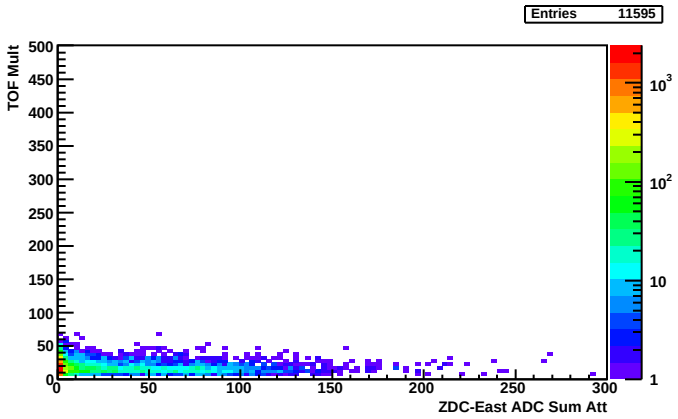

BBQ-VP004 (HI threshold)
Entries 185520
TAC
QT Input Channel

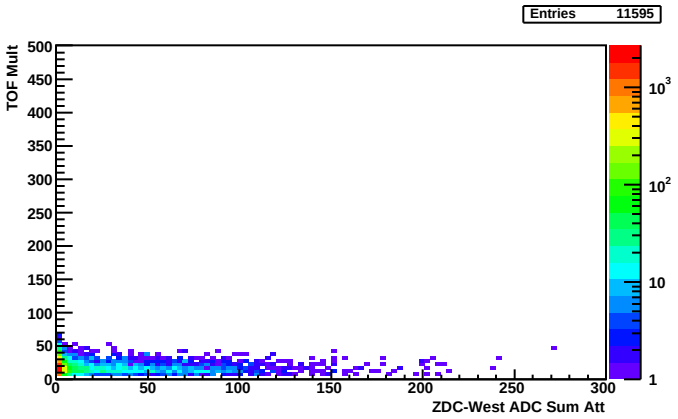

Entries 11595
TOF Mult
BBC-L-East ADC Sum

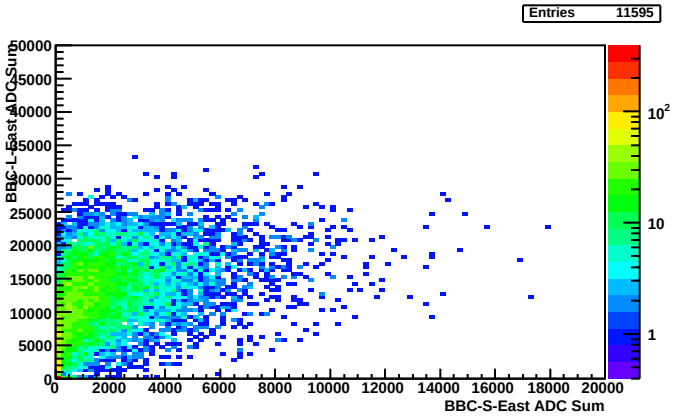

Entries 11595
TOF Mult
BBC-L-West ADC Sum

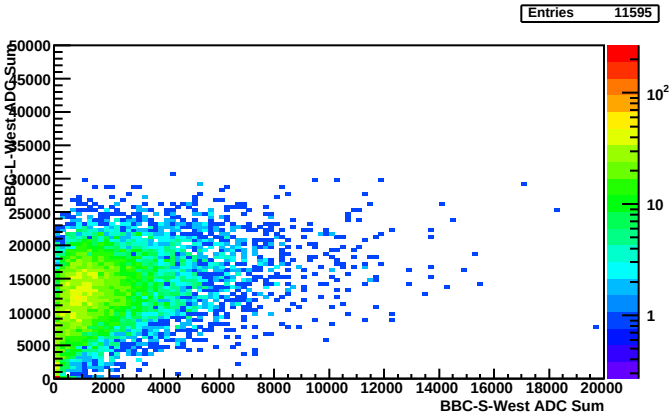

Entries 11595
TOF Mult
BBC-S-East ADC Sum

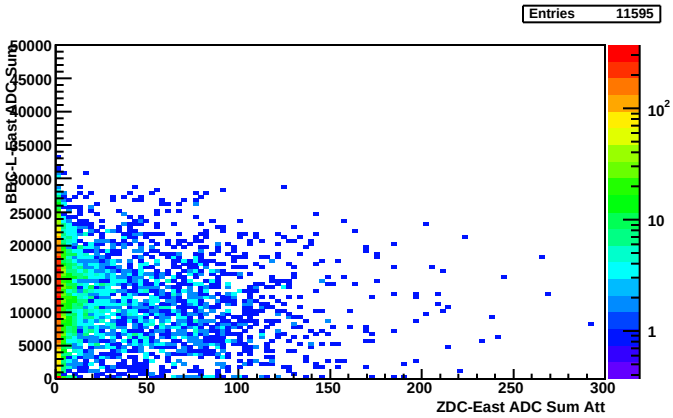

Entries 11595
TOF Mult
BBC-S-West ADC Sum

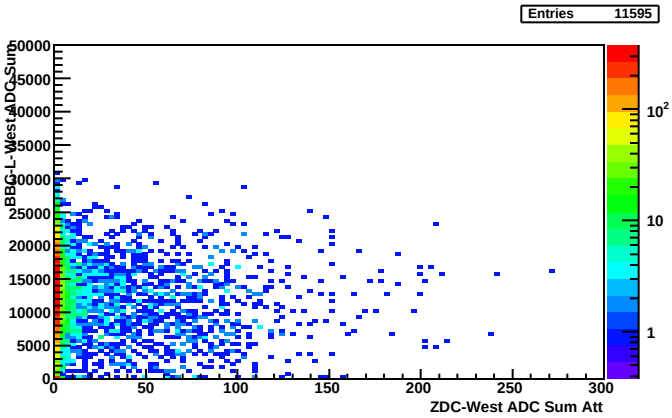

Entries 11595
TOF Mult
ZDC-East ADC Sum Att

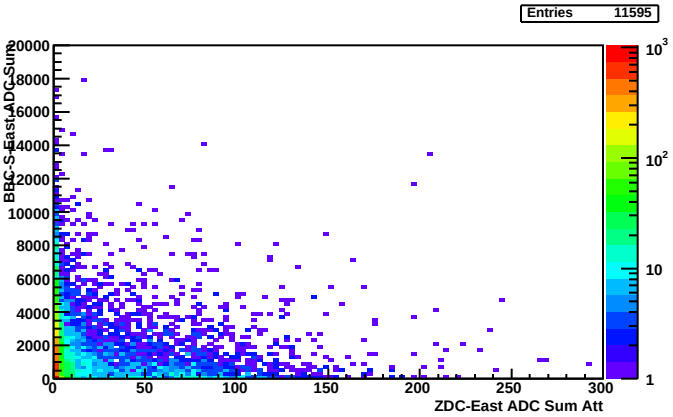

Entries 11595
TOF Mult
ZDC-West ADC Sum Att

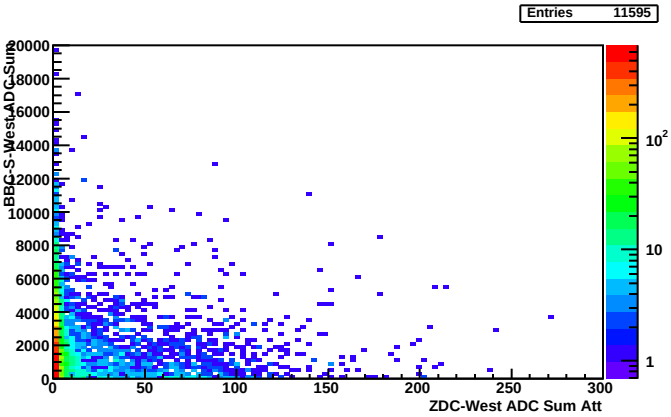

Entries 11595
BBC-L-East ADC Sum
BBC-S-East ADC Sum

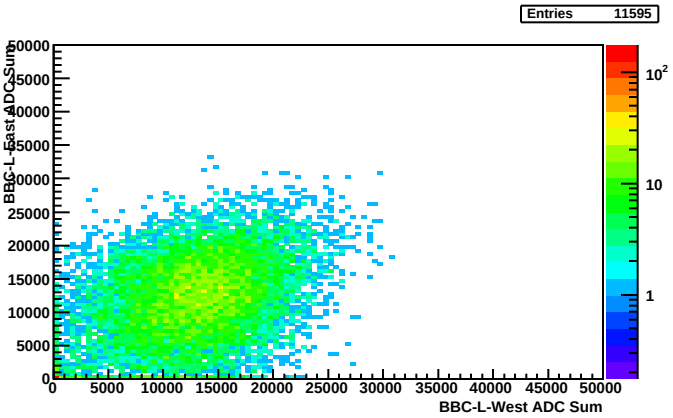

Entries 11595
BBC-L-West ADC Sum
BBC-S-West ADC Sum

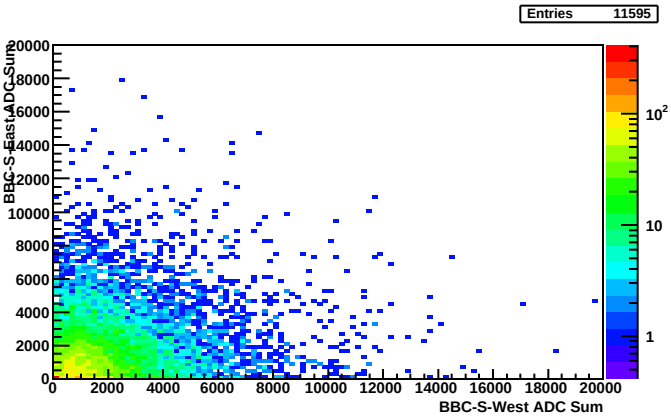

Entries 11595
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

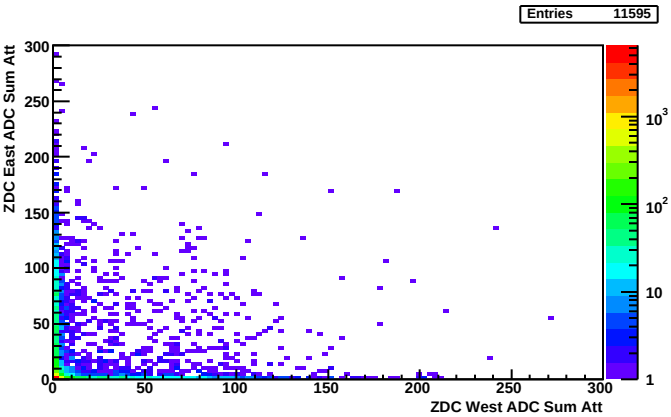

Entries 11595
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

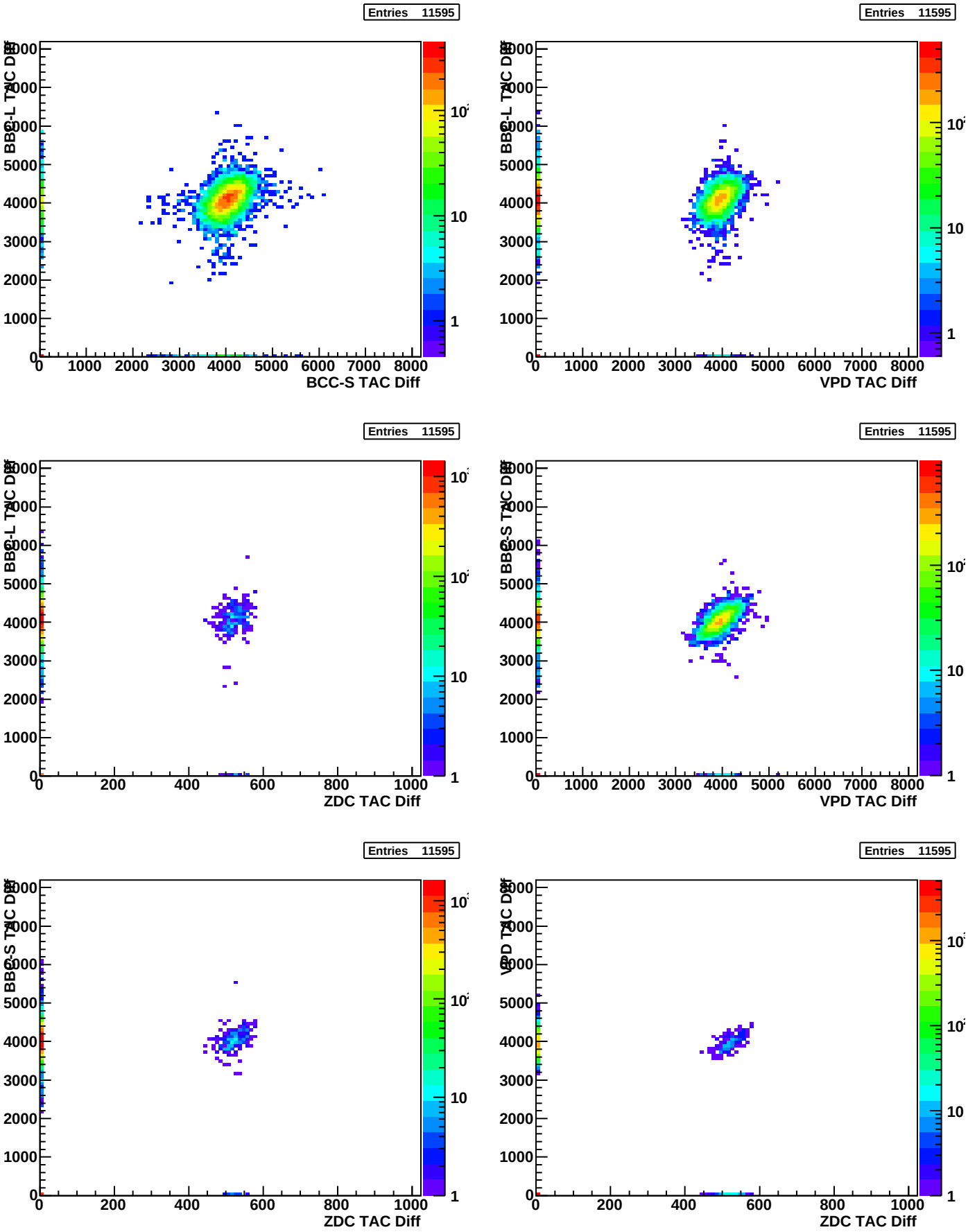

Entries 11595
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

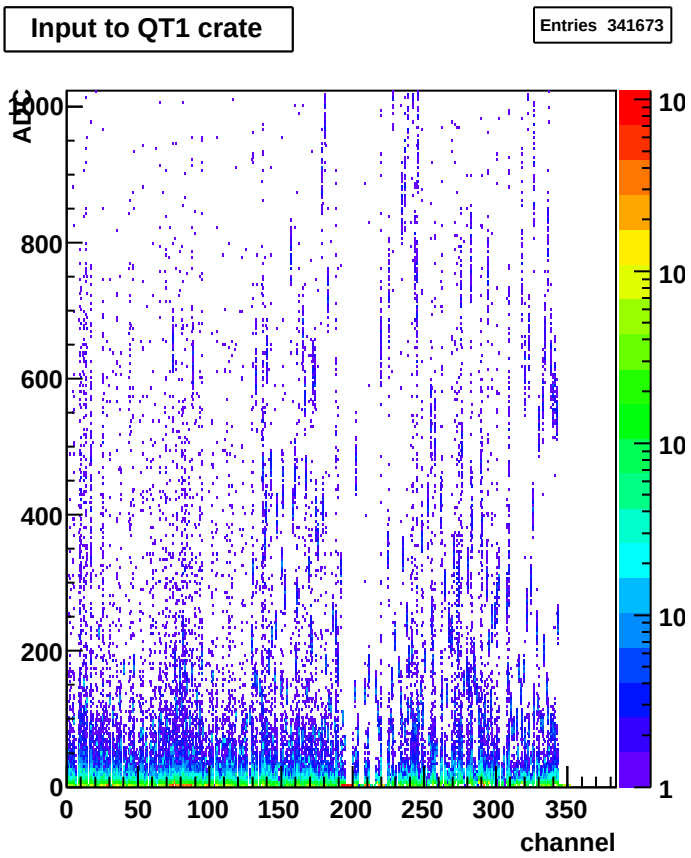

Entries 11595
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

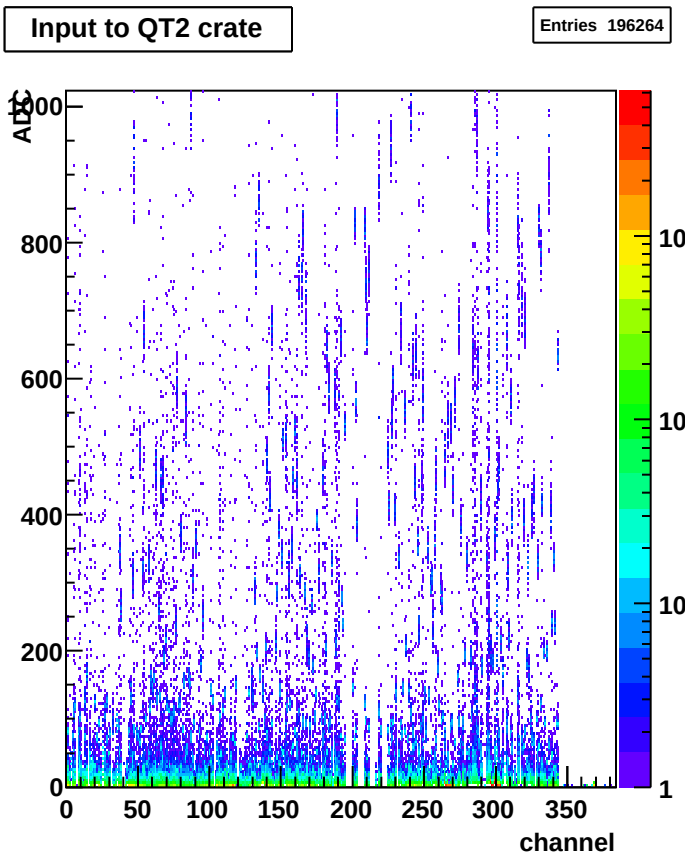

Entries 11595
BBC-L-East ADC Sum
BBC-L-West ADC Sum

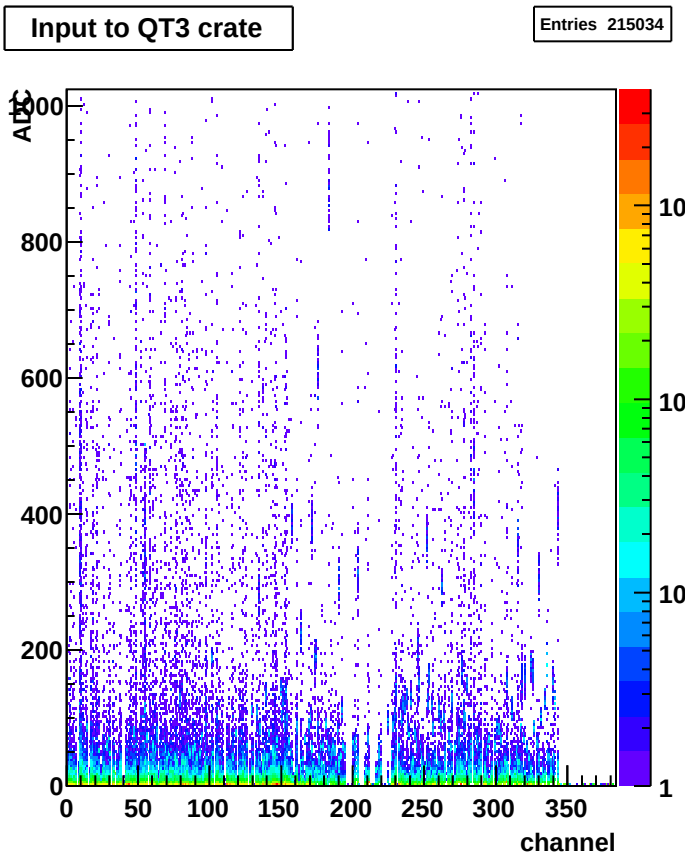

Entries 11595
BBC-S-East ADC Sum
BBC-S-West ADC Sum

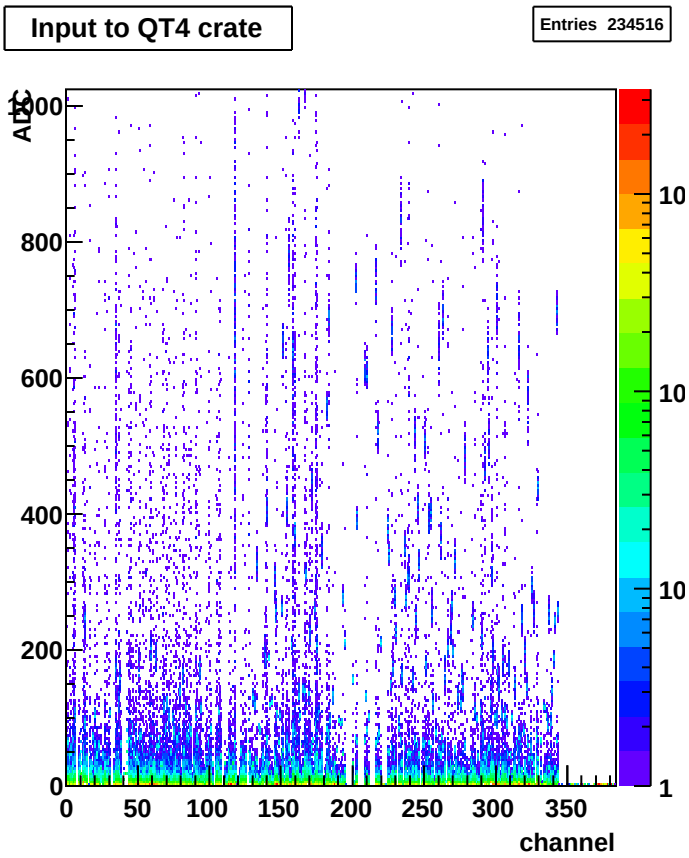

Entries 11595
ZDC East ADC Sum Att
ZDC West ADC Sum Att

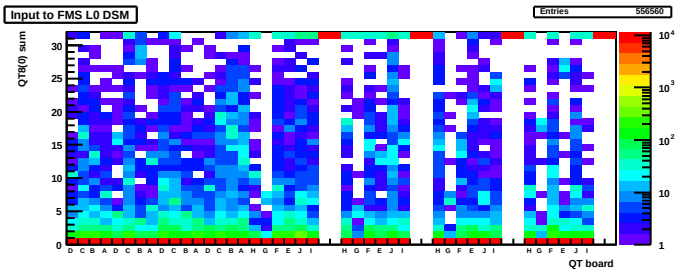

Entries 11595
BBC-L TAC Diff
BCC-S TAC Diff
Entries 11595
BBC-L TAC Diff
VPD TAC Diff
Entries 11595
BBC-L TAC Diff
ZDC TAC Diff
Entries 11595
BBC-S TAC Diff
VPD TAC Diff
Entries 11595
BBC-S TAC Diff
ZDC TAC Diff
Entries 11595
VPD TAC Diff
ZDC TAC Diff

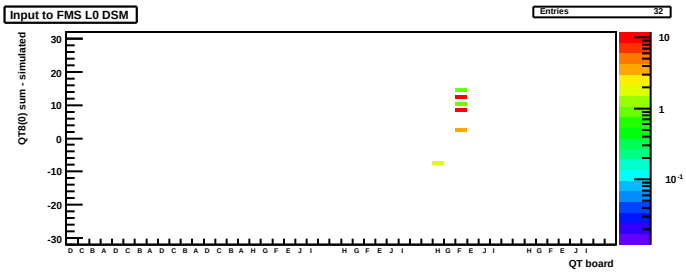

Input to QT1 crate
Entries 341673
ADC
channel

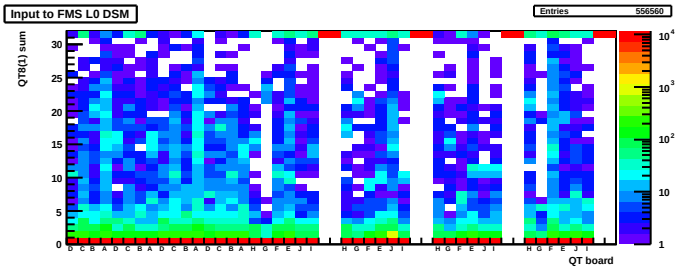

Input to QT2 crate
Entries 196264
ADC
channel

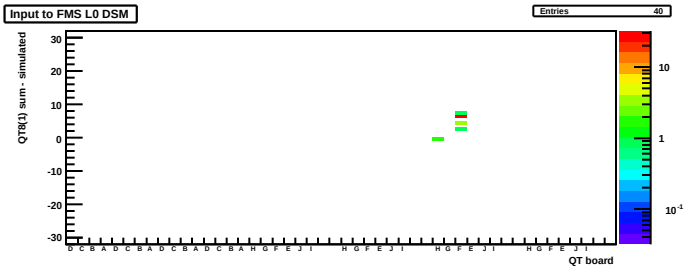

Input to QT3 crate
Entries 215034
ADC
channel

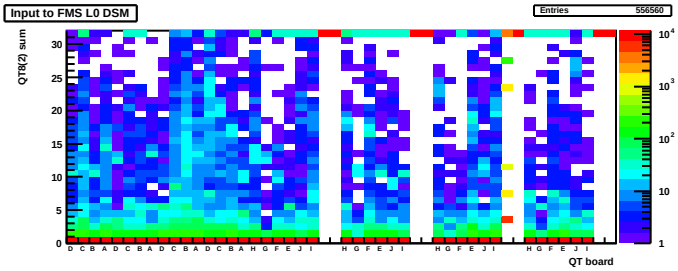

Input to QT4 crate
Entries 234516
ADC
channel

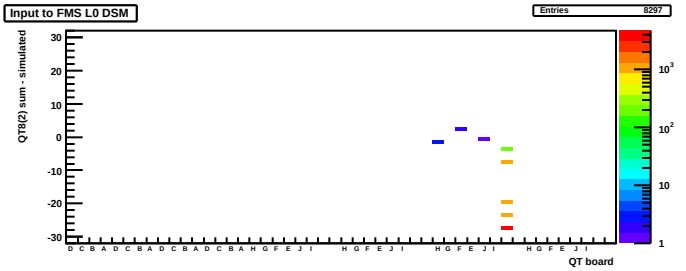

Input to FMS L0 DSM
Entries 556560
QT8(0) sum
QT board

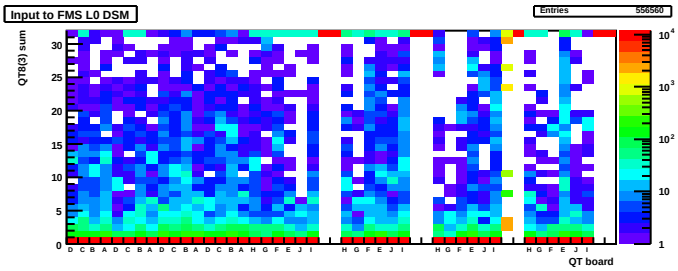

Input to FMS L0 DSM
Entries 32
QT8(0) sum - simulated
QT board

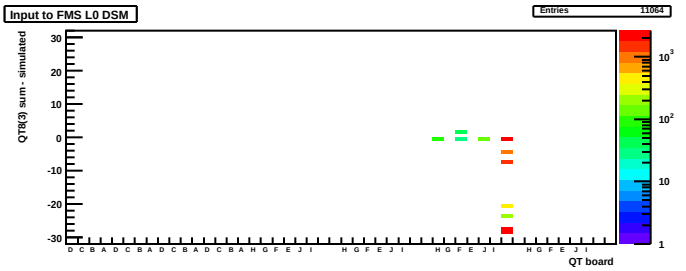

Input to FMS L0 DSM
Entries 556560
QT8(1) sum
QT board

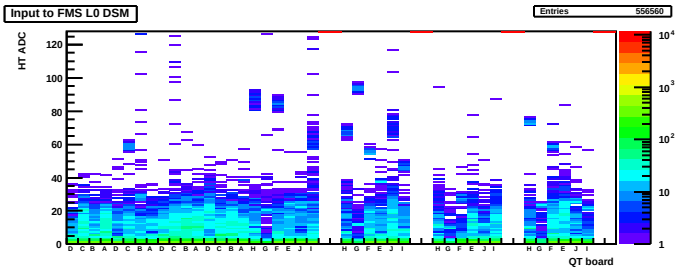

Input to FMS L0 DSM
Entries 40
QT8(1) sum - simulated
QT board

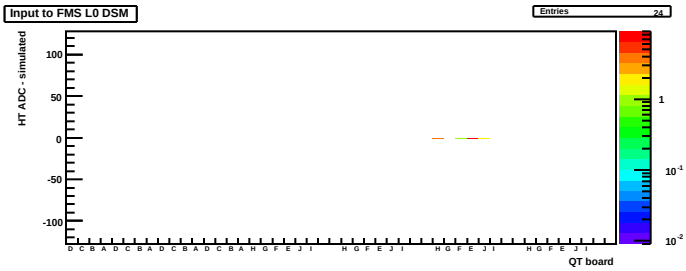

Input to FMS L0 DSM
Entries 556560
QT8(2) sum
QT board

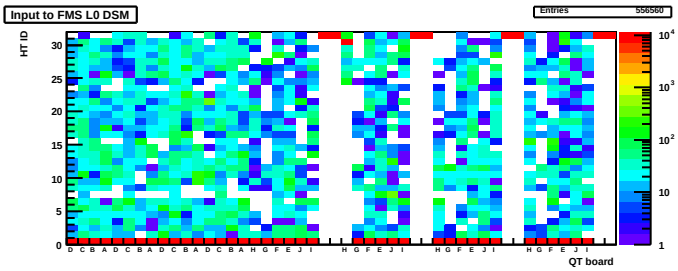

Input to FMS L0 DSM
Entries 8297
QT8(2) sum - simulated
QT board

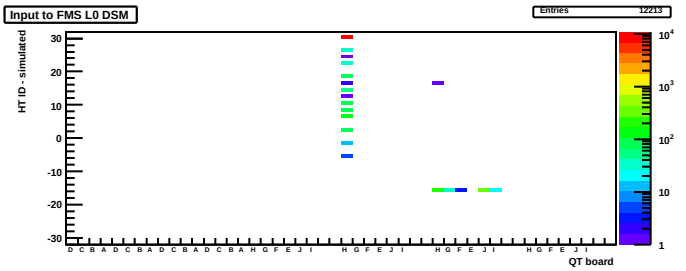

Input to FMS L0 DSM
Entries 556560
QT8(3) sum
QT board

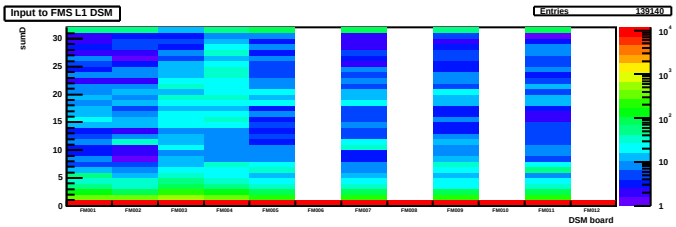

Input to FMS L0 DSM
Entries 11064
QT8(3) sum - simulated
QT board

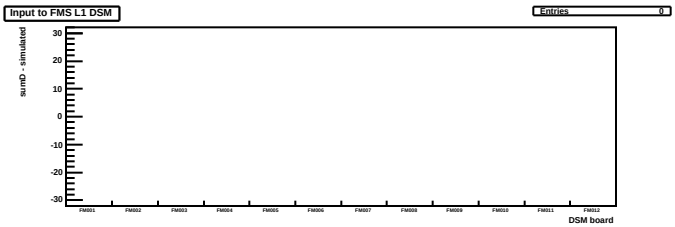

Input to FMS L0 DSM
Entries 556560
HT ADC
QT board

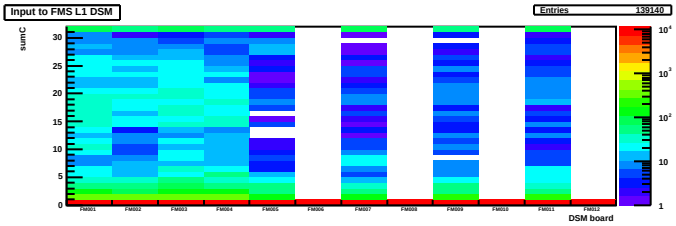

Input to FMS L0 DSM
Entries 24
HT ADC - simulated
QT board

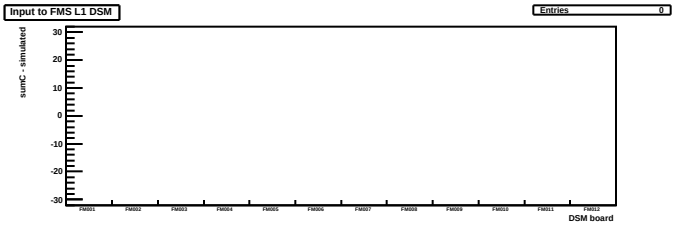

Input to FMS L0 DSM
Entries 556560
HT ID
QT board

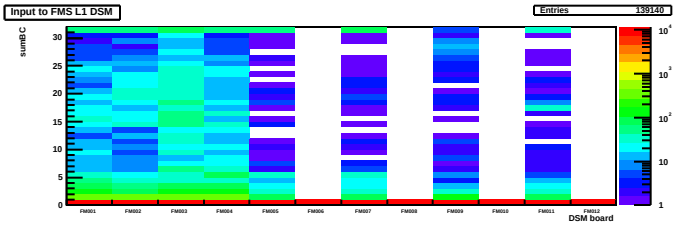

Input to FMS L0 DSM
Entries 12213
HT ID - simulated
QT board

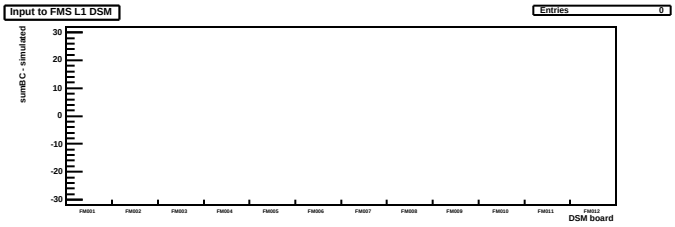

Input to FMS L1 DSM
Entries 139140
sumD
DSM board

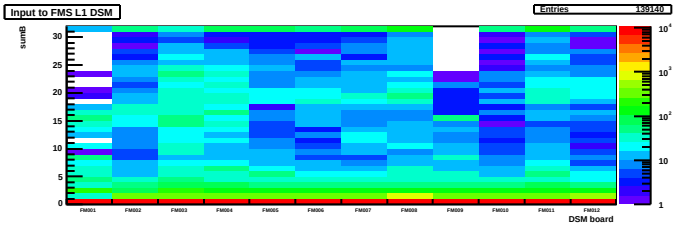

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

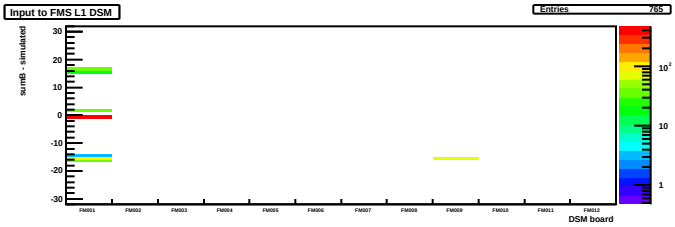

Input to FMS L1 DSM
Entries 139140
sumC
DSM board

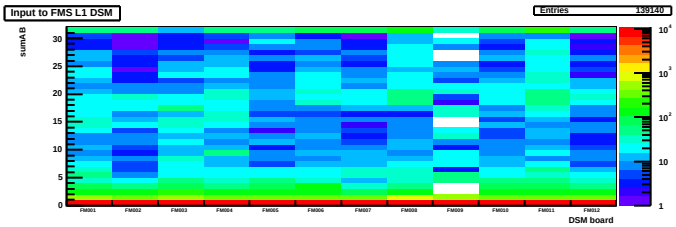

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

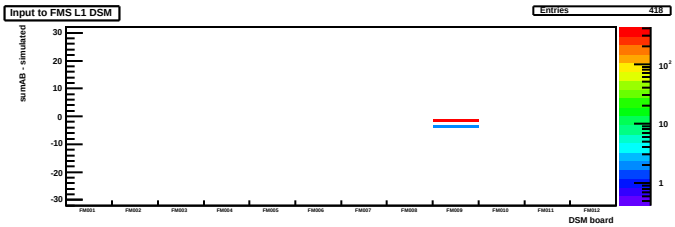

Input to FMS L1 DSM
Entries 139140
sumBC
DSM board

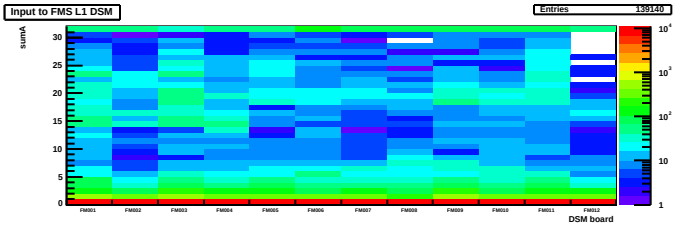

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

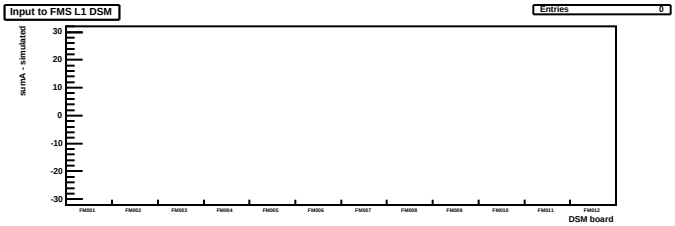

Input to FMS L1 DSM
Entries 139140
sumB
DSM board

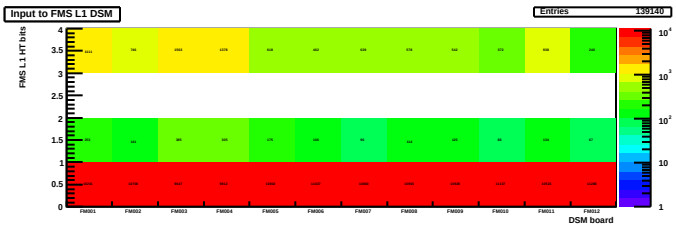

Input to FMS L1 DSM
Entries 765
sumB - simulated
DSM board

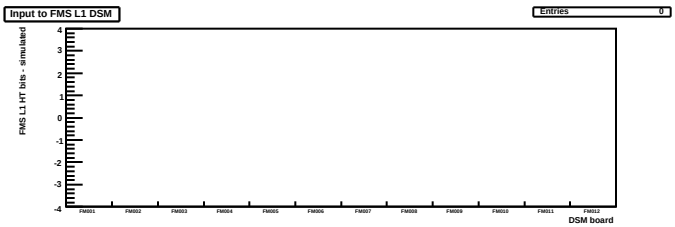

Input to FMS L1 DSM
Entries 139140
sumAB
DSM board

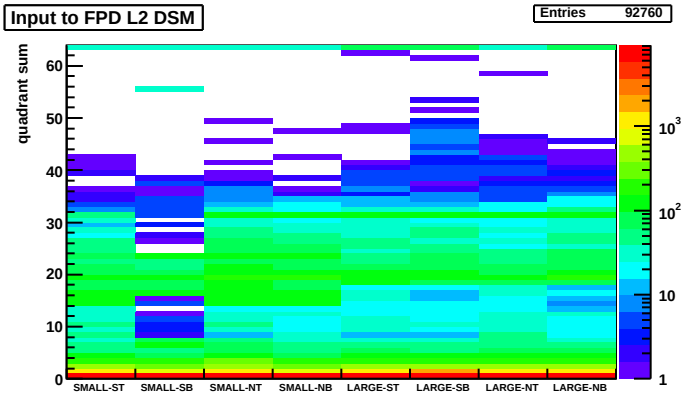

Input to FMS L1 DSM
Entries 418
sumAB - simulated
DSM board

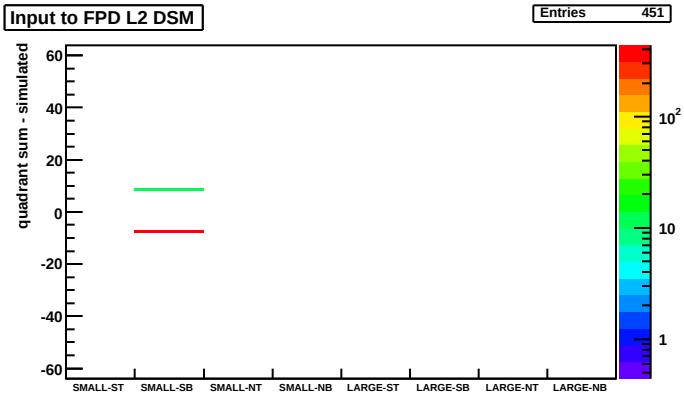

Input to FMS L1 DSM
Entries 139140
sumA
DSM board

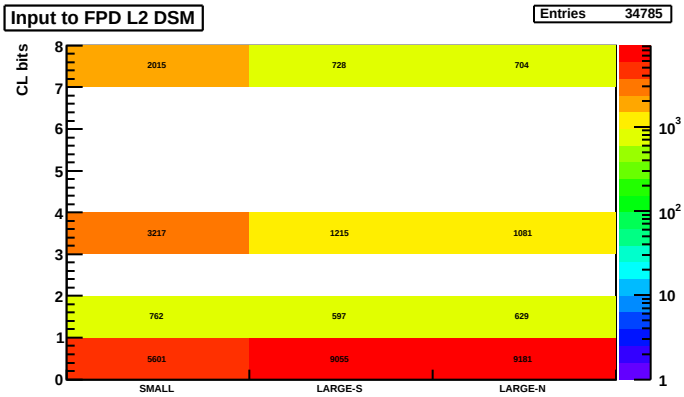

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

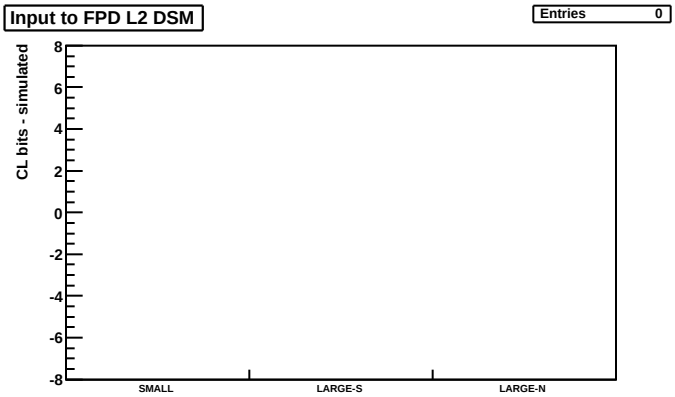

Input to FMS L1 DSM
Entries 139140
FMS L1 HT bits
DSM board

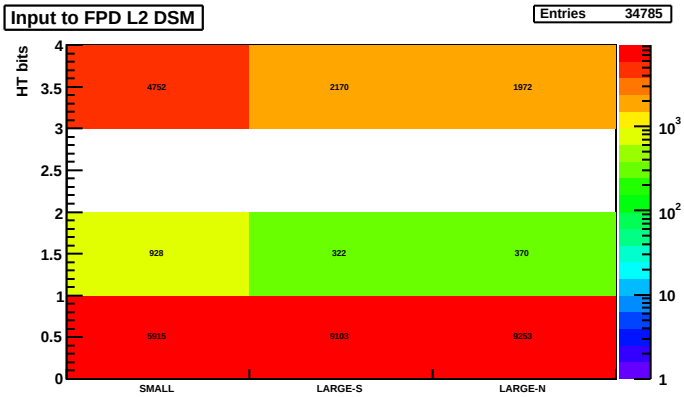

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

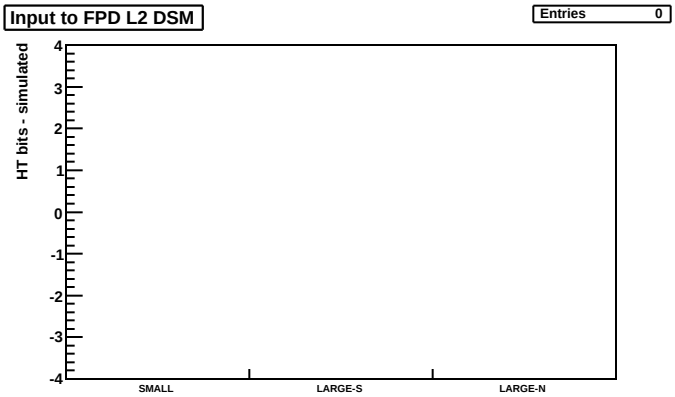

Input to FPD L2 DSM
Entries 92760
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

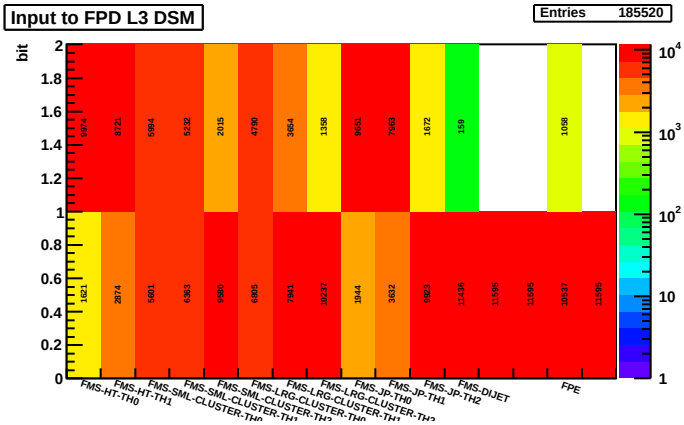

Input to FPD L2 DSM
Entries 451
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

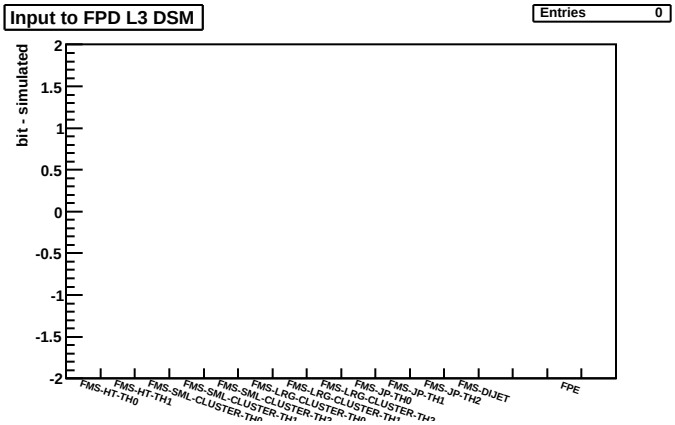

Input to FPD L2 DSM
Entries 34785
CL bits
2015
728
704
3217
1215
1081
762
597
629
5601
9055
9181
SMALL
LARGE-S
LARGE-N

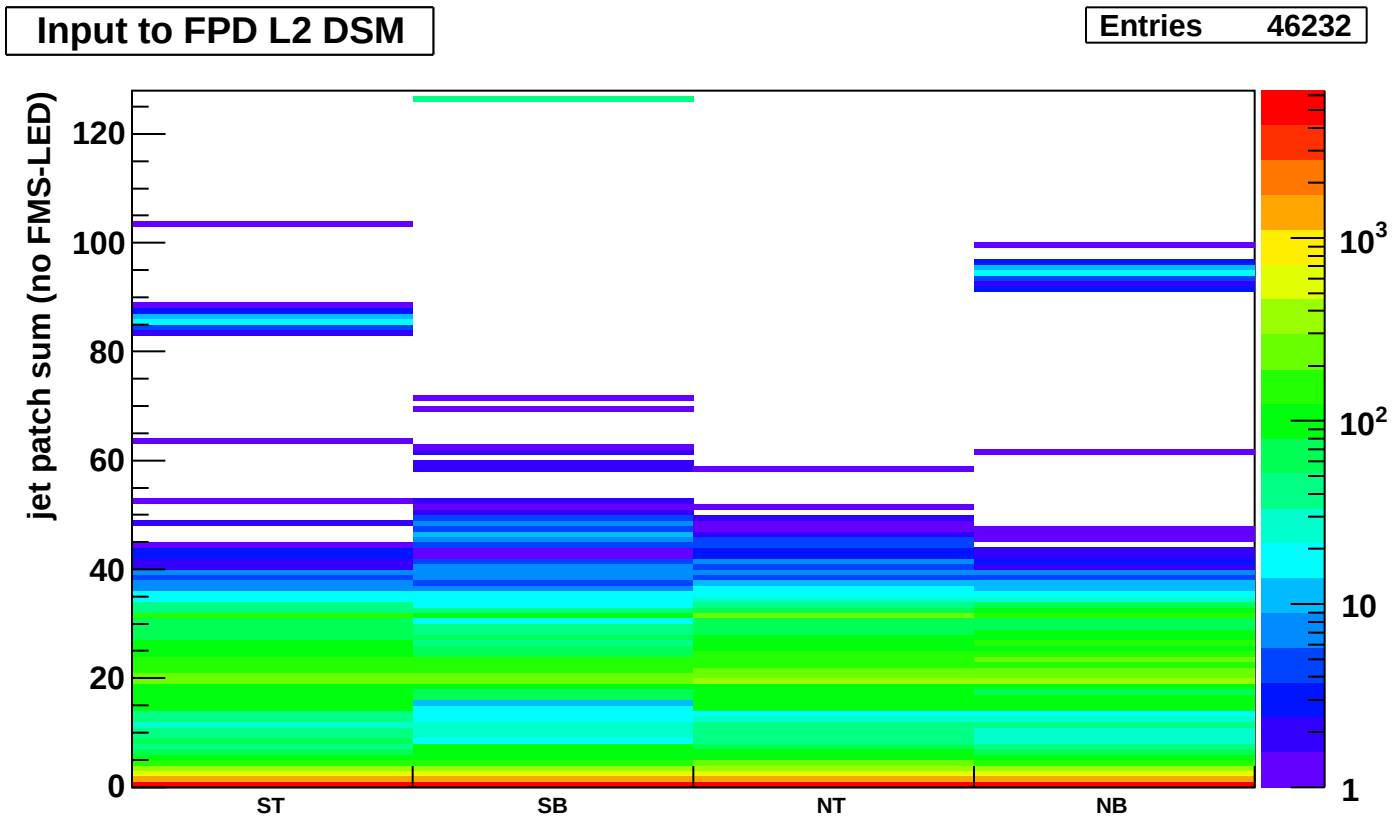

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

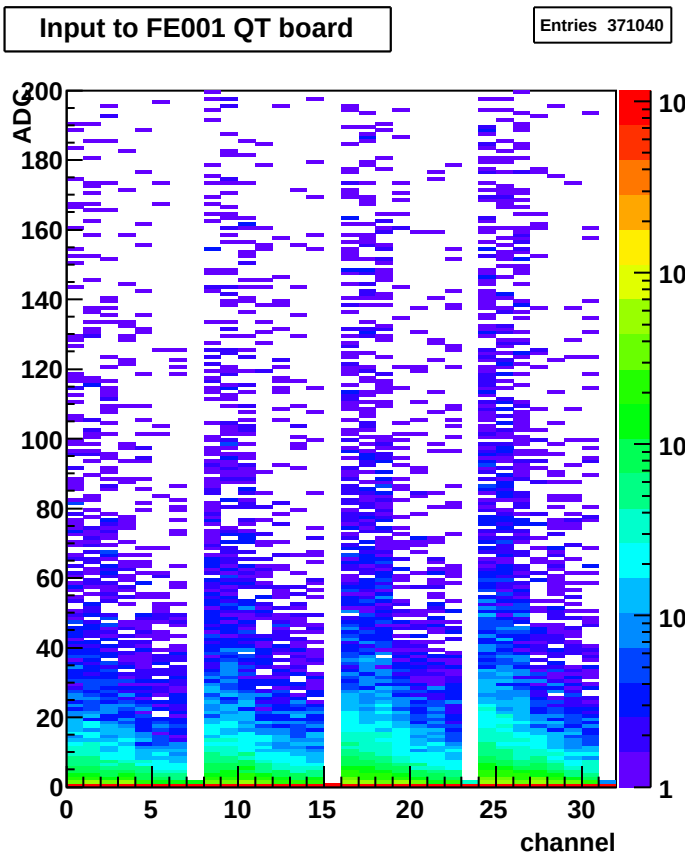

Input to FPD L2 DSM
Entries 34785
HT bits
4752
2170
1972
928
322
370
5915
9103
9253
SMALL
LARGE-S
LARGE-N

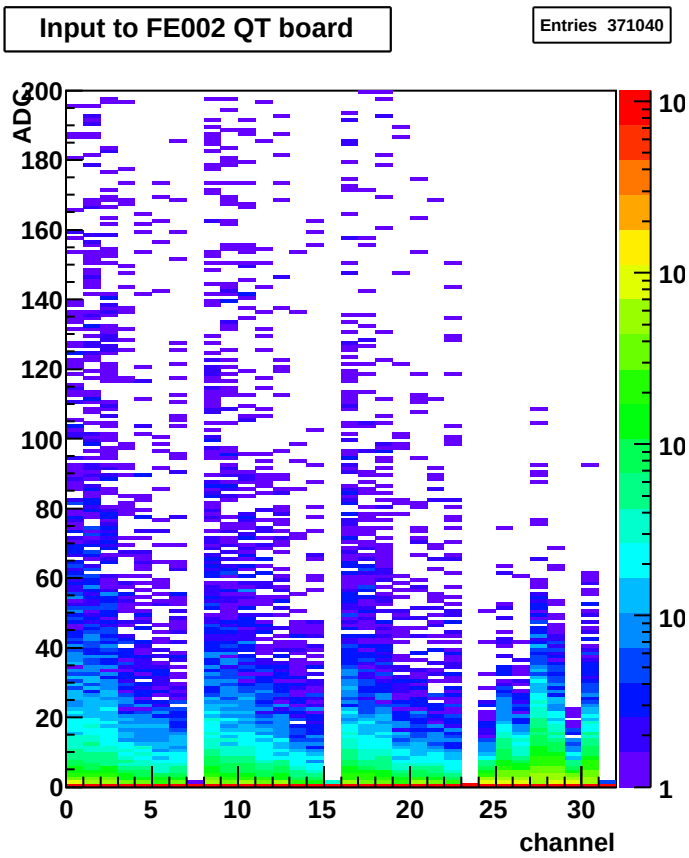

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

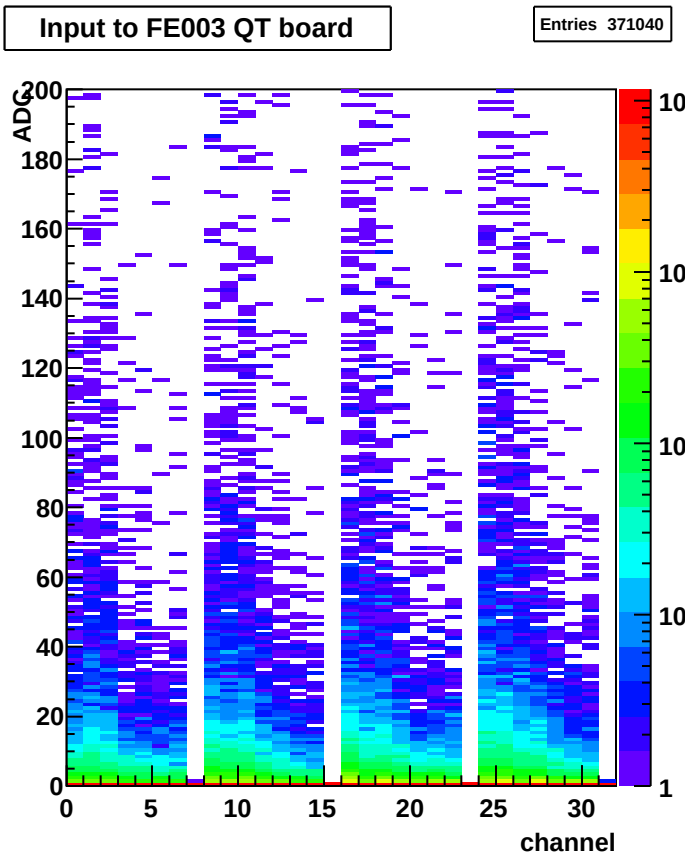

Input to FPD L3 DSM
Entries 185520
bit
9974
8721
5994
5232
2015
4790
3654
1358
9651
7063
1672
159
1098
1821
2874
5601
6383
9990
6805
7941
10237
1944
3622
9923
11436
11595
11595
10537
11595
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

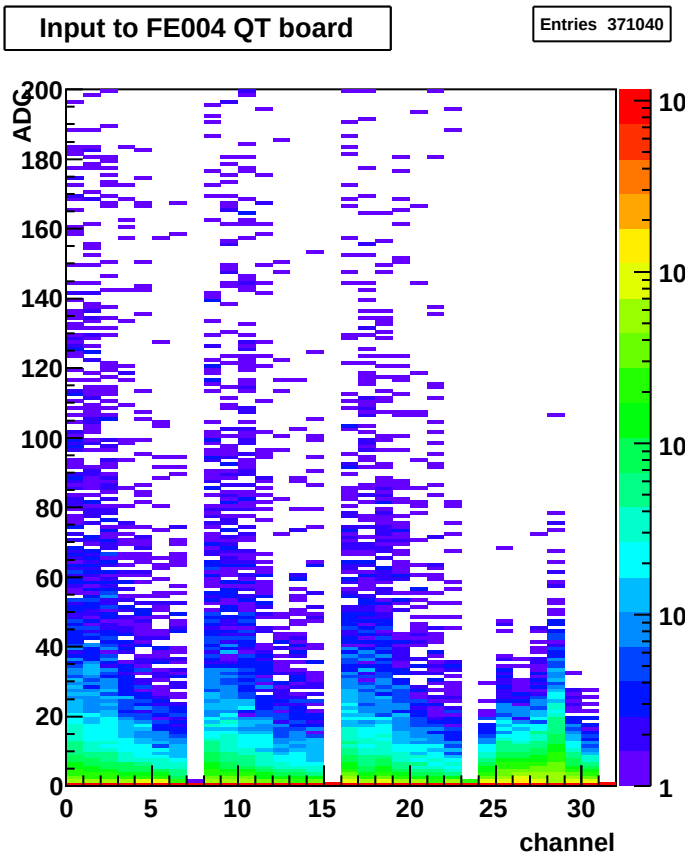

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

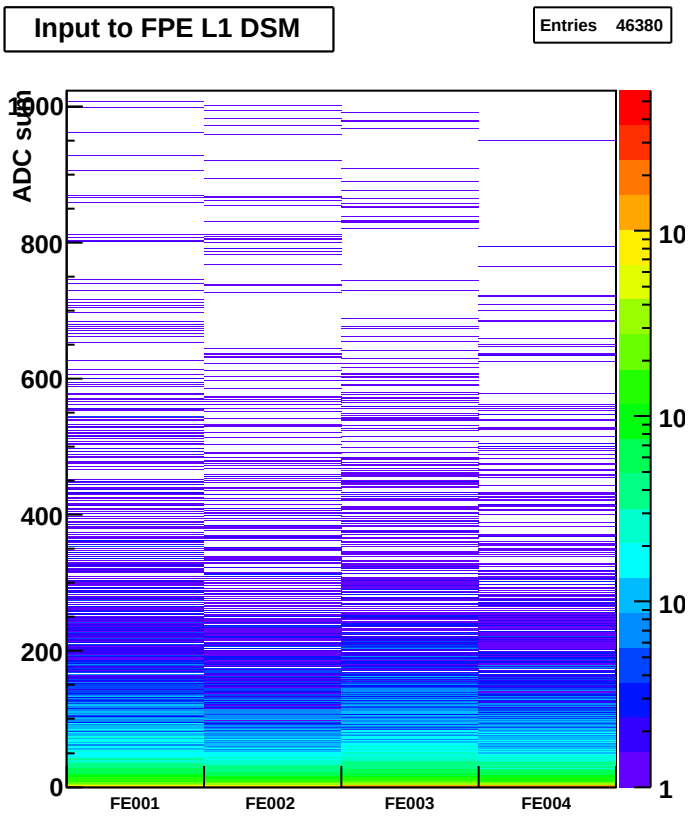

Input to FPD L2 DSM
Entries 46232
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3

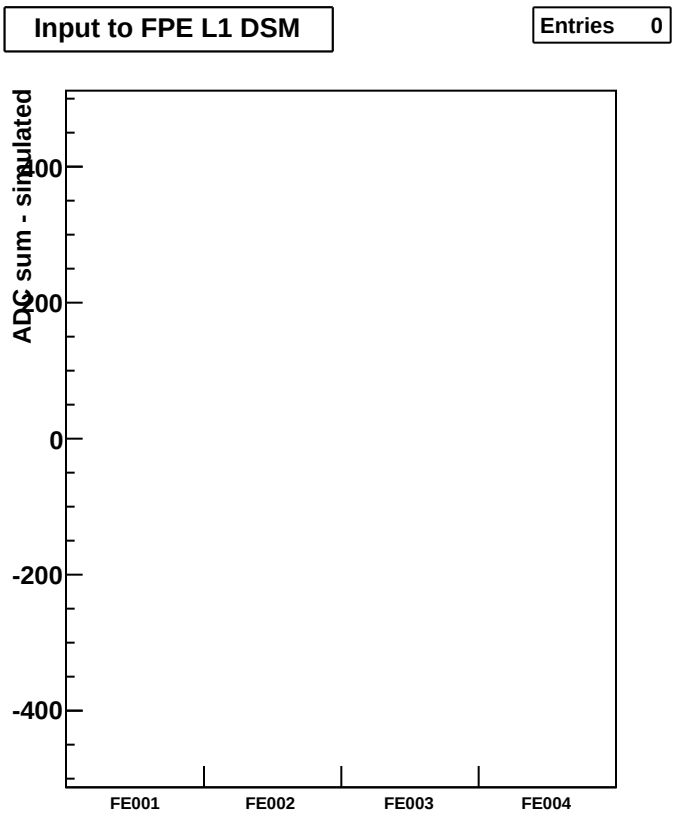

Input to FE001 QT board
Entries 371040
ADC
channel

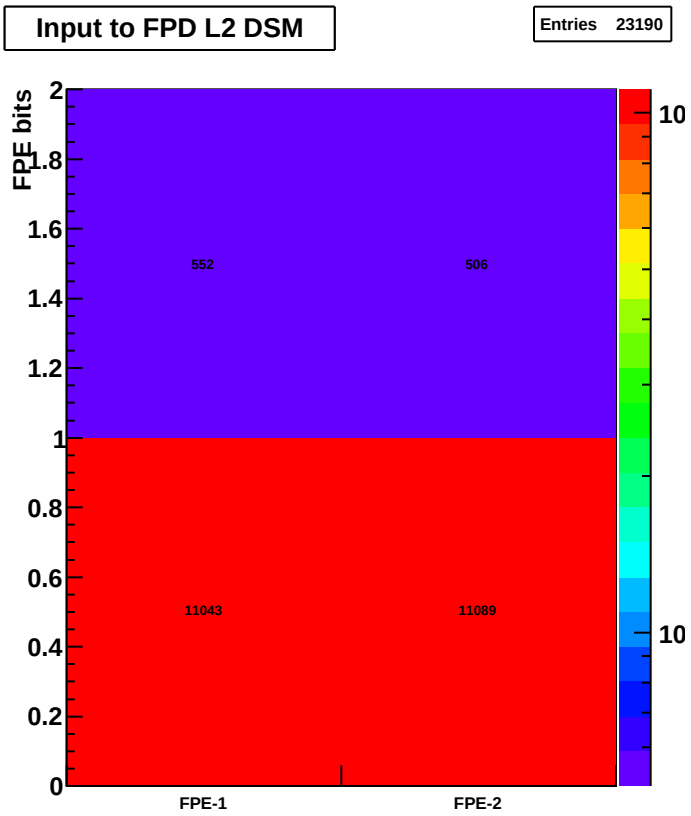

Input to FE002 QT board
Entries 371040
ADC
channel

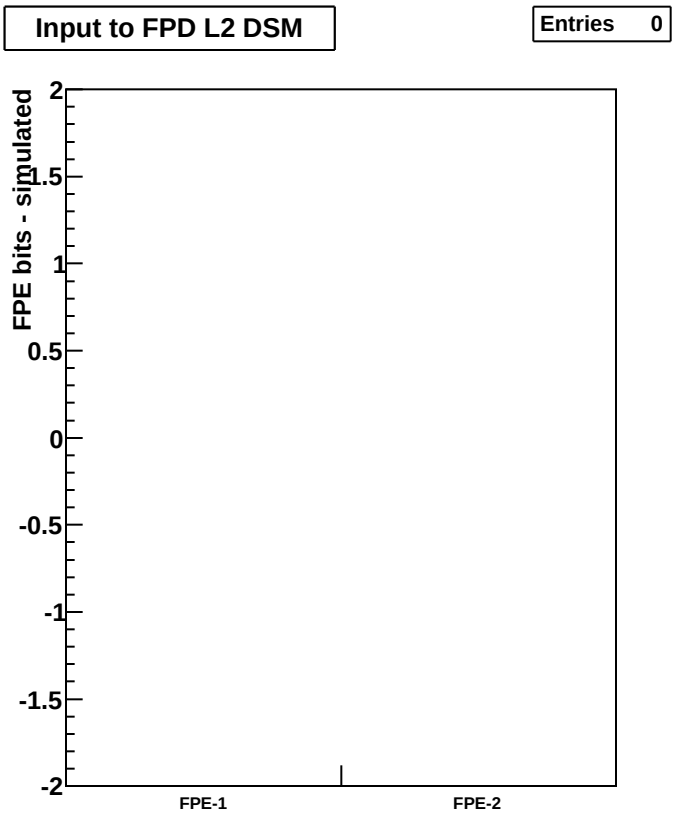

Input to FE003 QT board
Entries 371040
ADC
channel


Input to FE004 QT board
Entries 371040
ADC
channel


Input to FPE L1 DSM
Entries 46380
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 23190
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
552
506
11043
11089
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2